

Plan of Action

Steps

S₁ 1) Selector Variable For Arithmetic & Logic Circuits.
i.e, choose arithmetic or logic ckt
for output

S₀ 2) Selector For AND or XOR

S₃ 3) Selector For A_i or A_i' in

S₂ 4) Selector For B_i' or c

5) Equation for C [Intermediate Variable]

6) " for C_{in} [Input carry determined
by control bits]

Get selector var expressions \rightarrow 3 to 8 active
low decoder

Use Quad 2 to 1 for multiplexing

Use 4 bit adder.

cs2	cs1	cs0	Functions	X:	Y:	C _{in}
0	0	0	A-1	A _i	1	0
0	0	1	A-B-1/A+B'	A _i	B _i '	0
0	1	0	A-1	A _i	1	0
0	1	1	-A/A'+1	A _i '	0	1
1	0	0	A-B/A+B+1	A _i	B _i '	1
1	0	1	AB	A _i B _i	0	0
1	1	0	A ⊕ B	A ⊕ B	0	0
1	1	1	A ⊕ B	A ⊕ B	0	0

Arithmetic Unit

① operations (Decrement, Subtract with Borrow, Negation, Subtraction)

1, 2, 4
A_i & A_i'
S₃ (selection)

2, 4
B_i' & C_{in}
S₂

Inputs:-

~~Selection S₃~~

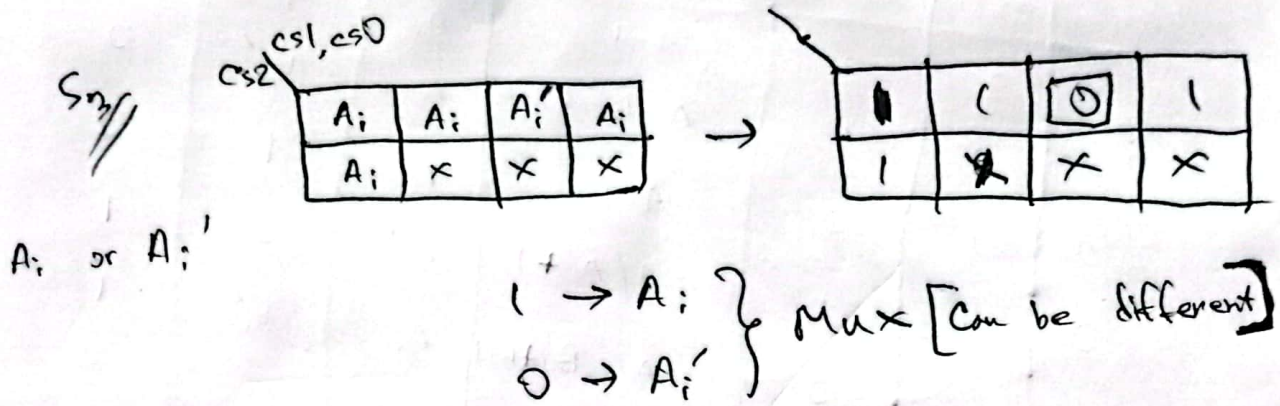
Here, C → selection
 (For 3) 0000
 (For 1) 1111

Basically use Quad 2 to 1 MUX
to control input to adder.

Here, S₃, S₂ & C → Expressions of control variables.

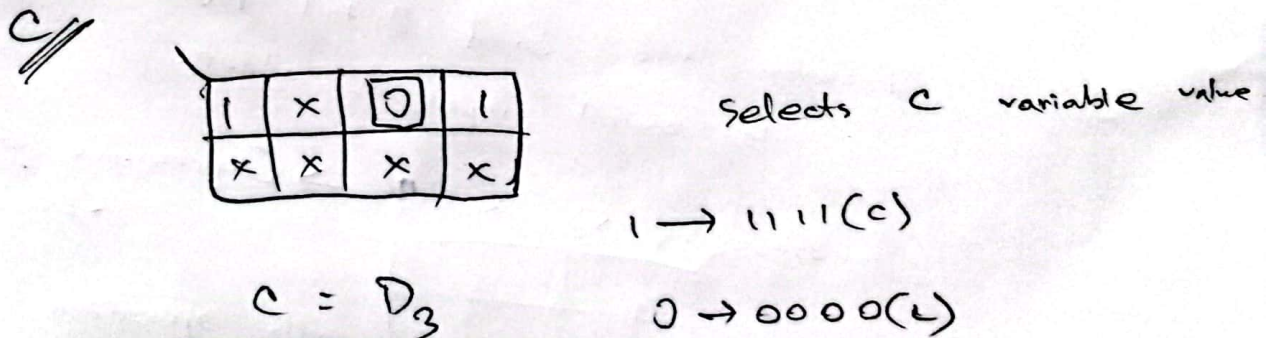
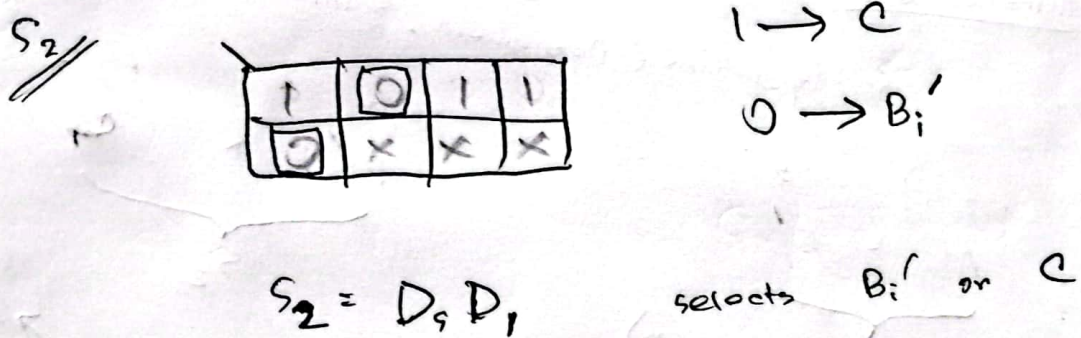
The numbers imply operations.

K Maps



$$S_3 = D_3 \text{ (Active low decoder)}$$

We will use decoders to control input.



$S_0 //$ → Selector For XOR & AND.

x	x	x	x
x	0	1	1

0 → AND

1 → XOR

$$S_0 = D_5$$

$S' //$ → Selector For logical or arithmetic operation.

1	1	1	1
1	0	0	0

$$S' = D_7 D_6 D_5$$

$$S_0 = \cancel{S'} = \cancel{D_7 D_6 D_5}$$

1 → Arithmetic

0 → logical

$C_{in} //$

0	0	1	0
1	x	x	x

$$C_{in} = \cancel{\overline{D_4 D_3}}$$

$$C_{in} = \overline{D_4 D_3} \quad \left. \begin{array}{l} \text{or } D_2 D_1 D_0 \end{array} \right\} \text{Need to check}$$