Zolatron Registers

Last edited: 7 Jun 2025

IRQ_REG – $00F6

7	6	5	4	3	2	1	0
ZD_IRQ	**RTC_ALARM**						
ZolaDOS program on Raspberry Pi has raised an interrupt	RTC alarm triggered an interrupt: 0=No 1=Yes						

STDIN_STATUS_REG – $00F4

7	6	5	4	3	2	1	0
(Reserved)	DUART_RxB_BUF_FULL_FL	DUART_RxB_DAT_RECVD_FL	DUART_RxB_NUL_RCVD_FL	(Reserved)	STDIN_BUF_FULL_FL DUART_RxA_BUF_FULL_FL	STDIN_DAT_RCVD_FL DUART_RxA_DAT_RECVD_FL	STDIN_NUL_RCVD_FL DUART_RxA_NUL_RCVD_FL
	Buffer full Port B	Data received Port B	Null received Port B		Buffer full Port A	Data received Port A	Null received Port A
						Port	B

We're currently using STDIN_xx_FL versions as Port A on the DUART is treated as the STDIN interface.

STDIN_CLEAR_FLAGS – This gets ANDed with STDIN_STATUS_REG to clear the ***lower*** 4 bits.

SYS_REG – $00F5

7	6	5	4	3	2	1	0
ZD_INT_FL	**(Reserved)**	**LCD SIZE**	**(Reserved)**	**(Reserved)**	**SYS_SPI**	**SYS_PARALLEL**	**SYS_EXMEM**
ZolaDOS program on Raspberry Pi has raised an interrupt		0=16x2, 1=20x4			System is fitted with SPI interface board. 0=No, 1=Yes	System is fitted with parallel interface board. 0=No, 1=Yes	System is fitted with extended memory board. 0=No, 1=Yes

Constants:

```
LCD_TYPE_16x2 = 0
LCD_TYPE_20x4 = 1
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ZD_CTRL_REG – PAGE 6

7	6	5	4	3	2	1	0
ZD_CTRL_EXCL_EXT							
Whether to exclude extension from max filename length *0 = No 1 = Yes							