

mBldcm Register Map ver. 1.10

1. Summary of Register Map

Offset	Register Name	R/W/H (*1)	Bit					Note
			31-4	3	2	1	0	
0x0	FREQTGT	R/W	FREQTGT					-
0x4	PHASE	R/W/H	RESERVED	PHASE				-
0x8	CTRL	R/W/H	RESERVED		BRK		EN	CTRL.BRK is not implemented now.
0xC	STAT	R	RESERVED			REFLECTEDFREQ	STOP	-

(*1) ... 'R' means read-only, 'W' means write-only and invalid value is read when read. 'R/W' means readable and writable. 'R/W/H' means R/W and the value can be changed by hardware.

2. Descriptions of Bit Fields

2.1. FREQTGT

Bit Field	Initial Value	Description
FREQTGT	32'h00000000	Target frequency of rotation (Rotate per second)

2.2. PHASE

Bit Field	Initial Value	Description
PHASE	4'h00	Phase of rotation (Range: 0 to 11)

2.3. CTRL

Bit Field	Initial Value	Description
BRK	-	RESERVED
EN	1'h0	Enable signal output. This bit is set, then mBldcm outputs a signal to excite a motor. Cleared, not output.

2.4. STAT

Bit Field	Initial Value	Description
STOP	1'h1	Indicates that motor is stopped. (0: Not stopped, 1: Stopped)
REFLECTEDFREQ	1'h1	Indicates that the value of FREQTGT is reflected. (0: Not reflected, 1: Reflected)