

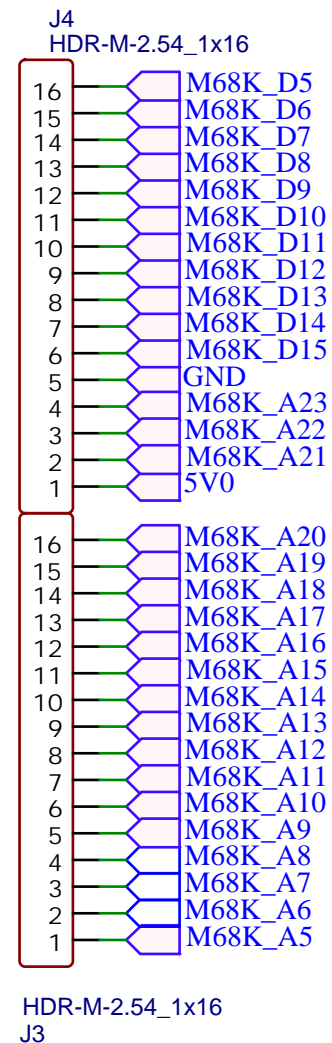
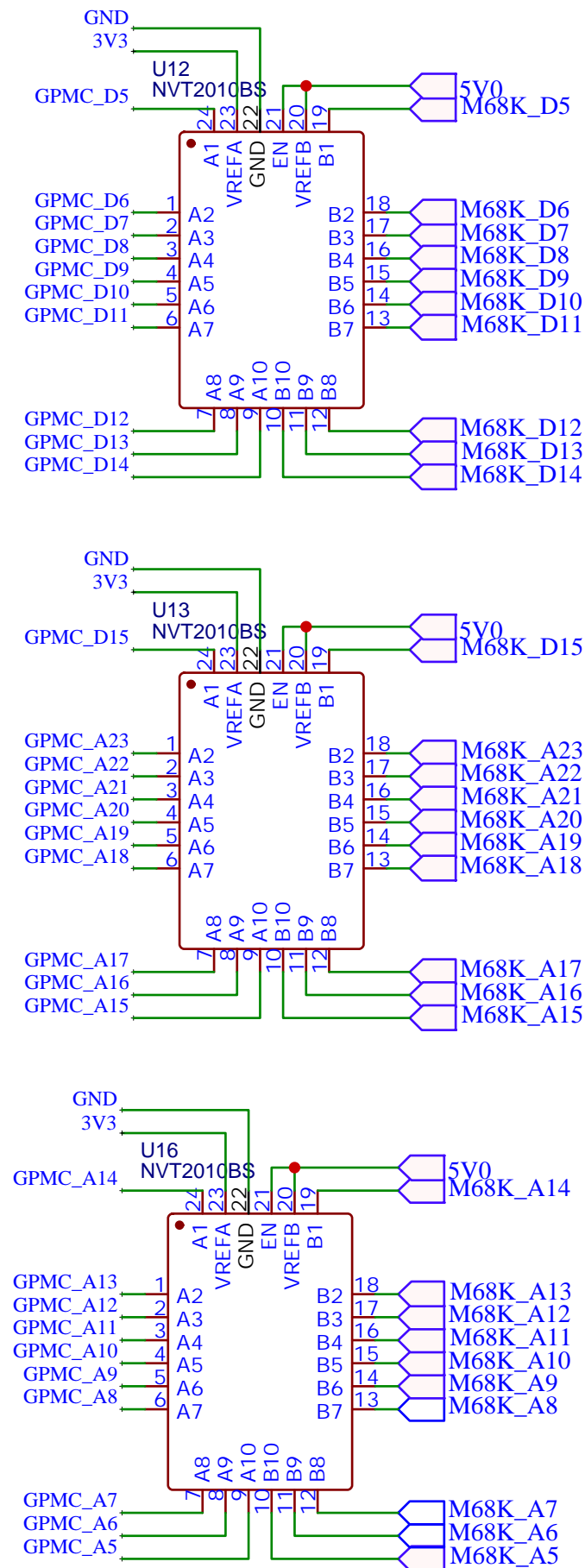
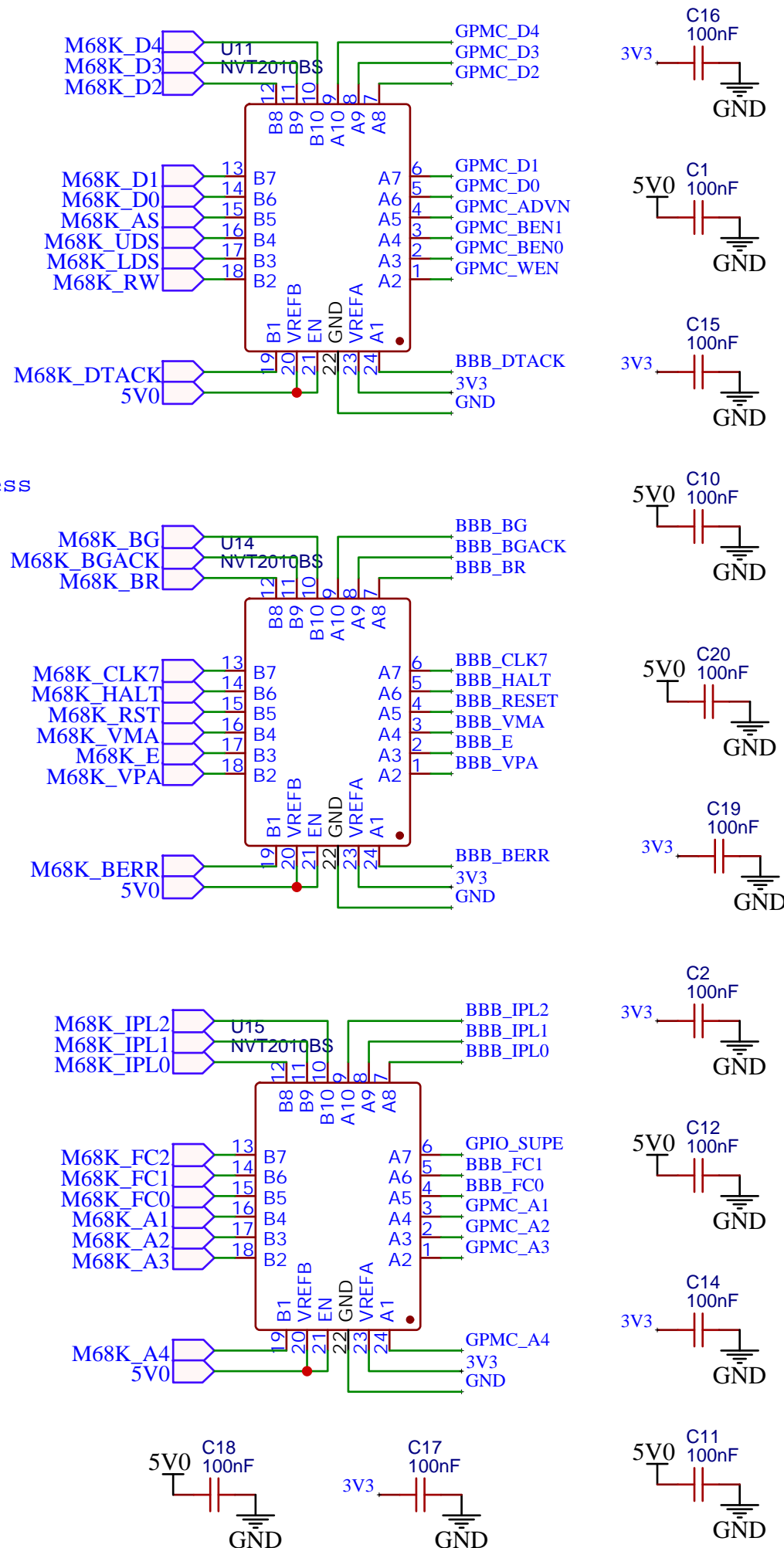
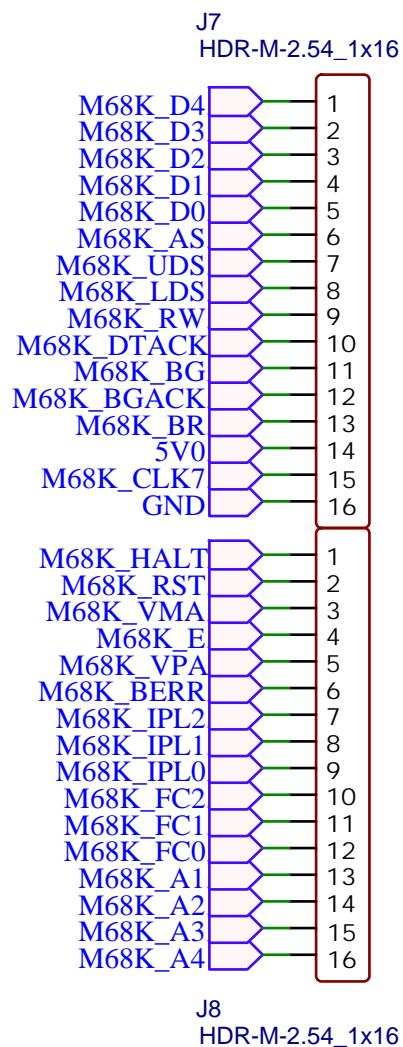
UDS: D15~D8
LDS: D7~D0

- 1 Lower byte access
UDS high:LDS low
- 2 Higher byte access
UDS low:LDS high
- 3 Word access
UDS low:LDS low

The BE0n/BE1n(byte enable) terminal is the following.

- 1 Lower byte access
BE0 low:BE1 high
- 2 Higher byte access
BE0 high:BE1 low
- 3 In the case of word access
BE0 low:BE1 low

VREFA: 3.3V
VREFB: 5.0V



FC2 = GPIO_SUPE
FC1 = A24 | GPIO_IACK
FC0 = ~A24 | GPIO_IACK

