Welcome to the

First International Workshop on High Performance Big Data Computing (HPBDC '15)

Goals and Objectives of the Workshop

- The amount of data is exploding
- Big Data middleware need to be designed with highperformance and scalability to process the exploding data
- Modern HPC systems and middleware (MPI, Parallel File Systems) have embraced new technologies like multicore/many-core, accelerators, RDMA networking, NVRAM, SSDs, etc. for many years
- Big Data middleware (Hadoop, HBase, Spark, etc.) have not embraced these new technologies completely
- HPC and Big Data are going into divergent trajectories
- Can they fall into convergent trajectories?
- What are the challenges to achieve such convergence?

Thanks

- Authors who submitted papers
- Program Committee Members
- Keynote and Invited Speakers
- Speakers, Session Chairs, Panel Moderator, and Panelists
- Attendees

Overview of Today's Program

- 9:00-10:00 am Keynote I: Dan Stanzione (TACC)
- 10:00-10:30 am Coffee Break
- 10:30-11:30 am Session I (Contributed Papers)
- 11:30-12:15 pm Invited Talk I: Jianfeng Zhan (ICT/CAS)
- 12:15-13:15 pm Lunch
- 13:15-14:15 pm Keynote II: Zhiwei Xu (ICT/CAS)
- 14:15-15:00 pm Invited Talk II: Raghunath Nambiar (Cisco)
- 15:00-15:15 pm Coffee Break
- 15:30-16:30 pm Session II (Contributed Papers)
- 16:30-18:00 pm Panel, Moderated by Jianfeng Zhan (ICT)
 Panelists: D. K. Panda (OSU), Dan Stanzione (TACC)
 Zhiwei Xu (ICT/CAS) and Xiaodong Zhang (OSU)
- 18:00-18:10 pm Closing Remarks

Some Guidelines

- Let's try to achieve a true workshop style interaction
 - Questions and discussions for each presentation
 - Keep time constraints in mind
 - Longer discussions can be done during the break/lunch time
- Speakers can check their laptops and presentations in advance (during the break)

Final Thanks ...

- Qin Feng (Workshop Chair)
- Carrie Stein (Conference Organizer)
- Xiaodong Zhang (ICDCS '15 General Chair)