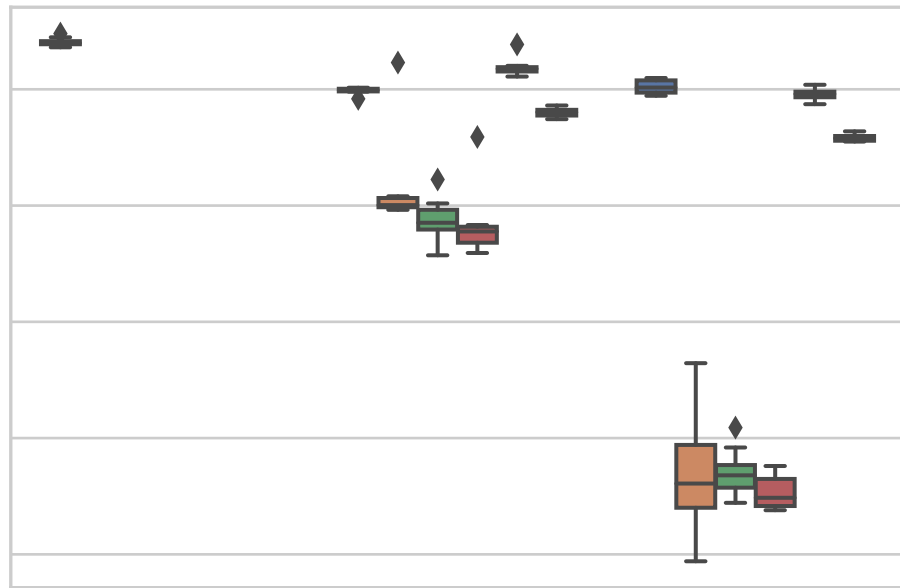


99-th Percentile Latency



Baseline

ANY

CPUMEM

$SUT_{N_{cph}}$

SUT_1

SUT_2

SUT_4

SUT_8

SUT_{16}

SUT_{32}