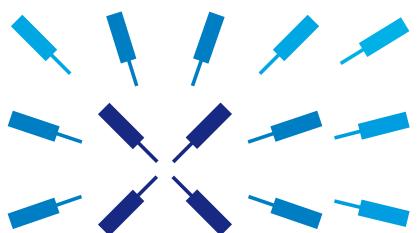


HF2 User Manual - ziControl Edition



Zurich
Instruments

HF2 User Manual - ziControl Edition

Zurich Instruments AG

Publication date Revision 49900

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Revision History

Revision 49900, 22-Dec-2017:

Document maintenance and editorial updates related to the 17.12 software release. Starting with LabOne 17.12, all HF2LI instruments support the LabOne web interface independent of whether the HF2LI-WEB option is installed or not.

Revision 45800, 29-Jun-2017:

Document maintenance and editorial updates related to the 17.06 software release.

Revision 42300, 06-Jan-2017:

Document maintenance and editorial updates related to the 16.12 software release.

Revision 38200, 14-July-2016:

Document maintenance and editorial updates related to the 16.04 software release.

Highlights of the changes and additions to the HF2LI product are:

- Zoom FFT: removed pk/rtHz and pk²/Hz units
- Zoom FFT: fixed frequency axis offset by 1 position
- HF2CA: fixed scaling of demodulator samples
- Specifications: removed environment policy

Revision 34390, 22-Dec-2015:

Document maintenance and editorial updates related to the 15.11 software release.

Highlights of the changes and additions to the HF2LI product are:

- New Option HF2LI-WEB: LabOne control software is now available for all HF2 series instruments
- Sweeper: faster sweeps due to improved instrument communication
- Modulation: change in FM generation method improves spectral purity. Limited to narrow-band FM signals

The HF2LI-WEB LabOne Web Interface comes with several new software tools that replace or extend the tools available in the Zurich Instruments ziControl software:

- PID: Tune function replaced by PID Advisor with extended DUT model library
 - SW Trigger: scope-like data capture of streamed data, unavailable in ziControl
 - Sweeper: LabOne Sweeper Reference replaces ziControl Sweeper Calibration and Reference
 - Sweeper: new sweep parameters available in LabOne Sweeper: modulation amplitude and index
-

-
- Sweeper: advanced and application mode: configuration assistant for settling time/inaccuracy and omega suppression
 - Sweeper: Q-factor extraction unavailable in LabOne Sweeper
 - Spectrum: LabOne Spectrum tab replaces ziControl zoomFFT tool
 - Spectrum: modes FFT(Theta), FFT(f), FFT(dTheta/dt) unavailable in ziControl
 - Spectrum: noise power analysis unavailable in LabOne Spectrum tab
 - Plotter: LabOne Plotter replaces ziControl Spectroscope. Larger selection of plotted data, support for multiple curves, more powerful plotting and analysis tools

Revision 26211, 30-Sep-2014:

Document maintenance - editorial updates.

- Sweeper: Sinc filter speed improvement
- Improved locking range for PLL and ExtRef mode (external reference)
- Up to date LabOne APIs

Revision 23220, 24-Apr-2014:

As of software release 14.02, Zurich Instruments combines the installers for the HF2 Series and UHF Series Instruments. Hence the installation of ziBase is superseded by the installation of LabOne package. Further, all programmer's reference has been moved to a separated LabOne Programming Manual document as Zurich Instruments provide one API that suits both HF2 and UHF users. The following sections of this document have been updated:

- The chapter on instrument programming has moved to the LabOne Programming Manual
- Updated getting started section with LabOne installation instructions
- New feature (HF2LI without HF2LI-option): added support for demodulation using second oscillator
- New feature (HF2LI-PID): added support for PID setpoint sweep
- New feature (Sweeper): added phase unwrap for polar coordinate display
- Specifications: official support for Windows 8
- Specifications: modified initial oscillator accuracy of HF2LI-UHS and HF2IS-UHS options from ± 0.15 ppm to ± 0.5 ppm
- Added section regarding location of log files in the Troubleshooting section
- Several minor editorial edits

Revision 15296, 20-Dec-2012:

Document update for 12.08 software release:

- Specifications: changed the AC range from 3.5 V to 3.3 V, and introduced AC signal limitation when AC coupling is used (max 0.6 V)
- Specifications: 100 V supply systems (Japan) require external transformer - updated specification and getting started
- Specifications: added performance diagram and test specifications for lock time and dynamic reserve
- HF2PLL: increased PLL gain for low quality factors ($Q = 4$ to 10) by up to factor 100, increased the range of PLL time constant parameter
- HF2LI-PID option: revised functional description, improved and expanded tutorials, added recipe to find PID parameters manually
- HF2LI-RT option: added several examples
- HF2IS: added description of impedance measurement with Nyquist plot in HF2TA section
- Added LabVIEW VIs to perform Q-Control and Tip Protection
- Added status indicator for supply voltage in Connectivity tab

Revision 13029, 26-Jul-2012:

Important document update for 12.02 software release:

-
- added support for arbitrary input unit scaling for HF2LI/HF2PLL/HF2IS
 - added integrated preamplifier unit conversion for HF2LI/HF2PLL/HF2IS
 - added flexible lock-in filter dynamics setting (BW, NEPBW, effective TC, TC per order)
 - reworked sweeper with multi-quantity support (frequency, phase, amplitude, offset), reference sweeps, calibration sweeps, persistent sweeps, ping pong sweeps, Nyquist plot function, HF2PLL Peak Analyzer (Q-Factor calculator)
 - reworked spectroscope with frequency analysis support
 - moved noise analysis tools from numerical tab to FFT spectrum analyzer
 - reworked FFT spectrum analyzer with windowing, new operation mode (continuous and block), and 2nd cursor
 - PID option inputs: added many features, new input units (frequency, Theta, others), PID Advisor (closed and open simulation, several DUT models), PID auto tuner, PID setpoint toggle, external PID setpoint, dual frequency resonance tracking (DFRT), cascaded PID
 - PID option outputs: added feature to output several complex quantities on auxiliary outputs, e.g. PID sideband analyzer, PID output (e.g. dissipation monitor dA), PID default output value
 - PLL option: improved HF2PLL Advisor
 - RT option: added named register, register formats (hex, int, float32), load and save of register configuration, improved performance of data function, reorganized chapter
 - added description for Python and MATLAB to programming chapter
 - reorganized programming and API descriptions
 - added support for Sync TTL output on DIO 0/1 connectors
 - updated tutorial chapter, added new tutorial for HF2LI-PID Quad-PID Controller option
 - revised and improved clarity of the specification chapter, added performance diagrams and test specifications
 - updated section on signal processing basics with settling time, filter characteristics
 - HF2TA: added applications and performance test specifications
 - added recommendation for preamplifier cable ordering
 - added quick links for customer support
 - revised all graphical user interface panels

Revision 7921, 26-May-2011:

Important document update for 11.02 software release:

- added Zoom FFT / FFT spectrum analyzer panel
- added Sinc filter to eliminate omega and 2-omega components to HF2LI
- added description of new HF2LI-PID option
- added support for rectangular output waveform (Signal Outputs)
- improved frequency sweeper panel interface
- added browsable history of the command log
- added output current specification
- added new tutorials first time HF2LI user
- performance: increased cumulative demodulator sample rate to up to 700 kSamples/s
- RT option: increased RT processor speed by factor of 4, improved graphical user interface, increased the number of available user registers to 64
- PLL option: added 2-omega PLL mode, improved PLL Advisor

Revision 5991, 22-Dec-2010:

Important document update for 10.06 software release:

- added specification of full range input sensitivity
 - added recalibration requirements, improved oscilloscope
 - updated lock-in triggering section
 - added documentation on noise measurement tool
 - added host computer hardware requirements
 - many updates in the functional descriptions
-

-
- added node definition chapter
 - updated RT and API chapters
 - updated software installation section
 - added output phase noise specification
 - clarification of NEP bandwidth
 - added AM/FM modulation and PLL tutorials
 - added HF2TA data sheet
 - added Linux installation requirements
 - refined internal oscillator specifications
 - added documentation of PLL Advisor
 - added description of the noise equivalent power bandwidth (NEPBW)
 - added description on SSH port forwarding

Revision 4502, 16-Jun-2010:

Major document revision for 10.03 software release:

- clarified the LabVIEW programming section

Revision 4368, 30-May-2010:

- PLL and AM/FM modulation options for HF2LI
- completely revised the functional description
- created 2 separated chapters for HF2IS/HF2LI graphical user interface descriptions
- added noise measurement and external reference tutorials
- exchanged all screenshots
- update of real-time section and all programming interfaces

- added maximum ratings table
- reduced auxiliary input sampling rate from 500 kSamples/s to 400 kSamples/s
- corrected internal oscillator accuracy (standard non-UHS specification)
- added detailed reference specifications

Revision 2670, 20-Oct-2009:

- dual-harmonic mode supported
- dual external reference mode supported
- new saving section in GUI
- time constant range massively increased
- added section on operation modes
- multi-frequency kit and real-time kit are different for HF2LI and HF2IS
- updated product selector
- added ultra-high stability option
- updated all images of graphical user interface

Revision 2002, 5-Aug-2009:

- first version of HF2 Series user manual
-

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Declaration of Conformity

The manufacturer

Zurich Instruments
Technoparkstrasse 1
8005 Zurich
Switzerland

declares that the product

HF2 Series (HF2LI, HF2IS), 50 MHz, 210 MSamples/s

fulfills the requirements of the European guidelines

- 2004/108/EC Electromagnetic Compatibility
- 2006/95/EC Low Voltage
- 2011/65/EU Restriction of Hazardous Substances (RoHS)
- 1907/2006/EC Registration, Evaluation, Authorization, and Restriction of Chemicals (REACH)

The assessment was performed using the directives according to [Table 1](#).

Table 1. Conformity table

EN 61326-1:2006	Emissions for industrial environments, immunity for industrial environments
EN 55011	Group 1, class A and B (the product was tested in typical configuration)
EN 61000-4-2	CD 4 kV, AD 8 kV
EN 61000-4-3	10 V/m 80% AM 80 MHz - 1 GHz
	3 V/m 80% AM 1 MHz - 2 GHz
	1 V/m 80% AM 2 MHz - 2.7 GHz
EN 61000-4-4	2 kV power line
	1 kV USB line
EN 61000-4-5	1 kV line-line, 2 kV line-earth
EN 61000-4-6	3 V 80% AM, power line
EN 61010-1:2001	Safety requirements for electrical equipment for measurement, control and laboratory use



Figure 1. CE Logo

Chapter 1. Getting Started

This first chapter guides you through the initial set-up of your HF2 Instrument in order to make your first measurements. This chapter comprises of:

- Package content and accessories list
- Software installation instructions
- Powering-on the device, connecting the device via USB, and performing basic operation checks on the instrument
- List of essential handling and safety instructions

This chapter is delivered as a hard copy with the instrument upon delivery. It is also the first chapter of the HF2 User Manual.

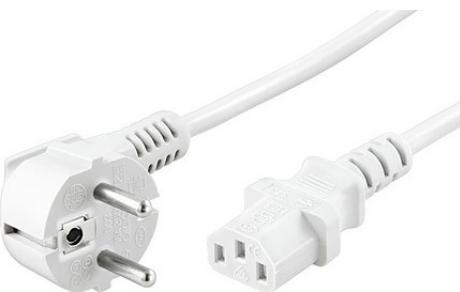
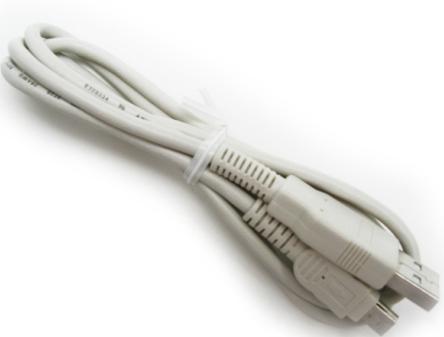
1.1. Inspect the Package Contents

If the shipping container appears to be damaged, keep the container until you have inspected the contents of the shipment and have performed basic functional tests.

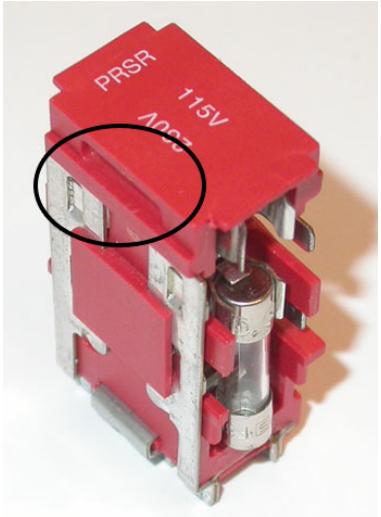
You must verify that:

- You have received 1 Zurich Instruments HF2 Instrument
- You have received 1 power cord with a power plug suited to your country
- You have received 1 USB cable
- A printed version of the "Getting Started" section
- Additional cables have been added to the delivery if an HF2 pre-amplifier has been delivered at the same time
- The line voltage selector on the HF2 Instrument power inlet indicates the correct line voltage of your country (115 V/60 Hz, or 230 V/50 Hz). While Zurich Instruments configures the power system when an instrument is initially delivered, no liability derives from potential wrong configuration or incorrect configuration at any point in time during the lifetime of the instrument
- The "Next Calibration" sticker on the rear panel of the Instrument indicates approximately 2 years ahead in time. Zurich Instruments recommends calibration intervals of 2 years
- The serial number of the instrument are displayed on a sticker on the back panel
- For Japanese users only: you are supposed to operate the HF2 Instruments with an external 100V to 110V transformer in order to have reliable measurement results. Please verify having received the transformer included in your delivery.

Table 1.1. Package contents for HF2 Instruments

	 Power cord (example: EU norm)	 USB cable
--	--	---

1.1. Inspect the Package Contents

 A black power inlet with a red digital display showing "230V". A red circle highlights the display. Below the display is a power switch with a "0" and a "1" position. At the bottom are two electrical outlets.	 A red fuse holder with a black circle highlighting the fuse area. The text "PRSR" and "115V 1000mA" is visible on the top of the holder.
Power inlet with selected 230 V/50 Hz power system	Fuse holder. Requires 2 x 20 mm fast-acting fuses with 800 mA current limit. To extract the fuse holder use a small screwdriver in the indicated spot to lift it out of the casing
S/N HF2-DEV1023 	"Next Calibration" sticker on the back panel of the instrument
 A black 100 V to 110 V transformer with a power cord. A yellow label on the side of the transformer provides technical specifications: "Input: 100VAC, 50/60Hz, 1.5A", "Output: 110VAC, 50/60Hz, 1.0A", "Type: SU-2000E", and "ZURICH INSTRUMENTS LTD.".	

Carefully inspect your HF2 Instrument. If there is mechanical damage or the instrument does not seem to operate after the [software installation](#), please consult the [handling instructions](#) and the [troubleshooting section](#), then notify the Zurich Instruments support team at <support@zhinst.com> as soon as possible.

1.2. Handling and Safety Instructions

The HF2 Instrument is a sensitive piece of electronic equipment, which under no circumstances should be opened, as there are high-voltage parts inside which may be harmful to human beings. There are no serviceable parts inside the instrument. Do not install substitute parts or perform any unauthorized modification to the product. Opening the instrument immediately cancels the warranty provided by Zurich Instruments.

Do not use this product in any manner not specified by the manufacturer. The protective features of this product may be affected if it is used in a way not specified in the operating instructions.

The following general safety instructions must be observed during all phases of operation, service, and handling of the instrument. The disregard of these precautions and all specific warnings elsewhere in this manual may affect correct operation of the equipment and its lifetime.

Zurich Instruments assumes no liability for the user's failure to observe and comply with the instructions in this user manual.

Table 1.2. Safety Instructions

Ground the instrument	The instrument chassis must be correctly connected to earth ground by means of the supplied power cord. The ground pin of the power cord set plug must be firmly connected to the electrical ground (safety ground) terminal at the mains power outlet. Interruption of the protective earth conductor or disconnection of the protective earth terminal will cause a potential shock hazard that could result in personal injury and potential damage to the instrument. For more information on the ground and earth scheme, refer to section Section 9.5 .
Measurement category	This equipment is of measurement category I (CAT I). Do not use it for CAT II, III, or IV. Do not connect the measurement terminals to mains sockets.
Maximum ratings	The specified electrical ratings for the connectors of the instrument should not be exceeded at any time during operation. Please refer to Chapter 9 for a comprehensive list of ratings.
Do not service or adjust anything yourself	There are no serviceable parts inside the Instrument.
Software updates	Frequent software updates provide the user with many important improvements as well as new features. Only the last released software version is supported by Zurich Instruments.
Overseas travel	Consider that a power system change without changing the orientation of the fuse holder will damage the fuses, or make the instrument behaving unpredictably
Warnings	Instructions contained in any warning issued by the instrument, either by the software, the graphical user interface, notes on the

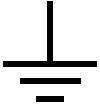
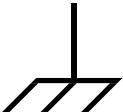
	instrument or mentioned in this manual must be followed.
Notes	Instructions contained in the notes of this user manual are of essential importance for the correct interpretation of the acquired measurement data.
High voltage transients due to inductive loads	When measuring devices with high inductance, take adequate measures to protect the Signal Input connectors against the high voltages of inductive load switching transients. These voltages can exceed the maximum voltage ratings of the Signal Inputs and lead to damage.
Location and ventilation	This instrument or system is intended for indoor use in an installation category II and pollution degree 2 environment as per IEC 61010-1. Do not operate or store the instrument outside the ambient conditions specified in Chapter 9 . Do not block the ventilator opening on the back or the air intake on the side of the chassis and allow a reasonable space for the air to flow.
Cleaning	To prevent electrical shock, disconnect the instrument from AC mains power and disconnect all test leads before cleaning. Clean the outside of the instrument using a soft, lint-free, cloth slightly dampened with water. Do not use detergent or solvents. Do not attempt to clean internally.
AC power connection and mains line fuse	For continued protection against fire, replace the line fuse only with a fuse of the specified type and rating. Use only the power cord specified for this product and certified for the country of use. Always position the device so that its power switch and the power cord are easily accessed during operation.
Main power disconnect	Unplug product from wall outlet and remove power cord before servicing. Only qualified, service-trained personnel should remove the cover from the instrument.
RJ45 sockets	The four RJ45 sockets on the back panel labeled "Peripheral ZCtrl 1/2" and "ZSync In/Out" are not intended for Ethernet LAN connection. Connecting an Ethernet device to these sockets may damage the Instrument and/or the Ethernet device.
Operation and storage	Do not operate or store at the instrument outside the ambient conditions specified in Chapter 9 .
Handling	Do not drop the Instrument, handle with due care, do not store liquids on the device as there is a chance of spilling and damage.

When you notice any of the situations listed below, immediately stop the operation of the Instrument, disconnect the power cord, and contact the support team at Zurich Instruments, either through the website form or by email at <support@zhinst.com>.

Table 1.3. Unusual Conditions

Fan is not working properly or not at all	Switch off the Instrument immediately to prevent overheating of sensitive electronic components.
Power cord or power plug on instrument is damaged	Switch off the Instrument immediately to prevent overheating, electric shock, or fire. Please exchange the power only with a power cord specified for this product and certified for the country of use.
Instrument emits abnormal noise, smell, or sparks	Switch off the Instrument immediately to prevent large damage.
Instrument is damaged	Switch off the Instrument immediately and secure it against unintended operation.

Table 1.4. Symbols

	Earth ground
	Chassis ground
	Caution. Refer to accompanying documentation
	DC (direct current)

1.3. Software Installation

The HF2 Series Instrument is operated from a host computer with the LabOne software. To install the LabOne software on a PC administrator rights are required. In order to simply run the software later, a regular user account is sufficient. Instructions for downloading the correct version of the software packages from the Zurich Instruments website are described below in the platform dependent sections. It is recommended to regularly update to the latest software version provided by Zurich Instruments. Thanks to the Automatic Update check feature, the update can be initiated with a single click from within the user interface as shown in [Section 1.4](#).

1.3.1. Installing LabOne on Windows

The installation packages for Zurich Instruments LabOne software are available as Windows installer .msi packages. The software is available on the Zurich Instruments download page, www.zhinst.com/downloads. Please ensure that you have administrator rights for the PC where the software is to be installed and that you download the correct software installer for the PC's processor architecture (32-bit or 64-bit), for help see [the section called “Determining PC Architecture on Microsoft Windows”](#). See www.zhinst.com/labone/compatibility for a comprehensive list of supported Windows systems.

Determining PC Architecture on Microsoft Windows

In case you are unsure which Windows architecture you are using, it can be checked as follows:

- Windows 7: Control panel → System and Security → System/System type
- Windows 8: Control panel → System → System/System type
- Windows 10: Settings → System → About/System type

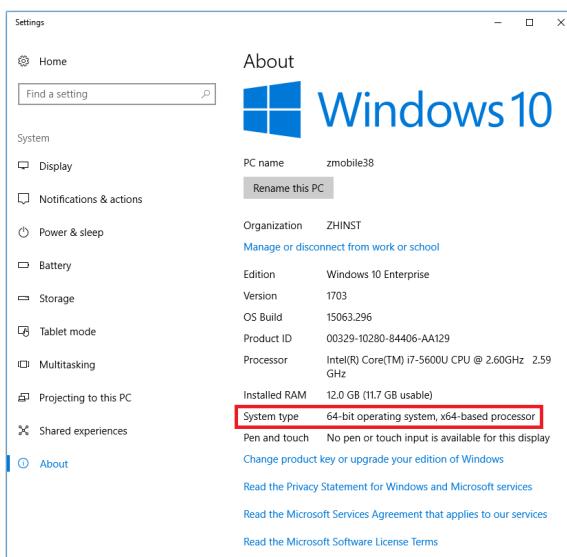


Figure 1.1. Find out the OS addressing architecture (32-bit or 64-bit)

Windows LabOne Installation

1. The HF2 Series Instrument should not be connected to your computer during the LabOne software installation process
2. Start the LabOne installer program with a name of the form `LabOne32/64-XX.XX.XXXXXX.msi` by a double click and follow the instructions. Windows Administrator rights are required for installation. The installation proceeds as follows:

- On the welcome screen click the **Next** button.

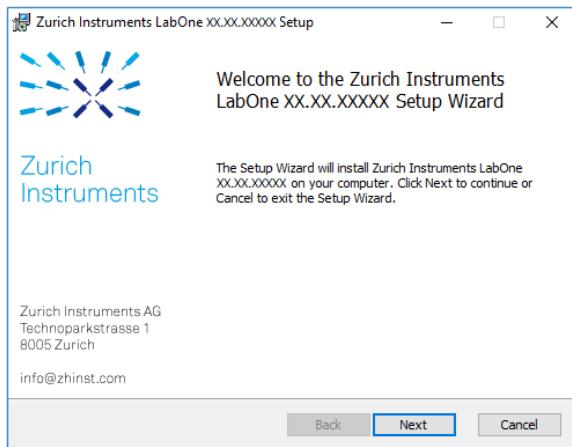


Figure 1.2. Installation welcome screen

- After reading through the Zurich Instruments license agreement, check the "I accept the terms in the License Agreement" check box and click the **Next** button.
- Review the features you want to have installed. For the HF2 Series Instrument the "HF2 Series Device", "Web Server" and "API" features are required. Please install the features for other device classes as well as required. If you would like to install shortcuts on your desktop area enable the feature "Desktop Shortcuts". To proceed click the **Next** button.

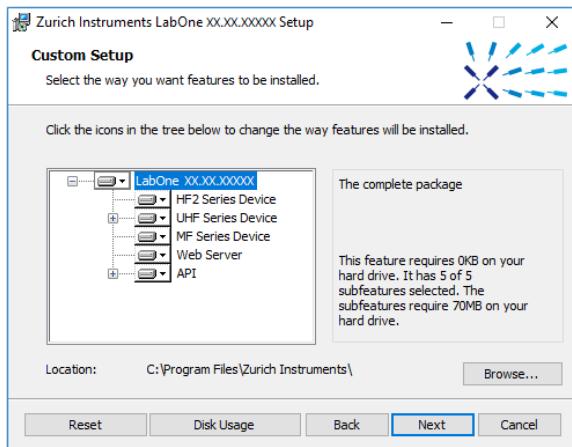


Figure 1.3. Custom setup screen

- Select whether the software should periodically check for updates. The software will not update automatically even with enabled periodic check for updates. This setting can later be changed in the user interface. To proceed click the **Next** button.

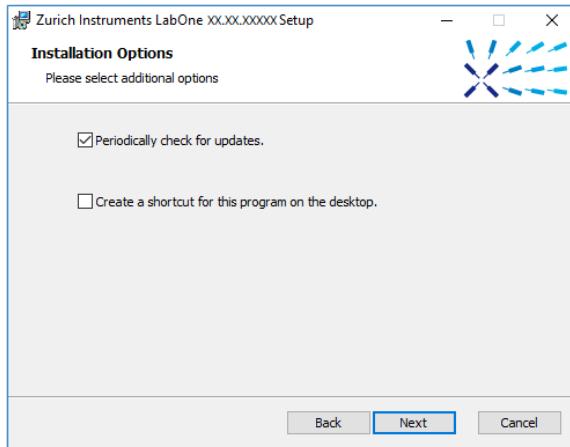


Figure 1.4. Automatic update check

- Click the **Install** button to start the installation process.
- Windows will ask up to two times to reboot the computer. Make sure you have no unsaved work on your computer. Actually a reboot is practically never required, so that one may safely click **OK**.

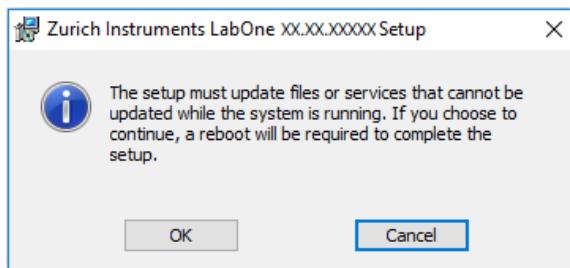


Figure 1.5. Installation reboot request

- On Windows Server 2008 and Windows 7 it is required to confirm the installation of up to 2 drivers from the trusted publisher Zurich Instruments. Click on **Install**.



Figure 1.6. Installation driver acceptance

- Click **OK** on the following notification dialog.

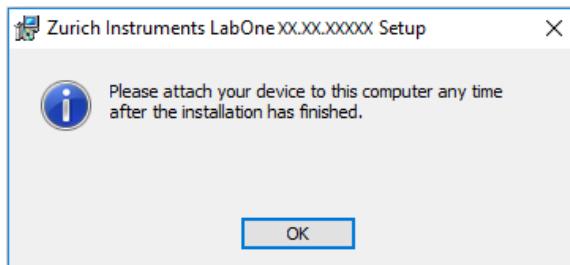


Figure 1.7. Installation completion screen

3. Click **Finish** to close the Zurich Instruments LabOne installer.

Warning

Do not install drivers from another source and therefore not trusted as originating from Zurich Instruments.

Windows ziControl Installation

1. Unpack the file with a name of the form `ziControl-00.00.0.00000-win.zip` and run the `Setup.exe` installer contained in the unpacked folder.
2. Choose the destination directory for the ziControl software, by default:
`C:\Program Files\Zurich Instruments\ziControl`
and for the National Instruments products, by default:
`C:\Program Files\National Instruments`
Click on **Next**.
3. License agreement for National Instruments Software. The installer package contains the LabVIEW Run-Time Engine by National Instruments, which is free of charge. This Run-Time Engine must be installed on any Windows system where you plan to run executables that were built with the LabVIEW Application Builder. If you accept the license click on **I accept the license agreement** and click on **Next**.
4. Summary of the installation, click **Next** to proceed to start the installation.
5. Click **Finish** to finish installation. All the required software is now installed on the computer.

Note

If you encounter problems regarding the application font, go to the installation directory of ziControl and adapt the font settings in the configuration file `ziControl.ini`. Alternatively, you can also start the application `ziFontConfig.exe` (located in the installation directory) with elevated access rights in order to adjust it automatically.

1.3.2. Installing LabOne on Linux

Requirements

Ensure that the following requirements are fulfilled before trying to install the LabOne software package:

1. Officially, Ubuntu 14.04 LTS and 16.04 LTS (amd64) are supported although in practice LabOne software may work on other platforms. Please ensure that you are using a Linux distribution that is compatible with Ubuntu/Debian.
2. You have administrator rights for the system.
3. The correct version of the LabOne installation package for your operating system and platform have been downloaded from the Zurich Instruments [downloads page](#):
 - LabOneLinux<arch>-<release>.<revision>.tar.gz, for example:

```
LabOneLinux32/64-16.12.41721.tar.gz
```

Please ensure you download the correct architecture (32-bit/64-bit) of the LabOne installer. The `uname` command can be used in order to determine which architecture you are using, by running:

```
uname -m
```

in a command line terminal. If the command outputs "x86_64" the 32-bit version of the LabOne package is required, if it displays "x86_64" the 64-bit version is required.

Linux LabOne Installation

Proceed with the installation in a command line shell as follows:

1. Extract the LabOne tarball in a temporary directory:

```
tar xzvf LabOneLinux<arch>-<release>.<revision>.tar.gz
```

2. Navigate into the extracted directory.

```
cd LabOneLinux<arch>-<release>.<revision>
```

3. Run the install script with administrator rights and proceed through the guided installation, using the default installation path if possible:

```
sudo bash install.sh
```

The install script lets you choose between the following three modes:

- Type "a" to install the Data Server program, the Web Server program, documentation and APIs.
 - Type "u" to install udev support (only necessary if HF2 Instruments will be used with this LabOne installation and not relevant for other instrument classes).
 - Type "ENTER" to install both options "a" and "u".
4. Test your installation by running the software as described in the next section.

Linux ziControl Installation

Please ensure that the LabOne package has been installed as described above in [the section called "Linux LabOne Installation"](#). The ziControl installer package optionally installs the LabVIEW Run-Time Engine by National Instruments, please install it to use ziControl if it's not already available on your system.

Please proceed with the installation in a command line shell as follows:

1. Extract the ziControl tarball in a temporary directory:

```
tar xzvf ziControl-<release>.<revision>-linux.tar.gz
```

2. Navigate into the extracted directory.

- ```
cd ziControl-<release>-<revision>-linux
```
3. Run the install script with administrator rights and proceed through the guided installation, using the default installation path if possible:
- ```
sudo bash install.sh
```
- The install script lets you choose between the following three modes:
- Type "a" to install ziControl and ziFontConfig.
 - Type "l" to install the LabVIEW Run-Time Engine 2010 SP1 32-bit from National Instruments as a deb-package.
 - Type "ENTER" to install both the options "a" and "l".
4. Start the graphical user interface ziControl by typing:

```
ziControl &
```

Note

If you encounter problems regarding the application font, go to the installation directory of ziControl and adapt the font settings in the configuration file `ziControl.ini`. Alternatively, you can also invoke the shell command `gksu ziFontConfig` in order to adjust it automatically.

Running the Software on Linux

The following steps describe how to start the LabOne software in order to access and use your instrument in the User Interface.

1. Check whether the HF2 Data Server is already running using the "ziService" program:

```
$ ziService status
```

If udev support was installed, the HF2 Data Server program "ziServer" should already be running. If not, start the Data Server manually at a command prompt:

```
$ ziServer
```

If udev support was installed, then the HF2 Data Server program is automatically started upon plugging in the HF2's USB cable and powering the instrument.

2. Start the ziControl User Interface:

```
$ ziControl
```

You should be able to access your instrument. In case of problems please consult the [troubleshooting section](#) at the end of this chapter.

Important

Do not use two Data Server instances running in parallel, only one instance may run at a time.

If your command log window is flooded with messages after starting the HF2LI Data Server stop the program; it is likely that another instance of the Data Server is already running. Verify whether a Data Server is already running as described above using the ziService program.

Uninstalling LabOne and ziControl on Linux

The LabOne software package copies an uninstall script to the base installation path (the default installation directory is `/opt/zi/`). To uninstall the LabOne package please perform the following steps in a command line shell:

1. Navigate to the path where LabOne is installed, for example, if LabOne is installed in the default installation path:

```
$ cd /opt/zi/
```

2. Run the uninstall script with administrator rights and proceed through the guided steps:

```
$ sudo bash uninstall_LabOne<arch>-<release>-<revision>.sh
```

```
$ sudo bash uninstall_ziControl-<release>-<revision>.sh
```

1.4. Software Update

1.4.1. Updating LabOne using Automatic Update Check

In case "Periodically check for updates" has been enabled during the LabOne installation and LabOne has access to the internet, a notification will appear on the Device Connection dialog whenever a new version of the software is available for download. In that case, clicking on the button "Update Available" shown in [Figure 1.8](#) will start a download the latest LabOne installer for Windows or Linux, see [Figure 1.9](#). After download, proceed as explained in [Section 1.3](#) to update LabOne.



Figure 1.8. Device Connection dialog: LabOne update available

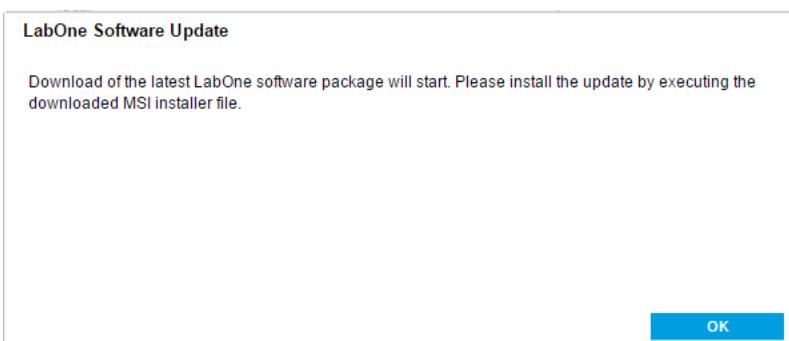


Figure 1.9. Download LabOne MSI using Automatic Update Check feature

1.5. Troubleshooting

This section aims to help the user solve and avoid problems whilst using the software and operating the instrument.

1.5.1. Common Problems

Your HF2 Series Instrument is an advanced piece of laboratory equipment which has many more features and capabilities than a traditional lock-in amplifier. In order to benefit from these, the user needs access to a large number of settings in the LabOne User Interface. The complexity of the settings might overwhelm a first-time user, and even expert users can get surprised by certain combinations of settings. To avoid problems, it's good to use the possibility to save and load settings in the Config Tab. This allows one to keep an overview by operating the instrument based on known configurations. This section provides an easy-to-follow checklist to solve the most common mishaps.

The software cannot be installed or uninstalled: please verify you have Windows administrator rights. Windows systems: if prompted or required install the .NET Framework, see [Section 1.5.3](#).

The Instrument does not turn on: please verify the power supply connection and inspect the fuse. The fuse holder is integrated in the power connector on the back panel of the instrument.

The HF2 ziControl starts with unexpected messages: although Windows XP is fully supported by the HF2 software, Windows XP is known for its USB weaknesses that can generate conflicts between drivers and/or some USB peripherals (e.g. web cams). On Windows XP even non-aggressor devices can lead to conflicts. This problem can also occur with later versions of Windows, but less likely. For computers with many USB peripherals it might be beneficial to remove the unnecessary ones and to update the drivers.

The LabOne Session Dialog shows a "WEB Option missing" message and the user interface screen is almost empty: your instrument does not support measurements with the browser-based LabOne user interface. Use the ziControl user interface instead. Note that nonetheless you need to install the LabOne software package and the ziControl user interface from the same software release.

The HF2 Instrument turns on but delivers obviously wrong measurements: please verify the power system setting on the back panel of the device is set to the power system of your country (110 V / 60 Hz, 220 V / 50 Hz). Make sure the fuse holder is set to the correct power supply position. This means that the wanted power supply label, 230 V or 115 V, must be positioned beside the edge of the power socket (e.g. not beside the power switch).

The HF2 Instrument performs poorly in a country with 100 V supply system (e.g. Japan): if no 100 V to 110 V transformer is used, the internal power supplies might be below specifications and some circuits might perform worse than specification. Users in countries with 100 V supply system are warmly recommended to use an external transformer (delivered with the instrument).

The HF2 Instrument shows limited data throughput on USB: although the host computer requirements are not particularly demanding, highest performance in USB throughput will require a performing desktop. The USB might be limiting the data throughput, please see [Table 9.4](#) for more details. Many concurrent transfers on the USB will limit the individual transfer. In particular the Scope should be turned off when not needed by the application. The status of the USB transfer can be monitored in the Status Tab.

The Instrument performs poorly in single-ended operation: the signal inputs of the instrument might be set to differential operation. Please ensure that differential input mode is turned off in the Lock-in Tab or In / Out Tab.

The HF2 Instrument has a high input noise floor: the USB cable connects the Instrument ground to computer ground, which might inject some unwanted noise to the measurements results. In order to decouple the computer from the Instrument consider using an electrically isolating USB range extender supporting 480 Mbit/s data transfer rate. Zurich Instruments recommends the models USB 2.0 Ranger 2201 (Icron technologies) and U-Link USB 2.0 extender (Sewell). The power supply delivered with the range extender may need to be exchanged with a more stable power supply for optimum noise performance.

The Instrument performs poorly at low frequencies (below 10 kHz) : the signal inputs of the instrument might be set to AC operation. Please verify to turn off the AC switch in the Lock-in Tab or In / Out Tab.

The Instrument performs poorly during operation: the demodulator filters might be set too wide (too much noise) or too narrow (slow response) for your application. Please verify if the demodulator filter settings match your frequency versus noise plan.

The Instrument performs poorly during operation: clipping of the input signal may be occurring. This is detectable by monitoring the red LEDs on the front panel of the instrument or the Input Overflow (OVI) flags on the Status Tab of the user interface. It can be avoided by adding enough margin on the input range setting (for instance 50% to 70% of the maximum signal peak).

The Instrument performs strangely when working with the HF2-MF Multi-frequency Option: it is easily possible to turn on more signal generators than intended. Check the generated Signal Output with the integrated oscilloscope and check the number of simultaneously activated oscillator voltages.

The Instrument performs close to specification, but higher performance is expected: after 2 years since the last calibration, a few analog parameters are subject to drift. This may cause inaccurate measurements. Zurich Instruments recommends re-calibration of the Instrument every 2 years.

The Instrument measurements are unpredictable: please check the Status Tab to see if any of the warning is occurring (red flag) or has occurred in the past (yellow flag).

The Instrument does not generate any output signal: verify that signal output switch has been activated in the Lock-in Tab or the In / Out Tab.

The Instrument locks poorly using the digital I/O as reference: make sure that the digital input signal has a high slew rate and clean level crossings.

The Instrument locks poorly using the auxiliary analog inputs as reference: the input signal amplitude might be too small. Use proper gain setting of the input channel.

The sample stream from the Instrument to the host computer is not continuous: check the communication (COM) flags in the status bar. The three flags indicate occasional sample loss, packet loss, or stall. Sample loss occurs when a sampling rate is set too high (the instruments sends more samples than the interface and the host computer can absorb). The packet loss indicates an important failure of the communications to the host computer and compromises the behavior of the instrument. Both problems are prevented by reducing the sample rate settings. The stall flag indicates that a setting was actively changed by the system to prevent UI crash.

The Instrument is connected but there is no communication to the computer: check the clock fail (CF) flag in the status bar. This abnormal situation can occur if "Clk 10 MHz" is selected as Clock Source but no clock signal is fed to the Instrument. If Internal clock source is selected and the flag is still active, then the situation might indicate a serious hardware failure. In this case contact Zurich Instruments support team at <support@zhinst.com>.

The LabOne User Interface does not start (when running the LabOne on a PC): verify that the LabOne Data Server (ziServer.exe for HF2 or ziDataServer.exe for other instruments) and

the LabOne Web Server (`ziWebServer.exe`) are running via the Windows Task Manager. The Data Server should be started automatically by `ziService.exe` and the Web Server should be started upon clicking "Zurich Instruments LabOne" in the Windows Start Menu. If both are running, but clicking the Start Menu does not open a new User Interface session in a new tab of your default browser then try to create a new session manually by entering `127.0.0.1:8006` in the address bar of your browser.

The user interface does not start or starts but remains idle: verify that the ziServer HF2 Instrument has been started and are running on your host computer.

1.5.2. Location of the Log Files

The log files of the LabOne server programs are most easily accessed by clicking on **Logs** in the LabOne Device Connection dialog. The Device Connection dialog opens on software start-up or upon clicking on **Session Dialog** in the Config tab of the user interface.

Alternatively, the log files can also be found are found in the directories specified below under Windows.

- ziServer
 - started by service: C:\Windows\Temp\Zurich Instruments\LabOne\ziServerLog
 - started manually: C:\Users\[USER]\AppData\Local\Temp\Zurich Instruments\LabOne\ziServerLog
- ziControl: C:\Users\[USER]\Documents\LabVIEW Data and the file is called com.zhinst.ziControlStatusLog.txt.

Note

The AppData folder is hidden by default under Windows. A quick way of accessing it is to enter `%appdata%\..` in the address bar of the Windows File Explorer.

1.5.3. Windows .NET Framework Requirement

The Zurich Instruments LabOne software installer requires the Microsoft .NET Framework to be installed on Windows systems. This is normally already installed on most Windows systems but may need to be additionally installed on some computers running Windows XP and Vista. If the .NET Framework is not available a message will be shown that this requirement is missing when the LabOne installer is started.

It is possible to check whether and which version of the Microsoft .NET Framework is installed on your system under Windows Start → Control panel → Add or Remove Programs. The minimum requirement is Microsoft .NET Framework 3.5 Service Pack 1. In case the required version is not installed, it can be installed through Windows Update tool (Windows Start → Control panel → Windows Update).

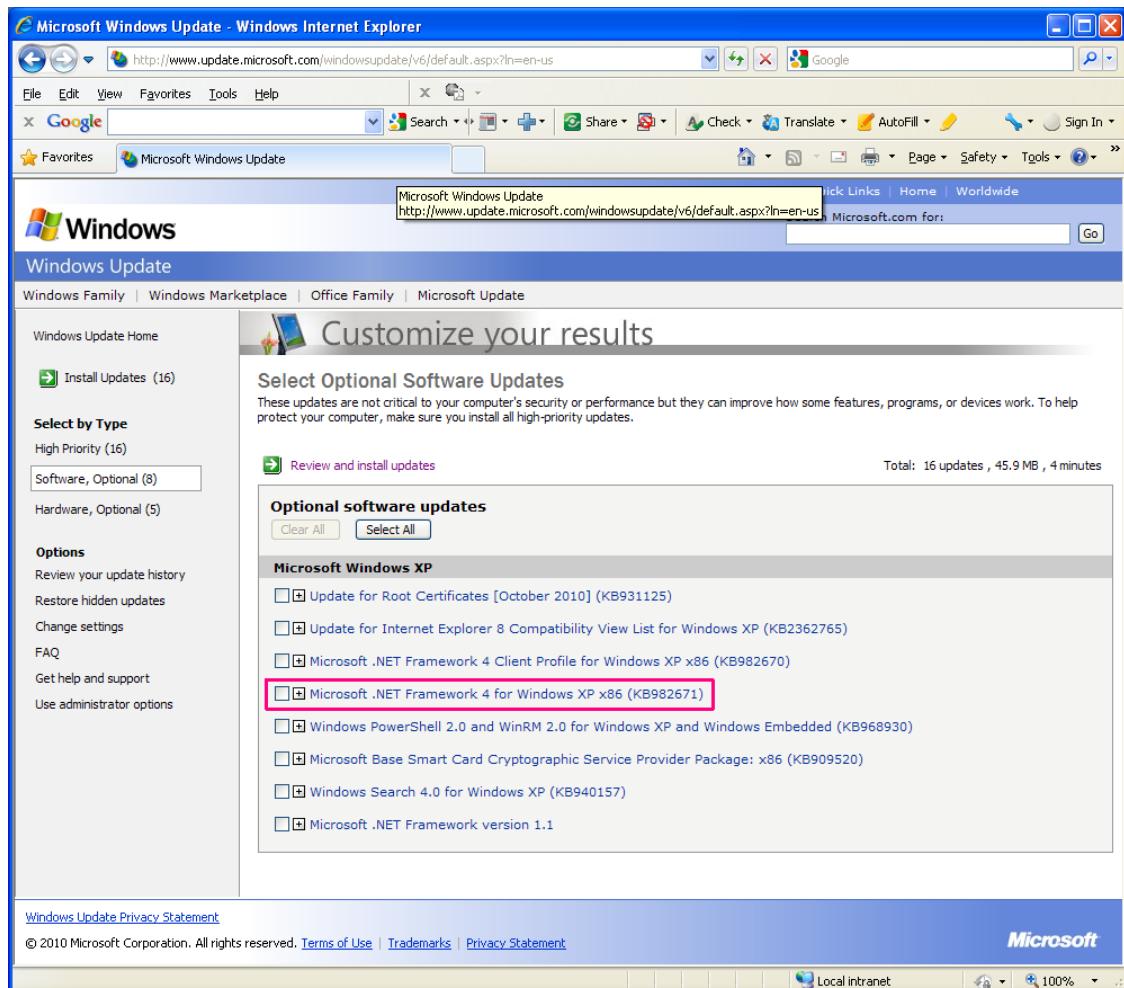


Figure 1.10. Installation of the .NET Framework.

Chapter 2. Functional Overview

This chapter helps you to quickly get acquainted with the main features, the panels, and the operating modes of the HF2 Series. A product selector is provided listing the key features of the products in order to support the selection and ordering. This section is intended as overview and therefore has a coarse level of detail without containing detailed descriptions.

2.1. Features

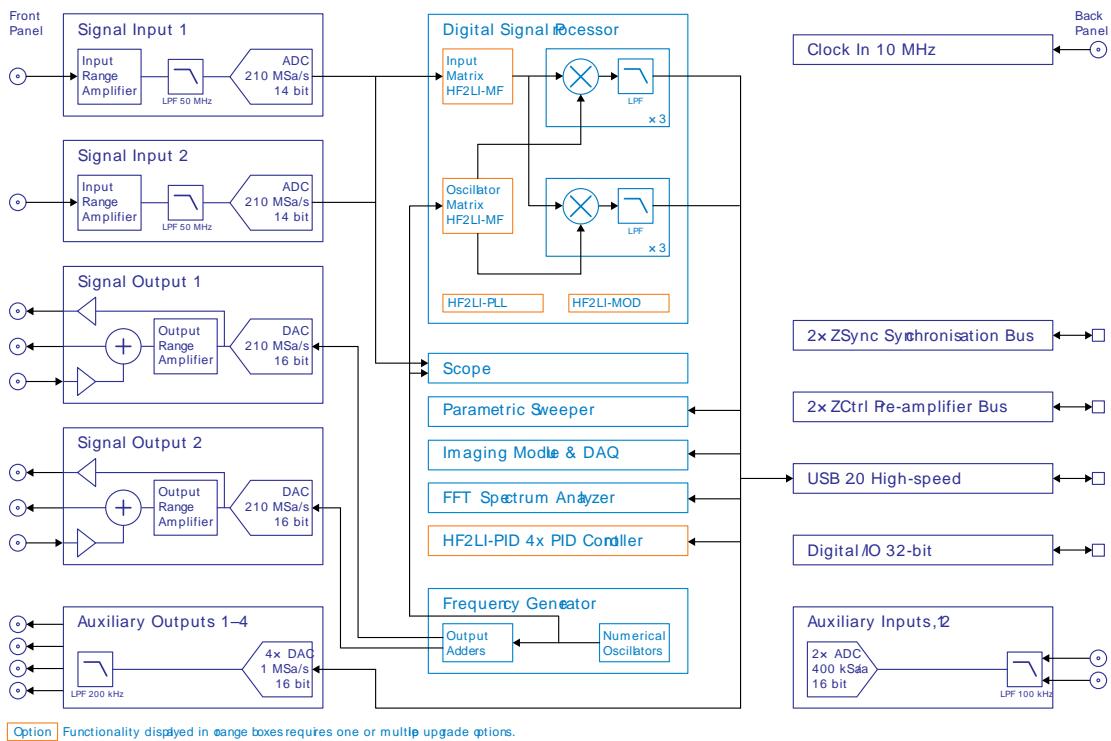


Figure 2.1. HF2 functional diagram

The HF2 Instrument as in Figure 2.1 consists of 4 high-frequency analog blocks, 2 low-frequency auxiliary blocks, the internal digital processing block (light-blue), and the hardware interfaces (mostly available on the back panel of the instrument).

The signal to be measured is connected to one of the two high-frequency analog inputs where it is amplified to a defined range, filtered, and digitized at very high speed. The resulting samples are fed into the digital signal processing block for demodulation by means of up to 8 dual-phase demodulators. The demodulators output samples flow into the embedded RISC processor for further processing or to be sent to the host computer. The samples are also sent to the auxiliary outputs in order to be available on the front panel of the HF2 Instrument.

The numerical oscillators generate sine and cosine signal pairs that are used for the demodulation of the input samples and also for the generation of the high-frequency output signals. For this purpose, the Output Mixers generate a weighted sum of the generator outputs to generate the multi-frequency signal that can be used as a stimulation signal. The 2 high-frequency output stages provide analog to digital conversion, signal scaling (range), add of an external AC or DC signal, and a synchronization signal.

Operating Modes

- Internal reference mode
- External reference mode
- Auto reference mode
- Dual-channel operation
- Dual-harmonic mode
- Multi-harmonic mode

- Arbitrary frequency mode

High-frequency Analog Inputs

- 2 low-noise high-frequency inputs
- Differential & single-ended operation (A, -B, A-B)
- Variable input range
- Variable input impedance
- AC/DC coupling

High-frequency Analog Outputs

- 2 low-noise high-frequency outputs
- Large output range
- Variable output range settings
- 1 synchronization signal for each output
- 1 adder signal for each output

Auxiliary Analog Input/Outputs

- 4 auxiliary high-speed outputs
- 2 auxiliary high-speed inputs
- User defined signal on auxiliary output

Demodulators & Reference

- Up to 8 dual-phase demodulators
- Up to 8 programmable numerical oscillators
- Programmable demodulators filters
- Very-high resolution internal reference
- 64-bit resolution demodulator outputs

Measurement Tools

- Spectroscope
- Numerical
- Oscilloscope
- Frequency response analyzer
- FFT spectrum analyzer

User-programmable Embedded Processor (Option)

- Microblaze 32-bit RISC
- 64 MHz operation allows implementation of real-time control loops
- 32-bit floating-point unit
- 64 kB internal memory (maximum program size)
- 64 MB external memory DDR2

Other Interfaces

- USB 2.0 high-speed 480 Mbit/s host interface

- DIO: 32-bit digital input-output port
- ZSync: 2 ports for inter-instrument synchronization bus (ZI proprietary)
- ZCtrl: 2 ports for control/power bus for external pre-amplifiers (ZI proprietary)
- Clock input connector (10 MHz)

Software Features

- ziControl, graphical interface with control of up 16 remote/local devices from one GUI
- ziServer multi-mode multi-connection server
- ziAPI for extended programmability in C, LabVIEW, MATLAB, and Python - programming examples included
- Console: text interface to connect virtually any programming language

2.2. Front Panel Tour

The front panel BNC connectors and control LEDs are arranged in 5 sections as shown in Figure 2.2 and Figure 2.3 and listed in Table 2.1. The HF2LI and HF2IS have the same connectors and connector functionality on their front and back panel.

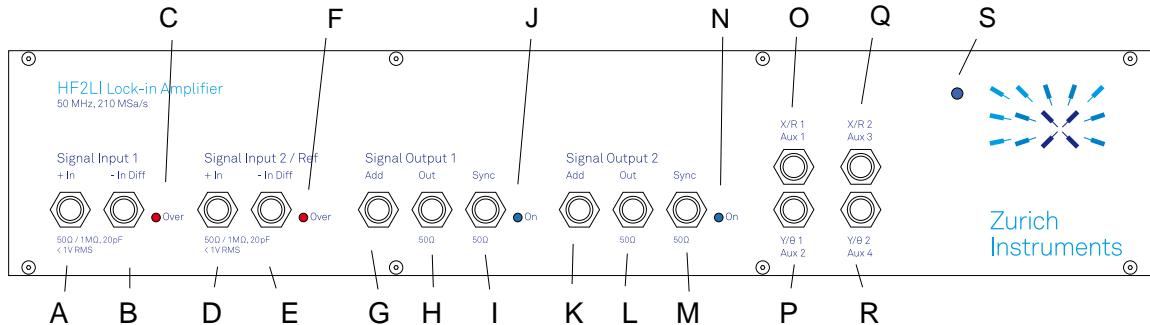


Figure 2.2. HF2LI front panel

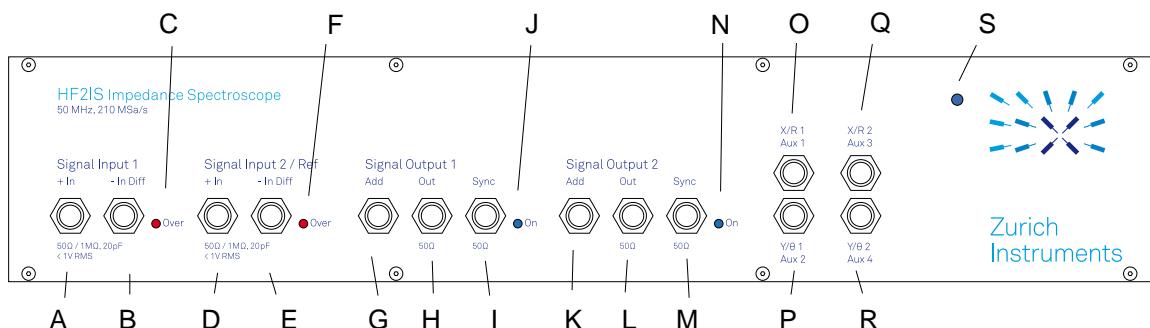


Figure 2.3. HF2IS front panel

Table 2.1. HF2 Series front panel description

Position	Label / Name	Description
A	Signal Input 1 + In	single-ended input
B	Signal Input 1 - In Diff	negative input (when not used, has to be internally shorted to ground with switch on graphical user interface)
C	Signal Input 1 Over	this LED indicates that the input signal saturates the A/D converter
D	Signal Input 2 / Ref + In	single ended input / reference input for external reference mode
E	Signal Input 2 / Ref - In Diff	negative input (when not used, has to be internally shorted to ground with switch on graphical user interface)
F	Signal Input 2 Over	this LED indicates that the input signal saturates the A/D converter
G	Signal Output 1 Add	the signal applied to the connector is added (analog add) to the output signal
H	Signal Output 1 Out	high-frequency output
I	Signal Output 1 Sync	the output signal before the output gain stage for use as synchronization or monitoring signal; the amplitude voltage

Position	Label / Name	Description
		calculates as ratio of the corresponding output amplitude and its range setting
J	Signal Output 1 On	this LED indicates that the signal output is turned on
K	Signal Output 2 Add	the signal applied to the connector is added (analog add) to the output signal
L	Signal Output 2 Out	high-frequency output
M	Signal Output 2 Sync	the output signal before the output gain stage for use as synchronization or monitoring signal; the amplitude voltage calculates as ratio of the corresponding output amplitude and its range setting
N	Signal Output 2 On	this LED indicates that the signal output is turned on
O	X/R 1 / Aux 1	this connector provides either the in-phase signal of the demodulator (X1), the magnitude (R1), or an auxiliary output signal Aux 1
P	Y/Θ 1 / Aux 2	this connector provides either the quadrature signal of the demodulator (Y1), the phase (Θ1), or an auxiliary output signal Aux 2
Q	X/R 2 / Aux 3	this connector provides either the in-phase signal of the demodulator (X2), the magnitude (R2), or an auxiliary output signal Aux 3
R	Y/Θ 2 / Aux 4	this connector provides either the quadrature signal of the demodulator (Y2), the phase (Θ2), or an auxiliary output signal Aux 4
S	Power	instrument mains power-on LED

2.3. Back Panel Tour

The back panel is the main interface for power, control, service and connectivity to other ZI instruments. Please refer to [Figure 2.4](#) and [Table 2.2](#) for the detailed description of the items.

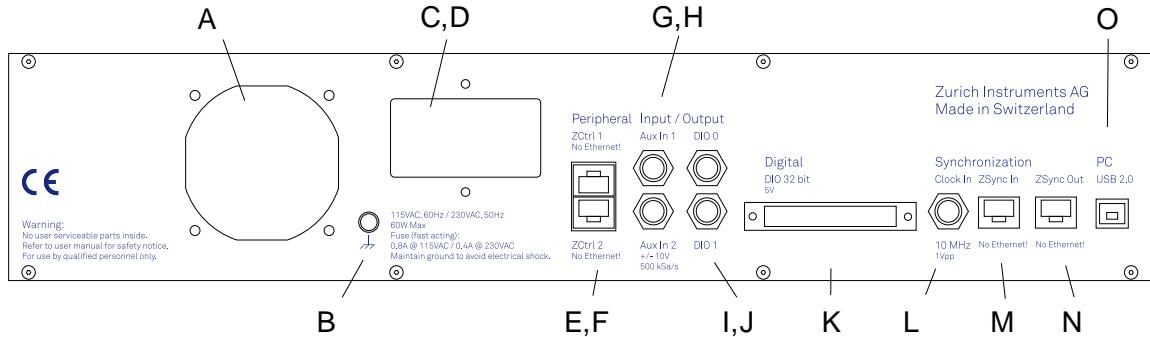


Figure 2.4. HF2 Series back panel

Table 2.2. HF2 Series back panel description

Position	Label / Name	Description
A	-	ventilator (important: keep clear from obstruction)
B	Earth ground	4 mm banana jack connector for earth ground purpose, electrically connected to the chassis and the earth pin of the power inlet
C	Power inlet	power inlet with On/Off switch
D	Power system	select between 115 V and 230 V power system
E	ZCtrl 1	peripheral pre-amplifier power & control bus 1 - attention: this is not an Ethernet plug, connection to an Ethernet network might damage the instrument
F	ZCtrl 2	peripheral pre-amplifier power & control bus 2 - attention: this is not an Ethernet plug, connection to an Ethernet network might damage the instrument
G	Aux In 1	auxiliary high-sampling rate input 1
H	Aux In 2	auxiliary high-sampling rate input 2
I	DIO 0	digital input/output 0
J	DIO 1	digital input/output 1
K	DIO	digital input/output 0-31
L	Clock In	clock input (10 MHz)
M	ZSync In	inter-instrument synchronization bus input - attention: this is not an Ethernet plug, connection to an Ethernet network might damage the instrument
N	ZSync Out	inter-instrument synchronization bus output - attention: this is not an Ethernet plug, connection to an Ethernet network might damage the instrument
O	USB	host computer connection

2.4. Ordering Guide

The HF2 Series is a product line comprising an impedance spectroscope and a digital lock-in amplifier covering advanced requirements for laboratory equipment. The HF2 Series provides best-in-class performance, wide operation range, intuitive handling and excellent accuracy.

Table 2.3 provides an overview of the available products in the HF2 Series. Upgradeable features are options that can be purchased anytime without need to send the instrument to Zurich Instruments - the upgradeable features consist of a firmware upgrade.

Table 2.3. HF2 Series product codes for ordering

Product code	Product name	Description	Upgrade in the field possible
HF2LI	HF2LI Lock-in Amplifier	base lock-in amplifier	-
HF2LI-MF	HF2LI-MF Multi-frequency	option	yes
HF2LI-PLL	HF2LI-PLL Dual Phase-locked Loop	option	yes
HF2LI-PID	HF2LI-PID Quad PID Controller	option	yes
HF2LI-MOD	HF2LI-MOD AM/FM Modulation	option	yes
-	-	-	-
HF2PLL	HF2PLL Phase-locked Loop	bundle of the HF2LI plus the HF2LI-PLL and the HF2LI-PID options	-
-	-	-	-
HF2IS	HF2IS Impedance Spectroscope	base impedance spectroscope	-
HF2IS-MF	HF2IS-MF Multi-frequency	option	yes
-	-	-	-
HF2TA	HF2TA Current Amplifier	low-noise transimpedance amplifier	yes

Table 2.4. Product selector

Feature	HF2LI	HF2LI + HF2LI-MF	HF2IS	HF2IS + HF2IS-MF
Internal reference mode	yes	yes	yes	yes
External reference mode	yes	yes	-	-
Auto reference mode	yes	yes	-	-
Dual-channel operation (2 independent measurement units)	yes	yes	yes	yes
Sinusoidal generators	2	2	2	2
Superposed output sinusoidals per generator	1	up to 6	up to 4	up to 8

Feature	HF2LI	HF2LI + HF2LI-MF	HF2IS	HF2IS + HF2IS-MF
Dual-harmonic mode	yes	yes	-	-
Multi-harmonic mode	-	yes	-	-
Arbitrary frequency mode	-	yes	yes	yes
Number of demodulators	6	6	4	8
Simultaneous freq. supported (fundamentals/harmonics)	2/4	6/-	4/-	8/-
Signal input select switch matrix	-	yes	yes	yes
Oscillator select switch matrix	-	yes	-	-
50 MHz, 210 MS/s, 0.8 μ s TC	yes	yes	yes	yes
DSP technology	128 bit	128 bit	128 bit	128 bit
Dynamic reserve	120 dB	120 dB	-	-
Lock-in range	50 MHz	50 MHz	-	-
USB 2.0 480 Mbit/s	yes	yes	yes	yes
Instrument software	LabOne User Interface, ziAPI, ziServer software	ziControl, ziAPI, ziServer software	ziControl, ziAPI, ziServer software	
Frequency response analyzer	yes	yes	yes	yes
Oscilloscope	yes	yes	yes	yes

2.5. Operating Modes

2.5.1. Internal Reference Mode

The internal reference mode takes advantage of the internal HF generators inside the HF2 Instrument. There are 6 frequency generators in the HF2LI and up to 8 frequency generators in the HF2IS. The output of these generators are added numerically inside the instrument avoiding complicated external analog signal adders and the resulting signal is fed to the device under test. The internal reference mode is the preferred mode as the signal recovery works at its best as the generated frequency is known inside of the instrument. The signal acquisition works immediately and there is no delay lock-time.

The internal reference mode is supported with single-channel and dual-channel operation. This is possible as the HF2 Instrument includes 2 independent measurement units that are working autonomously. Each of the measurement units provides analysis for one fundamental and 2 harmonic frequencies in parallel (sometimes called dual-harmonic mode). In total, the HF2LI can measure 2 fundamental and 4 harmonic frequencies, while the HF2IS can measure 4 frequencies. The number of frequencies increases with the multi-frequency options.

The demodulator samples are available in analog format on the auxiliary outputs of the HF2 Instrument and digitally on the connected computer transferred over the USB interface. The auxiliary outputs generate an analog signal after a linear digital to analog conversion at high sample rate. There are 2 pairs of analog signals allowing to output any 2 of the demodulation sample streams. All demodulator streams are available on the computer and can be further analyzed or stored in the local drives.

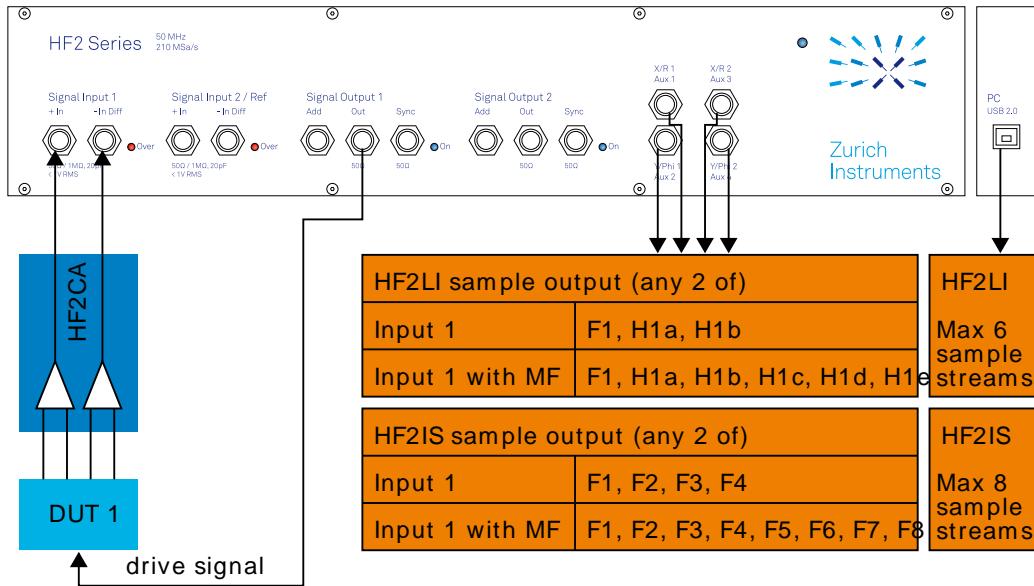


Figure 2.5. HF2 internal reference mode / single-channel

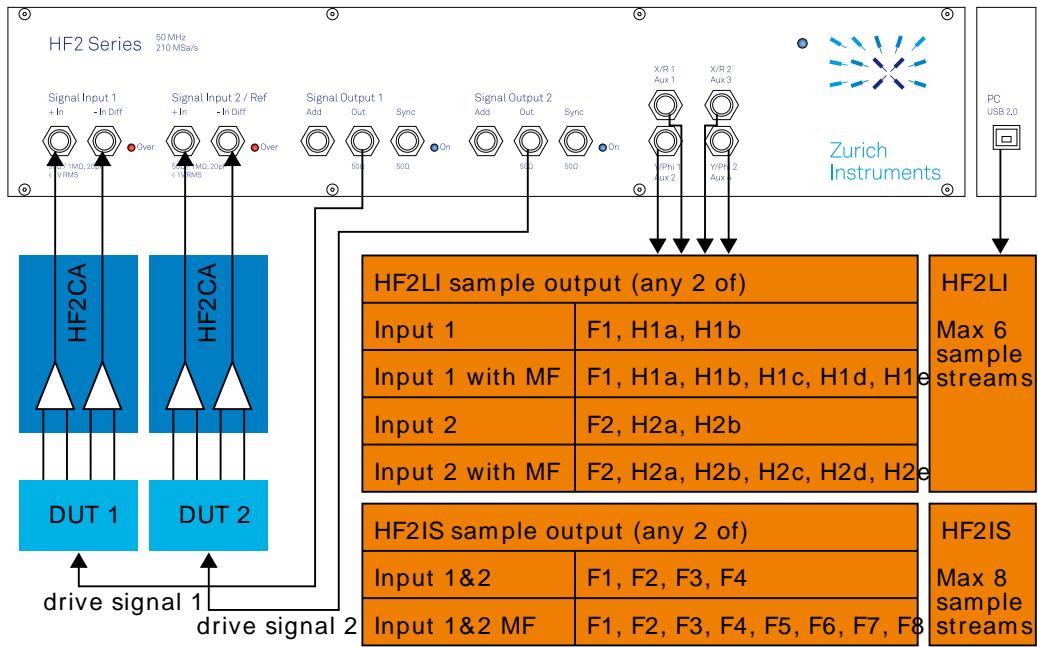


Figure 2.6. HF2 internal reference mode / dual-channel

2.5.2. External Reference Mode

The external reference mode uses external reference sources to recover the signal of interest inside the HF2 Instrument. In this mode, the internal frequency generators are not used to stimulate the DUT. As the signal reference is an arbitrary periodic signal, a certain amount of time is required for the HF2LI to lock on the reference and to be able to recover the signal of interest reliably. This lock time depends on several parameters, but most important on the level and phase noise of the reference.

The external reference mode is supported with single-channel and dual-channel operation. This is possible as the HF2 Instruments includes 2 independent measurement units that are working autonomously. In single-channel mode, the reference can be fed into the Input 2/Ref connector on the front panel. This alternative provides an unmatched capability to use references with small amplitudes as they can be amplified by the signal path of Input 2. In dual-channel operation, the external TTL references are fed into the HF2 by means of the DIO0 and DIO1 connectors on the back panel.

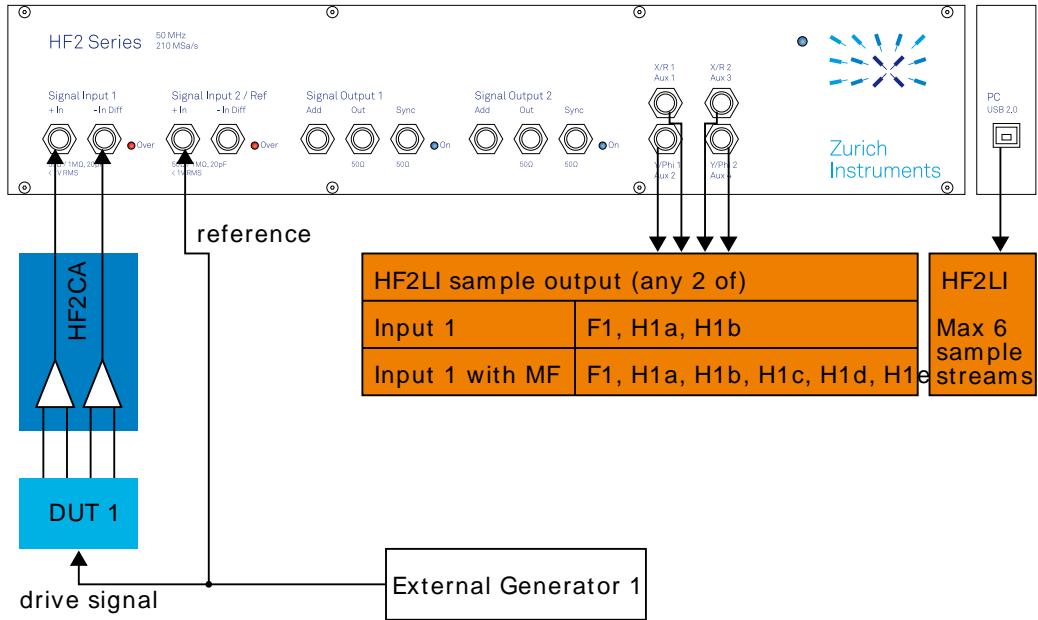


Figure 2.7. HF2 external reference mode / single-channel

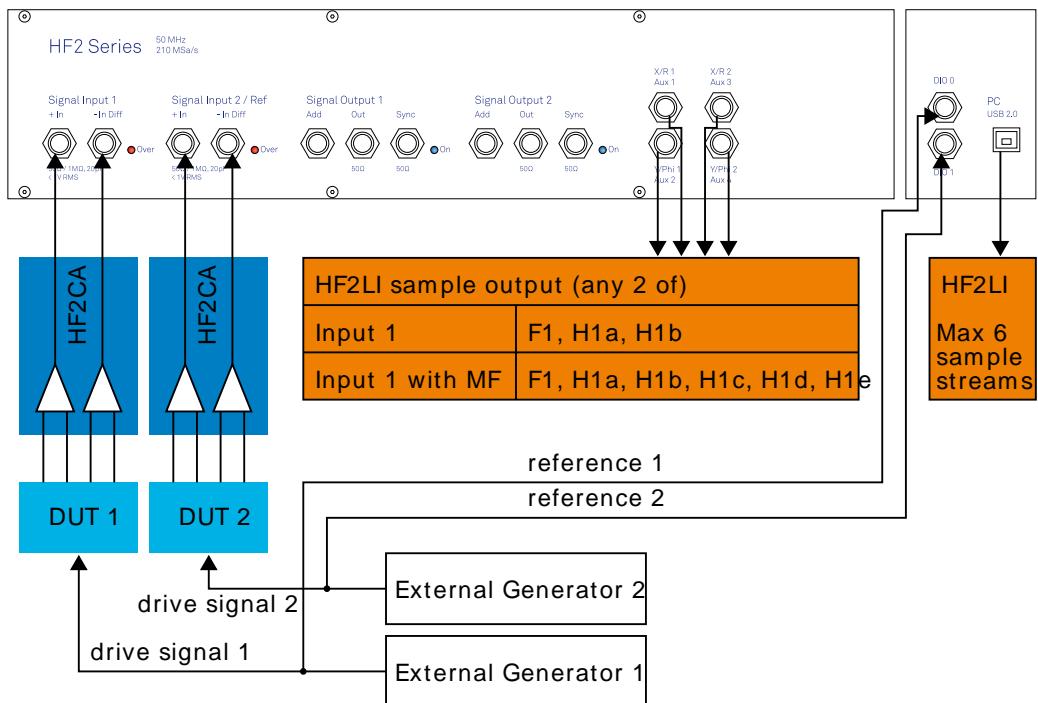


Figure 2.8. HF2 external reference mode / dual-channel

2.5.3. Auto Reference Mode

The auto reference mode makes use of the internal PLLs to recover the reference frequency directly from the signal coming from the DUT. In this mode, the internal frequency generators are not used to stimulate the DUT. As the reference is inherently contained in the sampled signal, a dedicated PLL is able to lock on the frequency and to recover the reference and the signal of interest. This process is suited for signals with enough amplitude and signal-to-noise ratio. Further the reference recovery requires a certain amount of time that depends on several parameters like the level and the phase noise of the measured signal.

The auto reference mode is supported with single-channel and dual-channel operation. This is possible as the HF2 Instrument includes 2 independent measurement units that are working autonomously. In dual-channel mode it is sufficient to connect the signals captures at the DUTs to the Input 1 and Input 2 connectors of the HF2 Instrument. The HF2 Series support both single-ended and differential input signals ideal for fixed and floating ground applications.

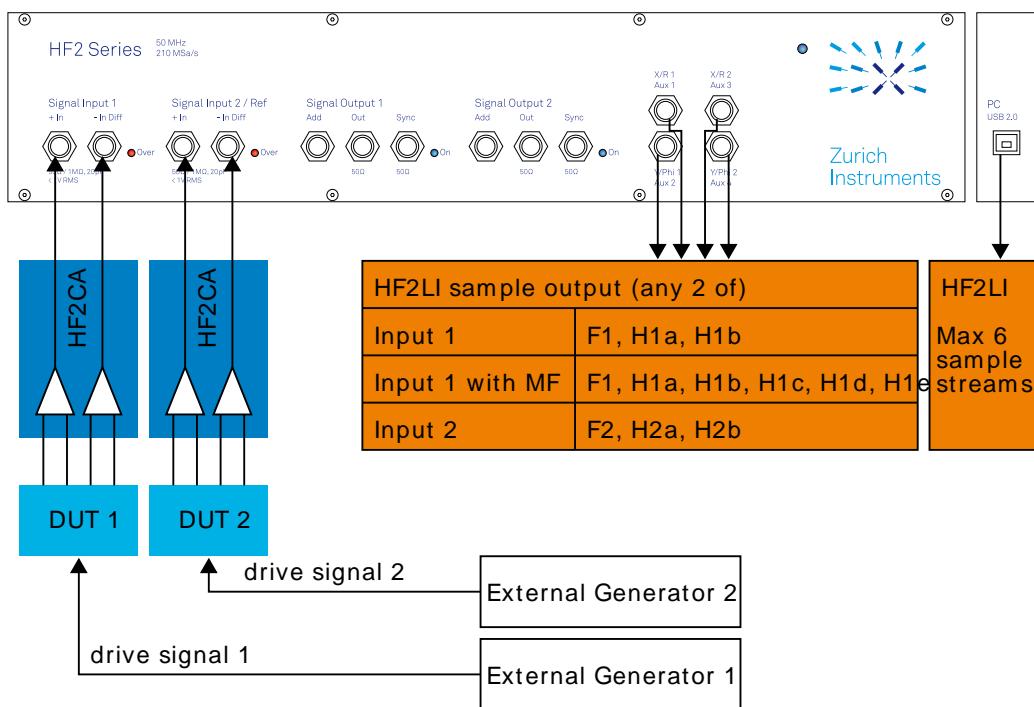


Figure 2.9. HF2 auto reference mode / dual-channel

2.5.4. Multi-frequency Operation

The multi-frequency operation is the powerful extension provided by the HF2 Series increasing the number of frequencies that can be analyzed in parallel. Moreover, the multi-frequency considerably expands the multiplexing options the user has with respect of input channels and demodulator clocks. Please note that the HF2IS-MF is different than the HF2LI-MF (see Table 2.4) as different features and different number of demodulators are activated.

For the HF2LI the multi-harmonic mode and the arbitrary frequency mode are distinguished. In multi-harmonic mode it is possible to analyze a signal at the fundamental frequency and at 5 harmonics at the same time, and the arbitrary frequency mode is the extension to analyze a signal of interest at 6 completely independent frequencies.

For the HF2IS only the arbitrary frequency mode is relevant.

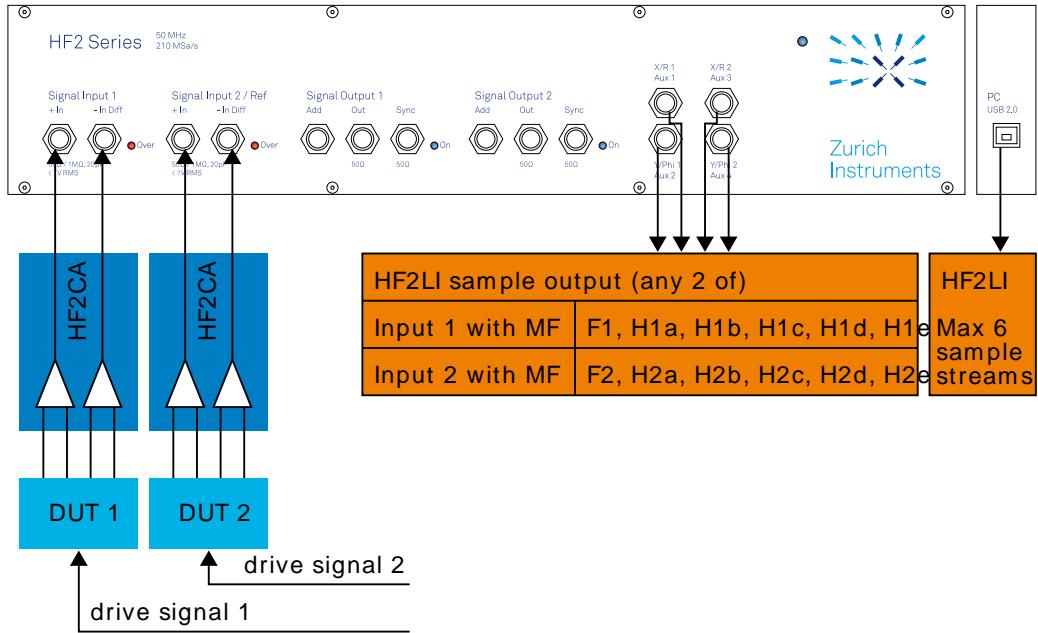


Figure 2.10. HF2 multi-harmonic mode

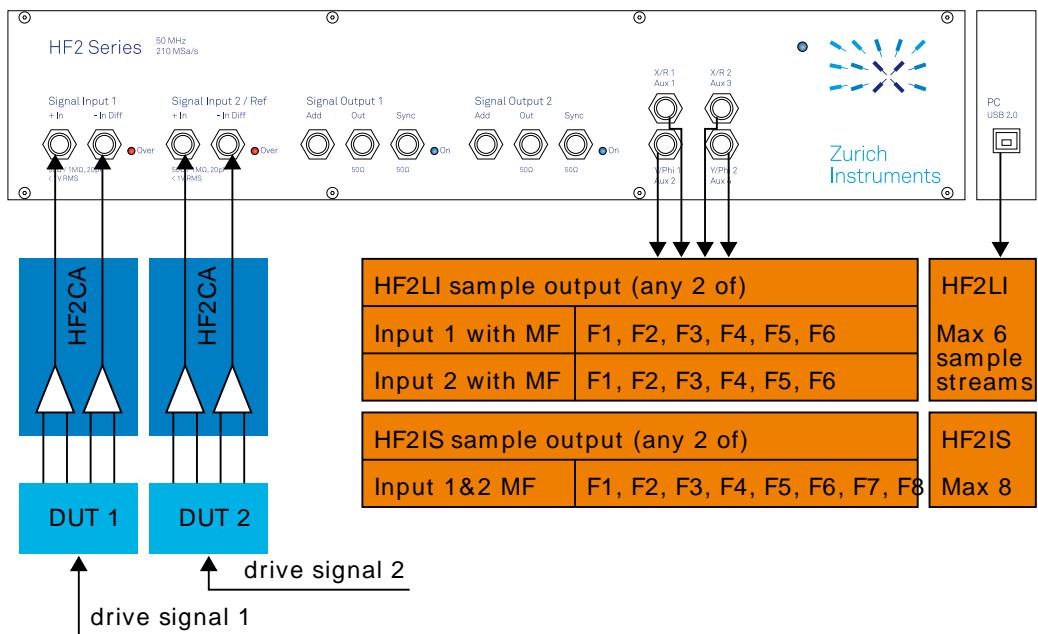


Figure 2.11. HF2 arbitrary frequency mode

Chapter 3. Tutorials

The tutorials in this chapter aim to help users perform their initial measurements with HF2 lock-in amplifiers using the ziControl graphical user interface. The tutorials require some basic laboratory equipment and equipment handling knowledge. For the tutorials, you'll need the following material:

- 1 USB 2.0 cable (supplied with your HF2 Instrument)
- 3 BNC cables (2 optional)
- 1 male shorting cap (optional)
- 1 oscilloscope (optional)
- 1 T-piece (optional)

Note

For all tutorials, you must have the ziControl package installed as described in the [Getting Started Chapter](#). Further the ziServer and ziService installed by the LabOne must be running on your computer. If you are unsure, check in the Windows Task Manager that the `ziServer.exe` and `ziService.exe` tasks are running (make sure you show processes for **all** users). If you are using Linux, use the command `ps -ef | grep ziServer`. In case the Server is not running you find information on how to start it in the [Software Installation Section](#). Finally, run the graphical user interface ziControl from the Windows Start Menu or using the Linux command `ziControl &`.

3.1. HF2LI First Time User

This tutorial covers basic operation of the HF2LI lock-in amplifier with the ziControl graphical user interface.

The ziControl graphical user interface is provided as the primary interface to the HF2LI but it is not the only program that can run the instrument. Typically, the user will use ziControl to set up the instrument and then either use ziControl to take the measurements or run (possibly concurrently) some custom programs. The ziControl window is divided into two sections, the Settings section at the top and the Tools section at the bottom. The Tools section is common for all users whereas the Settings section differs according to the number of installed options.

Note

This tutorial aims to give a walk-through of the main features of the ziControl graphical user interface. Please also see [Section 4.1](#) for an overview of the UI's layout and [Chapter 4](#) in general for a thorough description of all the available settings available in ziControl for your instrument.

3.1.1. The Lock-in Tab

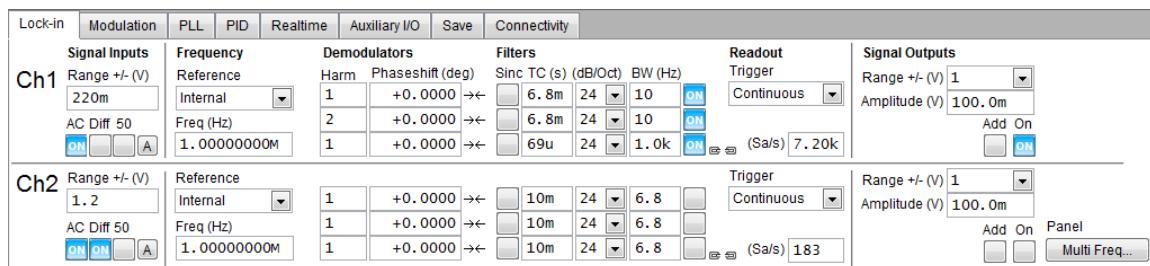


Figure 3.1. The Lock-in tab

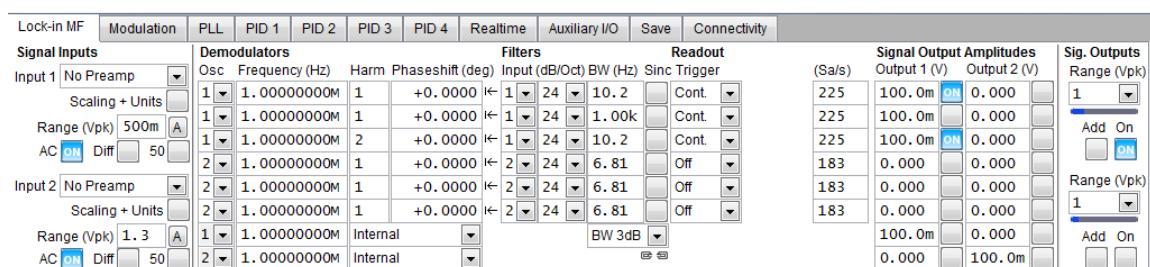


Figure 3.2. The Lock-in MF tab

Open the Lock-in tab in the settings section of the user interface. If you find the Lock-in MF tab instead, it means that the instrument has the HF2-MF option installed. In this case the Lock-in tab is not accessible. It is not possible to switch between the two tabs because the settings influence each other. [Section 4.2.1](#) provides the full documentation of the Lock-in tab while [Section 4.2.2](#) describes the Lock-in MF tab.

The Lock-in section is split into "Ch1" to control Signal Input 1/Signal Output 1 and "Ch2" to control Signal Input 2/Signal Output 2. The two channels are in all aspects equivalent: in this tutorial we will consider Ch1. The Signal Input section contains a Range that can be set to a value

between 1 mV and 1.6 V, the largest amplification of the input signal is achieved for 1 mV. The input has protection diodes that clip signals with amplitude above 5 V.

Important

Please respect the compliance to the maximum ratings listed in [Table 9.2](#) to prevent damage to the instrument.

The AC button sets the coupling type: AC coupling has a cutoff frequency of 1 kHz. The AC coupling consists of a blocking capacitor between two input amplifier stages: this means that a DC signal larger than 5 V will saturate the front amplifier even if AC coupling is enabled. The Diff button sets single-ended/differential measurement mode: in the differential mode, the voltage difference between the +In and -In is amplified whereas in single-ended mode, the voltage at the +In connector is amplified. The 50 Ω button toggles the input impedance between low (50 Ω) and high (approx 1 M Ω) input impedance. 50 Ω input impedance should be selected for signal frequencies above 10 MHz to avoid artifacts generated by multiple signal reflections within the cable. With 50 Ω input impedance, one will expect a reduction of a factor of 2 in the measured signal if the signal source also has an output impedance of 50 Ω . The A (Auto range) button automatically sets the input range to be twice the input signal.

Next, one finds the reference signal/demodulation section. For the demodulation, the lock-in also needs a reference signal. The reference source can be

- internal: the lock-in internal signal generator will produce the reference/excitation signal, or
- external: the reference is generated externally and supplied to the lock-in. Possible choices include the Signal Input 1 (auto reference mode), in which case the phase information is of course zero, Signal Input 2, in which case the phase information is retained; in these two cases, the bandwidth for the reference signal is the full bandwidth, 50 MHz. Other choices are the two Analog Inputs and two Digital Input/Output (DIO) located on the rear side of the HF2. The bandwidth for the Analog inputs is 20 kHz, the bandwidth for the DIO is 2 MHz (the DIO accepts a TTL signal with lows/highs of <0.8 V/>3.3 V).

For the purpose of this tutorial, set the reference to Internal and the frequency to 1 MHz.

Under the section Demodulators the user can select which harmonics and filter bandwidths to use for demodulation. It is not uncommon to need to measure different harmonics (integer multiples of the fundamental frequency, in this case 1 MHz). Select the first harmonic to 1 for the first demodulator (the first line), set the filter order to 24 dB/oct (this is 80 dB/dec, an attenuation of 10⁴ for a tenfold frequency increase) and type 10 Hz into the BW control (the digital filters of the HF2 are described in [Section 10.3](#)). Users are sometimes interested in the second harmonic that may be generated by nonlinear processes in their device under test: select harmonic 2 for the second demodulator and type the same values for the filter order and BW as in the previous case. You can also measure the same fundamental harmonic with a larger bandwidth: first make sure that the chain symbol is unlinked by clicking on it if necessary (otherwise the newly typed settings will be copied to all demodulators), then set harmonic to 1, order to 24 dB/oct and BW to 1 kHz. Measuring with different bandwidths can provide the signal average and transient values. Click on the enable button next to the filters to read out the values from the 3 demodulators.

Next, set the Trigger to Continuous and the Rate to 7.20 kSa/s (rate settings can only be sub-multiples of 460 kSa/s, the maximum readout rate for one demodulator): in this case, the HF2LI will send the demodulated signal sampled at this rate through the USB. Due to the finite bandwidth of the USB connection the maximum cumulative demodulator sample rate is 700 kSa/s, which can be split over the active demodulators, see [Table 9.4](#). In this example we're using 3 active demodulators, therefore, since the sample rates are sub-multiples of 460 kSa/s the maximum possible readout rate for each demodulator is 230 kSa/s. Note that, according

to the Nyquist sampling theorem, the sampling rate should be at least twice as fast as the maximum frequency present in the signal, in order to reconstruct the demodulated signal (this is not important if you only need one data point or the standard deviation of the demodulated signal). Since the low-pass filters do not have an infinite roll-off (the attenuation is not infinite past the filter's 3 dB frequency), it is common to set the sampling rate to about 8 times higher than the filter bandwidth.

Next, in the output section, select the excitation amplitude to 100 mV and the output range to be the smallest possible but at least twice as large as than the amplitude for minimum harmonic distortion. Connect Signal Output 1 to Signal Input 1 +In with a BNC cable and click on the On button in the ziControl Signal Outputs section.

3.1.2. The Numerical Tool

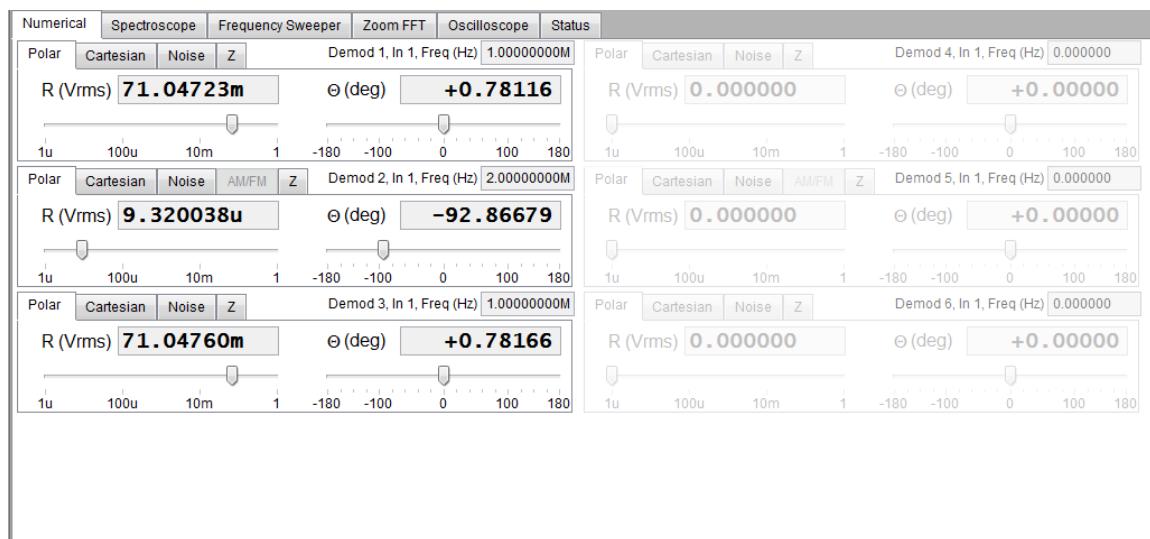


Figure 3.3. The Numerical tab

In the Numerical tab located in the Tools section, you should read 71 mV RMS for the R component of demodulator 1, (demodulating at 1 MHz). The RMS corresponds to the 100 mV divided by $\sqrt{2}$. The phase value will depend on the BNC cable length (for lengths shorter than one meter, the phase is approximately a few degrees). Demodulator 3 (also at 1 MHz) will show the same amplitude, but the digits fluctuate more, since the measurement bandwidth and therefore the noise, is larger. Demodulator 2 reads only a few μ V because at 2 MHz (the second harmonic) there is only a little component of the signal, coming from the harmonic distortion of the HF2LI output and input stages.

3.1.3. The Spectroscopic Tool

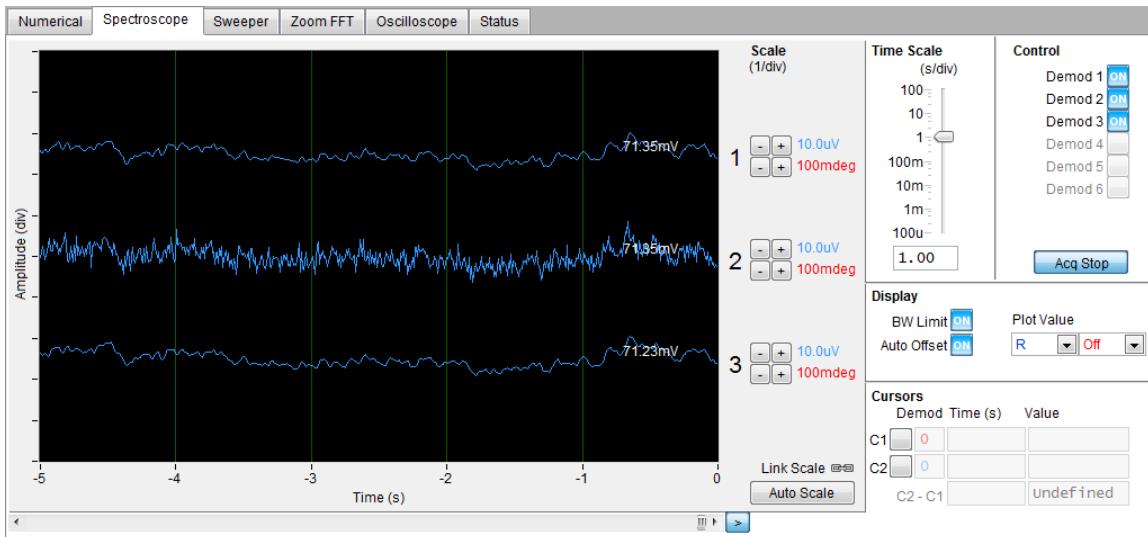


Figure 3.4. The Spectroscope

Now click the Spectroscopic tab in the tools section. Here one can display the demodulated values over time. Select demodulators 1, 2 and 3, make sure that Acq Stop is not pressed, and then press the Auto Scale button. The demodulated traces for these three demodulators are displayed, offset to one another: as before, demodulators 1 and 3 have the same average value, but a larger noise amplitude is clearly visible in the third trace. If the Auto Offset button is enabled, an offset is continuously subtracted from the traces, so the traces are displayed in the middle of the panel. The vertical axis V/Div and deg/Div can be set individually by the +/- buttons for each demodulator. The vertical scale adjustment can also be linked together through Link Scale button. The horizontal axis scale s/DIV can be set through the Time Scale slider. One can inspect the older data by dragging the time slide below the horizontal axis: the amount of stored data depends on the computer memory (the higher the sample readout rate, the shorter will be the displayed trace). To display the most recent data, click the greater than symbol next to the slide. Please note that the displayed plot suffers from aliasing if the demodulator operates with wide filters, the sample readout rate is large, and the trace is zoomed out to span a long temporal interval: this effect is due to the limited resolution of the computer screen versus the effectively available samples - enabling the BW Limit partially solves this problem by adding another filter on the displayed samples.

3.1.4. The Oscilloscope Tool

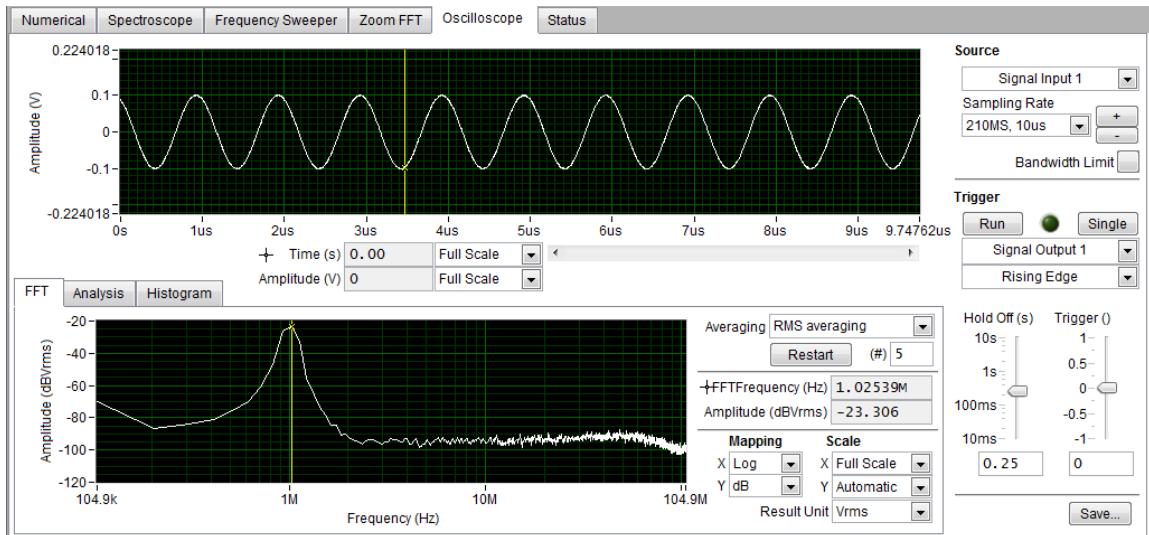


Figure 3.5. The Oscilloscope

Let us proceed to the Oscilloscope tab. This displays the digitized signal at the signal inputs and outputs of the HF2LI. It is a 2048 point wave trace that is useful for visualizing the signal to demodulate; it also replaces the need for an external oscilloscope. Select Signal Input 1 from Source and Signal Output 1 from Trigger and press the Run button. The 1 MHz input signal is visible as 10 full cycles if the Sampling Rate is set to 210 MS, 10 μ s. Decreasing the sampling rate to display a longer time interval should be done carefully because it may lead to undersampling (i.e. aliasing due to violation of the Nyquist criteria): for instance setting the sampling rate to 26 kSa/s, 80 ms, will produce a correct looking sinusoidal, but at the wrong frequency. The Bandwidth Limit button may reduce aliasing effects without removing them completely.

The update rate of the oscilloscope frames is controlled by the Hold Off slider: the minimum interval between two traces is 10 ms. This is a low value which increases the load of the USB bandwidth and may lead to [USB sample loss](#) - therefore avoid using small hold off values if not needed.

A Fourier transform of the trace is displayed in the graph below: the frequency resolution is coarse because the time trace contains 2048 points. Averaging of the Fourier power spectra can be enabled to increase the SNR ratio. Moreover, the FFT result is available as spectral density (power per unit bandwidth) by selecting the Result Units V_{RMS}/\sqrt{Hz} or V_{RMS}^2/Hz . The oscilloscope trace can be saved in ASCII format with the Save button.

3.1.5. The Sweeper Tool

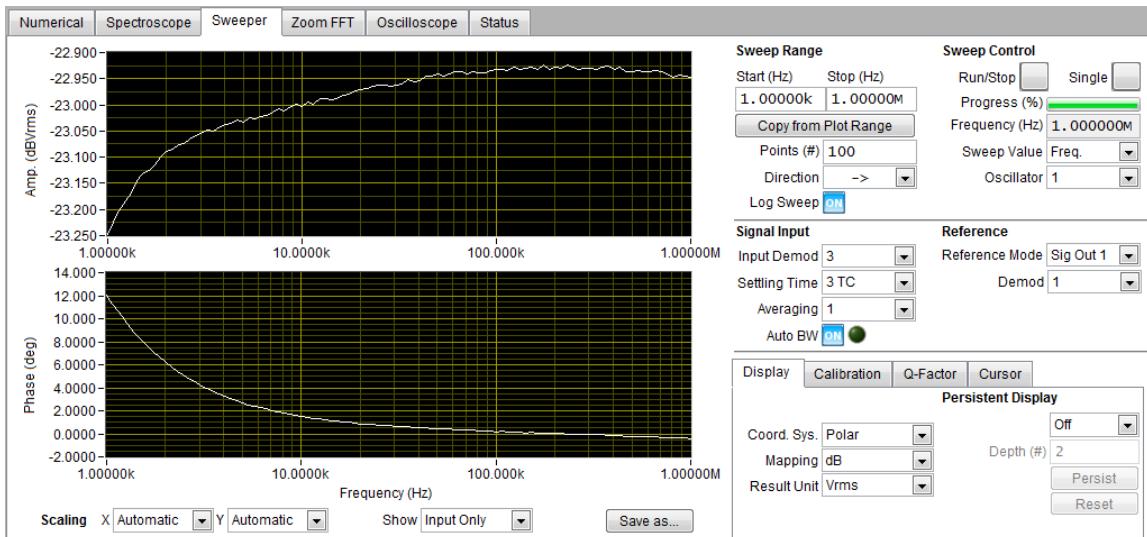


Figure 3.6. The Sweeper

Next is the Sweeper tool: it turns the HF2LI into a frequency response analyzer, giving the transfer function of a device under test in the form of a Bode plot. In AFM applications this is useful to easily identify the resonance frequency of a cantilever as well as the phase delay. The sweeper tool can also be used to sweep parameters other than frequency: phase, time constant, amplitude and auxiliary output voltage.

As a frequency sweeper example, we will execute a logarithmic sweep of 100 points between 1 kHz and 1 MHz. Set the Sweep Range Start to 1 kHz and Stop to 1 MHz, 100 points and enable the Log Sweep. Select the reference to be output 1, output 2 or one of the 6 demodulators. This will allow the user to observe the ratio between the measured value and any of the selected reference signal. Click on Run/Stop for continuous sweeping or on Single for a single sweep. Toggle the AC input coupling in the Lock-in settings, and observe the attenuation in the response around 1 kHz in AC coupling, since the AC coupling has a cutoff frequency of approximately 1 kHz. You will notice that despite the excitation signal is 100 mV, the Amplitude (upper graph) shows 1 (or 0 dB) when Input/Ref is selected in the Show pull-down list: the sweeper displays the transfer function of the device under test by dividing the measured signal by the user-defined reference signal. One can also show the absolute measured input value only by selecting Input Only or the reference value by selecting Ref Only. The Bode plot that is displayed can be saved pressing the Save button - the data file in ASCII format contains 8 columns. The first 6 columns are F (Hz), R (V_{RMS}), Θ (deg), NEPBW (Hz), R_{ref}(V) and Θ_{ref} (deg). The remaining two columns are the display variables and units that are chosen under the Display tab. The Settling Time pull-down allows the user to control the amount of time between setting at a new frequency and taking a reading: during this time, the filter will settle towards its final value. 1 TC means a minimum (guaranteed) settling to 90%, 3 TC to 95% and 10 TC to 99% of the total signal change. Minimum guaranteed settling is specified because the USB latency will add to the wait time.

During the logarithmic sweep the NEPBW (noise equivalent power bandwidth) is adjusted for each frequency point and displayed under the Filters BW field under the Lock-in tab. The adjustment is due to the fact that the sweep is logarithmic and the sweep frequency steps are not equally spaced. In order to account for all signal power (and power densities), the measurement bandwidth must be changed accordingly. This can be done automatically through the Auto BW button. For an explanation of the NEPBW, see [Signal Processing](#) chapter. Note that in this configuration, if the signal to noise ratio is large, there will not be any effect when disabling Auto BW, since the noise signal is negligible when measured with (almost) any NEP bandwidth. Averaging can also help to improve the signal-to-noise ratio during the sweep.

As an example of noise measurement, disconnect the BNC cable from Signal Output 1 and connect it to Signal Output 2. In the Lock-in tab, turn off the Signal Output 1, and generate a 100 kHz / 100

mV excitation Signal Output 2 (remember to turn on the output in the Signal Outputs section). In the frequency sweeper perform a single sweep with Auto BW enabled. A relatively wide peak will appear at 100 kHz, as the measurement was performed with wide NEPBW. Switch the X scaling to Manual and zoom into the region around 100 kHz; click the Copy From Range button to use the new boundaries for the sweep as selected in the graph and again perform a single sweep. The peak at 100 kHz will appear narrow, reflecting the change in the measurement bandwidth.

3.1.6. The zoomFFT Tool

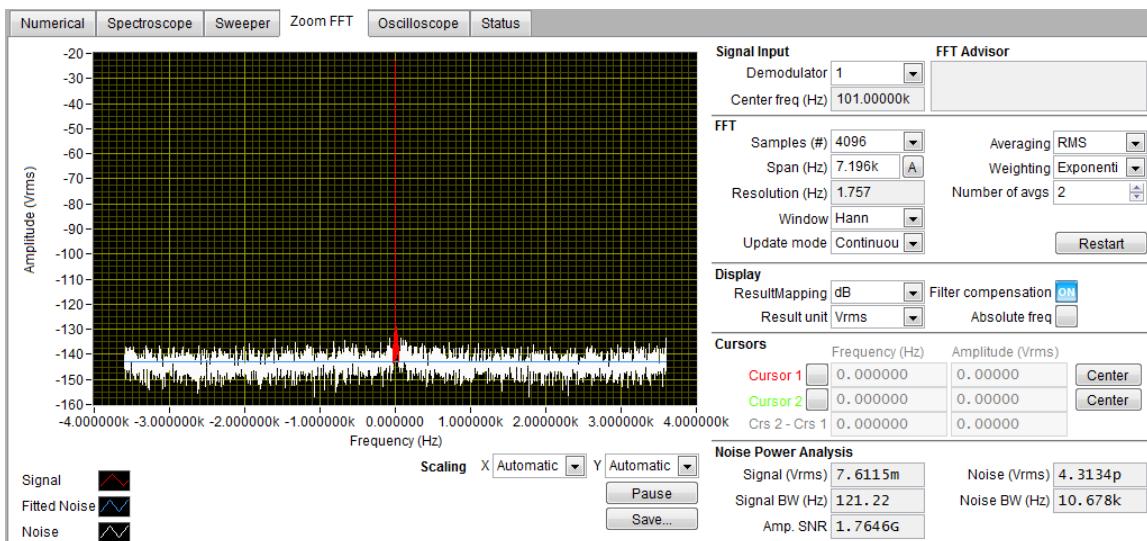


Figure 3.7. The zoomFFT tab

The zoomFFT tool (more information (see Section 10.6) allows the user to measure the frequency spectrum around a specific frequency: this is done by performing the Fourier transform of the demodulated X and Y (or in-phase and quadrature) components of the signal (more precisely of the quantity $X+jY$, where j is the imaginary unit). The reason is that the demodulation process shifts the spectrum of the input signal by the demodulation frequency and the Fourier transform of the demodulated $X+jY$ corresponds to the frequency spectrum of the input signal around the demodulation frequency. zoomFFT and FFT coincide when the demodulation frequency is zero. The frequency resolution that can be achieved in this way is given by the sampling rate divided by the number of recorded samples, and is therefore much higher than the frequency resolution obtained in the Oscilloscope tab. The zoomFFT approach is more efficient than the FFT on raw samples in which one digitizes a long time trace, performs the Fourier transform and retains only the portion of the frequency spectrum of interest while discarding the rest.

We continue from the previous section with the BNC connecting Signal Output 2 to Signal Input 1, and 100 mV, 100 kHz sine wave. In the Lock-in tab, type 101 kHz in the reference frequency for Ch 1, 48 dB/oct, 500 Hz bandwidth, a Readout sampling rate of 7.2 kSa/s and Filter compensation enabled. The high order filters and sampling rate compared to the bandwidth are so chosen to avoid aliasing. In the zoomFFT tab, select Demodulator 1 for Signal Input. A peak appears at 1 kHz to the left of the center frequency. Increasing the number of lines in FFT will result in a finer frequency resolution. The Filter compensation button compensates for the demodulator filter, by dividing the FFT spectrum by the demodulator filter transfer function: that is why the input signal does not appear attenuated despite being outside the filter bandwidth (1 kHz and 500 Hz respectively). Filter compensating facilitates the job of researchers who want to find the noise background level.

3.1.7. The Status Tool

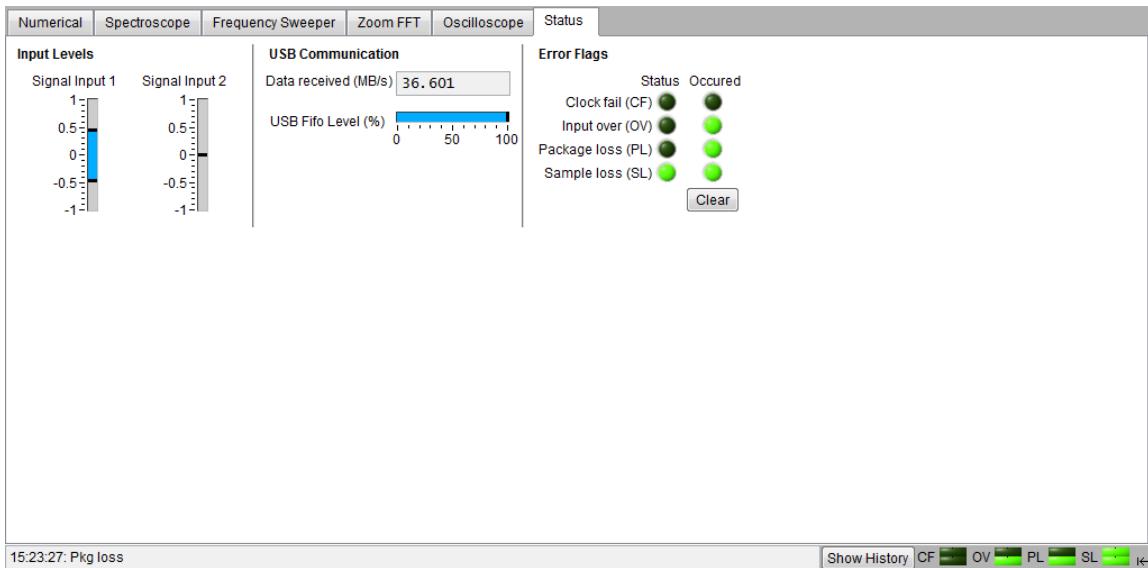


Figure 3.8. The Status tab

In the Status tab, several general status settings are displayed, such as the input signal displayed as percentage of the full input range, the USB FIFO level and input overloads. For instance, setting the Readout rate to a value of 230 kSa/s and turning on 3 or more demodulators will saturate the USB bandwidth and result in the Sample loss (SL) warning LED going on; data points are dropped by the USB subsystem and lost. To prevent data loss, a smaller readout rate or fewer demodulators should be used simultaneously. Other error flags are also displayed here, such as Input overload (OV, at the same time a red LED will turn on the front panel of the HF2LI) and Clock Fail (CF) if an externally supplied 10 MHz reference clock (not the external reference frequency for demodulation) does not meet the requirements in terms of amplitude or frequency. Once the cause for the warnings has been identified and the solved, the Status LED goes off but the Occurred LED still stay on to remind the user that an abnormal situation occurred in the past: all status flags can cleared by using either the Clear button or the arrow at the bottom right corner of ziControl.

3.1.8. The Auxiliary I/O Tab

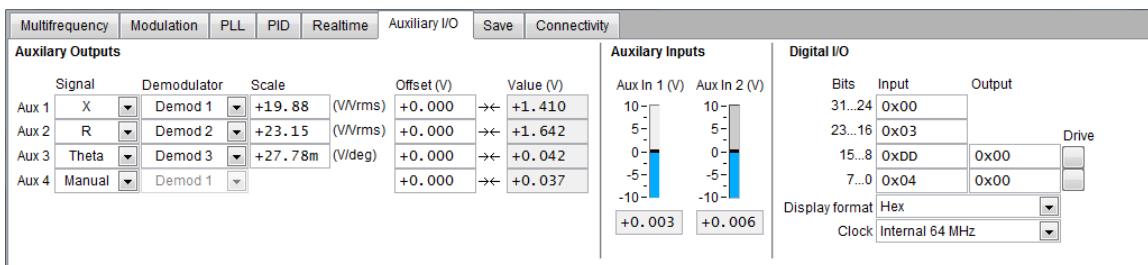


Figure 3.9. The Auxiliary I/O Tab

The Auxiliary I/O tab in the settings section controls the 4 Auxiliary Outputs on the right side of the HF2LI front panel, as well as the 2 Auxiliary Inputs and the DIO connector on the rear side. In the Lock-in tab, set the Reference to Input 1 (Auto): the HF2LI finds the correct input signal frequency at 100 kHz (this illustrates the use of the external reference). On the Auxiliary I/O, output the signal amplitude by selecting R from the Signal pull-down menu from Aux 1 and Demod 1 from Demodulator. Set the Scale factor to 10 V/V_{RMS}: you should read 0.712 V in the output Value (V) tab, which corresponds to the amplitude of the signal as you can read it in the Numerical tab, multiplied by the scale factor. If one is interested in small variations of the signal amplitude, an

offset can be applied to the output: type -0.712 in Offset (V) (or alternatively click the counter facing arrows next to Offset (V)): Value (V) should now read 0.

3.1.9. The Save Tab

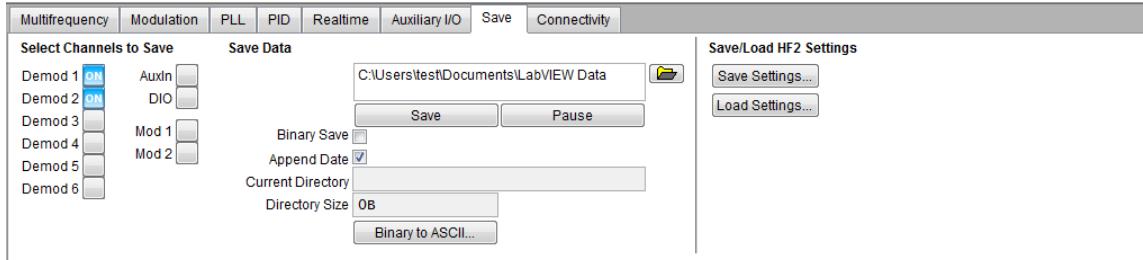


Figure 3.10. The Save tab

In the Save tab, the user can save data from multiple demodulators into a directory whose path can be specified by clicking on the folder icon. Select the two demodulators you want to save, Demod 1 and Demod 2 from Select Channels to Save (make sure that demodulators 1 and 2 are active in the Lock-in tab), select Append Date (this will create a directory with the timestamp appended to the save directory base name to avoid overwriting an existing save directory) and click on the Save button. Data from demodulators 1 and 2 will be saved at the readout rate programmed in the Lock-in tab. To stop saving data, click again on the Save button.

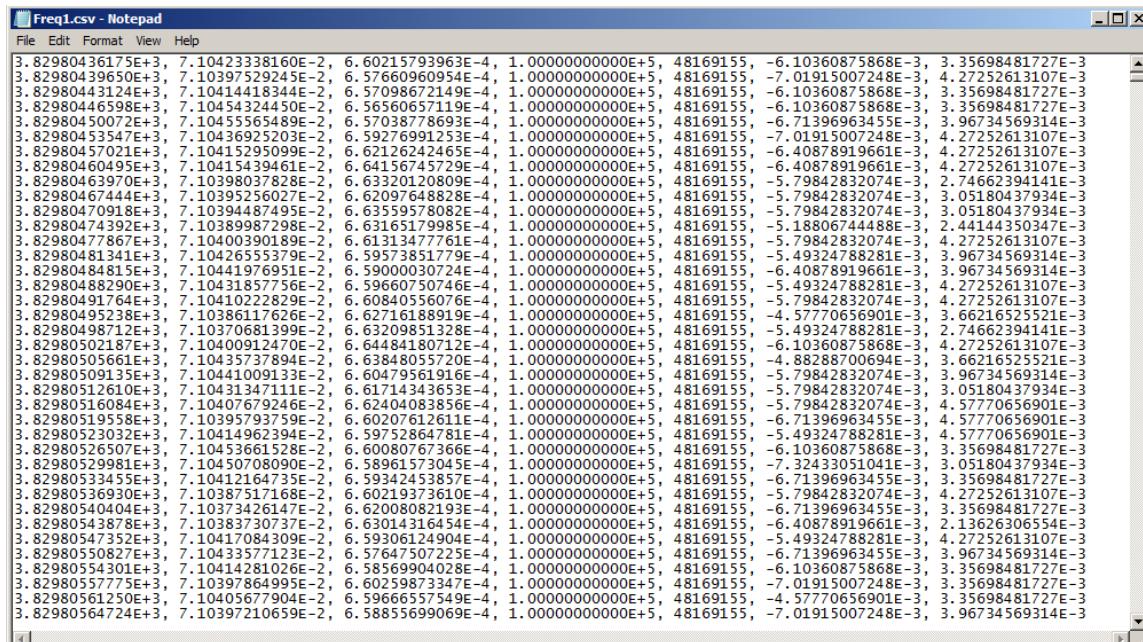


Figure 3.11. The file containing the saved demodulated samples in ASCII format: the 7 columns relate to Timestamp [s], X [V_{RMS}], Y [V_{RMS}], Frequency [Hz], DIO value [decimal], AUX In 1 [V], AUX In 2 [V]

The newly created directory contains a CSV file for each saved demodulator, which you can read with any text editor if the file was saved in ASCII format (Binary Save not selected). If fast readouts are needed, binary save is an option for high performance save without the overhead for the conversion to ASCII. The data can be converted to ASCII at the end of the save with the Binary to ASCII button.

The HF2LI settings can be saved to file and reloaded (for instance after power-up of the HF2LI). Since settings are saved in ASCII format as node-value pairs, the settings can be inspected and modified using a normal text editor. For more information of how instrument settings are organized in the node-tree, see the [Section 6.1.3](#).

3.1.10. The Connectivity Tab

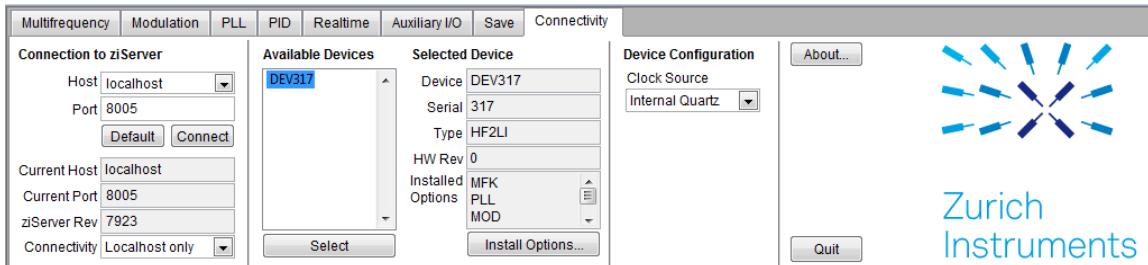


Figure 3.12. The Connectivity tab

The Connectivity tab shows all the devices connected to the computer and additional information on the HF2LI that is selected, such as the options installed. When multiple devices are available, the user may select the device to be controlled with the current instance of ziControl. The connectivity tab also shows that ziControl connects to ziServer, via TCP/IP by making a loop-back connection to the local computer at port 8005. ziServer manages all communications between the HF2LI and all programs that want to access the HF2LI, such as ziControl and any user program, and it therefore must be always running.

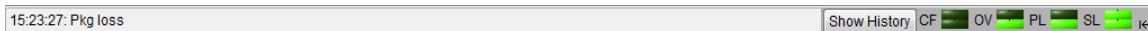


Figure 3.13. The Command Line display

3.2. Simple Loop

3.2.1. Preparation

In this tutorial you generate a signal with the HF2 Instrument and measure that generated signal with the same Instrument. This is done by connecting Signal Output 1/Out with Signal Input 1/+In with a BNC cable. This tutorial shows a single-ended operation, meaning that there is no signal going into the Signal Input 1/-In connector. For proper operation, the Channel 1 must be set to single-ended operation, or alternatively the Input 1 - connector must be shorted to ground using a male shorting cap. Optionally it is possible to connect the generated signal at Output 1 to an oscilloscope by using a T-piece and an additional BNC cable.

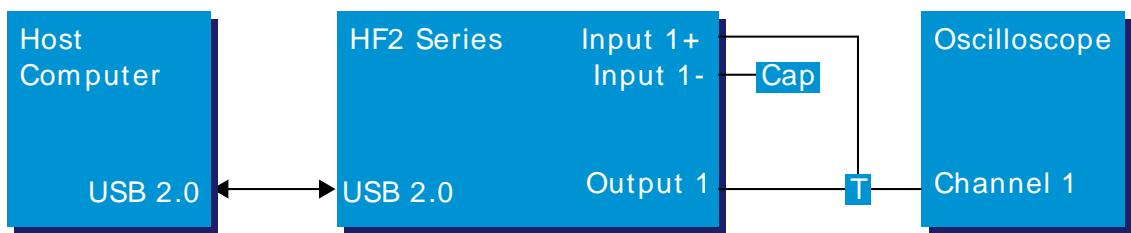


Figure 3.14. Tutorial simple loop setup

Note

This tutorial is both for HF2LI Lock-in Amplifier and HF2IS Impedance Spectroscopy users.

Connect the cables as described above. Make sure the HF2 unit is powered on, and then connect the HF2 to your computer with a USB 2.0 cable. Finally launch the ziControl application (Start Menu/Programs/Zurich Instruments/ziControl).

3.2.2. Generate the Test Signal

Apply the following settings in order to generate a 2.5 MHz signal of 0.5 V amplitude on Signal Output 1/Out.

- Go to the Lock-in tab and set frequency of Channel 1 to 2.5 MHz: click on the field, enter "2.5 M" or "2.5E6" and press **<TAB>** on your keyboard to confirm the data
- In the Output 1 section, set the Range pull-down of 1 V
- In the Output 1 section, set the amplitude to 0.5 V by entering 0.5 followed by a **<TAB>**
- By default all physical outputs of the HF2 are inactive to prevent damage to connected circuits. Now it is time to turn on the main output switch by clicking on the button labeled "On". The switch turns to blue indicates now "ON"
- If you have an oscilloscope connected to the setup, you are able to see your generated signal

Table 3.1. Settings: generate the test signal

Output range Ch1	1 V
Oscillator frequency Ch1	2.5 MHz

Oscillator amplitude Ch1	0.5 V
Output Ch1	ON

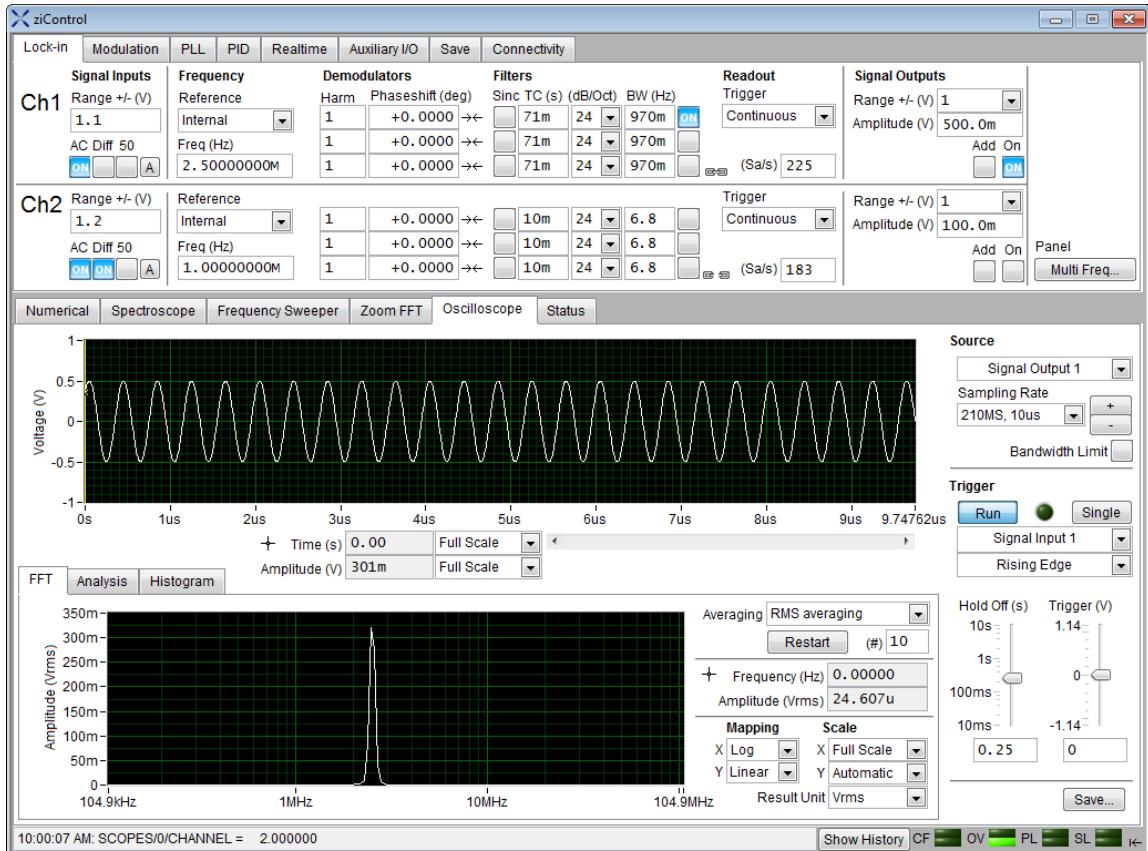


Figure 3.15. ziControl displaying the generated signal

3.2.3. Acquire the Test Signal

Next, you adjust the input parameters in order to acquire signals with the appropriate input range. To do this, you switch the signal source and the trigger of the Scope to Signal Input 1. Then you adjust the Signal Input 1 range to 1 V.

Table 3.2. Settings: acquire the test signal

Oscilloscope source	Input 1
Oscilloscope trigger select	Signal Input 1
Oscilloscope time scale	210 MS, 10 us
Oscilloscope trigger	RUN
Signal Input Ch1 range	1 V (approximated to 0.98 V)
Signal Input Ch1 AC / Diff / 50	ON / OFF / OFF

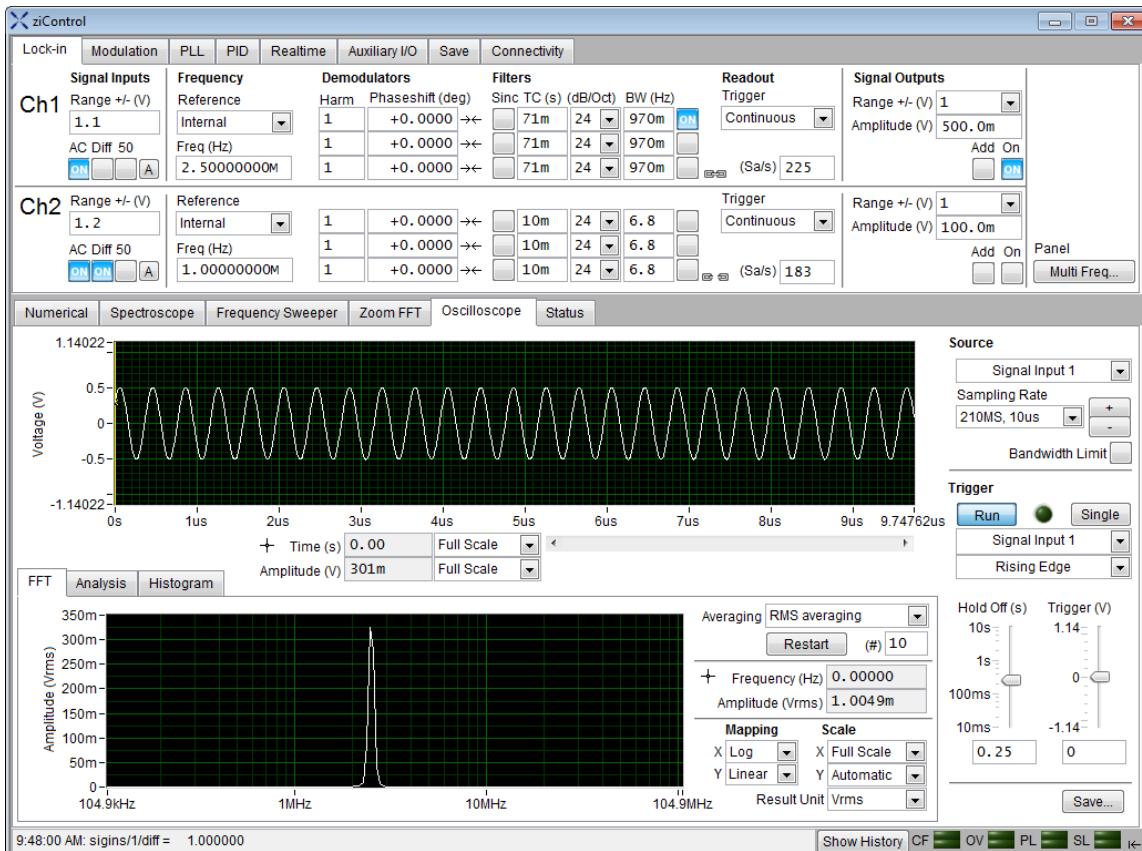


Figure 3.16. ziControl displaying the acquired signal

The Scope displays the measured signal at Input 1. Having set the input range to 1 V ensures that no signal clipping occurs. If you try and set the input range to 0.3 V you see the effect in the Scope window. Note how the red "Over" LEDs on the front panel of the HF2 indicates the error condition and the set OV (OVI) status flag on the right-bottom corner of the window. Set back the input range to 1 V and then clear the flag by clicking \leftarrow .

The Scope is a very handy tool to quickly check settings before proceeding to more advanced measurements, please refer to [Section 4.4.5](#) for a full description of features available in the Scope.

3.2.4. Measure the Test Signal

Next you use the demodulators of the HF2 to measure acquired test signal. You will use the Numerical and the Spectroscopy tab from the graphical user interface. First apply the following settings (choose any of the 6 available demodulators).

Table 3.3. Settings: measure the test signal

Time constant (TC)	10 ms (approximated to 10.2 ms)
Filter slope	12 dB/Oct
Resulting measurement bandwidth (BW)	\sim 10 Hz
Demodulator readout rate	100 Hz (approximated to 112 Hz)
Demodulator enable	ON

These settings set the demodulation filter to second-order low-pass operation with a 10 ms time constant. The corresponding bandwidth is calculated automatically according to [Equation 10.3](#)

provided in [Chapter 10](#). The output of the demodulator filter is read out with 100 Hz, implying that 100 data samples are sent to the host PC per second. These samples are viewed in the Numeric and Spectroscope tab that we examine next.

The Numeric tool provides the space for 6 measurement panels corresponding to the 6 demodulators. Each of the panels has the option to display the samples in Cartesian (X,Y) or polar format (R,Θ) together with the set frequency. The unit of the (X,Y,R) values is V_{RMS}.

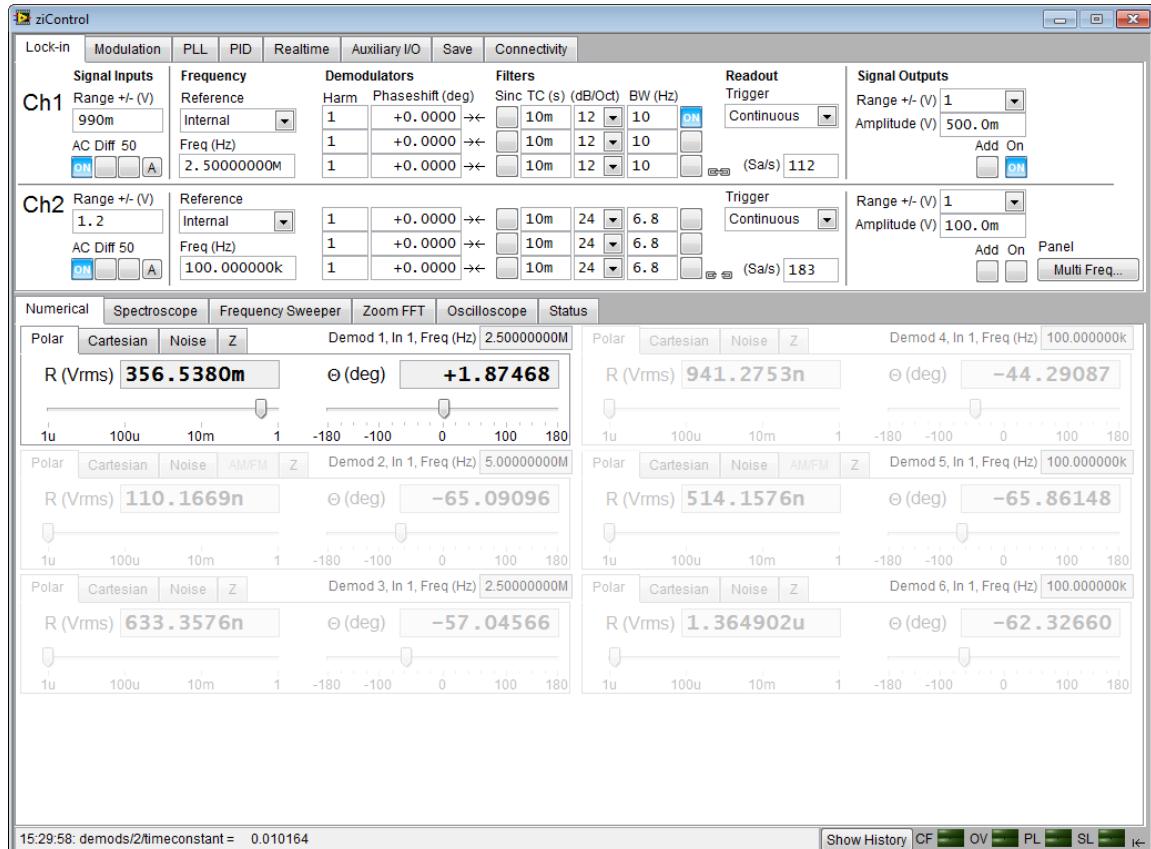


Figure 3.17. ziControl Numeric tab

If you wish to play around with the settings, you could now change the amplitude of the generated signal, and observe the effect on the demodulator output.

Next, we'll have a look at the SpectroscopePlotter tab. This tab provides a time plot of the demodulator outputs. It is possible to plot up to 6 signals continuously as (X,Y) or (R,Θ) pairs, to set different scales, or to make detailed measurements with 2 cursors. For a detailed description of the spectroscope's functionality please refer to [the functional description of the Spectroscope Tool](#).

Start with the following settings, and later you may use the sliders to obtain a different view. In order to set the scales, press the related "+" and "-" buttons. You may try different scale settings. The "Auto Scale" button might also help to find suited scale settings for (X,Y,R,Θ).

Table 3.4. Settings: plot the demodulated test signal

Time scale slider	10.4 s/DIV
Signal scale	20 μ V/DIV

Phase scale	100 m°/DIV
Display settings	R and Theta
BW Limit / Auto Offset switches	ON / ON
Enable demodulator switch	ON

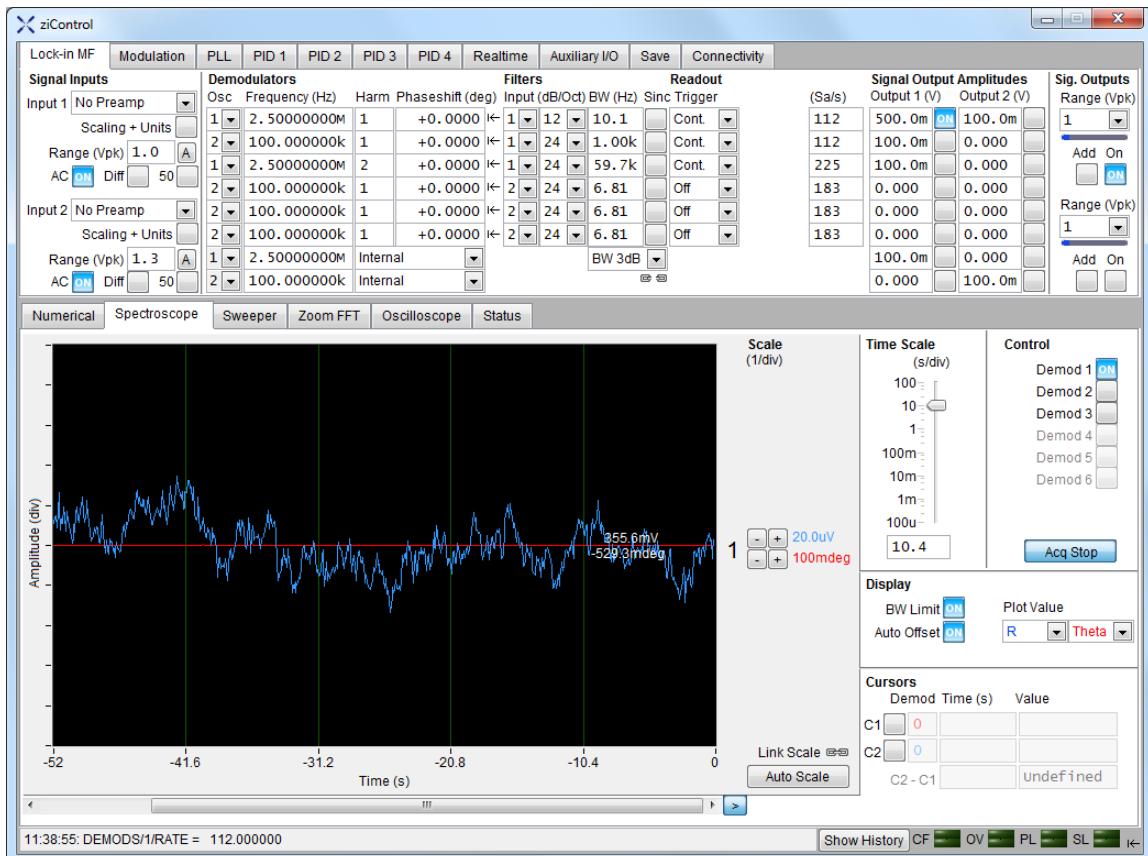


Figure 3.18. ziControl spectroscope view with plot of demodulator 1 output (TC = 10 ms)

3.2.5. Different Filter Settings

As last step of this tutorial you change the filter settings and see the effect on the measurement results. For instance you change the time constant of the integration to 2 seconds.

Table 3.5. Settings: changing the filter settings

Time constant (TC)	2 s
Filter slope	12 dB/Oct
Resulting measurement bandwidth (BW)	~51 mHz

Increasing the time constant increases the integration time of the demodulators smoothing out the demodulator outputs. This averages the noise over time and the output of the filters is more stable. This manifests itself in a smoother curve of the demodulator data but also in a larger number of stable digits in the Numeric tab.

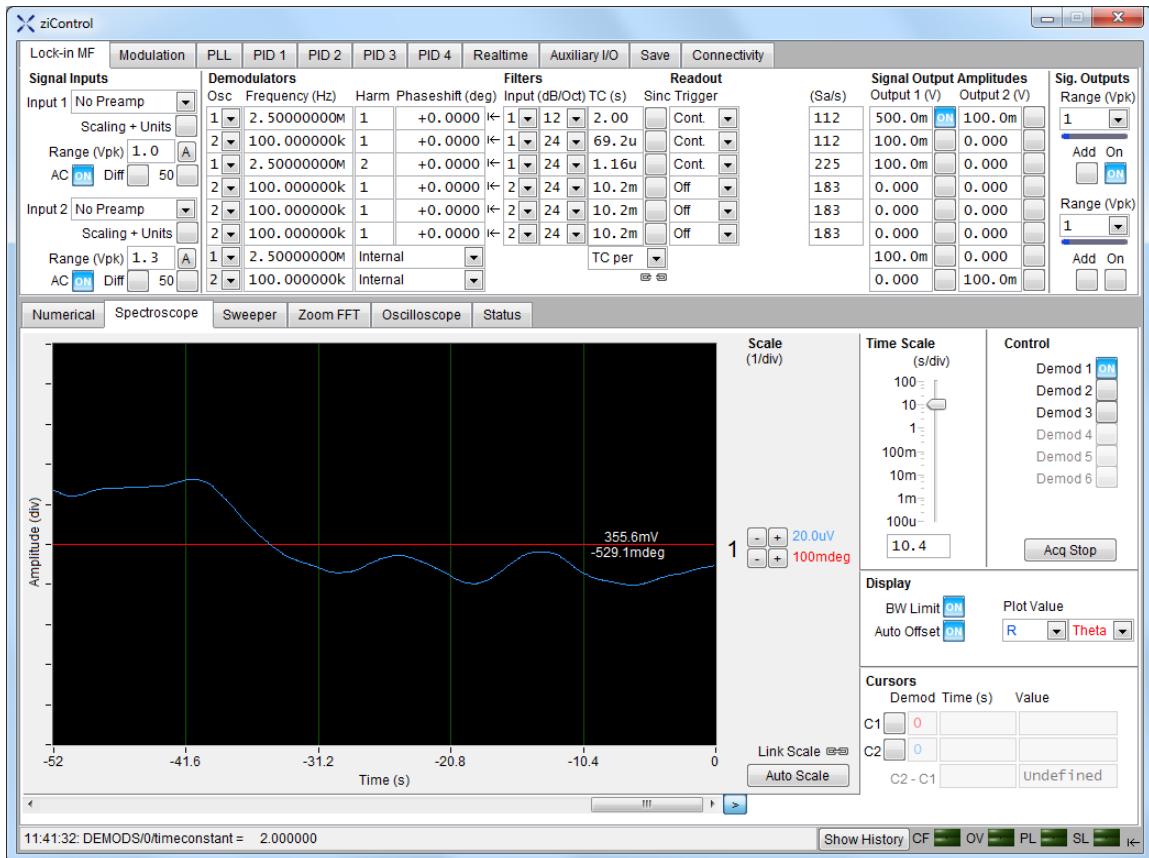


Figure 3.19. ziControl spectroscope view with plot of demodulator 1 output (TC = 2 s)

3.3. Dynamic Signals

3.3.1. Preparation

In this tutorial we generate a test signal of 2.5 MHz with an amplitude modulated at a frequency of 1 Hz. Then we measure the test signal using two different filter settings.

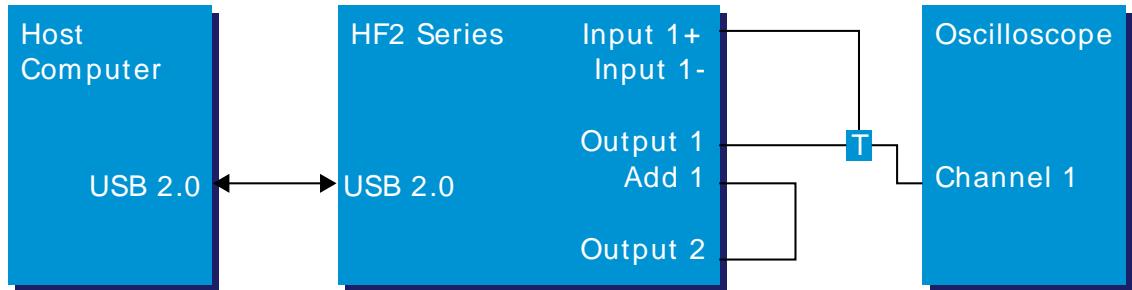


Figure 3.20. Tutorial dynamic signals setup

Note

You must have installed the LabOne and ziControl packages as described in the [Getting Started Chapter](#), and the ziServer must be running on your computer. If you are not sure about that, check the in the Windows Task Manager whether there are the **ziServer.exe** and the **ziService.exe** tasks running. Alternatively check the ziServer as described in the [Programming Chapter](#).

Note

This tutorial can be performed both on the HF2LI Lock-in Amplifier and on the HF2IS Impedance Spectroscopy and will use the Input connector. The generation of multi-frequency signals is simple on the HF2LI with the HF2-MF option or on the HF2IS, where there is no need to make use of the ADD connector.

Connect the cables as described above. Make sure the HF2 unit is powered on, and then connect the HF2 to your computer with a USB 2.0 cable. Finally launch the ziControl application (Start Menu/Programs/Zurich Instruments/ziControl).

3.3.2. Generate the Test Signal

In this section you generate a 2.5 MHz sinusoidal signal whose amplitude oscillates at 1 Hz. This is also called the beat signal. In order to obtain this test signal you add two sinusoids of the same amplitude but with a 1 Hz difference in the frequency.

Table 3.6. Settings: generate the test signal

Output range Ch1	1 V
Oscillator frequency Ch1	2'500'000 Hz
Oscillator amplitude Ch1	0.3 V / ON
Oscillator frequency Ch2	2'500'001 Hz
Oscillator amplitude Ch2	0.3 V / ON
Output Ch1 Add	ON

When connecting an oscilloscope to the Output 1 connector, you should be able to observe the superposition of the 2 sinusoids. To see the acquired signal inside the ziControl graphical user interface switch to the Scope tab. The Scope view looks like this with the following settings.

Table 3.7. Settings: acquire the test signal

Oscilloscope source	Signal Input 1
Oscilloscope trigger select	Signal Input 1
Oscilloscope sampling rate	6.4 kSa, 320 ms
Oscilloscope trigger	RUN
Signal Input Ch1 range	1 V (approximated to 0.98 V)
Signal Input Ch1 AC / Diff / 50	ON / OFF / OFF

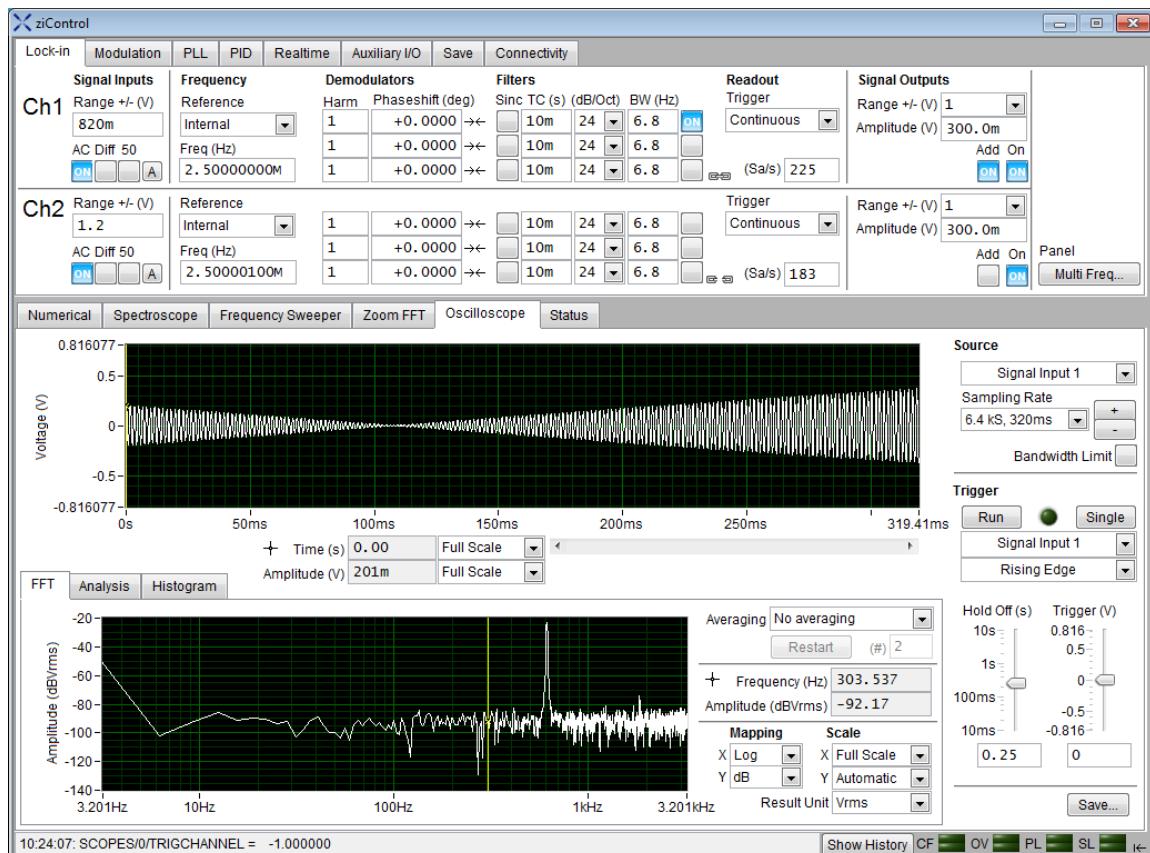


Figure 3.21. ziControl displaying the acquired signal

The beat signal has a maximum amplitude of 0.6 V, thus it falls within the set range of 1 V. The range setting will prevent any higher voltage than what is set - even if 2 sinusoids of 0.7 V amplitude each would be added like done in this section, the output would be clipping at 1 V which is the set range. Try to change the output range to 0.1 V, and see how the output voltage is changed to prevent inconsistent settings.

3.3.3. Measure the Test Signal

First you change to the spectroscope tool, set the scale in order to view an interesting set of data, and set the demodulator filters to a low time constant to measure the amplitude of the 2.5 MHz signal (Hull curve).

Table 3.8. Settings: filter with a low time constant

Time constant (TC)	10 ms (approximated to 10.2 ms)
Filter slope	12 dB/Oct
Resulting measurement bandwidth (BW)	~10 Hz
Demodulator readout rate	100 Hz (approximated to 112 Hz)
Time scale slider	1.58 s/DIV
Signal scale	100 mV/DIV
Phase scale	20 °/DIV

These settings set the demodulation low-pass filter to a 10 ms time constant (the corresponding bandwidth is around 10 Hz) and the filter slope to second order. The output of the filter is sampled at a rate of 100 Hz, and the samples are sent to the host computer.

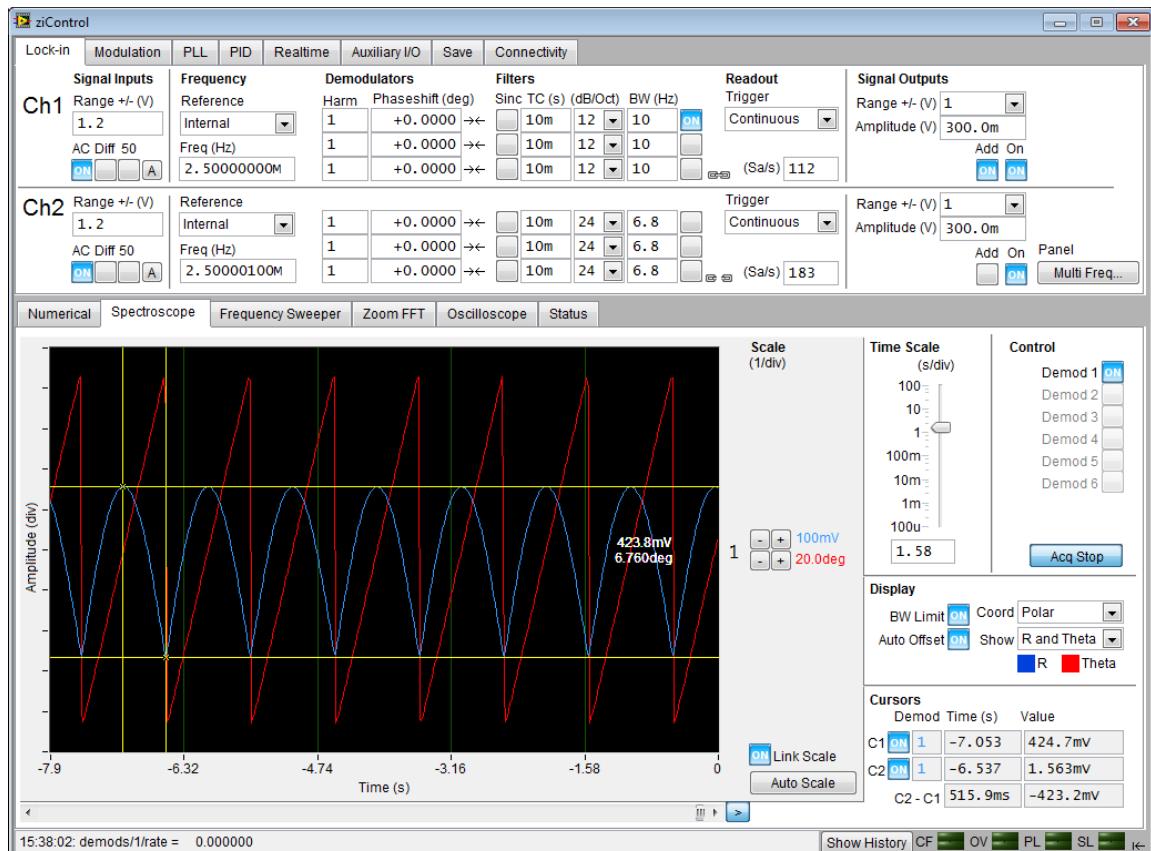


Figure 3.22. ziControl displaying the measured signal

If you stop the acquisition by pressing the button "Acq Stop" you can conveniently measure the amplitude of the 1 Hz signal by using the 2 cursors C1 and C2: 394.4 mV_{RMS}, half period 498.1 ms. You can achieve higher measurement precision by using a even lower time constant (e.g. 1 ms), increasing the readout rate (e.g. 1.8 kHz), and zooming into the Spectroscopic view using a different time scale.

Next you use a high time constant to separate the 2 original sinusoids even though they are superposed in one signal. In the Lock-in tab apply the following settings.

Table 3.9. Settings: filter with a high time constant

Time constant (TC)	2 s
--------------------	-----

Filter slope	24 dB/Oct
Resulting measurement bandwidth (BW)	~35 mHz
Demodulator readout rate	100 Hz (approximated to 112 Hz)

These settings set the demodulation low-pass filter to a time constant of 2 s, with a resulting measurement bandwidth of 35 mHz. With these settings the HF2 is able to distinguish between the signal component at 2'500'000 Hz and the signal component at 2'500'001 Hz as the measurement bandwidth is considerably less than the frequency spacing of the 2 signal components. The output of the demodulator is stable after a settling time.

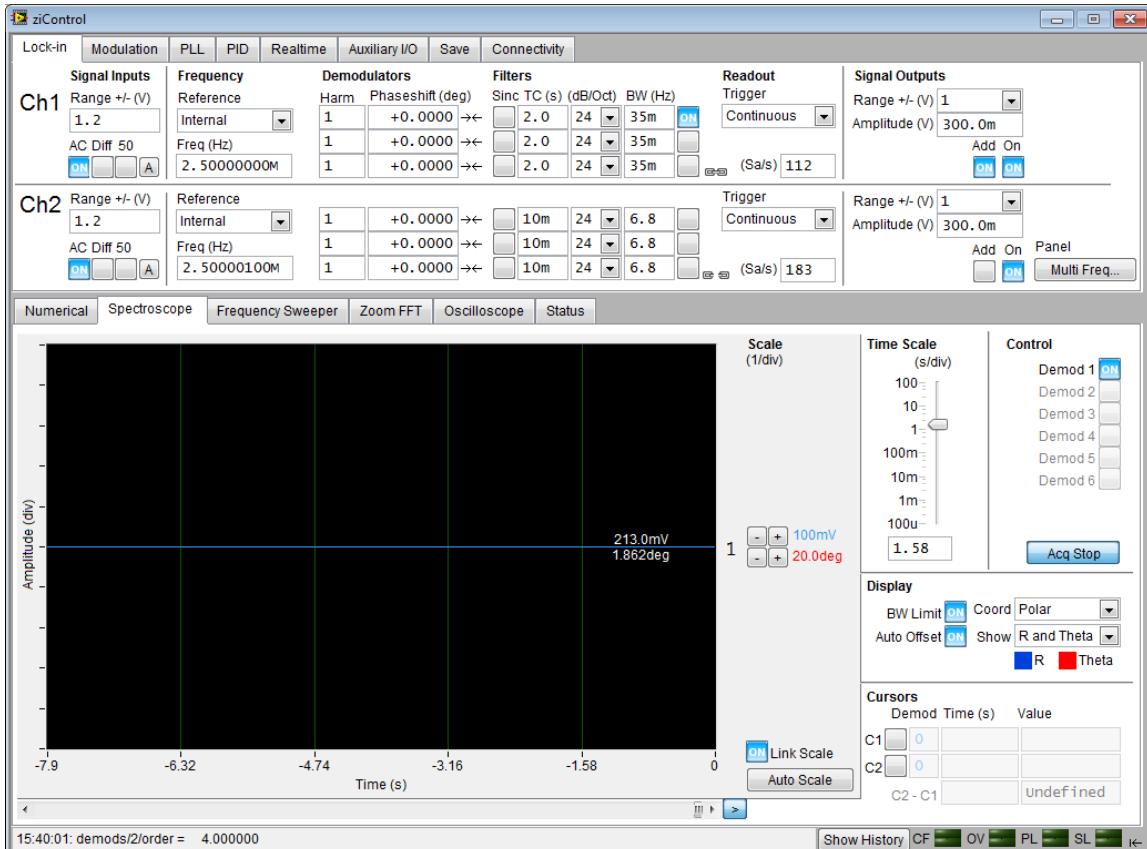


Figure 3.23. ziControl displaying the measured signal

The output of the demodulator does not show any oscillations like before: the numerical value is 212.214 mV_{RMS}. If you switch to the oscilloscope view, you see that the signal at Input 1 is still beating as before, while the demodulator filter is set such to ignore the interferer at 2'500'001 Hz. Try to switch off the interferer.

Table 3.10. Settings: remove the interferer

Signal output Ch2 switch	OFF
--------------------------	-----

The numerical value changes to 212.8 mV_{RMS} with an angle of 53.6°. The time it takes to settle depends on various parameters like filter setting and switch-off timing. The difference in amplitude of the measurement at 2'500'000 Hz with or without interferer is in the range of 50 µV. With different filter settings it is possible to do better than that.

Consider this: you have 2 signals with relevant amplitude (0.3 V) interfering with each other as their frequency is very close (1 Hz at 2.5 MHz). The power of lock-in amplification consists of extracting

the relevant signal energy at exactly one frequency. The "immunity" from nearby interferer is the capability to ignore them. This is a simple definition of the dynamic reserve.

3.3.4. Filter Setting Discussion

This section aims to summarize a few basic concepts of filtering in connection with lock-in amplification. In this tutorial, you have used different filter settings to measure different signal properties.

Table 3.11. Settings: filter with a high time constant

Time constant	Measurement bandwidth	Measurement noise	Changes upon steady state change	Example
Low setting	High, capability to detect fast events	More noise in measurement result	Fast adaptation of result	BW = 10 kHz, capability to detect events at 2-5 kHz, prone to pick-up noise
High setting	Low, capability to determine the steady state	Less noise in measurement result	Slow adaptation of result	BW = 50 mHz, exact determination of steady state - events more frequent than 0.1 Hz are filtered

Filtering constitutes a trade-off between measurement speed and measurement accuracy. In order to measure fast events, it is necessary to open up the filters allowing also more noise in the measurement result. The opposite is to measure with narrow filters which increases the signal-to-noise ratio, but limits the capability to detect the changes in the signal of interest. This trade-off is common with any lock-in amplifier. The power of the HF2 is that it allows to do both at the same time thanks to the multiple demodulators per input channel.

3.4. External Reference

3.4.1. Preparation

In this tutorial we generate a test signal with the HF2 and use it as a reference signal for demodulation in the same way as we would do it with a reference signal coming from an external source.

This is done by connecting the Output 2 connector with the Input 2+ connector with a BNC cable. This tutorial shows a single-ended operation, meaning that there is no signal going into the Input 2- connector. Optionally, it is possible to connect the generated signal from Output 2 to an oscilloscope by using a T-piece and an additional BNC cable. The Output 1 connector is to be connected to the Input 1+ connector. This allows you to check the generated reference signal. The measurement setup is shown in the following figure.

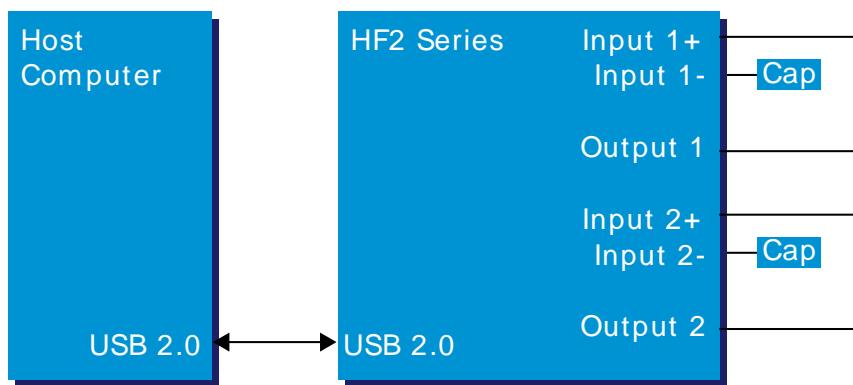


Figure 3.24. Tutorial external reference setup

Make sure the HF2 unit is powered on, and then connect the HF2 to your computer with a USB 2.0 cable. Finally, launch the ziControl application (Start Menu/Programs/Zurich Instruments/ziControl).

3.4.2. Generate the Reference Signal

In this section you generate a 1.0 MHz signal with a 1 V amplitude on Output 2 for use as the external reference. The settings for generating the reference signal are shown in the following table.

Table 3.12. Settings: generate the reference signal

Output range Ch2	1 V
Output amplitude Ch2	1.0 V / ON
Frequency Ch2	1 MHz
First demodulator Ch2	ON
Signal input range / AC / Diff / 50 Ch2	1.2 V / ON / OFF / OFF

When connecting an oscilloscope to the Output 2 connector, you should be able to observe the sinusoid. Alternatively, you can look at the signal in the ziControl Scope with the following settings.

Table 3.13. Settings: acquire the reference signal

Oscilloscope source	Signal Input 2
---------------------	----------------

Oscilloscope trigger	Signal Input 2
Oscilloscope time scale	3

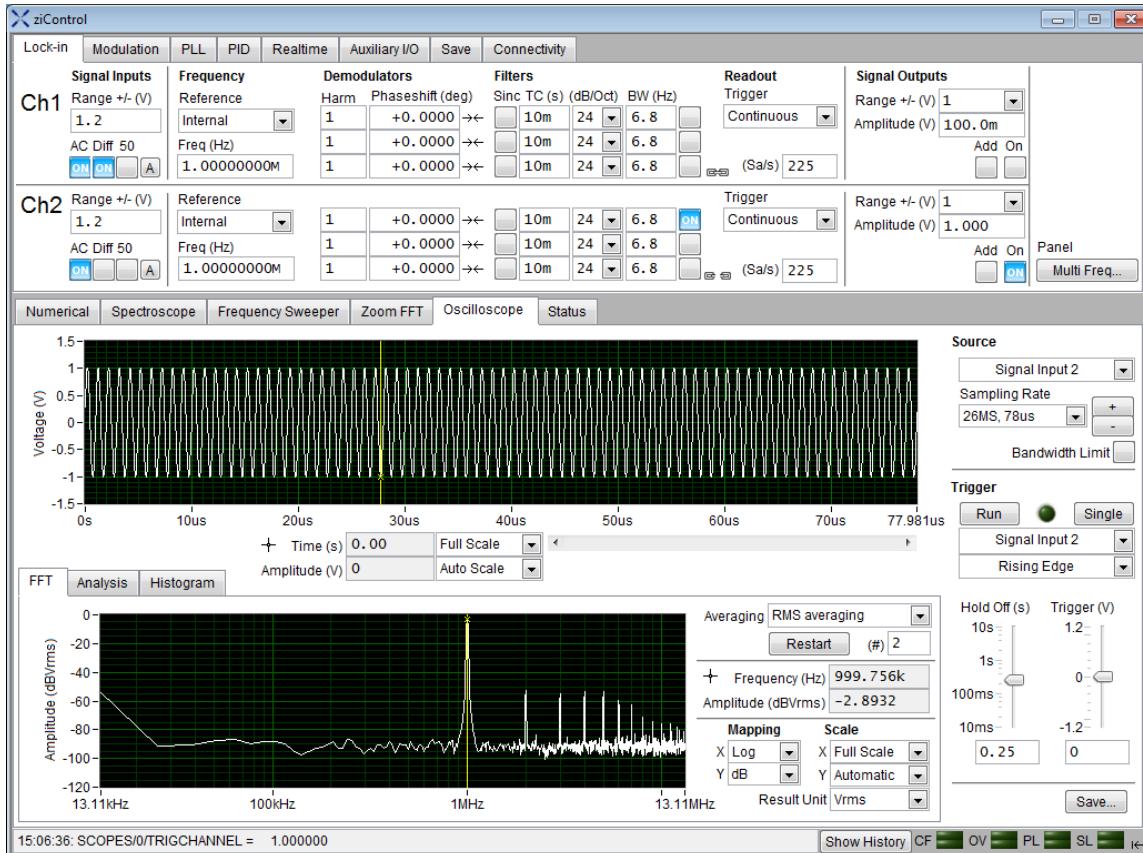


Figure 3.25. ziControl displaying the reference signal

Note

The FFT display can be used to check the frequency contents of the signal. Conveniently use the view settings automatic for the X axis and for the Y axis with logarithmic scale set (dB). The RMS averaging can be enabled to reduce the noise floor in the display.

3.4.3. Activate the External Reference Mode

In this section we activate the external reference mode. Based on the external reference, we demodulate a separate signal of interest.

Table 3.14. Settings: generate the signal of interest

Output range Ch1	1 V
Output amplitude Ch1	1.0 V / ON
Frequency Ch1	1 MHz
First demodulator Ch1	ON
Signal input range / AC / Diff / 50 Ch1	1.2 V / ON / OFF / OFF

Use the reference pull-down selector for channel 1 and set it to "Signal Input 2". This uses the signal present at channel 2 to demodulate the signal present at channel 1. Switch to the Numerical tab.

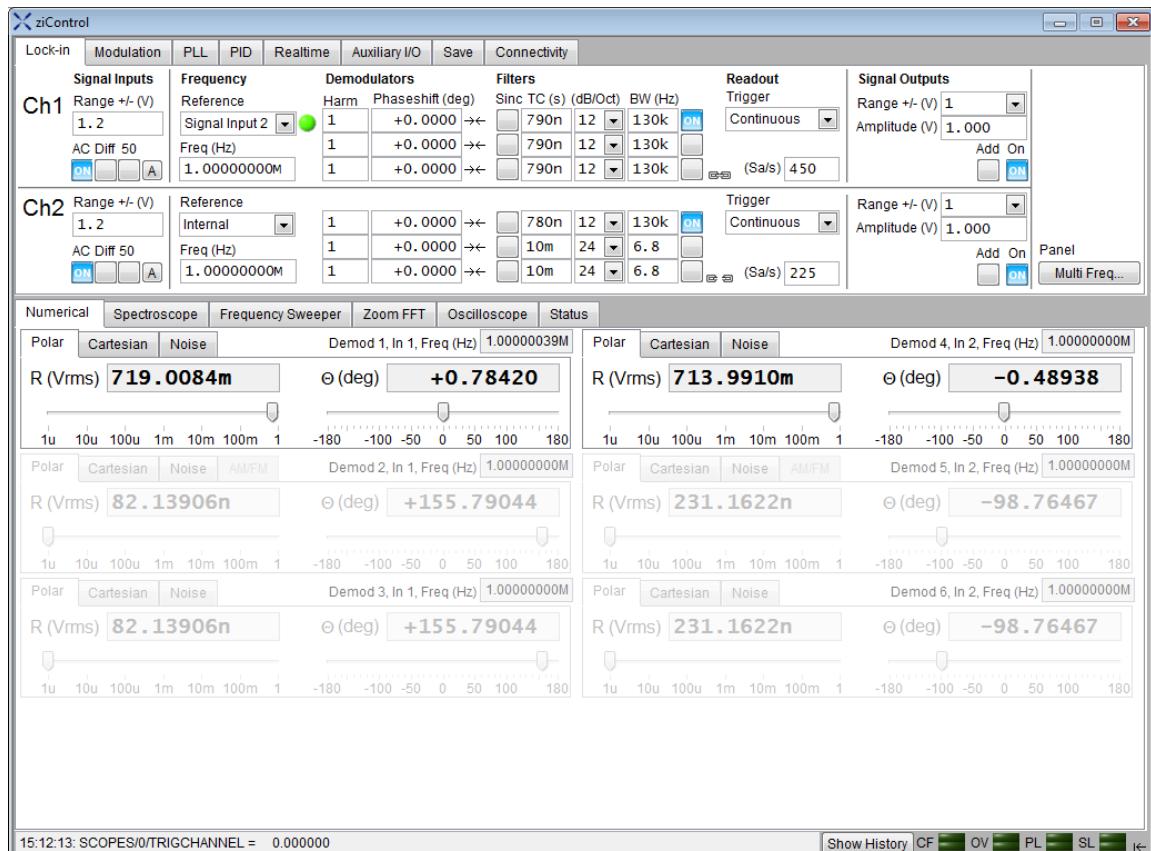


Figure 3.26. ziControl enabling external reference mode

Once the external reference mode has been enabled, the frequency of channel 1 changes continuously, adapting to the frequency of the external reference signal. This can be verified by changing the frequency of channel 2 and noting how the frequency of channel 1 follows. A green indicator appears besides the reference selection for channel 1 indicating that the instrument has locked to an external reference.

In the demodulation process, the measurement signal is not multiplied directly with the external reference signal. Instead, the measurement signal is multiplied with newly generated reference signal from the internal oscillator, using only the frequency and phase information of the external reference. The continuous toggling of the oscillator frequency shows that the newly generated reference is continuously adjusted to the external reference.

The oscillator adapting to the external reference signal can be observed by enabling Output 1. If Output 1 has been connected to Input 1+, the signal can be examined directly using the oscilloscope tool of ziControl with the settings in the following table.

Table 3.15. Settings: acquire the signal of interest

Oscilloscope source	Signal Input 1
Oscilloscope trigger	Signal Input 1
Oscilloscope sampling rate	26 MS, 78 us

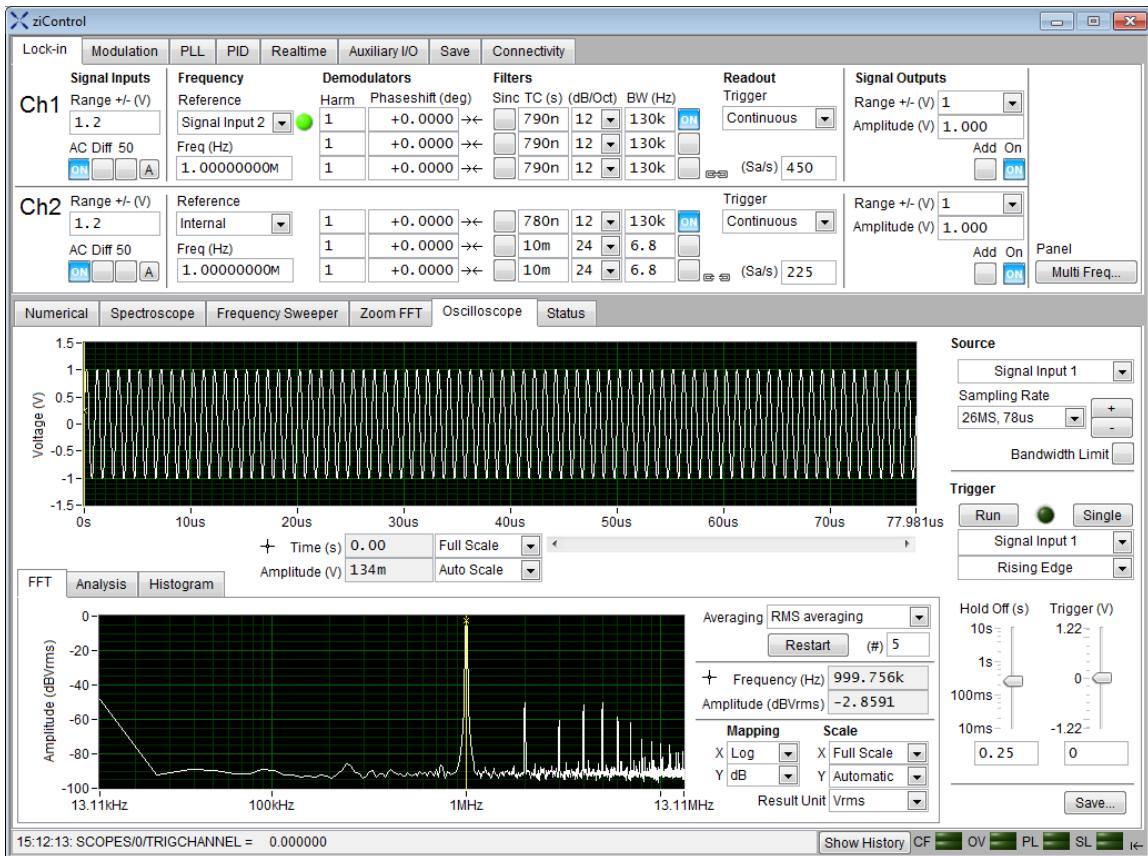


Figure 3.27. ziControl displaying the regenerated signal of interest

3.4.4. Change External Reference Input

In this section you will modify the setup to use DIO 0 as the external reference instead of Signal Input 2. This is useful in practice since it means that the two sensitive Signal Inputs of the Instrument remain available for actual measurements. The modified setup is shown on Figure 3.28. Note that the DIO 0 connector is located on the back panel of the HF2 Instrument.

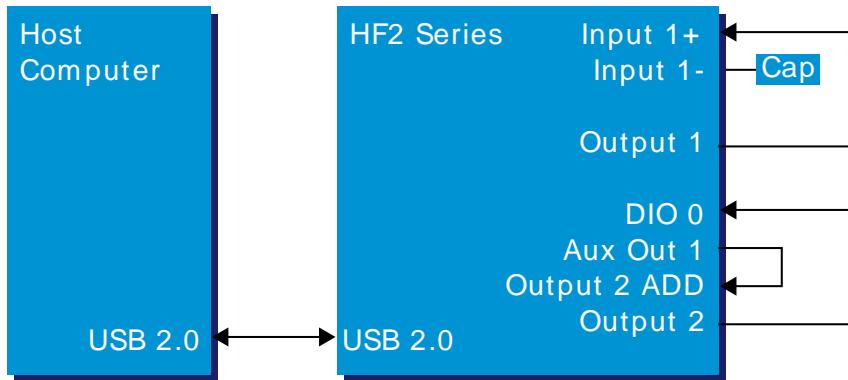


Figure 3.28. Tutorial external reference using DIO 0 setup

It is important to make sure that DIO 0 is configured as an input before connecting anything to it. This can be done using the Auxiliary I/O view in ziControl as shown in the following figure. Note that the button to the right of bits 7...0 should be off.

When using the DIO 0 as the external reference signal, it should be taken into account that this is a digital I/O, which should be operated at TTL levels. Therefore the Aux 1 output is connected to the Add connector of Output 2, to provide a DC shift of the test signal and thus make it TTL compatible.

The settings used for generating the test signal are shown in the following tables. The resulting signal will have a DC offset of 1.5 V and an amplitude of 1 V and will thus oscillate between 0.5 V and 2.5 V, which is TTL compatible.

Table 3.16. Settings: generate the test signal

Output range Ch2	1 V
Output amplitude / ADD Ch2	1.0 V / ON
Frequency Ch2	1 MHz
First demodulator Ch2	ON

Table 3.17. Settings: generate the DC shift

Aux 1 Signal	Manual
Aux 1 Offset	1.5 V

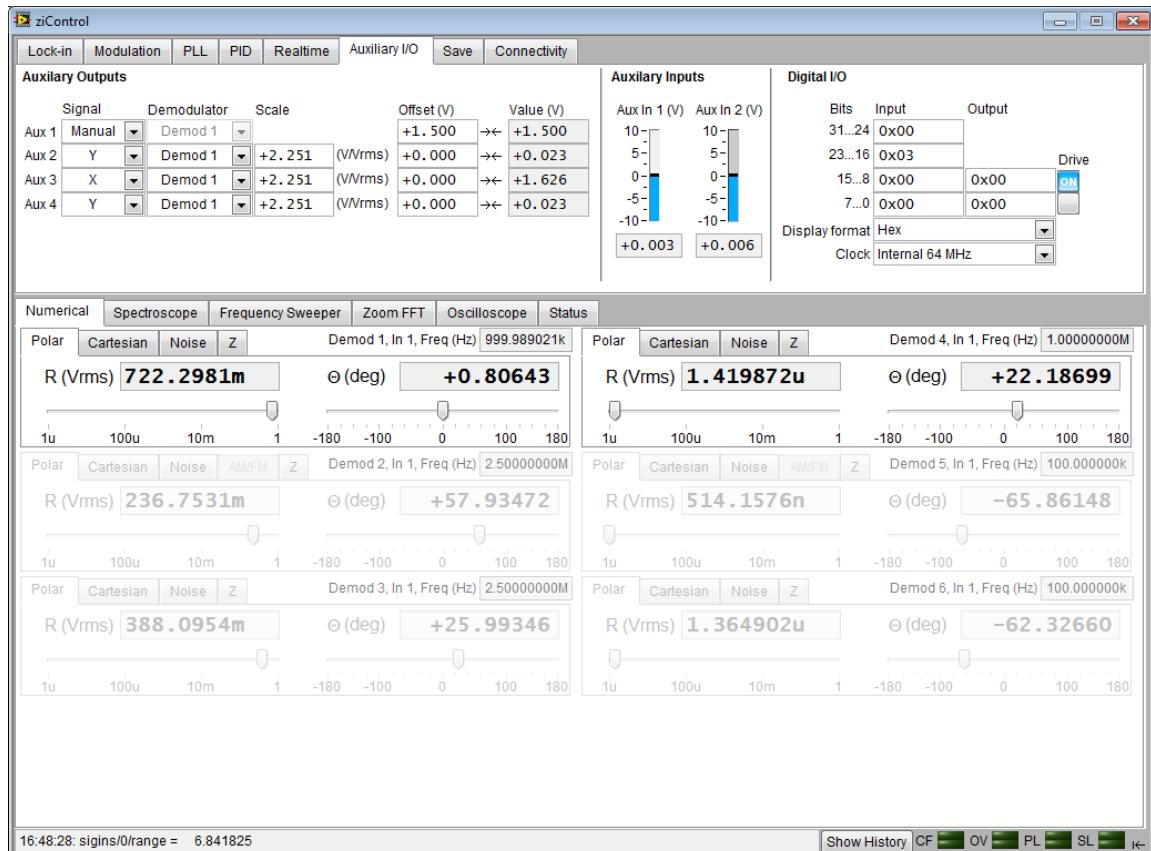


Figure 3.29. Configuring DIO 0 as reference input

Finally use the reference pull-down selector for channel 1 (in the Lock-in tab) and set it to "DIO 0". This makes channel 1 lock on DIO 0 (as shown on Figure 3.30). The frequency of channel 1 should start updating similarly to what was described in Section 3.4.3.

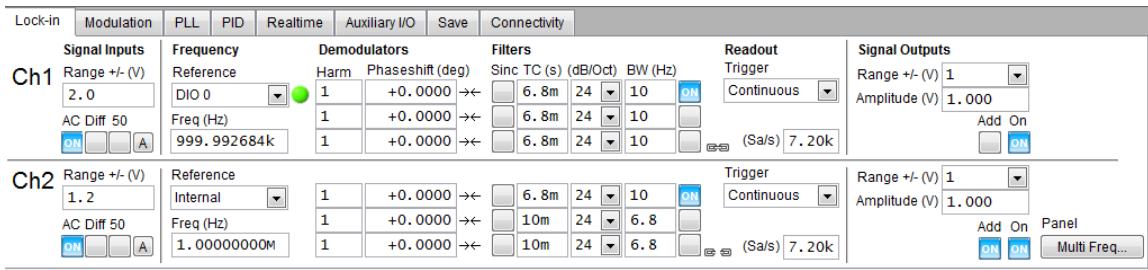


Figure 3.30. Setting DIO 0 as reference for Channel 1

3.5. Noise Measurement

Lock-in amplifiers can be used to measure the noise on a signal. By quantifying the noise of a system one can estimate the maximum achievable performance.

3.5.1. How Does a Lock-in Measure Noise?

A lock-in amplifier measures the signal amplitude close to a given reference frequency with a defined bandwidth around this reference frequency. The noise in an input signal near the reference frequency appears as noise in the lock-in amplifier signal output.

The noise is the standard deviation of the measured X or Y value and is measured by first calculating the average, X_{avg} , over a defined period of time. Then, this signal, X_{avg} , is subtracted from the X value to get the deviation. Finally, the RMS (root-mean-square) is calculated, corresponding to the total noise power of the input signal within a defined bandwidth around the reference frequency. The value is correct for input noise with Gaussian distribution of the noise power, which is normally the case.

Most of the times the noise spectral density is of interest, which is the normalization of the X_{noise} made independent of the filter bandwidth. To calculate the noise spectral density from the calculated RMS noise, one needs to divide the measured value by the square root of the bandwidth \sqrt{BW} . The noise spectral density has the units V/\sqrt{Hz} .

The related equations are $X_{noise} = RMS(X - X_{avg})/\sqrt{BW}$, and $Y_{noise} = RMS(Y - Y_{avg})/\sqrt{BW}$ respectively. The X and Y noise are expected to be identical.

3.5.2. Measuring the Noise of the HF2LI/HF2IS

A LabVIEW example (ziExample-HF2-Noise.vi) is available to measure the noise in an input signal. To measure the equivalent input noise of the HF2, remove all BNC connectors from the input of the device and apply the following settings in ziControl.

Table 3.18. Settings: Measure HF2 Noise

Signal input range / AC / Diff / 50 Ch1	0.01 V / ON / OFF / ON
First demodulator Ch1	BW = 100 Hz, dB/Oct = 24
Frequency Ch1	1 MHz
Output switch Ch1	OFF

Run the example, ziExample-HF2-Noise.vi. Make sure that the correct Demodulator is selected. The noise spectral density should now show a value close to $5 \text{ nV}/\sqrt{\text{Hz}}$. By changing the settings in the user interface, the noise behavior of the device can be analyzed in more detail. For example, changing the reference frequency to 10 kHz will slightly increase the spectral noise density, because of flicker noise that is larger at lower frequencies and generally present in all electronic circuits.

3.6. Amplitude Modulation

Note

This tutorial is addressed to HF2LI lock-in amplifier users that have purchased both HF2-MF multi-frequency and HF2-MOD AM/FM modulation options.

Amplitude modulation (AM) and frequency modulation (FM) refer to the modulation of an oscillating signal $s(t) = A \cos(\omega t + \varphi)$, the so-called carrier. A and $\omega t + \varphi$ are the amplitude and the phase of the signal, respectively. [Figure 3.31](#) depicts the phasor representation of $s(t)$. The phasor follows a circle with radius A , and the phase wraps around after a full revolution of 360° . The signal $s(t)$ is the projection of the phasor on the abscissa.

In the case of AM signals, the amplitude A , i.e. the phasor length, is time dependent, as in [Figure 3.31\(b\)](#). In the case of FM signals, the phase offset φ is time dependent and the phasor has a constant amplitude, see [Figure 3.31\(c\)](#).

Amplitude and frequency modulation, best known from radio transmission, are also common lock-in detection techniques.

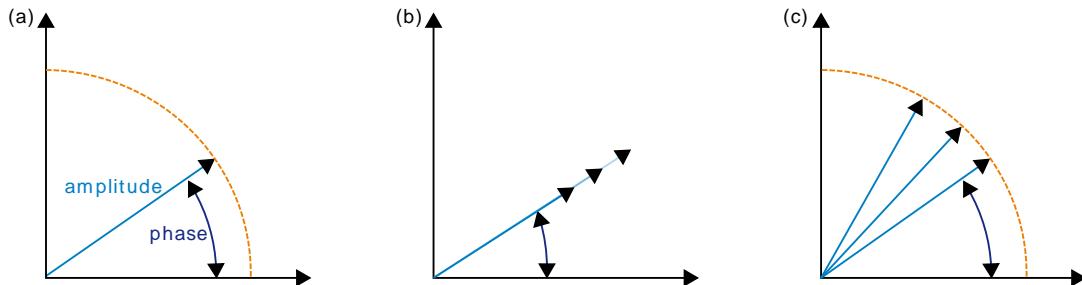


Figure 3.31. A sinusoidal signal represented as a phasor: the signal corresponds to the projection on the x axis. Amplitude (b) and frequency (c) modulated signals affect the amplitude of the phasor or its phase

3.6.1. What is Amplitude Modulation?

In the time domain, amplitude modulation of the carrier signal produces a variation of the carrier amplitude proportional to the amplitude of the modulating signal. For example, when the amplitude of a carrier with a frequency $f_c = \omega_c / 2\pi$ is modulated by a signal with frequency $f_m = \omega_m / 2\pi$ (where $f_m < f_c$), the resulting signal has the form

$$s(t) = [A + M \sin(\omega_m t)] \sin(\omega_c t + \varphi) \quad (3.1)$$

$$= A \sin(\omega_c t + \varphi) + \frac{M}{2} \cos[(\omega_c - \omega_m)t + \varphi] - \frac{M}{2} \cos[(\omega_c + \omega_m)t + \varphi] \quad (3.2)$$

where A and M are the amplitudes of the fast and slow modulations respectively and φ the phase offset. There is no restriction on the magnitude of M compared to A . The information of interest is encoded in these three parameters, A , M and φ .

In the frequency domain, the AM signal $s(t)$ is composed of three frequencies: the carrier at f_c and two additional sidebands at $f_c - f_m$ and $f_c + f_m$, as shown in [Equation 3.2](#). The two sidebands have equal amplitude $M/2$, half of the modulating signal, and the carrier amplitude is independent on the modulation amplitude.

The traditional way of measuring an AM signal is called double (or tandem) demodulation and requires two lock-in amplifiers: the first one demodulates the signal at f_c with a bandwidth that is at least as large as f_m (but smaller than $f_c - f_m$). This is to ensure that the full amplitude of the modulation signal is retained. The demodulated signal after the first lock-in becomes

$$s(t) \cdot \cos(\omega_c t) \xrightarrow{\text{after filtering}} d_1(t) = \frac{A + M \sin(\omega_m t)}{2} \cos \varphi$$

Equation 3.3. Tandem demodulation

In $d_1(t)$, the two sidebands are now located at the same frequency f_m , while the carrier appears as a DC component. When the demodulated signal $d_1(t)$ is fed to a second lock-in amplifier, the result of the second demodulation at f_m is proportional to $M \cos \varphi$.

In order to recover the amplitude M it is necessary to measure the φ so one can divide the result by the factor $\cos \varphi$. To measure the phase one can use a third lock-in to demodulate $s(t)$ at the carrier frequency f_c with a bandwidth smaller than f_m as shown in [Figure 3.32](#).

Instead of using a tandem configuration, the HF2-MOD option allows the user to demodulate directly at the three frequencies f_c and $f_c \pm f_m$ simultaneously. The three parameters A , M and φ can be measured and displayed with a single instrument.

Internally, the HF2LI generates the phases $\omega_c t$ and $\omega_m t$ from which it produces $(\omega_c - \omega_m)t$, $\omega_c t$ and $(\omega_c + \omega_m)t$. This ensures the correct phase relationship for the demodulations of the sidebands.

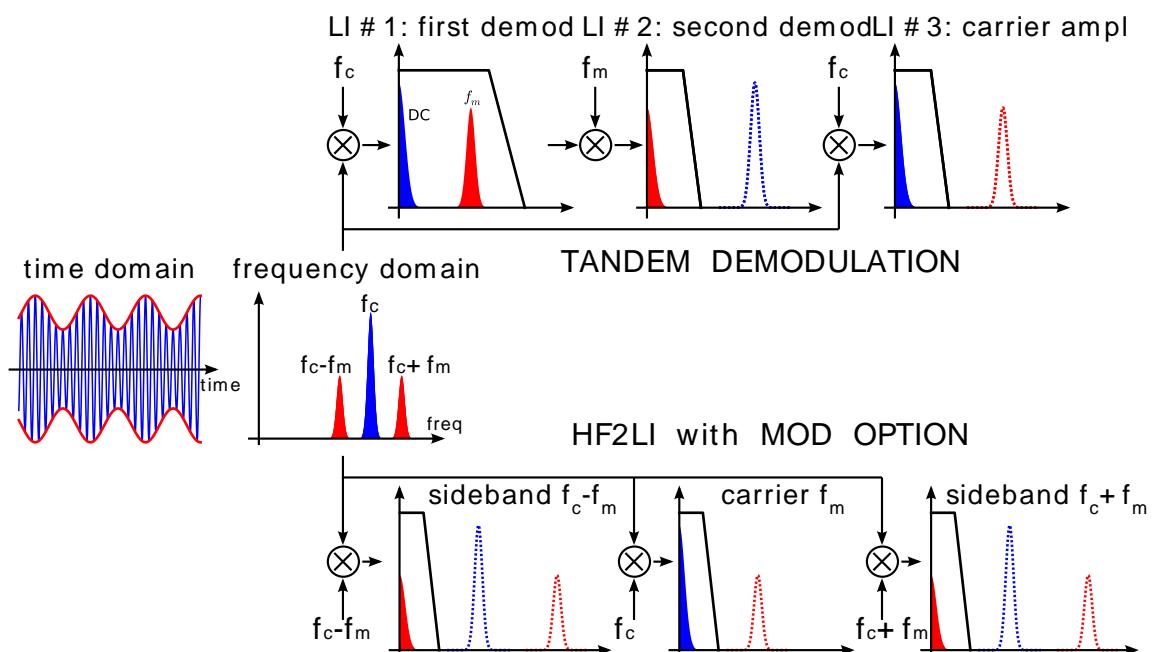


Figure 3.32. Comparison between tandem demodulation and the HF2-MOD option of an AM modulated signal

3.6.2. Generate the Test Signal

In this tutorial, you are going to generate an AM signal with a carrier frequency of 1 MHz and a modulation frequency of 100 kHz. The signal is generated at Signal Output 2 and is demodulated by the first lock-in unit by feeding it into Signal Input 1. The HF2-MOD option requires the HF2-MF Multi-frequency option because each modulated signal requires at least two oscillators. Note that changing the Modulation tab settings will modify some of the settings found in the Lock-in MF tab. The reader is kindly referred to [Section 4.2.3](#).

Start by enabling the Signal Output 2 in the Lock-in MF tab and disabling all demodulator Output Amplitudes. This will ensure that only the desired components of the amplitude-modulated signal appear on the output.

Table 3.19. Settings: generate the AM signal

Signal Output 2 Enable	ON
Signal Output Amplitudes Demodulators 1-8	OFF

In the Modulation tab, in the MOD 2 section, select the following parameters:

Table 3.20. Settings: generate the AM signal

Carrier Oscillator (Osc)/Frequency	Osc 1 / 1 MHz
Sideband 1 Oscillator (Osc)/Frequency	Osc 2 / 100 kHz
Carrier Mode	AM
Generation Carrier/Modulation Amplitude	200 mV / 100 mV
Generation Carrier/Modulation Enable	ON / ON
MOD 2 Enable	ON

This generates an AM signal with two sidebands of equal amplitude. To look at this signal, connect Signal Output 2 to Signal Input 1 of the HF2LI. Select the correct input parameters: in the Lock-in tab, for Signal Input 1, make sure Differential mode and $50\ \Omega$ are disabled. Then click on the auto range button (marked by A). In the Scope tab, select Source to be Signal Input 1, Trigger to be Signal Output 2 and click on Run to activate the Scope. Observe how the carrier amplitude is modulated at 100 kHz as seen in [Figure 3.33](#). The FFT tab underneath shows three peaks: the carrier at 1 MHz and two sidebands at 0.9 and 1.1 MHz (see the FFT tab of [Figure 3.33](#)).



Figure 3.33. The AM signal generated by Mod2 as measured on Signal Input 1

3.6.3. Measure the Test Signal

In the Modulation tab, in the MOD 1 section, select the following parameters:

Table 3.21. Settings: measure the AM signal

Carrier oscillator (Osc)	1
Sideband 1 oscillator (Osc)	2
Carrier Mode	AM
Low-pass Filter BW (Carrier)	10 Hz
Low-pass Filter BW (Sideband 1)	10 Hz
MOD 1 Enable	ON
Demod 1, 2, 3 Readout (Lock-in tab)	Continuous

This sets the correct demodulation of the AM signal with the two sidebands. In the Numeric tab, look at the amplitude of the carrier, $142 \text{ mV}_{\text{RMS}}$ and of the two sidebands, $35 \text{ mV}_{\text{RMS}}$ each, one quarter of the carrier amplitude: this corresponds to a modulation index of 50%. On the Numerical indicator for Demodulator 2, click on the AM/FM tab: there you can read directly the measured modulation index h and the measured modulation index M .

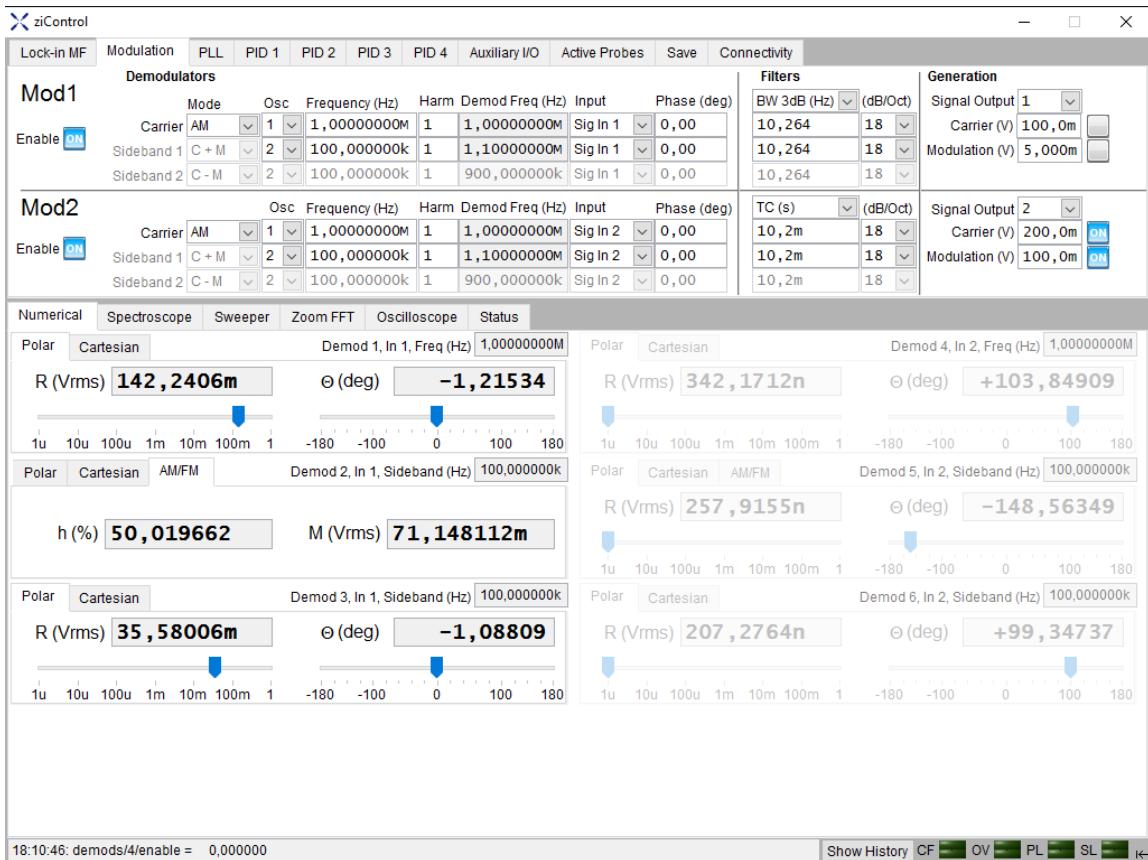


Figure 3.34. The numerical view of the demodulated signal

The HF2 directly demodulates simultaneously at all three frequencies, therefore using three demodulators: this appears in the Numerical tab, where 3 indicators are running, or in the Lock-in MF tab, where three readout outputs are enabled.

3.7. Frequency Modulation

Note

This tutorial is addressed to HF2LI lock-in amplifier users that have purchased both HF2-MF multi-frequency and HF2-MOD AM/FM modulation options.

3.7.1. What is Frequency Modulation?

In frequency modulation (FM), the amplitude of the modulating signal is proportional to the instantaneous frequency deviation from a fixed frequency. In the simplest case shown in [Figure 3.35\(a\)](#), the modulated signal

$$s(t) = A \cos \left[\omega_c t + \frac{\omega_p}{\omega_m} \sin(\omega_m t) + \varphi \right]$$

Equation 3.4. Frequency modulation

is produced when a carrier signal of frequency $f_c = \omega_c / 2\pi$ is modulated by a sinusoidal modulation with frequency $f_m = \omega_m / 2\pi$. The maximum variation of the frequency around the carrier frequency, the peak frequency deviation, is $f_p = \omega_p / 2\pi$. The physical information is encoded in the parameters A , f_p and φ .

Because the frequency is the time derivative of the phase (divided by 2π) and the phase is the argument of the cosine in equation [Equation 3.4](#), we can define the instantaneous frequency as

$$f(t) = f_c + f_p \cos(2\pi f_m t)$$

Equation 3.5. Instantaneous frequency

The spectrum of the FM signal of [Equation 3.4](#) is more complicated than in the case of amplitude modulation. It consists of the carrier and a series of pairs of sidebands at multiple integers of f_m around the carrier frequency, see [Figure 3.35\(d\)](#). The amplitudes of the carrier and sidebands are given by mathematical functions called Bessel functions usually indicated by J_n evaluated at the modulation index $h = f_p / f_m$. For instance, the n -th pair of sidebands is located symmetrically about f_m at $f_c \pm n f_m$ and its amplitude is $J_n(h)$.

A peculiarity of the Bessel functions is that they oscillate around zero: even for the carrier, as the modulation index is increased, its amplitude $J_0(h)$ decreases, crossing zero at $h \approx 2.41$ and then it increases in amplitude (in anti-phase) before reaching zero again at $h \approx 5.52$.

At low modulation indexes, the amplitude of higher sidebands is very low and can thus be ignored: this is called the narrow-band approximation. In this limit (it is customary to assume $h < 0.2$), only the two sidebands at $f_c \pm f_m$ have non-negligible amplitude and the signal $s(t)$ can be approximated by

$$\tilde{s}(t) = A [J_0(h) \sin(\omega_c t + \varphi) - J_1(h) \cos[(\omega_c + \omega_m)t + \varphi] + J_1(h) \cos[(\omega_c - \omega_m)t + \varphi]]$$

Equation 3.6. Approximated narrow-band FM signal

The first term is the carrier, the other two are the lower and upper sidebands. The problem of finding h (and the peak amplitude f_p) reduces now to comparing the amplitude of the first pair of sidebands and the carrier to the ratio $J_1(h)/J_0(h)$. A plot of the ratio $J_1(h)/J_0(h)$ and $J_2(h)/J_0(h)$ is shown in Figure 3.35(e).

Even though $\tilde{s}(t)$ looks very similar to an AM signal, there is a subtle but substantial difference: the phases of the sidebands are offset with respect to that of the carrier. This results in the sidebands being in quadrature with the carrier. For example, assume that $\varphi = 0$: demodulating $\tilde{s}(t)$ with the carrier signal $\sin(\omega_c t)$ gives the DC component (the carrier) but no sidebands; on the other hand, demodulating with the quadrature $\cos(\omega_c t)$, only the two sidebands at f_m are observed and no carrier is present. Because of this, FM detection can be done in a similar way as AM detection scheme, using the tandem configuration described previously in Section 3.6.1.

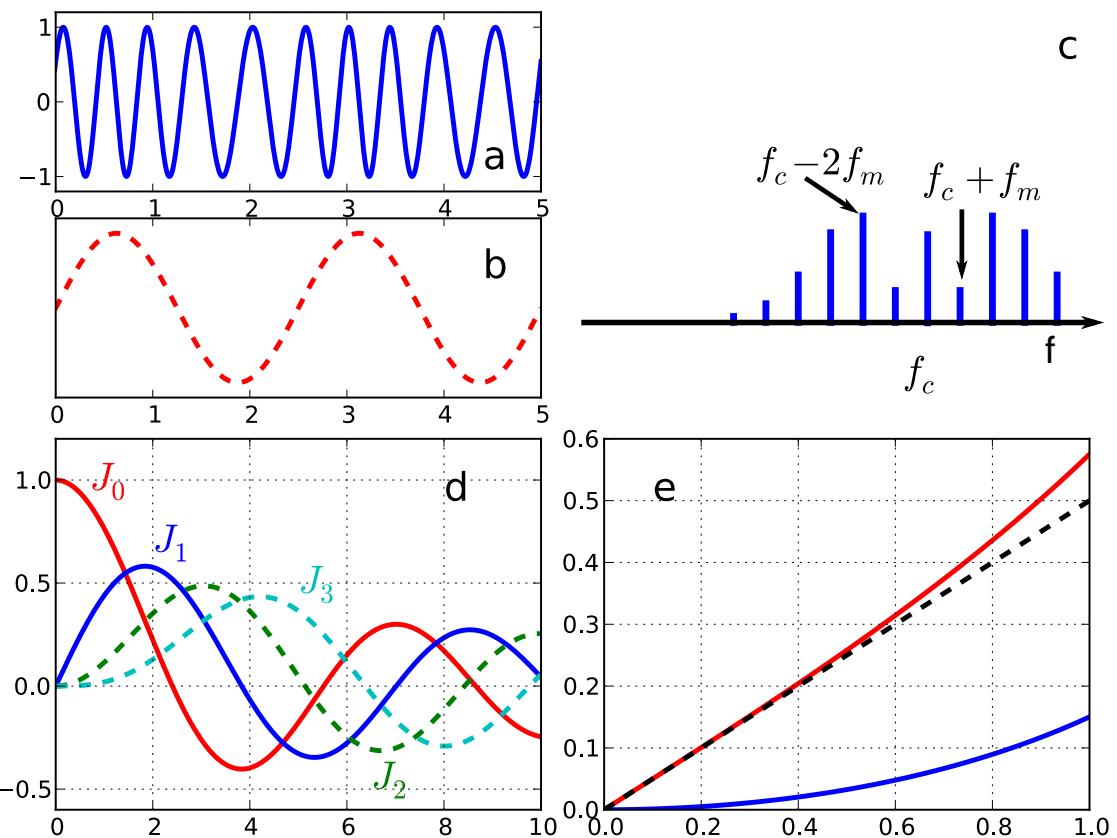


Figure 3.35. (a) A simple frequency modulated signal, (b) its instantaneous frequency, (c) the frequency domain spectrum of a FM signal is composed of an infinite series of sidebands, here depicted for $h = 0.35$, (d) n-th Bessel function versus h , (e) ratio $J_1(h)/J_0(h)$ (red line), $J_2(h)/J_0(h)$ (blue line), slope 0.5 line (black dashed line)

The HF2-MOD AM/FM Modulation option permits direct generation and demodulation of an FM signal. For demodulation, this option enables measurement of the parameters A , f_p , and φ .

Internally the HF2LI calculates the peak frequency f_p with the method described above, from the ratio $J_1(h)/J_0(h)$, proportional to the carrier and first sideband amplitude. Since this method is

valid only for narrow-band frequency modulation, users are advised to work at small values of the modulation index $h < 1$.

Another, intuitive way of demodulating an FM signal would be to use the PLL to track the frequency deviation Δf and to further demodulate this signal. However, using sideband demodulation with the HF2-MOD AM/FM Modulation option provides a better signal-to-noise ratio. This is because the signal can be averaged over several modulation cycles while keeping the bandwidth small.

3.7.2. Generate the Test Signal

In this tutorial, you are going to generate an FM signal with a carrier frequency of 1 MHz, a modulation frequency of 100 kHz, and a modulation index of 0.1. The signal is generated at Signal Output 2 and is demodulated by the first lock-in unit by feeding it into Signal Input 1.

Start by enabling the Signal Output 2 in the Lock-in MF tab and disabling all demodulator Output Amplitudes. This will ensure that only the desired components of the frequency-modulated signal appear on the output.

Table 3.22. Settings: generate the AM signal

Signal Output 2 Enable	ON
Signal Output Amplitudes Demodulators 1-8	OFF

In the Modulation tab, in the MOD 2 section, select the following parameters:

Table 3.23. Settings: generate the FM signal

MOD 2 Enable	ON
Carrier Oscillator (Osc)/Frequency	Osc 1 / 1 MHz
Sideband 1 Oscillator (Osc)/Frequency	Osc 2 / 100 kHz
Carrier Mode/Enable	FM / ON
Generation Carrier Amplitude/Enable	100 mV / ON
Generation Index	0.1

This generates an FM signal consisting of a carrier and two sidebands at $f_c \pm f_m$. To look at this signal, connect Signal Output 2 to Signal Input 1 of the HF2 Instrument. Select the correct input parameters in the Lock-in tab: for Signal Input 1, make sure Differential and 50Ω are turned off. Then click the auto range button. In the Scope tab, select Signal Input 1 as Source, Signal Output 2 as Trigger, and click on Run to activate the Scope. Observe that the carrier amplitude is constant. The periodic frequency variation is hardly visible. The FFT tab underneath shows the carrier at 1 MHz and the two sidebands. You can increase the frequency resolution by selecting a larger Time scale in the Source section of the Oscilloscope.

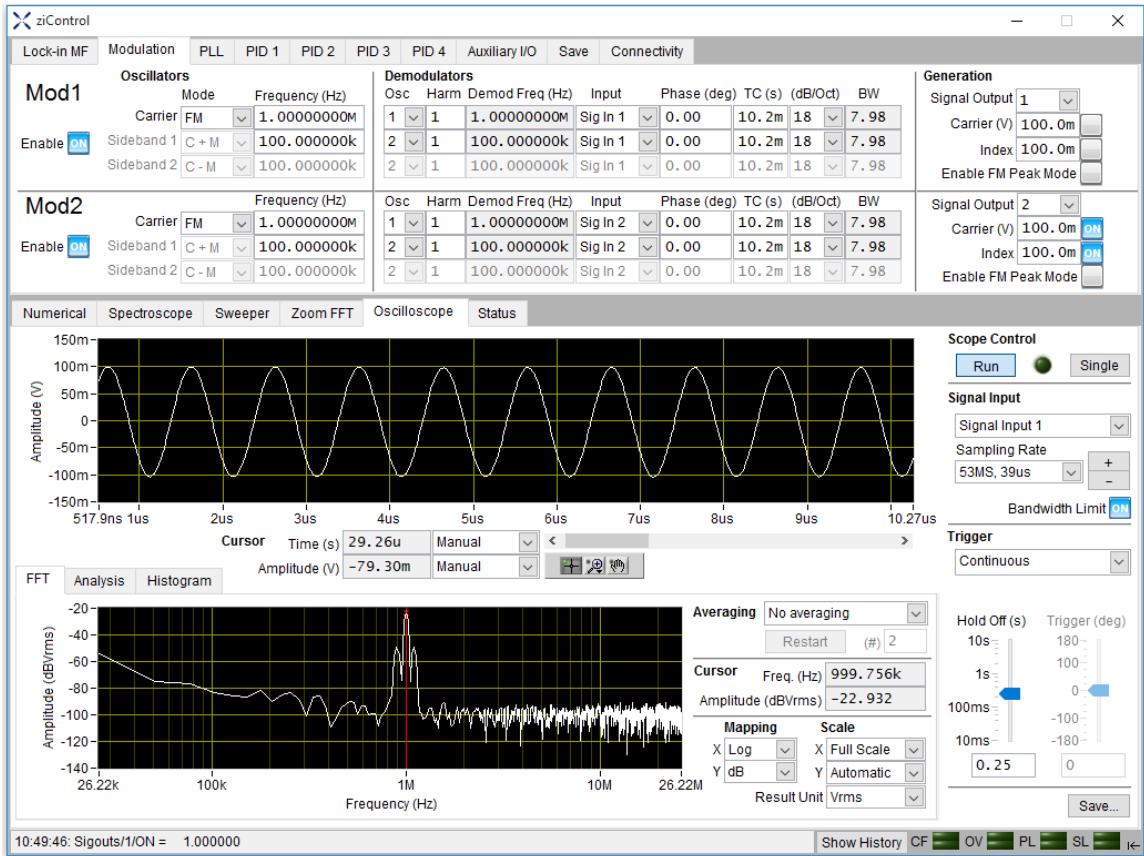


Figure 3.36. The FM signal generated by Mod2 as measured on Signal Input 1

3.7.3. Measure the Test Signal

In the Modulation tab, in the MOD 1 section, select the following parameters:

Table 3.24. Measure the FM signal

MOD 1 Enable	ON
Carrier oscillator (Osc)	1
Sideband 1 oscillator (Osc)	2
Carrier Mode	FM
Low-pass Filter BW (Carrier)	10 Hz
Low-pass Filter BW (Sideband 1)	10 Hz
Demod 1, 2, 3 Readout (Lock-in tab)	Continuous

3.7. Frequency Modulation

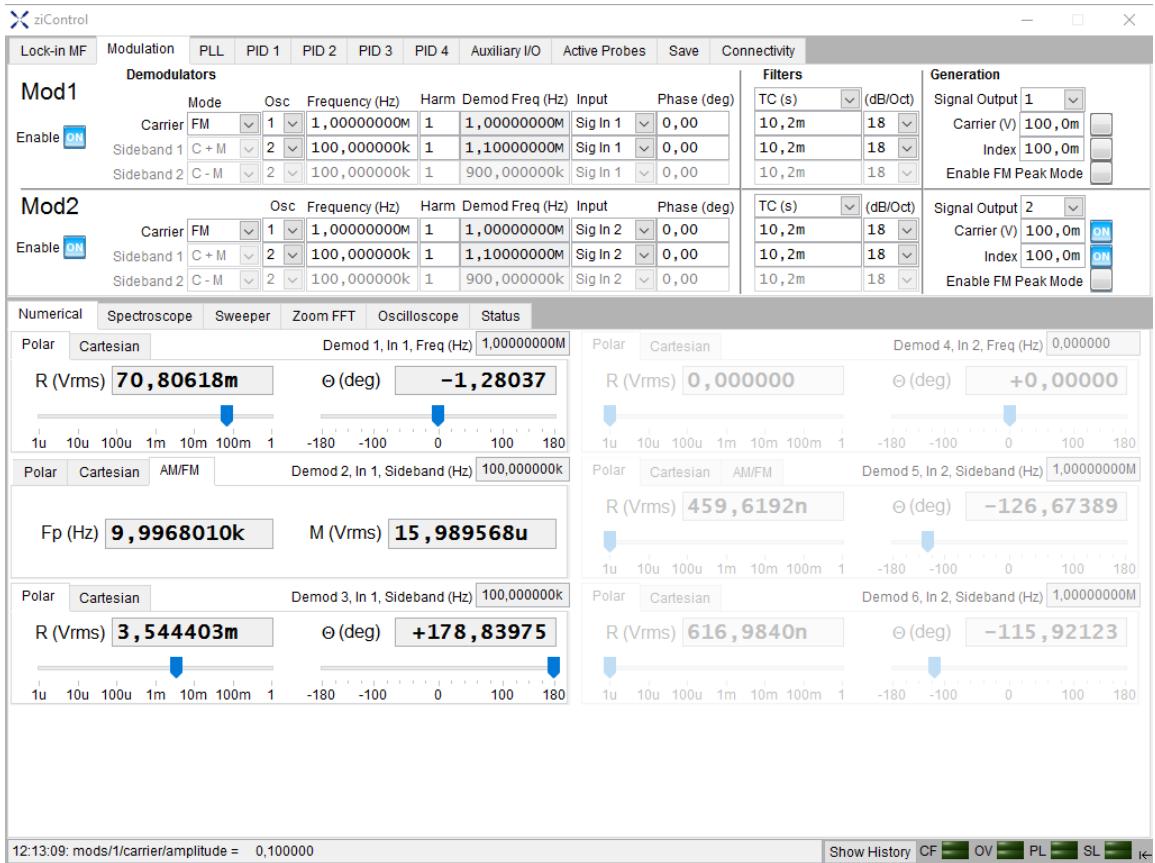


Figure 3.37. The numerical view of the demodulated signal

The HF2 Instrument directly demodulates simultaneously on each one of the three frequencies, therefore using three demodulators: this appears in the Numerical tab, where 3 indicators are running, or in the Lock-in MF tab, where three readout outputs are enabled.

3.8. Phase-Locked Loop

Note

This tutorial is addressed to HF2LI users that have purchased the HF2-PLL Dual Phase-locked Loop option.

3.8.1. What is a Phase Locked Loop?

The phase-locked loop (PLL) is a control system that produces a reference signal whose phase is related to that of the input signal. A common use of the PLL is to excite a resonating system: for such systems, there is a phase delay between the drive signal (produced by the PLL) and the system response which at resonance, is typically 90°. A change of the resonance frequency (for example as a result of the interaction between tip and sample as in AFM systems) induces a change of the phase delay between the drive and the response. The PLL is designed to adjust the drive signal frequency in such a way to restore the required phase delay. The maximum rate at which the PLL can restore the correct phase delay is called the bandwidth of the PLL and is one of the figures of merit of the PLL.

A PLL consists of three parts arranged in a feedback configuration: a phase detector (PD), a proportional-integral (PI) controller, and a variable frequency oscillator. The phase detector compares the phase of the input signal to that of the reference signal derived from the oscillator. Their phase difference must be made equal to the phase set point: any deviation from the set point is the error signal and has to be minimized. This is the role of the PI controller and the oscillator. If the error is positive, the reference signal is lagging behind the rate at which the input signal is advancing and the oscillator frequency has to increase. If the error is negative, the reference signal is ahead and the oscillator frequency has to be decreased. The name phase locked loop reflects the fact that the two signals are related by their phases. As a consequence of this, the reference signal has the same frequency as the input signal because the frequency is the derivative of the phase.

Defining Θ being the measured phase error between reference and input signal, f_c the center frequency which is typically set to a value close to the operating frequency of the PLL, T_i the integral gain and K_p the proportional gain of the PI controller, then the reference frequency generated by the variable oscillator satisfies

$$f_r = f_c + k_p \left[\Theta + \frac{1}{T_i} \int_t \Theta dt' \right]$$

Equation 3.7. PLL generated reference frequency

The purpose of the PLL is to accurately track frequency changes. For instance in the case of a FM input signal (see [Section 3.7.1](#)), in which the instantaneous frequency varies as $f(t) = f_c + f_p \sin(2\pi f_m t)$, one would like to extract f_p . If the modulation frequency f_m is too large, the PLL will not be able to track the frequency change and f_p will appear smaller than it actually is. The bandwidth of the PLL is therefore a relevant performance parameter and is defined as the frequency f_m at which the signal of interest f_p is measured to be attenuated by 3 dB compared to the original signal. When correctly set up, the PLL transfer function should be as flat as possible before rolling off at the 3 dB point. On the other hand, an unnecessarily wide PLL bandwidth will reject less noise.

Being a feedback control system with gain (given by the PI), the PLL is susceptible to instability if the parameters are not chosen correctly. The scientific literature abounds with methods describing different more or less complicated procedures to tune the PI (and the PLL).

3.8.2. Generate the Test Signal

In this tutorial, you are going to learn how to set up the PLL parameters in relation to the bandwidth of the signal you want to observe. To do this, you are going to generate an FM signal and make use of the PLL Advisor to find out the correct parameters.

HF2LI users that have the HF2-MOD option installed on their HF2 Instrument should select the following parameters for the Mod2 unit:

Table 3.25. Settings: generate the FM signal for PLL tracking

Carrier oscillator / frequency	Osc 5 / 1 MHz
Signal oscillator / frequency	Osc 6 / 20 kHz
Mode	FM Generator
Sideband	Both
Carrier amplitude / Peak frequency	100 mV / 2 kHz
Output select	Signal output 2

Connect a BNC cable between Output 2 and Input 1+ and turn Output signal On. You may need to check in the Lock-in MF tab that only demodulator 4 is enabled (typically you would need to turn off demodulator 8).

HF2LI users without the HF2-MOD option installed could use an external frequency generator to produce an FM signal with carrier frequency of 1 MHz, modulation frequency of 20 kHz and peak frequency of 2 kHz. Connect the output of the frequency generator to the HF2LI Input 1+.

3.8.3. Measure the Test Signal

The objective is to accurately measure accurately the frequency modulation with suitable PLL settings.

The PLL is a feedback system that has four adjustable parameters: the time constant and the filter order of the phase detector and the proportional gain and time constant of the PI controller. As a rule of thumb, the bandwidth of the phase detector could be set to about twice the target PLL overall bandwidth: if the PD would be limiting the PLL bandwidth then the PLL transfer function would show overshoots and make the PLL unstable. On the other hand, a too large bandwidth on the PD would transfer more noise into the PI.

Apply the following settings to one of the PLLs:

Table 3.26. Settings: prepare the PLL for FM signal tracking

Center frequency / Frequency range	1 MHz / 100 kHz
Phase detector and PI controller settings	Auto Full Bandwidth

In the Auxiliary I/O tab set dF as the analog output for Aux 1, set the scale to 100 μ V/Hz (based on the expected maximum frequency swing) and connect a BNC cable from Aux 1 to Signal Input 2 to measure the frequency deviation dF of the signal on Input 1: we expect to observe a signal with amplitude of $2 \text{ kHz} \times 100 \mu\text{V}/\text{Hz} = 200 \text{ mV}$. Under the Lock-in tab, set the following parameters: AC coupling, 50 Ω disabled, Diff disabled, 300 mV input range; under the Oscilloscope tab, trigger on Oscillator 5 for users using the HF2-MOD option (or Continuous if using an external generator), and select Input signal 2, Sampling Rate 1.6 MS, 1.2 ms.

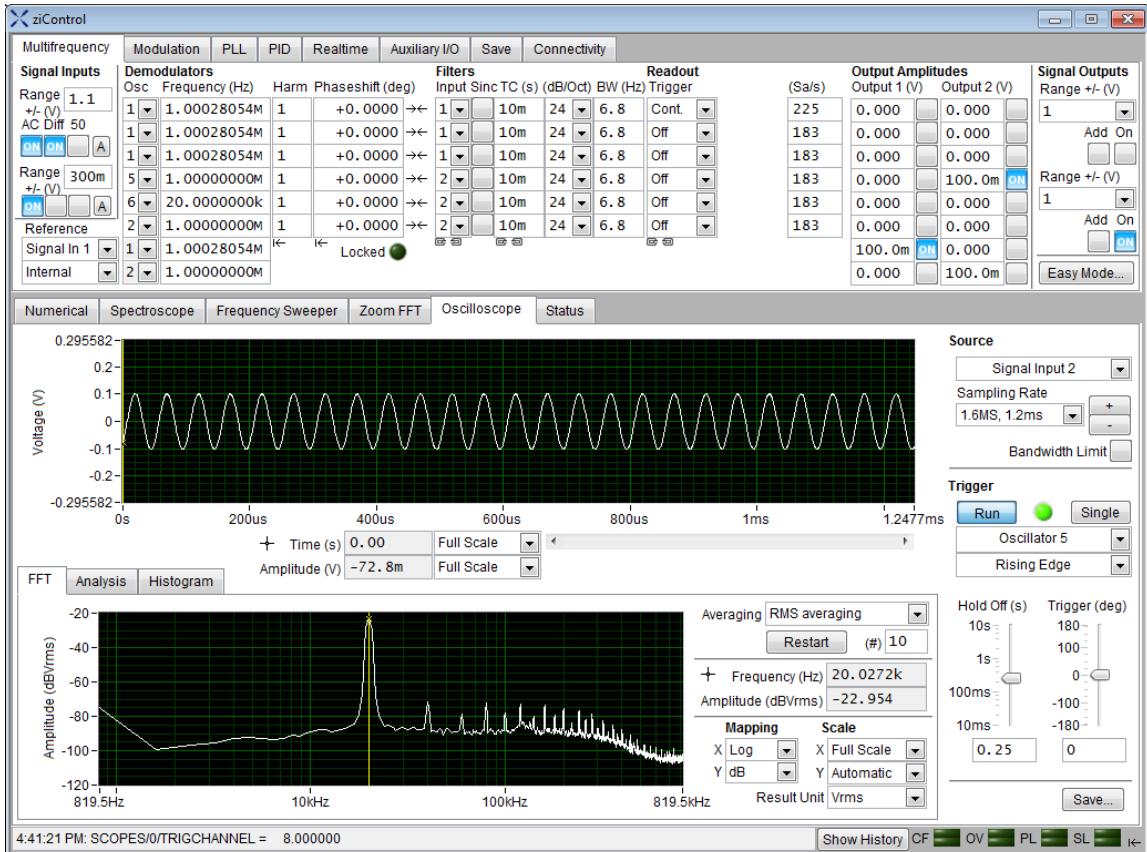


Figure 3.38. The HF2PLL dF output signal with gain of 100 V/Hz

Click on Run and observe the signal. Its amplitude is approximately 100 mV, which is only 50% only of the expected value for the depth frequency (you may also want to set PLL/0/AUXAVG to 1 using the text-based programming as described in [Section 6.2.2](#) to achieve a smooth signal, although this is not absolutely necessary). We must conclude that the PLL bandwidth is smaller than the modulation frequency, 20 kHz.

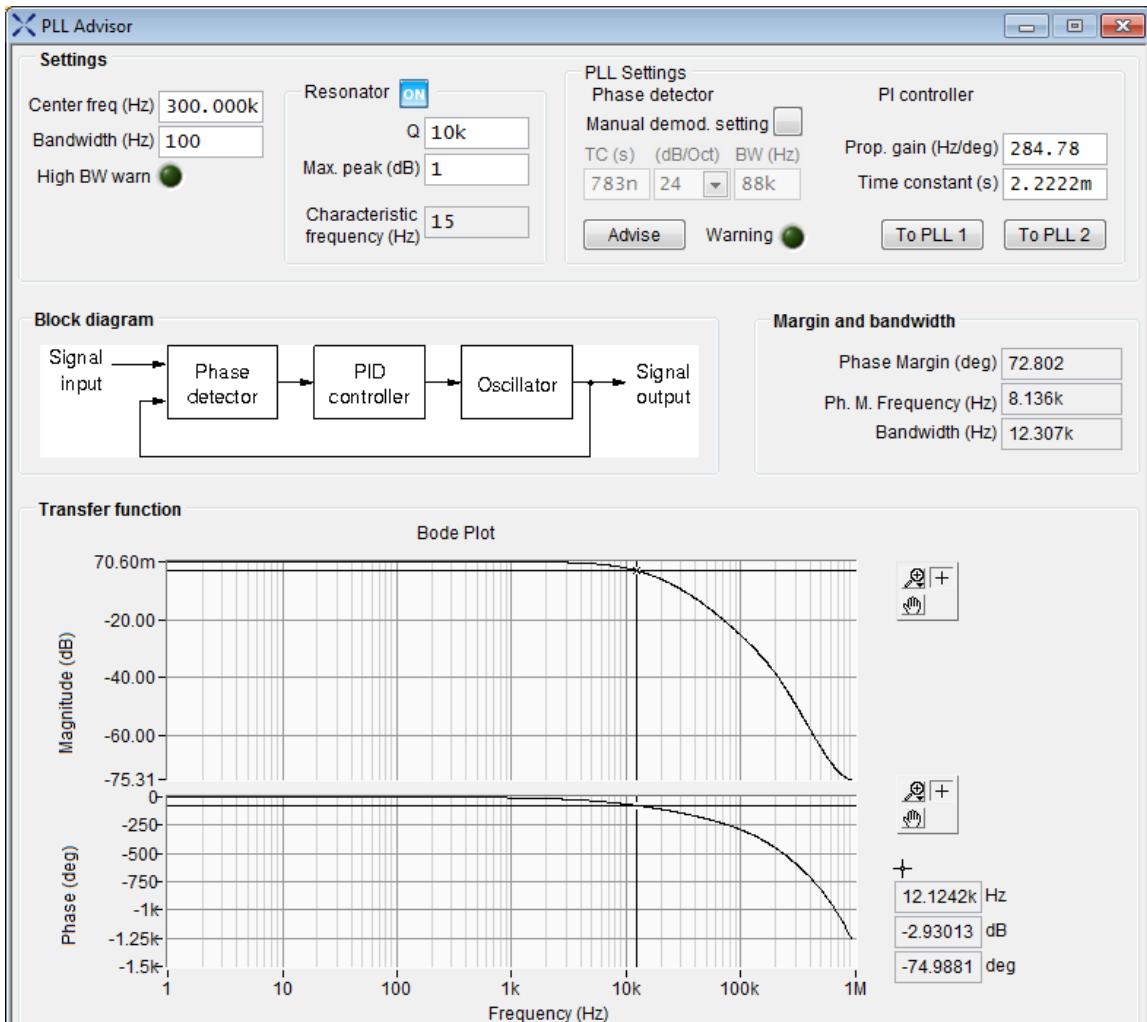


Figure 3.39. The HF2PLL Advisor showing the transfer function of the PLL for a given set of parameters

We are now going to find out the right parameters. Enable the PD filters settings and the PI settings and click on Advisor. You will be presented with a panel displaying the simulated PLL transfer function: you can change the parameters on the Advisor panel, look at the response and once you are satisfied, apply them to PLL1 or PLL2. The Advisor contains an algorithm with a pretty accurate model of the HF2LI PLL to let you predict with a high degree of confidence the PLL response.

On the Advisor, input the PLL current values for the time constant, the filter order, the proportional gain and the integration time (the values you will read from PLL1 in the PLL tab). Read the (simulated) PLL bandwidth by looking at the 3 dB point in the transfer function Bode plot, 11.7 kHz. (Alternatively, you can read the value of the 3 dB point in the numerical display at the right hand side). At 20 kHz, the attenuation is -6.17 dB, around 49%: this is very close to the experimentally observed value of 100 mV.

With these settings, the phase detector filter bandwidth is 88 kHz, exceeding the factor of 2 the rule mentioned before. Increase the time constant until the BW is 41 kHz (TC is now 1.7 μ s). Now increase the proportional gain, until the BW becomes approximately 20 kHz: with K_p at 350 we obtain a bandwidth of 20 kHz. On the Advisor, click the To PLL1 button: the current parameters are transferred to the PLL tab. On the oscilloscope, look at the new amplitude of the signal at Signal Input 2, about 150 mV, because 20 kHz is the PLL 3 dB point.

The Advisor closely reflects the response of the PLL. Figure 3.40 is a comparison between the Advisor (solid blue line) and the PLL (red dots) to a FM signal that is fed to the PLL input, using the parameters above: see how in the whole PLL range of interest, the difference between the two traces is less than 0.2 dB.

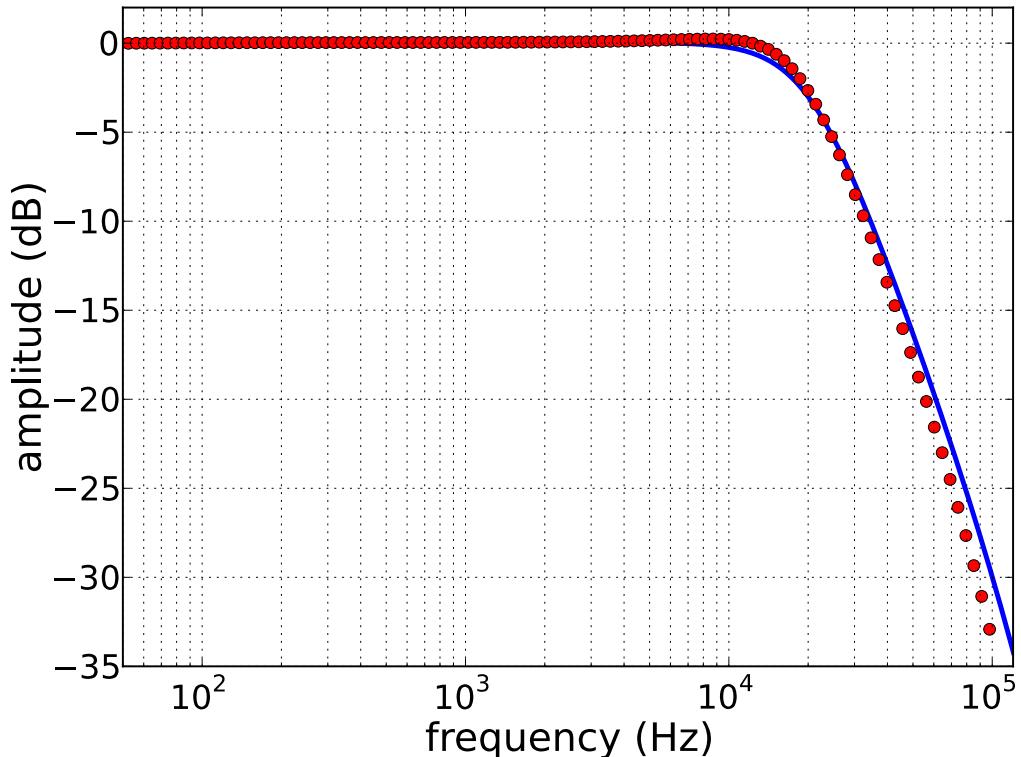


Figure 3.40. Comparison between the Advisor (blue line) and PLL response (red dots) to a FM signal: this confirms that the Advisor is an excellent model of the PLL and can be relied on to set up the correct parameters

What happens if the proportional gain is too high? Try to change it to 600. The transfer function presents now an overshoot of 4.3dB at 20.5 kHz. Transfer these parameters into PLL and measure the amplitude of dF , 300 mV, larger than the expected value of 200 mV by about 3.5 dB. In general a larger ratio K_p/T_i makes the PLL respond faster (i.e. it increases the bandwidth).

3.9. PLL/Resonator

One of the typical uses of the PLL is to drive a resonator at its resonance frequency, which may change as a result of perturbation by external forces (e.g. mass changes in quartz crystal micro balance measurements, or surface forces in AFM measurements). As mentioned in [Section 3.8.1](#), the PLL ensures a fixed phase difference between its input and the output signals (e.g. -90°), independently of the signal instantaneous frequency (within the PLL bandwidth). It also output the frequency shift from a center frequency which physically represents the amount of perturbation on the resonator.

This tutorial shows how to set up the PLL on a quartz resonator. In the example, a commercial quartz crystal with resonance frequency of approximately 16 MHz is shorted by a large resistor (to decrease the quartz large Q factor) and connected between Signal Output 1 and Signal Input 1; set the excitation voltage to 100 mV peak amplitude. We first need to find the resonance frequency: begin with a frequency sweep with wide range 15 MHz and 17 MHz, 300 points (it does not matter whether the sweep is logarithmic or linear or whether Auto BW is enabled). The sweep will show several resonances, each one consisting of an impedance maximum and minimum (this is the result of the way the quartz is usually cut). Zoom in the region of the resonances, using the magnification tool and the button Copy from Plot Range (more than once if necessary) and drag the yellow cursor on the first largest resonance: this resonator has a resonance frequency at 15.997 MHz, where the phase is -100° . One should not be surprised by this because at higher frequencies, the cable length significantly delays the signal propagation.

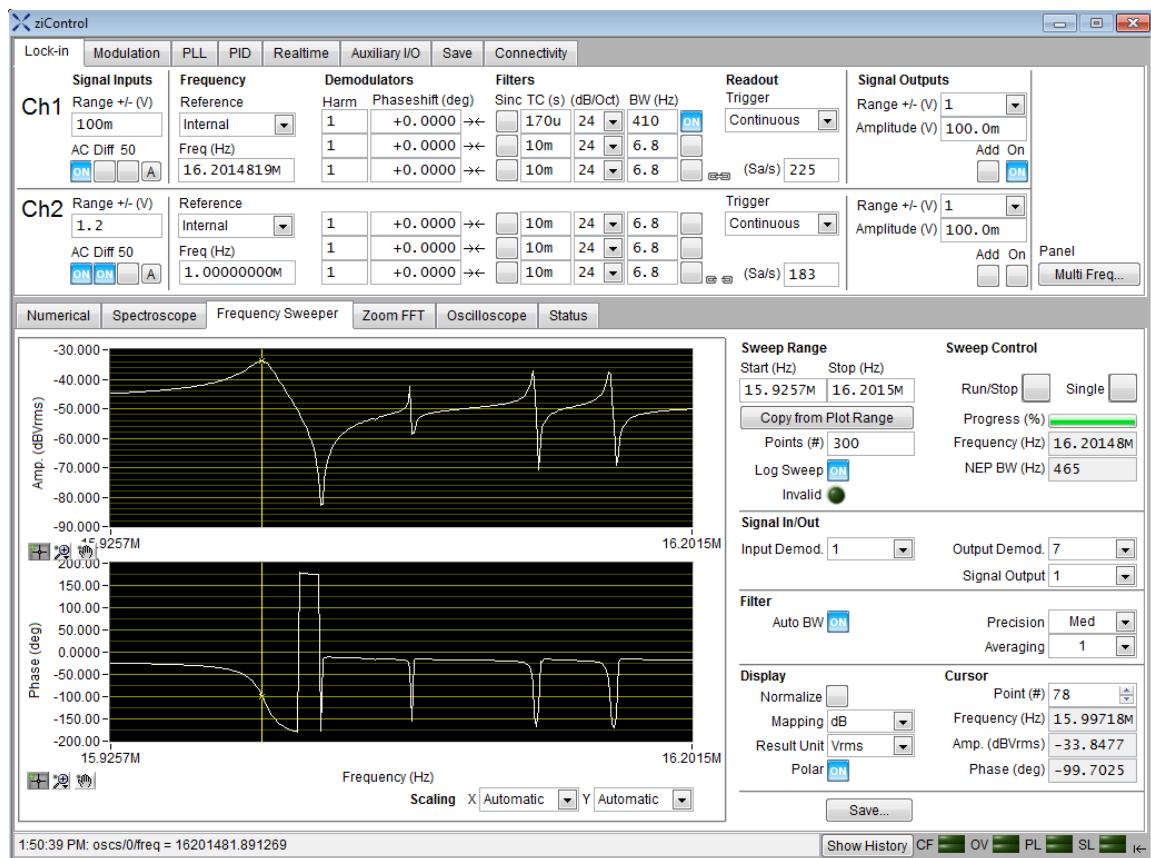


Figure 3.41. Frequency sweep of the quartz resonator (zoom in of the four resonances)

To the PLL tab, copy the center frequency and the phase setpoint (15.997 MHz and -100°) into the corresponding boxes of PLL1 and reduce the range to 100 kHz. To find an initial set of

parameters for the phase detector and PID controller, use the Advisor. Let us assume that we need a bandwidth of 1 kHz (this is just an academic exercise as the quartz is cased and its resonance frequency will not change so fast): type the approximate resonance frequency and the bandwidth (16 MHz and 1 kHz) into the boxes on the top left side. Disable auto full bandwidth and type 3 kHz in the BW (Hz) box for the filter settings (this is the trade-off between PLL stability and noise rejection mentioned above) with a 4th order filter. Furthermore, the Q factor is of the order of $2 \cdot 10^3$ (as evaluated from the width of the resonance in the Bode plot in the frequency sweeper tab), and select the maximum peak to be 1 dB. After clicking on Advise, the Advisor will propose a 20 Hz/deg for the proportional and 2.58 ms for the integral gain respectively. Apply these parameters to PLL1 using the button in the Advisor: you are now ready to enable the PLL, which should now easily lock on the resonance.

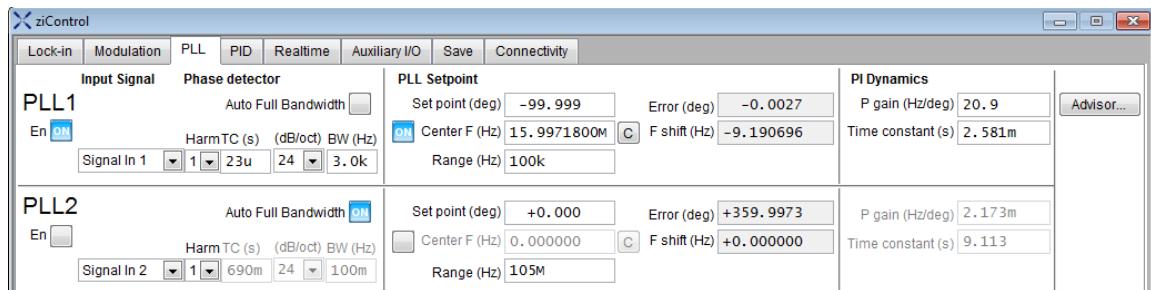


Figure 3.42. PLL tab with the parameters for the resonator

Users with the HF2-MF multi-frequency option could try to lock the second PLL on the third resonance, 3 dB weaker than the main one. The MF is needed to output the drive voltage from Output 1 instead of the default Output 2. (Users without MF could still make use of the second PLL, but they need to sum the excitation voltage from Output 2 to that of Output 1 by using the Signal Output 1 Add port; PLL2 can lock on the signal on Input 1, so there is no need to split the input signal from the resonator. A word of caution: there will also be an additional phase shift when using the Add port that should be kept into account when setting the PLL2 phase setpoint.)

The third resonance is much narrower than the first one and needs to be located with better precision: zoom in around it and find the frequency position of its peak. This specific resonator presents the resonance at 16.126 MHz, with a phase shift of -96° . Copy these values into PLL2 and apply the same parameters for the phase detector and the PID as for PLL1 (for instance by clicking on To PLL2 in the Advisor). Go to the Lock-in MF tab and enable Output 1 of demodulator 8 (the last one in the list), which is controlled by PLL2. Use an excitation voltage of 200 mV to drive the weaker resonance: Output 1 will contain a linear combination of the two excitation voltages with two different amplitudes. You can now enable both PLLs and they will lock on the two different resonances.

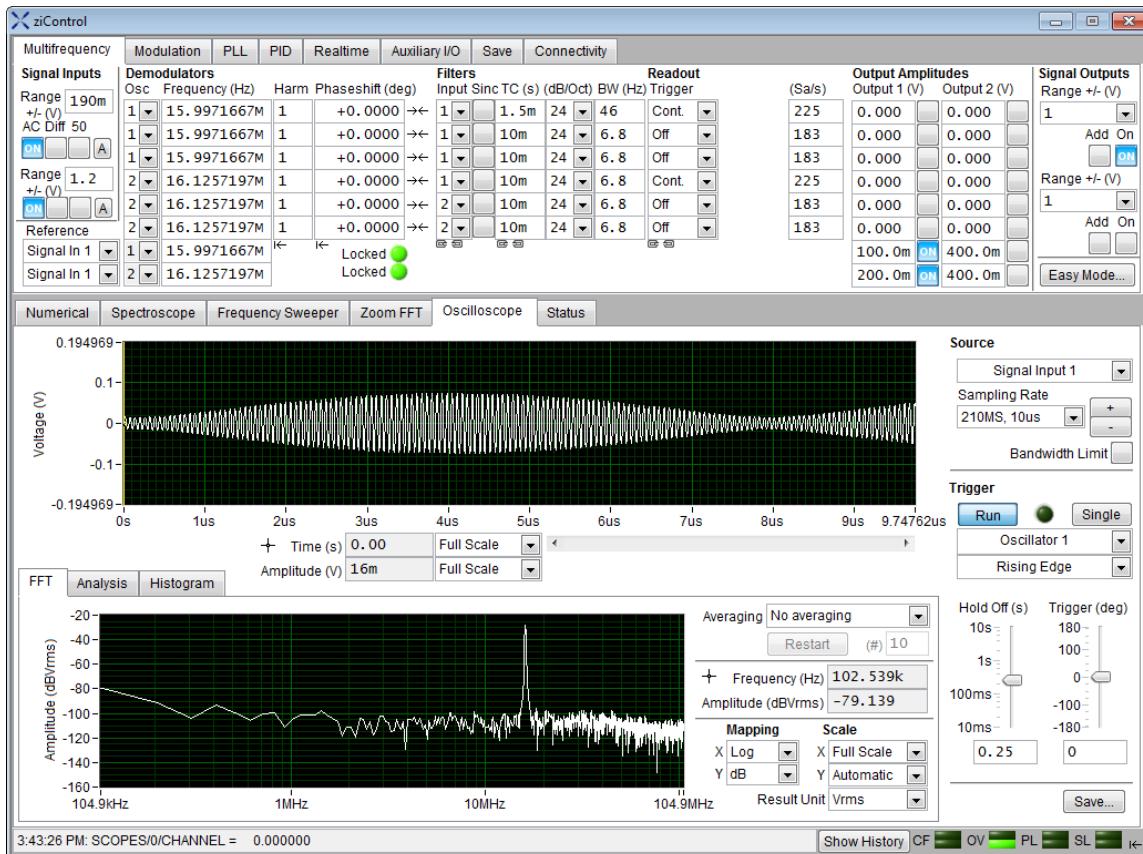


Figure 3.43. View of the Lock-in MF tab and the output signal in the Oscillator tab

Different PLL parameters could be used for the two resonances, provided the detection bandwidth of each PLL is small enough to suppress the signal from the nearby resonance. As an exercise, the user could use PLL1 to lock on the fourth resonance instead of on the first.

3.10. PID Controller with Auto Tune

Note

This tutorial only applies to HF2 Instrument users that have purchased the HF2-PID option.

In this tutorial you will learn how to use a PID controller that calculates the required signal output amplitude in order to achieve a desired demodulator X value. You will then auto tune the controller's P, I and D gains to optimize the controller's performance.

First connect the signal output 1 connector to the signal input 1 connector with a BNC cable to create a simple feedback loop. Power cycle the HF2 Instrument in order to attain the instrument's default configuration and enable signal output 1 in the Lock-in tab of ziControl. Furthermore, select a first order (6 db/Oct) demodulator filter with a bandwidth of 1.00 Hz and a sampling rate of 3.6 kSa/s for demodulator 1. This filter order and bandwidth create a slowly responding demodulator filter which will act as the system we aim to control. See [Figure 3.44](#) for an over of the configuration in the Lock-in tab.

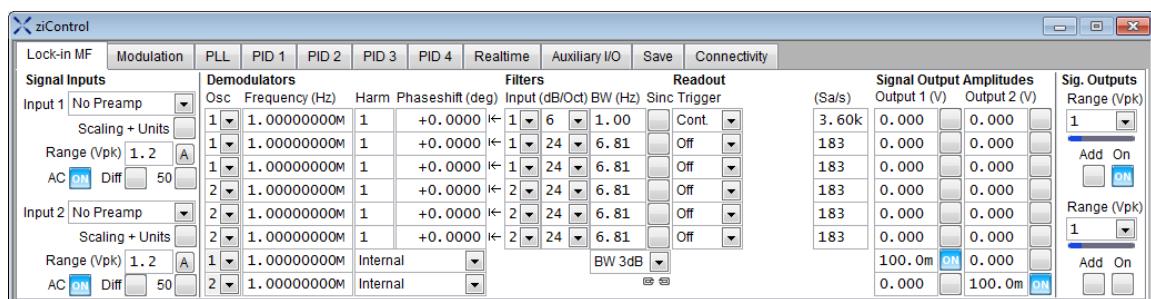


Figure 3.44. Lock-in tab configuration

In the PID 1 tab choose Demodulator: X on channel 1 as the controller's input, a Fixed setpoint of 100 mV_{RMS} and Signal 1: Amplitude on channel 7 as the controller's output. This corresponds to the default PID configuration which is shown in [Figure 3.45](#).

Switch to the Spectroscopic tab in the lower half of ziControl and ensure that the spectroscopic's display is set to plot the X value of demodulator 1. Now enable the PID controller, in the spectroscopic you should see the demodulator's output approach 100 mV_{RMS} as in [Figure 3.45](#).

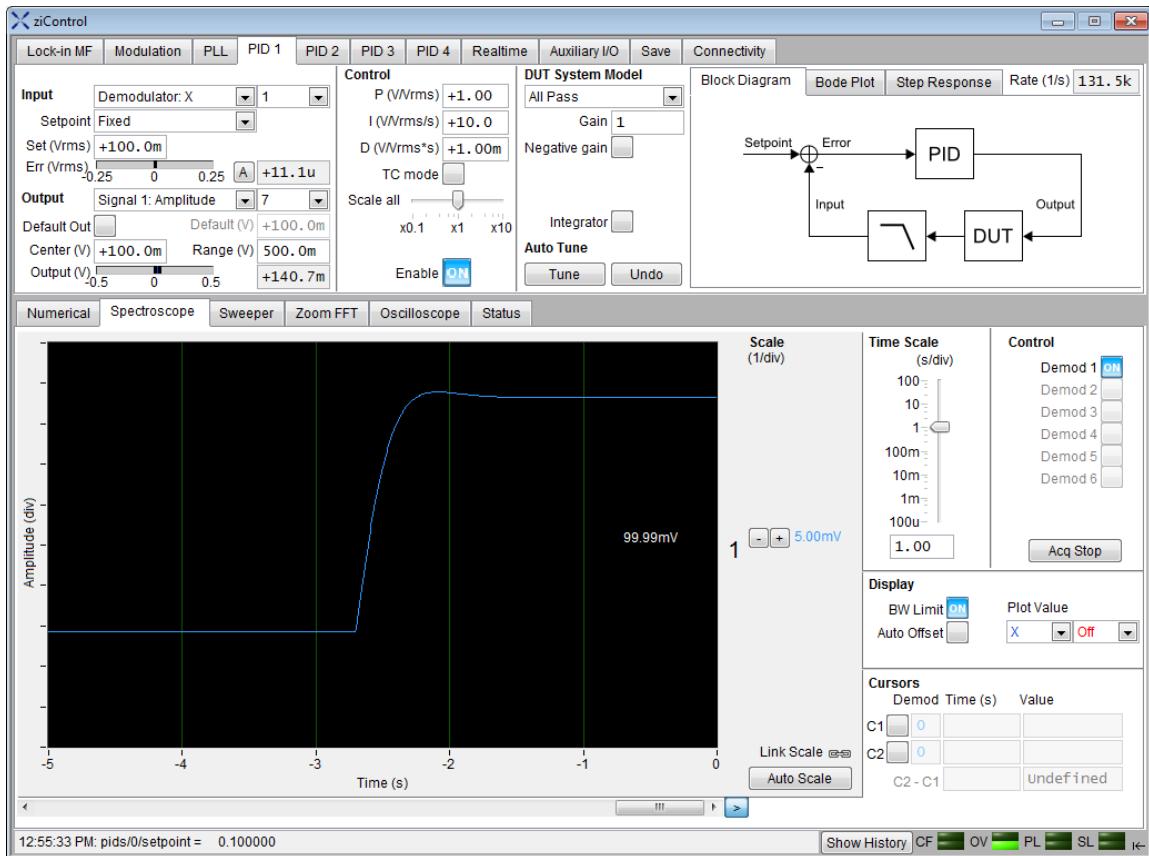


Figure 3.45. Initial PID configuration and spectroscope output with a fixed setpoint

In order to repeatedly view the controller's behavior with the current settings, change the setpoint to Toggle and set the two setpoints to the values $50 \text{ mV}_{\text{RMS}}$ and $100 \text{ mV}_{\text{RMS}}$ in the PID 1 tab. This configures the PID controller to toggle its setpoints between these two values at a rate of 1 Hz. After pressing Auto Scale in the Spectroscopic tab you should see output similar to that in Figure 3.46

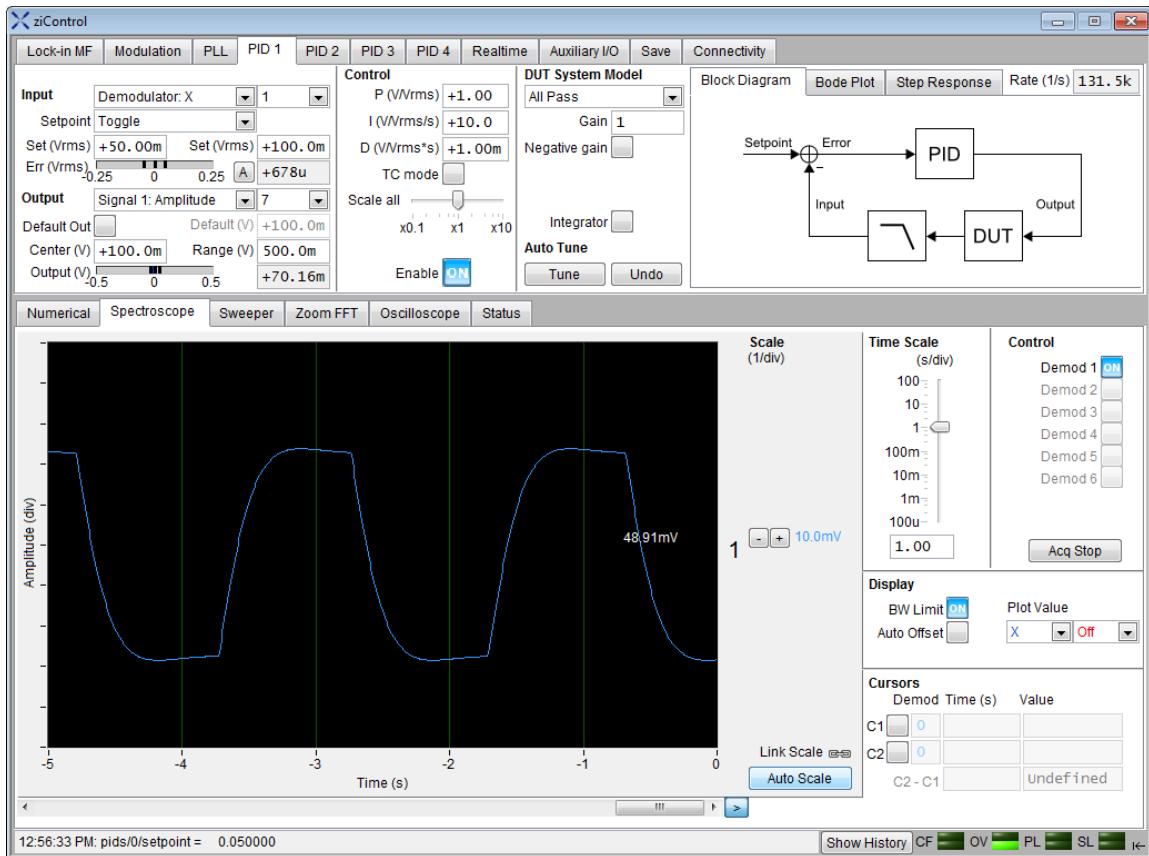


Figure 3.46. PID configuration and spectroscope output with two setpoints toggling between 50 mV_{RMS} and 100 mV_{RMS} using suboptimal PID control parameters

Now we'll optimize the PID controller's parameters using the Auto tune feature. Since we're using a simple feedback cable between signal output 1 and signal input 1, ensure that the System (DUT) Model used is All Pass and that the Gain is set to 707.1 in PID 1 tab, this models the cable (the DUT) appropriately as an all pass filter. The gain 707.1 corresponds to a gain of $1/\sqrt{2}$, which is needed due to the fact that the PID input, demodulator X, is in RMS units (we assume the cable itself has a gain of 1). Now press the Tune button, in the Spectroscopic tab you will see that the PID controller's response to the setpoint toggle is now much faster, see Figure 3.47. The Auto tune feature has calculated optimal parameters for the controller using the Ziegler-Nichols method taking the demodulator's filter values into account and then configured the PID to use them.

In order to compare with the old controller parameters, you can press the Undo button in the Auto Tune section of the PID 1 tab. You can compare the response of the actual close loop in the spectroscopic tab and the simulated step response calculated in the Step Response plot of the PID 1 tab.

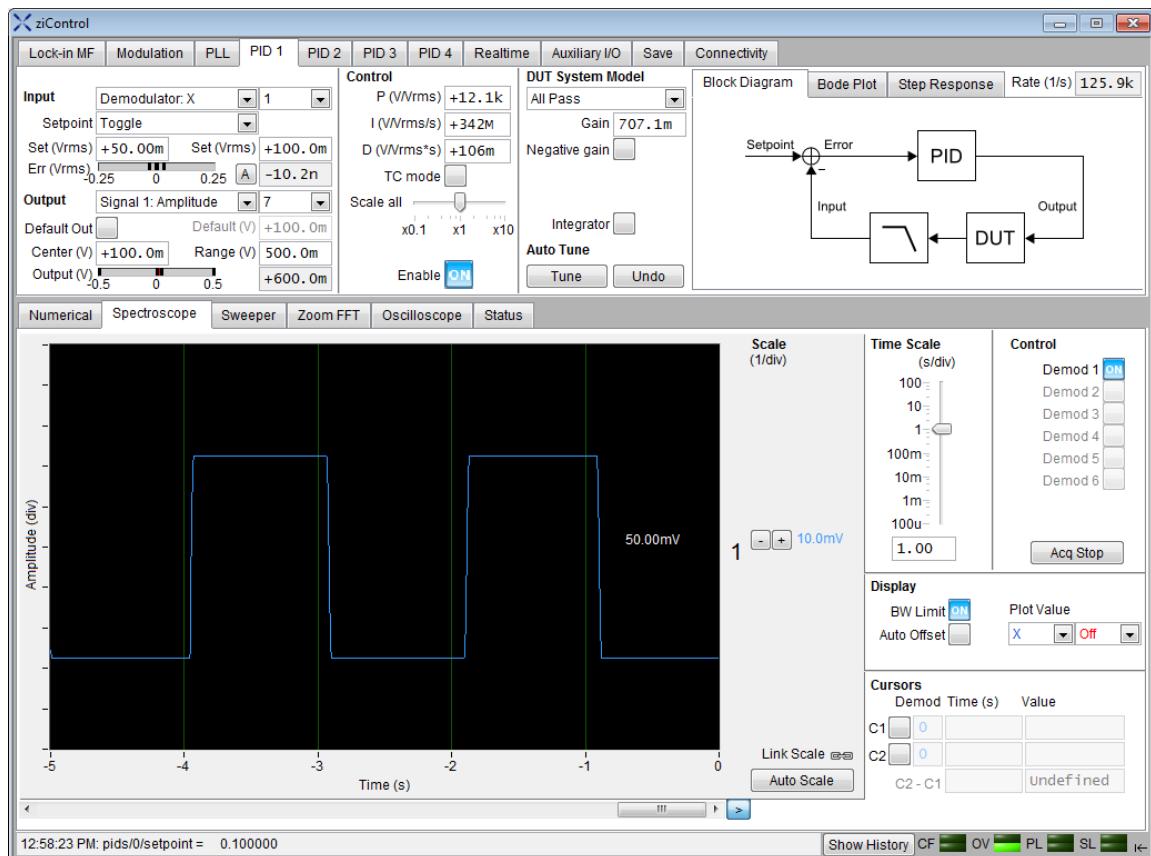


Figure 3.47. PID configuration and spectroscopic output with setpoints toggling between 50 mV_{RMS} and 100 mV_{RMS} using optimal PID control parameters calculated with Auto tune

This concludes your PID controller with Auto tune tutorial. Thank you for measuring with Zurich Instruments.

3.11. PID Controller Tuning Tools

Note

This tutorial only applies to HF2 Instrument users that have purchased the HF2-PID option.

The aim of this tutorial is to explain how to manually tune a PID controller's P, I and D gains by applying the Ziegler-Nichols method and to explain the PID tab's tuning tools. We will proceed by determining the PID parameters using the Ziegler-Nichols method to a simple control system by:

- manually determining the controller's gains by observing the output controller's response in the Spectroscope tab,
- more accurately and quickly determining the gains by using the Open Loop Bode plot in the PID tab,
- automatically determining the P, I and D gains using the Auto tune feature, which is partially based on the Ziegler-Nichols method.

3.11.1. The Ziegler-Nichols Closed-Loop Method

In the 1940s Ziegler and Nichols published a method for heuristically determining PID controller parameters that provide fast controller response whilst ensuring acceptable stability of the system. The basic idea is to experimentally extract information about the dynamics of the system that we wish to control. We do this by considering a proportional gain controller and searching for the P gain that brings this simpler control system to the limit of its stability. In general, if P is too small the system will be slow to respond to a change in the setpoint and if P is too large a change in the setpoint will cause the system to oscillate with ever increasing amplitude. We search for the value of P that creates oscillations in the system that neither decrease or increase upon a change in the set-point. We then use this P gain value, called the ultimate gain P_U , and the period of the induced oscillations T_U in order to calculate optimal P, I and D gains according to [Table 3.27](#). These values were determined by simulating and performing many experiments of common system processes found in industry.

Table 3.27. The values of P, I and D in terms of the critical gain P_U and corresponding oscillation period T_U according to the Ziegler-Nichols Method

P	I	D
$0.6*P_U$	$2*P/T_U$	$0.125*P*T_U$

After setting up the initial configuration according to [Section 3.11.2](#) we'll apply the following steps of the Ziegler-Nichols method in [Section 3.11.3](#) and [Section 3.11.4](#):

1. Set both the D and I gains of the controller to 0.
2. Increase the P gain, starting at 0, until we observe oscillations of constant amplitude in the value we desire to control. This value of P is called the critical gain P_U .
3. Measure the period T_U of the oscillations.
4. Calculate the controller's P, I and D gains according to [Table 3.27](#) using P_U and T_U .

3.11.2. Lock-in Setup and Configuration

First connect the signal output 1 connector to the signal input 1 connector with a BNC cable to create a simple feedback loop. Power cycle the HF2 Instrument in order to attain the instrument's

default configuration and enable signal output 1 in the Lock-in tab of ziControl. Furthermore, select a fourth order (24 db/Oct) demodulator filter with a bandwidth of 6.81 Hz and a sampling rate of 14.4 kSa/s for demodulator 1. These are the default demodulator settings which together with a feedback cable act as the system we aim to control. See [Figure 3.44](#) for an overview of the configuration in the Lock-in tab.

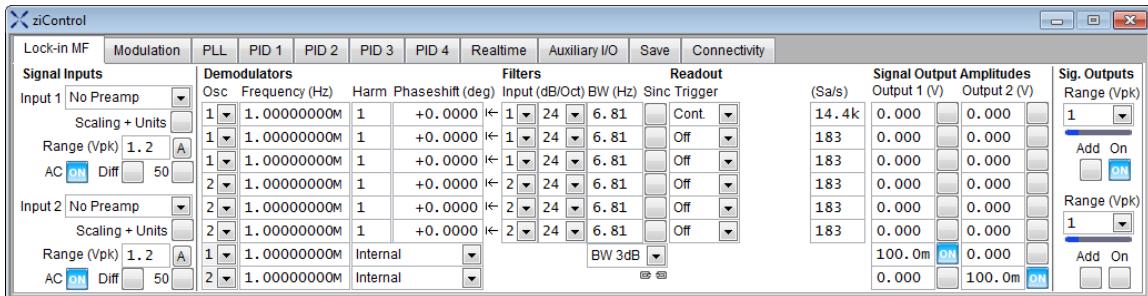


Figure 3.48. Lock-in tab configuration.

3.11.3. Determining the PID Parameters Manually

In this section we determine the controller's P, I and D gains by applying the Ziegler-Nichols method manually, without using the PID tab's Auto tune feature.

First, we appropriately configure the controller for Ziegler-Nichols. In order to observe the system's response to a change in the setpoint of the PID we change the Setpoint to Toggle and set the two setpoints to $90 \text{ V}_{\text{RMS}}$ and $100 \text{ V}_{\text{RMS}}$. This will change the PID setpoint between these two values at a rate of 1 Hz. For an overview of the PID tab's configuration see [Figure 3.49](#).

Note

We choose a relatively small change in the two setpoints in order to help avoid saturating the controller's outputs. A large value of P drives the outputs hard, causing them to either reach the user-imposed PID limits (specified via center and range) or the hardware limits of the HF2. If we're reaching the output limits, it's more difficult to tell whether the oscillations in the system really have constant amplitude or whether the outputs are simply saturated.

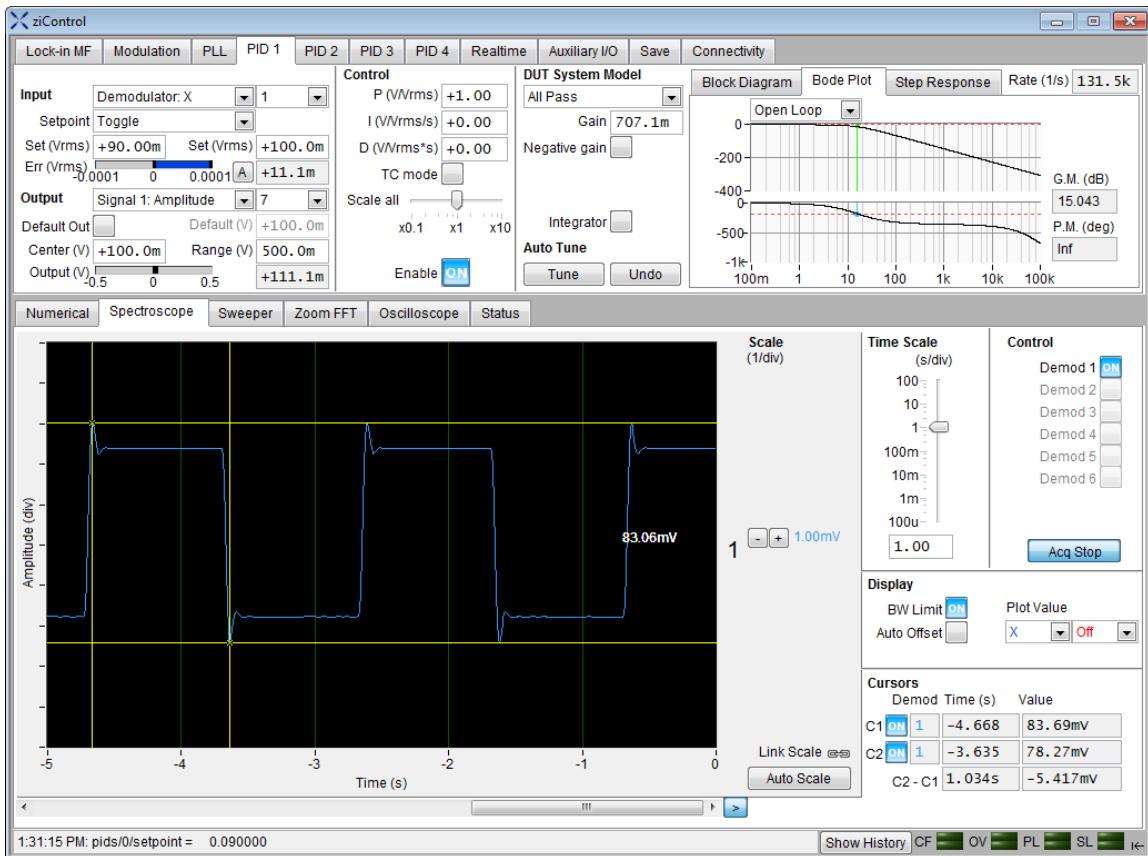


Figure 3.49. Spectroscopic output and initial PID configuration with two setpoints toggling between $90 \text{ mV}_{\text{RMS}}$ and $100 \text{ mV}_{\text{RMS}}$. With $P=1$, $I=0$, $D=0$ the controller performs poorly achieving neither upper nor lower setpoint. Since there is only a small overshoot upon a setpoint change, we have to increase P in order to incite oscillatory behavior

We now apply the steps detailed in Section 3.11.1 to our simple control system:

1. Set both the D and I gains of the controller to 0.
2. Increase the P gain, starting at 0, until oscillations of constant amplitude are observed in the demodulator X value. We see in Figure 3.49 that for $P=1$ oscillations are not yet observed, but rather a small overshoot from the controller's steady state. When $P=5.00$ we clearly see that the system exhibits oscillatory behavior and for $P=5.70$ we see that the system becomes instable. Now, we finely tune P to obtain oscillations with constant amplitude for and see that $P=5.61$ is a suitable value for the critical gain P_U , see Figure 3.50.
3. Now we measure the period T_U of the oscillations in the spectroscopic tab. First zoom in by adjusting the Time Scale so that a single oscillation can be clearly viewed in the spectroscopic. Then enable the cursors C1 and C2 and move them so that the vertical bars correspond to one period of the oscillations. The difference between the two cursors C2-C1 corresponds to T_U , see Figure 3.51.
4. Finally, we calculate the controller's P, I and D gains according to Table 3.27 with $P_U=5.61$ V_{RMS} and $T_U=64 \text{ ms}$ and find that we should set the controller's gains as $P=3.37$, $I=105$ and $D=0.0269$.

We see that with these gain values the controller does overshoot but quickly settles to the setpoint. See Figure 3.52 to see the result of the tuned controller.

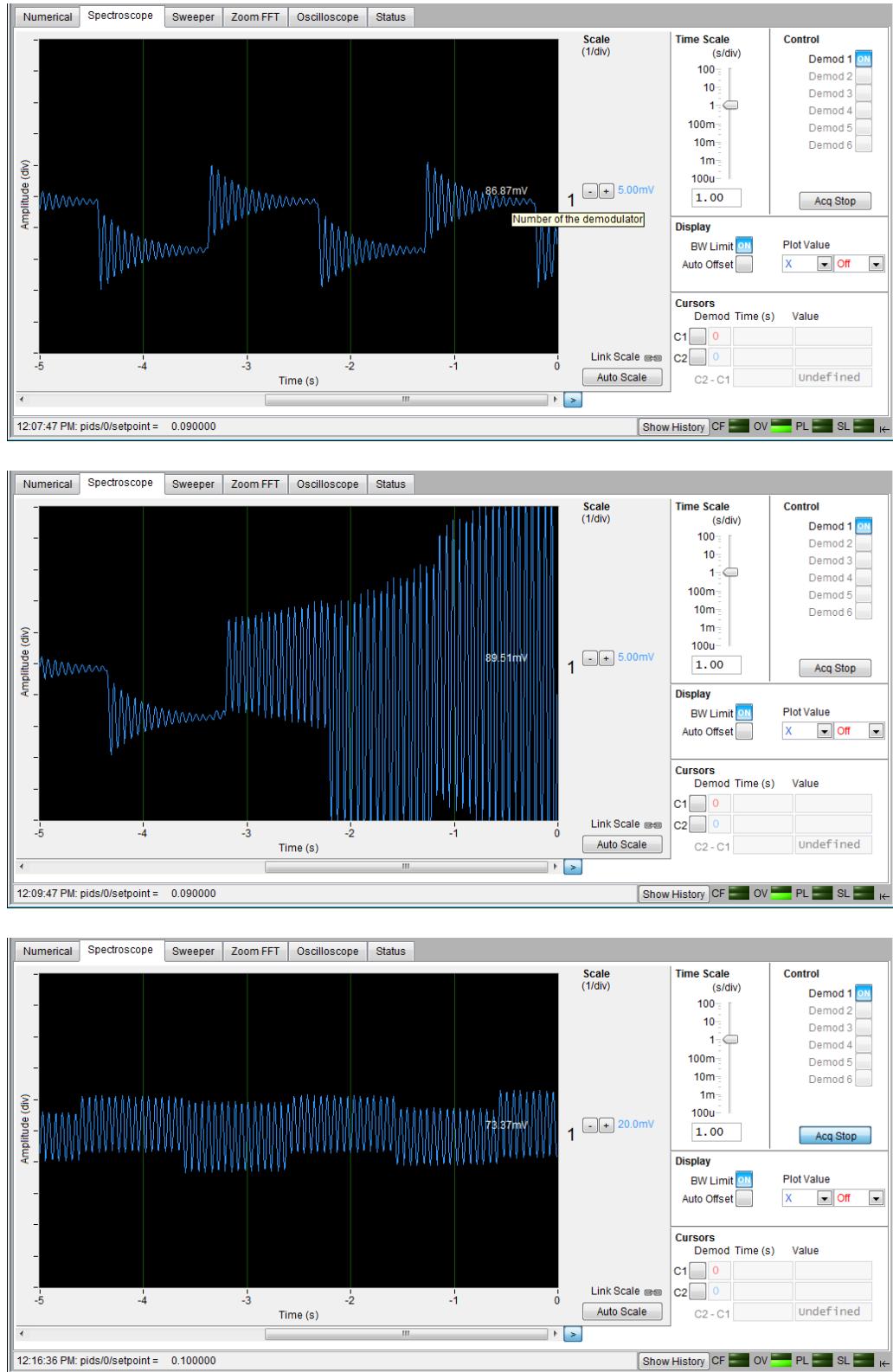


Figure 3.50. Three spectroscope snapshots with $P=5.00$, 5.70 , 5.61 (top,middle, bottom) showing demodulator output when the PID is configured to toggle between two setpoints, 90 mV_{RMS} and 100 mV_{RMS} , both I and D are kept equal to 0. The top snapshot demonstrates stable system behavior with $P=5.00$, in the middle shot P has been increased to 5.70 and demonstrates unstable behavior. The bottom snapshot shows stable oscillations which neither increase nor decrease in amplitude over time, here $P=5.61$ which corresponds approximately to the critical gain P_U

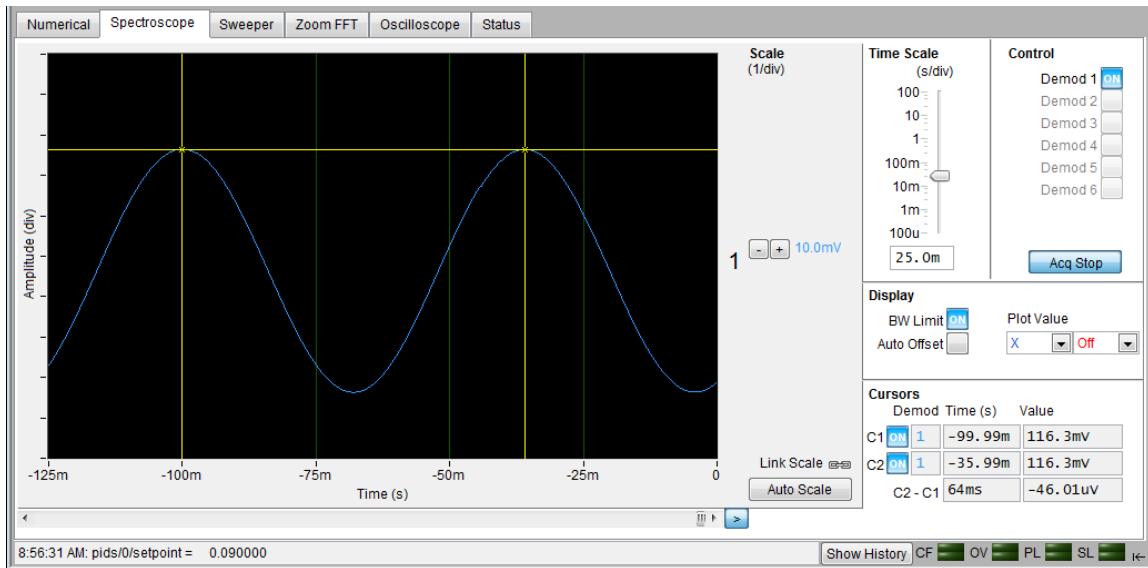


Figure 3.51. After we're satisfied with the value we've found for the critical gain P_U , we zoom in on a spectroscopy snapshot in order to measure the period of the oscillations, T_U . Using the cursor tools we see that for $P_U=5.61$ the corresponding period is $T_U=64.0$ ms

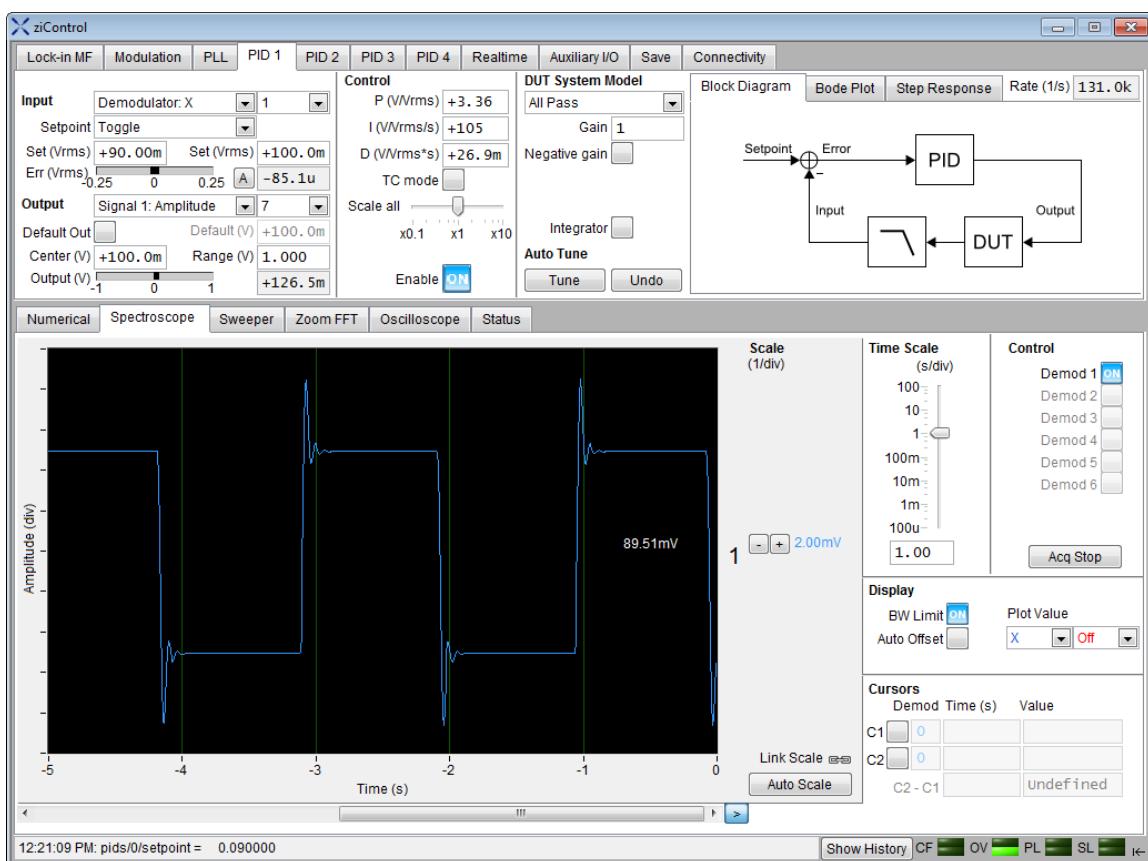


Figure 3.52. The tuned PID configuration and spectroscope snapshot

3.11.4. Determining the PID Parameters Manually using the Open Loop Bode Plot

In this section we apply the Ziegler-Nichols method using the tuning tools available in the PID tab. To use these tools we first have to provide a system model that describes the device under test. We specify the model in the DUT System model section of the PID tab. Since our device under test is a simple feedback cable, a suitable model for our system is the All Pass model and we set the gain to 0.7071. This gain corresponds to $1/\sqrt{2}$, appropriate since the PID input, demodulator X, is in RMS units (we assume the cable itself has a gain of 1). We don't need to select Negative Gain or add an Integrator to the model. See [Figure 3.53](#) for a screenshot of the model's parameters in the PID tab.

In the Bode Plot sub-tab of the PID tab the user can plot the transfer functions of various components of the system or the entire system itself. In particular we can plot the transfer functions of:

- PID
- System model for the Device Under Test (DUT)
- Demodulator filter (updated with the current settings)
- Combined system model and demodulator filter
- Open loop of the entire system: the system model, demodulator filter and PID controller
- Closed loop of the entire system: the system model, demodulator filter and PID controller; this describes the full feedback system

Note

With the exception of the PID and demodulator filter Bode plots, the reliability of the bode plots depends on the accuracy of the provided model for the DUT.

In the following, we'll make use of the Open Loop Bode Plot to determine the critical gain. The Open Loop plot can be used to tell whether the feedback system with the current P, I and D gains is stable or not. The top plot is the Bode magnitude plot and the bottom the Bode phase plot. By the definition of feedback systems, the closed loop system is unstable when the open loop gain is greater than 1 at the frequency where the open loop phase is -180° .

The phase margin and gain margin both give an indication of the proximity to instability. The phase margin is defined as the amount of phase lag required before the closed loop system becomes unstable. Similarly, the gain margin is defined as the smallest amount that the open loop gain can be increased before the closed loop system becomes unstable. The blue bar in the Open Loop Bode magnitude plot indicates the frequency where the gain margin is 0 dB (corresponding to a gain of 1 in linear units) and the blue bar in the Open Loop Bode phase plot indicates the frequency where the phase margin is zero. The actual phase margin (P.M.) and gain margin (G.M.) of the feedback system are displayed at the right-hand side of the Open Loop bode plot and are highlighted in green in the Open Loop Bode plots. The distance along the green line between the dashed red line and the black curve of the bode plot gives the gain margin, respectively, phase margin.

By definition, a gain margin of 0 dB corresponds to the point after which the system becomes unstable, and if we set I and D to zero, then the value of P that produces an open loop gain margin of 0 dB is exactly the critical gain P_U . This is visible in the open loop Bode plot as when the phase margin and gain margins can be found at the same frequency and the blue and green lines lie on top of one another. We now proceed to find this point experimentally by increasing the controller's P gain.

We start with the same configuration that we used in Section 3.11.3 and perform the steps of the Ziegler-Nichols method, this time choosing P_U based on the gain margin as displayed in the Open Loop plot:

1. Set both the D and I gains of the controller to 0.
2. Increase the P gain from 0 until the gain margin in the Open Loop Bode plot changes sign from positive to negative. Ideally we want to choose the value of P that creates a gain margin of 0 dB. See Figure 3.53 for Open Loop bode plots that correspond to stable, instable and critically stable closed loop systems. We obtain a gain margin of 0 when $P=5.6059$.
3. Measure the period T_U of the oscillations in the spectroscopic tab as in the previous section, shown in Figure 3.51. We again find that $T_U=64$ ms. Note, if the supplied DUT model is incorrect the simulated behavior and actual system behavior will not coincide and you will not be able to measure T_U .
4. Finally, we calculate the controller's P, I and D gains according to Table 3.27 and find that we should set the controller's gains as $P=3.364$, $I=105.1$ $D=0.02690$.



Figure 3.53. With $P=2,20,5.6059$ we obtain gain margins of 9.022, -10.978 and 0.000 dB respectively. These values correspond to stable, instable and critically stable closed loop systems.

Note

The axes limits in the Bode plots are calculated automatically, but it's possible to change them by double clicking on the current axes limit value, typing a new value and pressing [ENTER].

Note

We can also use the Closed Loop Bode plot to see whether the PID controller is stable or not: To the right-hand side of the plot there is a Stability Issue lamp which turns red if the current PID gains result in an unstable feedback system, see Figure 3.54.



Figure 3.54. The PID tab's Closed Loop Bode plot includes a lamp indicating whether the simulated feedback system is stable or not. As we've seen for $P=5.605$ the system is stable, whereas for $P=5.660$ it's unstable

3.11.5. Determining the PID Parameters using Auto tune

If the specified DUT system model is correct, we can use the Auto tune feature to calculate the PID gains automatically. After providing the model as in [Section 3.11.4](#), Auto tune determines the PID gains to be $P=3.3908$, $I=106.13$ and $D=0.027082$. The tuned PID is shown in [Figure 3.55](#). Since Auto tune is partially based on the Ziegler-Nichols method the P, I and D gains are very close to those determined manually in [Section 3.11.3](#).

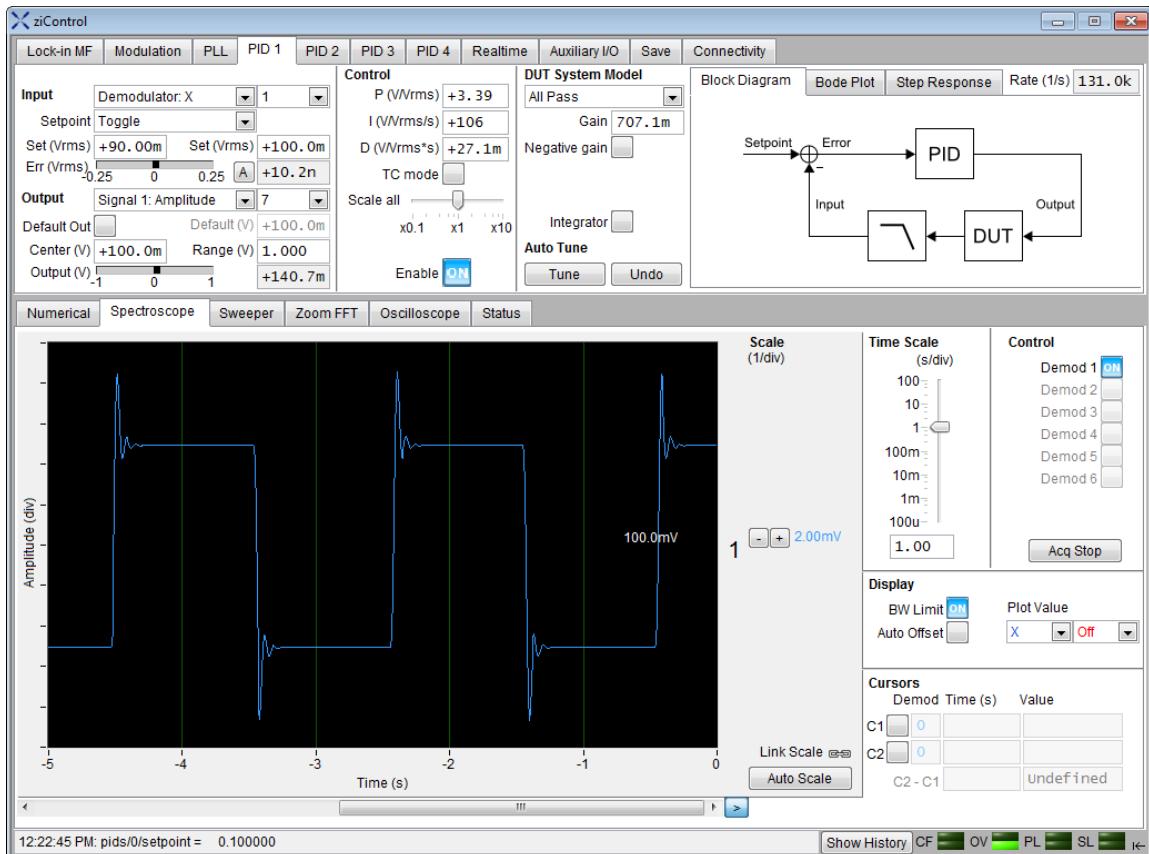


Figure 3.55. The tuned PID and output in the Spectroscopic after applying Auto tune

Note

We can view the simulated step response of our system in the Closed Loop Step Response Plot. Under the assumption of an accurate model, this allows us to view the system's response without enabling the PID, an example is shown in Figure 3.56.

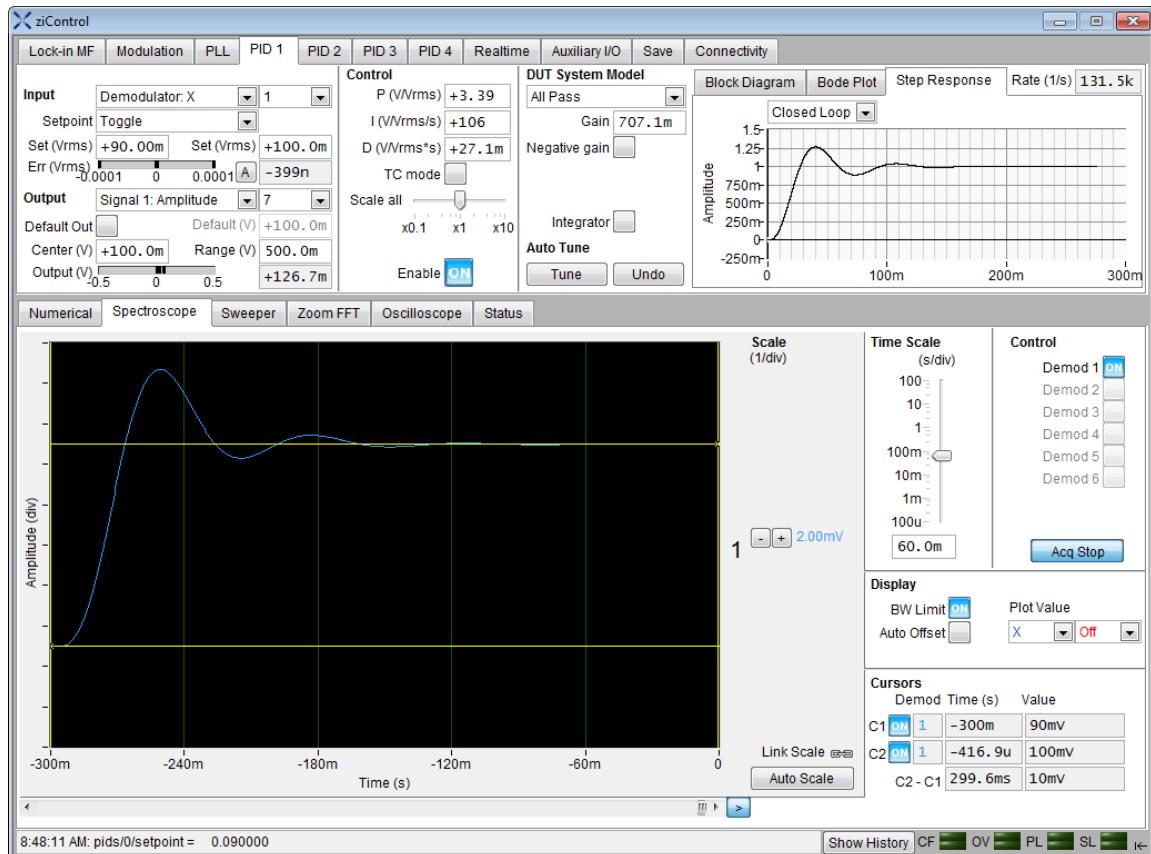


Figure 3.56. The PID tuned via Auto tune. The simulated closed loop step response coincides well with the true response shown in the Spectroscope.

Chapter 4. Functional Description HF2LI

This section contains the detailed description of all panels of the graphical user interface (GUI) ziControl for the HF2LI and HF2PLL products. This GUI is a LabVIEW based program that is delivered standard with all instruments.

On top of standard functionality like acquiring and saving data points this GUI provides a variety of measurement tools for the time and frequency domain. All of these features (and a few more) are also accessible by means of the programming interfaces described in [Chapter Programming](#).

4.1. Graphical User Interface Overview

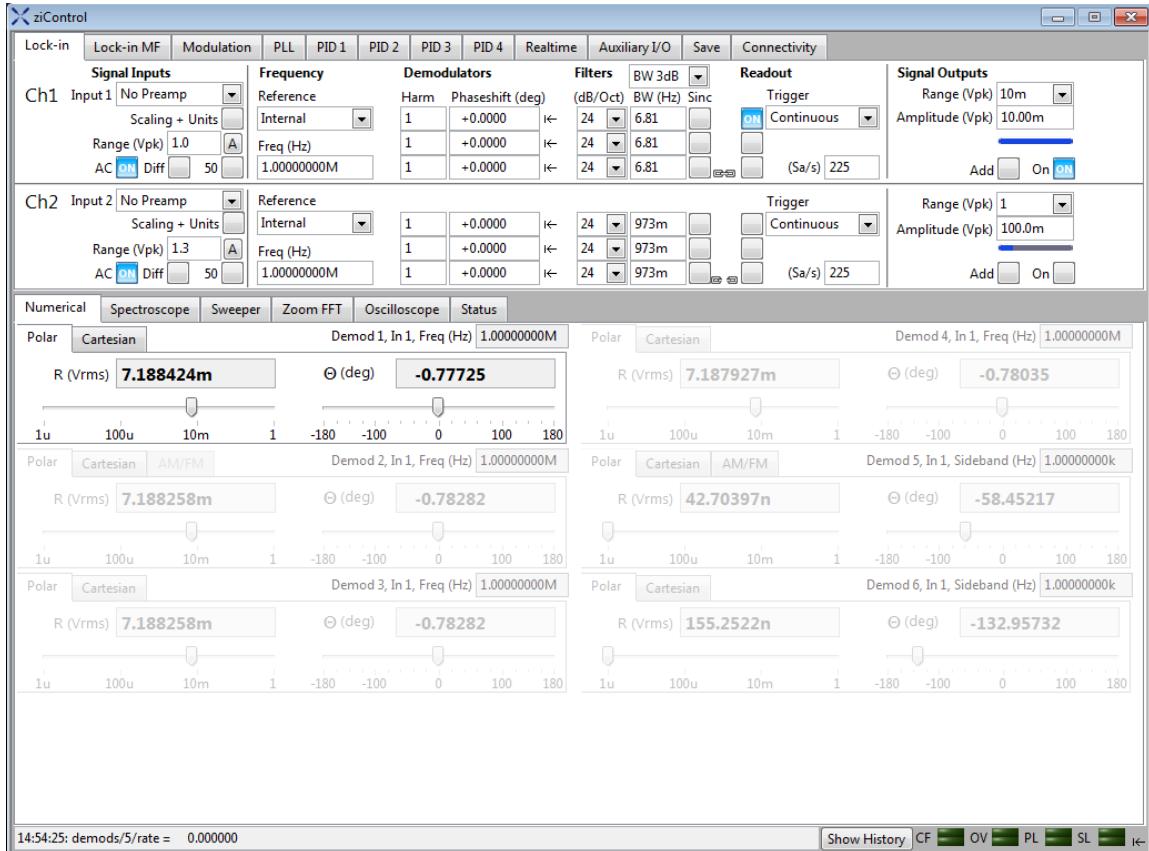


Figure 4.1. ziControl Overview

The GUI is divided into 2 sections as depicted in [Figure 4.1](#), each is subdivided in a tab structure. The **settings section** on top is dedicated to display and control the main settings of the instrument, whereas the **tools section** provides additional measurement functionality. A status line on the lower end shows the command history which ziControl exchanged with the ziServer plus a couple of status flags on the right hand side.

The following tabs are available in the settings section:

- Lock-in
 - Lock-in MF (available as upgrade option)
 - Modulation (available as upgrade option)
 - PLL (available as upgrade option)
 - PID (available as upgrade option)
 - Real-time (available as upgrade option)
 - Auxiliary I/O
 - Save
 - Connectivity
 - Active probes (displayed only when optional additional hardware is attached)

These are the tabs in the tools section:

- Numerical
- Spectroscope
- Frequency response sweeper
- ZoomFFT
- Oscilloscope
- Status

4.2. Settings Tabs

4.2.1. Lock-in Tab

Note

This tab is the lock-in control panel for all base instruments. This tab is not available for instruments with installed HF2LI-MF option.

Features:

- Control for 2 separate lock-in units with 3 demodulators each
- Auto ranging, scaling and definition of units for each input channel
- Control for 2 signal generators
- Range setting for signal inputs and signal outputs
- Flexible choice of reference source and trigger options
- Automated scale and unit adjustment for external amplifier

Description:

The Lock-in tab (Figure 4.2) is divided into 2 identical sections, separated by the horizontal line in the middle. The upper section is related to signal input 1 and output 1, and the lower section to signal input 2 and output 2.

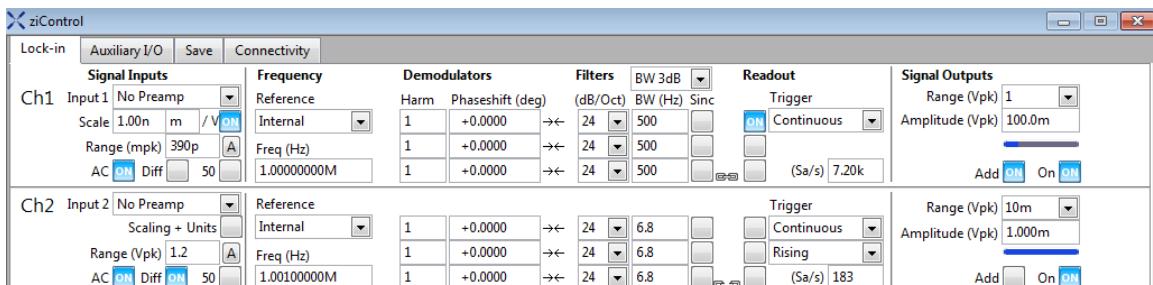


Figure 4.2. Lock-in Tab

Table 4.1. Lock-in Tab

Control/Tool	Options/Range	Description
Input selection	No Preamp	defines the active probe that is connected to the input of the HF2 instrument - in case an HF2 Series pre-amplifier is connected then this selection will make the measurement account for external pre-amplification and current conversion - please make sure to define the correct pre-amplifier model, channel, and ZCtrl connectivity port (on the back panel of the instrument)
	HF2TA 0, Ch1	
	HF2TA 0, Ch2	
	HF2CA 0	
	HF2TA 1, Ch1	
	HF2TA 1, Ch2	
	HF2CA 1	
Scale + Unit enable	OFF / ON	enables arbitrary input scaling to define a scaling factor for all measurements regarding this input channel and also change the unit which is displayed throughout the tools and settings

Control/Tool	Options/Range	Description
Scale factor	numeric scaling factor	positive or negative real number to scale all measurement values attributed to that particular input; only appears when Scaling + Units is enabled
Unit acronym	three letter acronym	three letters can be set to define a unit, e.g. PSI; only appears when Scaling + Units is enabled
Range (unit)	1 mV to 2.1 V (or equivalent after unit + scale is applied)	sets the range of input 1/2 in fine increments within the available range - the value is the absolute range of the signal including a potential DC offset - note: the value inserted by the user may be approximated to the nearest value supported by the HF2; Scaling + Units is fully accounted for in this setting
A = Auto Range button	[press once]	press this button to automatically set the input range to two times the maximum amplitude of the input signal over a measured time of 100 ms
AC switch	OFF: DC coupling ON: AC coupling	sets the input coupling for input 1/2 - an AC coupling inserts a high-pass filter at 1 kHz in the input path
Differential switch	OFF: single-ended input ON: differential input	selects whether to connect to a single-ended signal (one cable) or to a differential signal (two cables)
50 Ω switch	OFF: 1 MΩ ON: 50 Ω	sets the matching impedance for input 1/2
Reference selection	Internal (internal reference) Signal In 1 (auto/external reference) Signal In 2 (external/auto reference) Signal Aux In 1 (external reference) Signal Aux In 2 (external reference) Signal DIO 0 (external reference) Signal DIO 1 (external reference) From Ch1 / Ch2 (other internal reference)	internal reference mode: the lock-in reference is generated by internal oscillators auto reference mode: the lock-in reference is derived from the input signal external reference mode: the lock-in reference is generated externally and connected to the indicated connector other internal reference mode: the lock-in reference is the same as for the other lock-in unit (variation of the internal reference mode) note: frequency restrictions apply for some selections
Lock flag	OFF / ON	this flag indicates when the oscillator is locked to the selected reference frequency - note: this flag is only meaningful when external and auto reference modes are selected

Control/Tool	Options/Range	Description
Freq (Hz)	0 to 100 MHz	frequency for internal reference 1/2: this frequency can also be output with variable amplitude
Harm (1..6)	1 to 1023	selects the harmonic to be measured - enter 1 to measure the fundamental frequency and 2 for the 2nd harmonic, and so on
Phase shift (1..6)	-180° to 180°	defines the phase shift of the reference frequency at the input of the demodulator (works for internal and external reference modes) - this setting does not shift the signal output
Auto Zero button 	[press once]	this button shifts the phase of the reference at the input of the demodulator in order to achieve 0 phase at the demodulator output - this action maximizes the X output, zeros the Y output, zeros the Θ output, and leaves the R output unchanged
Filter Roll Off selection	1st: 6 dB/oct 2nd: 12 dB/oct 3rd: 18 dB/oct 4th: 24 dB/oct 5th: 30 dB/oct 6th: 36 dB/oct 7th: 42 dB/oct 8th: 48 dB/oct	sets the filter roll off (filter order) for the related demodulator
Filter Property Unit selection	BW 3 dB: 80 µHz (filter order = 8) to 200 kHz (filter order = 1) BW NEP: 90 µHz to 319 kHz TC eff.: 783 ns to 1900 s TC per order: 783 ns to 580 s	defines the filter properties displayed in either of these bandwidth or time constant units that differ by a scaling factor depending on the filter order - please refer to Table 10.1 for exact relation between TC, BW, and filter order - note: the value inserted by the user may be approximated to the nearest value supported by the HF2
Sinc Filter switch (1..6)	OFF: Sinc filter disabled for corresponding demodulator ON: Sinc filter enabled for corresponding demodulator	the Sinc filter is an additional filtering stage that permits to remove the omega and 2 times omega components - by using a large filter roll-off value, the user can effectively reduce the signal component at the second and third harmonics - for applications where large roll-off is not possible, the Sinc filter achieves the same effect - note: there are limitations regarding the maximum frequency and the frequency resolution allowed with Sinc filtering (see Table 4.2)
Block Lock button 	[press once]	this button locks the settings between the different demodulators - when lock is closed, then changing one value changes all others - when lock is open, changing one leaves others unchanged

Control/Tool	Options/Range	Description
Demodulator switch (1..6)	ON / OFF	switches to turn on and off each of the demodulators
Trigger selection	Continuous	automatic internal trigger with the sample rate defined in the next control field
	DIO 0	samples are sent to the host computer depending on DIO 0 triggering
	DIO 1	samples are sent to the host computer depending on DIO 1 triggering
	DIO 0 or 1	samples are sent to the host computer depending on DIO 0 and 1 triggering
	Undefined	a complex trigger has been programmed to the HF2 Instrument by means of the programming interfaces - this mode cannot be selected by the user of ziControl, but is the result of a complex trigger definition - note: this is a read-only position
Trigger Mode selection	Rising Edge	1 data sample is sent to the host computer for a rising edge on the trigger input
	Falling Edge	1 data sample is sent to the host computer for a falling edge on the trigger input
	Both Edges	1 data sample is sent to the host computer for a rising edge and falling edge on the trigger input
	Gate High	data samples are sent to the host computer as long as the defined DIO 0/1 input is high - it is possible to determine the number of demodulated samples that are sent to the host computer after a trigger by setting the readout rate and modulating the pulse length on the DIO 0/1
	Gate Low	data samples are sent to the host computer as long as the defined DIO 0/1 input is low - it is possible to determine the number of demodulated samples that are sent to the host computer after a trigger by setting the readout rate and modulating the pulse length on the DIO 0/1
	Undefined	a complex trigger has been programmed to the HF2 Instrument by means of the programming interfaces - this mode cannot be selected by the user of ziControl, but is the result of a complex trigger definition - note: this is a read-only position
Sampling Rate	0.22 Hz to 460 kHz	sets the filter readout rate, equivalent to the demodulator samples (measured data points) that are sent to the host computer per second - it is also the rate of data received by ziControl and saved to the computer hard disk - this setting has no impact on the sample rate on the physical auxiliary outputs - note: the value inserted by the

Control/Tool	Options/Range	Description
		user may be approximated to the nearest value supported by the HF2 (see Table 9.4)
Range selector	$\pm 10 \text{ mV}$	sets the output range for output 1/2 - this select determines the maximum output peak to peak range - this setting will make sure that no peaks above the setting are generated at the output, independent from the amplitude settings (V) - the output signal is clipped if the amplitude is higher than the range - if the range is changed to a value smaller than V, then V is automatically reduced to the new range
	$\pm 100 \text{ mV}$	
	$\pm 1 \text{ V}$	
	$\pm 10 \text{ V}$	
Amplitude (V _{pk})	0 to 10 V	sets the output voltage - the minimum setting is 0 V, the next level above zero depends on the signal output 1/2 range - note: the value inserted by the user may be approximated to the nearest value supported by the HF2
Amplitude indicator	0 to 100%	graphical indicator on how much of the set output range is currently used
Add switch	OFF: Add disabled	the Add input allows to add an external analog signal to the internally generated signal - when disabled, the signal at the Add input is ignored
	ON: Add enabled	
Output switch	OFF: output disabled	sets the main switch for output 1/2 - when the output is enabled, then the HF2 signal output is active and the blue LED on the front panel lights-up
	ON: output enabled	

For technical reasons, the Sinc filter imposes restrictions on possible demodulation frequencies. This restriction is important in the 10 kHz range, whereas it is small in the 10 Hz range, where most applications require the Sinc filter operate.

Table 4.2. Sinc filtering range and setting resolution

Demodulation frequency	Frequency setting resolution	Comments
Above 10 kHz	not supported	
10 kHz	220 Hz	
1 kHz	2.2 Hz	
100 Hz	0.022 Hz	
10 Hz	0.0002 Hz	
1 Hz	2 μ Hz	

4.2.2. Lock-in MF Tab

Note

Instruments with the HF2LI-MF option installed will show the Lock-in MF tab instead of the Lock-in tab. The HF2LI-MF option can be bought and installed at any time via remote servicing.

Compared to the basic version of the instrument the multi-frequency option offers the use of 6 independent numerical oscillators where individual output amplitudes can be defined for each of the two outputs.

Features:

- Control for 6 independent demodulation units for 2 input signals
- Auto ranging, scaling and definition of units for each input channel
- Control for 8 oscillators for internal reference demodulation and signal generation
- Flexible choice of reference source and trigger options
- Arbitrary input units and scaling for adjustment to external transducers

Description:

The HF2LI-MF option is delivered with a tab that permits to control all settings additional compared to the base lock-in amplifier. It is a crowded tab providing a very large flexibility with the settings. It is possible to achieve overall settings with various combinations.

Each line in the Demodulators section permits to control the inputs and the settings of one of the 6 demodulators. For instance it is possible to connect one of the 8 oscillators (Osc) to any of the Demodulators, permitting to a very flexible frequency allocation, e.g. any line in the tab can be configured to work with any oscillator. Line 7 and 8 are a bit different as the oscillator allocation 1 and 2 is given and cannot be changed.

On the signal generation side, it is easy to see that it is possible to generate Output 1 and Output 2 as a linear combination of up to 8 frequencies generated with said oscillators.

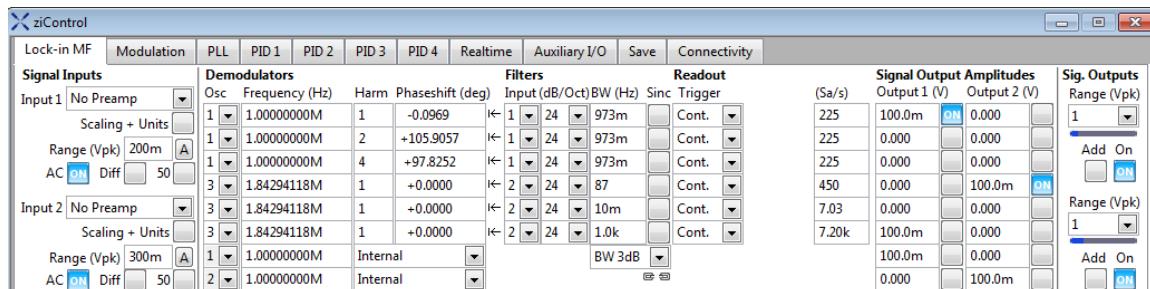


Figure 4.3. Lock-in MF Tab

Table 4.3. Lock-in MF Tab

Control/Tool	Options/Range	Description
Input selection	No Preamp	defines the active probe that is connected to the input of the HF2 instrument - in case an HF2 Series pre-amplifier is connected then this selection will make the measurement account for external pre-amplification and current conversion - please make sure to define the correct pre-amplifier model, channel, and ZCtrl connectivity port (on the back panel of the instrument)
	HF2TA 0, Ch1	
	HF2TA 0, Ch2	
	HF2CA 0	
	HF2TA 1, Ch1	
	HF2TA 1, Ch2	
	HF2CA 1	
Scaling + Units enable	OFF / ON	enables arbitrary input scaling to define a scaling factor for all measurements regarding this input channel and also change the unit which is displayed throughout the tools and settings

Control/Tool	Options/Range	Description
Scale	numeric scaling factor	positive or negative real number to scale all measurement values attributed to that particular input; only appears when Scaling + Units is enabled
Units	three letter acronym	three letters can be set to define a unit, e.g. PSI; only appears when Scaling + Units is enabled
Range (unit)	1 mV to 2.1 V (or equivalent after unit + scaling is applied)	set the range of input 1/2 in fine increments within the available range - the value is the absolute range of the signal including a potential DC offset - note: the value inserted by the user may be approximated to the nearest value supported by the HF2; Scaling + Units is fully accounted for in this setting
A = Auto Range button	[press once]	press this button to automatically set the input range to two times the maximum amplitude of the input signal over a measured time of 100 ms
AC switch	OFF: DC coupling ON: AC coupling	set the input coupling for input 1/2 - an AC coupling inserts a high-pass filter at 1 kHz in the input path
Differential switch	OFF: single-ended input ON: differential input	select whether to connect to a single-ended signal (one cable) or to a differential signal (two cables)
50 Ω switch	OFF: 1 MΩ ON: 50 Ω	set the matching impedance for input 1/2
Oscillator selection (1..6)	Osc 1 to Osc 6	select the oscillator for the related generator and demodulator
Frequency (Hz)	0 to 100 MHz	frequency for internal reference 1/2: this frequency can also be output with variable amplitude
Harm (1..6)	1 to 1023	selects the harmonic to be measured - enter 1 to measure the fundamental frequency and 2 for the 2nd harmonic, and so on
Phase shift (1..6)	-180° to 180°	defines the phase shift of the oscillator associated to the demodulator and is relevant for the reference signal as well as for the signal output (works for internal and external reference modes)
Reference selection	Internal (internal reference)	internal reference mode for input 1/2: the reference is generated by internal oscillators
	Signal Input 1 (auto reference)	auto reference mode for input 1/2: the reference is derived from the input signal
	Signal Input 2 (external reference)	external reference mode for input 1/2: the reference is the signal attached to Input 2 connector
	Signal Aux In 1 (external reference)	external reference mode for input 1/2: the reference is the signal attached to Aux In 1 connector

Control/Tool	Options/Range	Description
	Signal Aux In 2 (external reference)	external reference mode for input 1/2: the reference is the signal attached to Aux In 2 connector
	Signal DIO 0 (external reference)	external reference mode for input 1/2: the reference is the signal attached to DIO 0 connector
	Signal DIO 1 (external reference)	external reference mode for input 1/2: the reference is the signal attached to DIO 1 connector
Lock flag	OFF / ON	this flag indicates when the demodulator is locked to the selected reference frequency - note: this flag is only meaningful when external and auto reference modes are selected
Auto Zero button 	[press once]	this button shifts the phase of the reference at the input of the demodulator in order to achieve 0 phase at the demodulator output - this action maximizes the X output, zeros the Y output, zeros the Θ output, and leaves the R output unchanged
Signal Channel selection	1: the filter is connected to signal input 1	set the HF2 signal input that is connected to the related demodulator - the HF2 has 2 differential inputs that can be selected
	2: the filter is connected to signal input 2	
Filter Roll Off selection	1st: 6 dB/oct	set the filter roll off (filter order) for the related demodulator
	2nd: 12 dB/oct	
	3rd: 18 dB/oct	
	4th: 24 dB/oct	
	5th: 30 dB/oct	
	6th: 36 dB/oct	
	7th: 42 dB/oct	
	8th: 48 dB/oct	
Filter Property Unit selection	BW 3 dB: 80 μ Hz (filter order = 8) to 200 kHz (filter order = 1)	Filter properties can be set and displayed in either of these bandwidth or time constant units that differ by a scaling factor depending on the filter order - please refer to Table 10.1 for exact relation between TC, BW, and filter order - note: the value inserted by the user may be approximated to the nearest value supported by the HF2
	BW NEP: 90 μ Hz to 319 kHz	
	TC eff: 783 ns to 1900 s	
	TC per order: 783 ns to 580 s	
Sinc Filter switch (1..6)	OFF: Sinc filter disabled for corresponding demodulator	the Sinc filter is an additional filtering stage that permits to remove the omega and 2 times omega components - by using a large filter roll-off value, the user can effectively reduce the signal component at the second and third harmonics - for applications where large roll-off
	ON: Sinc filter enabled for	

Control/Tool	Options/Range	Description
	corresponding demodulator	is not possible, the Sinc filter achieves the same effect - note: there are limitations regarding the maximum frequency and the frequency resolution allowed with Sinc filtering (see Table 4.2)
Block Lock button 	[press once]	this button locks the settings between the different demodulators - when lock is closed, then changing one value changes all others - when lock is open, changing one leaves others unchanged
Trigger selection	Off	turn off demodulator
	Continuous	automatic internal trigger with the sample rate defined in the next control field
	DIO 0	samples are sent to the host computer depending on DIO 0 triggering
	DIO 1	samples are sent to the host computer depending on DIO 1 triggering
	DIO 0 or 1	samples are sent to the host computer depending on DIO 0 and 1 triggering
	Undefined	a complex trigger has been programmed to the HF2 Instrument by means of the programming interfaces - this mode cannot be selected by the user of ziControl, but is the result of a complex trigger definition - note: this is a read-only position
Trigger Mode selection	Rising Edge	1 data sample is sent to the host computer for a rising edge on the trigger input
	Falling Edge	1 data sample is sent to the host computer for a falling edge on the trigger input
	Both Edges	1 data sample is sent to the host computer for a rising edge and falling edge on the trigger input
	Gate High	data samples are sent to the host computer as long as the defined DIO 0/1 input is high - it is possible to determine the number of demodulated samples that are sent to the host computer after a trigger by setting the readout rate and modulating the pulse length on the DIO 0/1
	Gate Low	data samples are sent to the host computer as long as the defined DIO 0/1 input is low - it is possible to determine the number of demodulated samples that are sent to the host computer after a trigger by setting the readout rate and modulating the pulse length on the DIO 0/1
	Undefined	a complex trigger has been programmed to the HF2 Instrument by means of the programming interfaces - this mode cannot be selected by the user of ziControl, but is the result of a complex trigger definition - note: this is a read-only position

Control/Tool	Options/Range	Description
Sampling Rate (Sa/s)	0.22 Hz to 460 kHz	set the filter readout rate, equivalent to the demodulator samples (measured data points) that are sent to the host computer per second - it is also the rate of data received by ziControl and saved to the computer hard disk - this setting has no impact on the sample rate on the physical auxiliary outputs - note: the value inserted by the user may be approximated to the nearest value supported by the HF2 (see Readout Rate Overview)
Signal Output Amplitudes	0 to 10 V	set the output voltage for each individual demodulator for the two signal outputs 1/2, the granularity depends on the signal output 1/2 range - note: the value inserted by the user may be approximated to the nearest value supported by the HF2
Range selector	±10 mV	set the output range for output 1/2 - this select determines the maximum output peak to peak range - this setting will make sure that no peaks above the setting are generated at the output, independent from the amplitude settings (V) - the output signal is clipped if the amplitude is higher than the range - if the range is changed to a value smaller than V, then V is automatically reduced to the new range
	±100 mV	
	±1 V	
	±10 V	
Amplitude indicator	0 to 100%	graphical indicator on how much of the set output range is currently used
Add switch	OFF: Add disabled	the Add input allows to add an external analog signal to the internally generated signal - when disabled, the signal at the Add input is ignored
	ON: Add enabled	
Output switch	OFF: output disabled	set the main switch for output 1/2 - when the output is enabled, then the HF2 signal output is active and the blue LED on the front panel lights-up
	ON: output enabled	

4.2.3. Modulation Settings Tab

Note

The modulation tab appears only when the HF2LI-MOD option has been purchased and activated. Customers can purchase the HF2LI-MOD modulation option at any time, whether when ordering their instrument or after delivery. This option can be activated by the user or by Zurich Instruments via remote servicing.

Note

The HF2LI-MOD modulation option requires that the HF2LI-MF multi-frequency option to be purchased and activated.

Features:

- Control for AM and FM narrow-band demodulation
- Control for AM and FM generation
- Direct sideband analysis

Description:

For AM generation, three demodulators from the multi-frequency option are used to produce the carrier and the two sidebands (if both sidebands are selected). Two separate modulation units are provided, Mod1 unit activates Demodulators 1, 2 and 3, and using the Mod2 unit activates Demodulators 4, 5 and 6. Carrier and sideband amplitudes can be selected in the Output Amplitudes menu and the signal can be generated on Signal Output 1, Signal Output 2 or on both: selecting the output on the pull-down menu then selects the correct amplitudes on the Lock-in MF tab and activates them.

The demodulation of an AM signal also requires three demodulators as the Lock-in MF tab will show. If single sideband modulation/demodulation is selected, one demodulator less is required. In the Numerical tab, the Demodulator 2 (5 if Mod2 is used) is active and 3 (respectively 6) is grayed out.

One should not confuse oscillators with demodulators. In principle all oscillators and demodulators can be used for modulation schemes.

For FM generation, two demodulators are used (either 1,2 or 4,5) to generate the carrier frequencies and the sidebands according to the defined Carrier (V) amplitude setting and Modulation Index. FM detection on the other hand always requires 3 demodulators. By choosing higher harmonics for sideband demodulators one can select higher order sidebands.

Note

The resulting frequencies f_1+f_2 and f_1-f_2 are only available inside the instrument, and not displayed nor stored by ziControl. Therefore in the numerical tool, in the spectroscope tool, or in the samples saved on the hard disk, the modulation frequency is indicated instead of the true demodulation frequency.

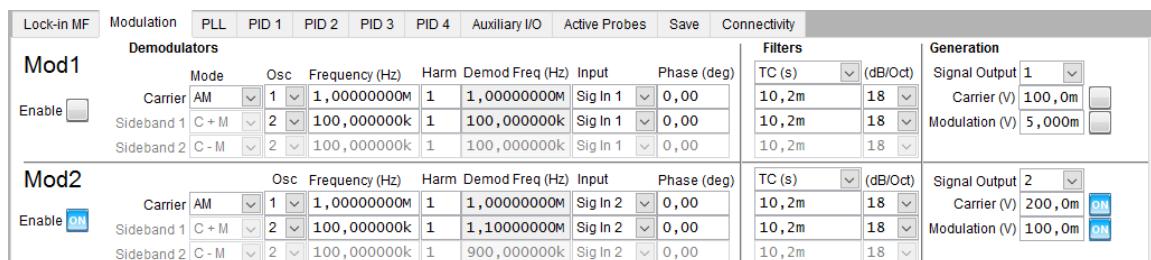


Figure 4.4. Modulation settings tab

Table 4.4. Modulation settings tab

Control/Tool	Options/Range	Description
Carrier Osc selection	1 (PLL 1), 2 (PLL 2), 3, 4, 5, 6	select the oscillator (or PLL) to be used as the carrier signal
Carrier Frequency (Hz)	0.8 μ Hz to 100 MHz	set the carrier signal frequency - note: the value inserted by the user may be approximated to the nearest value supported by the HF2
Frequency Harm	1 to 1023	set the harmonic of the carrier signal

Control/Tool	Options/Range	Description
Sideband 1/2 Osc selection	1 (PLL 1), 2, 3, 4 (PLL 2), 5, 6	select the oscillator (or PLL) to be used as the modulation signal
Sideband 1/2 Frequency	0.8 μ Hz to 100 MHz	set the modulation signal frequency - note: the value inserted by the user may be approximated to the nearest value supported by the HF2
Sideband 1/2 Harm	1 to 1023	set the harmonic of the modulation signal
Modulation Mode selection	Off	no action
	AM	MOD operates in amplitude modulation mode (generation and demodulation)
	FM	MOD operates in frequency modulation mode (generation and demodulation)
	Manual	MOD operates in manual mode where both sidebands are freely configurable
Sideband 1/2 mode selection	OFF	Sideband is not generated
	C+M	Sideband is generated at the carrier frequency plus the sideband frequency
	C-M	Sideband is generated at the carrier frequency minus the sideband frequency
Input Signal selection	1	demodulation is performed on Signal Input 1 - note: setting is grayed-out when not applicable
	2	demodulation is performed on Signal Input 2 - note: setting is grayed-out when not applicable
Time Constant, Filter Roll Off, Bandwidth	0.8 μ s to 580 s, 6 to 48 dB/Oct, 80 μ Hz to 200 kHz	set the filter properties of all modulation relevant filters simultaneously (see also Table 4.1)
Carrier Signal Amplitude	8 μ V to 10 V	set the amplitude of the carrier signal - note: the maximum value is limited by the range setting on the Lock-in tab
Modulation Amplitude (AM)/ Modulation Index (FM)	8 μ V to 10 V	set the amplitude of the modulation signal - note: the maximum value is limited by the range setting on the Lock-in tab - note: setting is grayed-out when not applicable
Signal Output selection	1	generated signal is available on Signal Output 1
	2	generated signal is available on Signal Output 2
	1&2	generated signal is available on both, Signal Output 1 and Signal Output 2

4.2.4. PLL Settings Tab

Note

The PLL tab appears only when the HF2LI-PLL option has been purchased and activated. Customers can purchase the HF2LI-PLL phased-locked loop option at any time, whether when ordering their instrument or after delivery. This option can be activated by the user or by Zurich Instruments via remote servicing.

The HF2LI-PLL feature provides 2 independent PLL for high speed, high accuracy, highly flexible tracking of frequency modulated signals. The PLL permits to recover such signals with a powerful user interface with the choice of many automatic and manual settings.

Features:

- Dual fully programmable 50 MHz phased-locked loops
- Programmable PLL center frequency and set point
- Programmable PLL phase detector filter settings and PI controller dynamics
- Comfortable PLL Advisor panel for transfer function analysis
- Advanced 2-omega PLL mode (requires access to HF2LI-MF option)
- Auto-zero functions for center frequency and set point

Description:

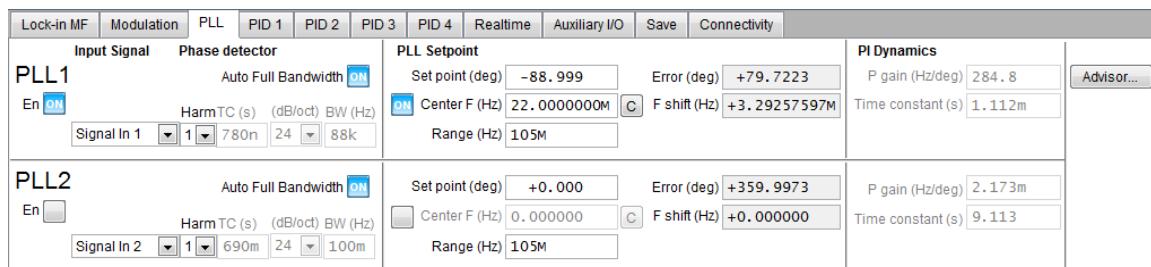


Figure 4.5. PLL Settings Tab

Table 4.5. PLL settings tab

Control/Tool	Options/Range	Description
PLL 1/2 enable	OFF: PLL disabled	disables the PLL - the related oscillators run at the frequency programmed in the corresponding registers
	ON: PLL enabled	enables the PLL - the related oscillators run controlled by the PLL
PLL 1/2 signal input select	Signal In 1	select the source for the reference signal of the PLL - this is the signal the PLL will lock to - the PLL can take a reference signal from either the high-speed analog inputs on the front-panel or the digital I/O and auxiliary inputs on the rear-panel
	Signal In 2	
	Aux In 1	
	Aux In 2	
	DIO 0	
	DIO 1	
PLL 1/2 phase detection mode	1 (normal PLL mode)	performs the phase detection on the first harmonic
	2 (2-omega PLL mode)	performs the phase detection on the second harmonic of the PLL drive frequency - with 2-omega mode the PLL lock is performed on the physical unit that takes place 2 times faster than the drive frequency - note: this advanced mode should only be used with manual center frequency and range setting - otherwise there is a chance to lock on the wrong frequency

Control/Tool	Options/Range	Description
PLL 1/2 phase detector mode	OFF: automatic full bandwidth	the phase detector low pass filter and the PI controller is defined with automatic settings derived from the PLL center frequency settings
	ON: manual mode	the phase detector low pass filter and the PI controller can be controlled with manual settings
PLL 1/2 phase detector time constant (TC)	0.8 μ s to 580 s	sets the time constant of the phase detector in the PLL - note: the value in this field is related to the value entered in the BW field
PLL 1/2 phase detectors filter order	1st: 6 dB/oct	sets the filter order for the phase-detector of the PLL
	2nd: 12 dB/oct	
	3rd: 18 dB/oct	
	4th: 24 dB/oct	
	5th: 30 dB/oct	
	6th: 36 dB/oct	
	7th: 42 dB/oct	
	8th: 48 dB/oct	
PLL 1/2 phase detector bandwidth (BW)	80 μ Hz (filter order = 8) to 200 kHz (filter order = 1)	sets the filter bandwidth of the phase-detector of the PLL - note: the value in this field is related to the value entered in the TC field
PLL 1/2 set point field	-180° to 180°	defines the set point of the PLL - the PLL will adjust the frequency of its oscillator to have this phase difference compared to the reference signal - the value can be copied from the current phase information from the demodulator 1/2 by pressing the button marked "C"
PLL 1/2 set point copy button	[press once]	updates the set point of the PLL by performing an auto zero of the phase error
PLL 1/2 phase error	-	indicates the phase error at the input of the PLL phase detector - if the PLL is in locked state, the phase error is very small (much smaller than 1 degree)
PLL 1/2 center frequency mode	OFF: automatic mode	the PLL center frequency is determined automatically - in this mode the instrument sweeps the operating range until it finds a suitable frequency - note: automatic center frequency mode only works for open loop systems; closed loop systems requires manual mode
	ON: manual mode	the PLL center frequency is determined manually by the field on the right
PLL 1/2 center frequency field	1 Hz to 50 MHz	defines the center frequency of the PLL - the actual PLL frequency can be copied to this field by pressing the button marked "C" - note: in order the copy to work, the PLL must be reasonably locked
PLL 1/2 center frequency copy button	[press once]	updates the center frequency of the PLL by performing an auto zero of the frequency shift

Control/Tool	Options/Range	Description
PLL 1/2 frequency shift	-	indicates the frequency shift between the actual PLL frequency and the PLL center frequency $f_{PLL} - f_{CENTER}$ - for example, if the center frequency is 1 MHz and the reference signal has a frequency of 1.001 MHz, the frequency shift is -1 kHz
PLL 1/2 frequency shift range	1 Hz to 100 MHz	limits the maximum frequency shift and therefore the minimum and maximum excitation frequency (PLL output) - this setting has the capability to prevent resonator damage by applying frequencies in the wrong range
PLL 1/2 PI proportional gain	5 mHz/deg to 140 kHz/deg	sets the proportional gain of the PI controller in the PLL - when disabled, the proportional gain is determined automatically based on the center frequency
PID 1/2 PI time constant	180 ns to 100 ks	set the integral time constant of the PI controller in the PLL - when disabled, the integral time constant is determined automatically based on the center frequency
Advisor	[press once]	invokes the PLL Advisor window to estimate the system transfer function

Note

The proportional gain G of the PI controller of the PLLs is too large by a factor of 2. This means that if it reads 100, the correct value should be 50. Since $K_P = G$ and $K_I = G / TC$, the integral gain will scale in the correct way after correcting G .

The PLL Advisor supports the user estimate the system transfer function consisting of PLL and external resonator. After the transfer function analysis, the user can feed the PLL with the settings calculated by the PLL Advisor and thus expect a predictable system behavior. Experience has shown that the PLL Advisor and the real PLL behave similarly as long as there is no overshoot in the transfer function.

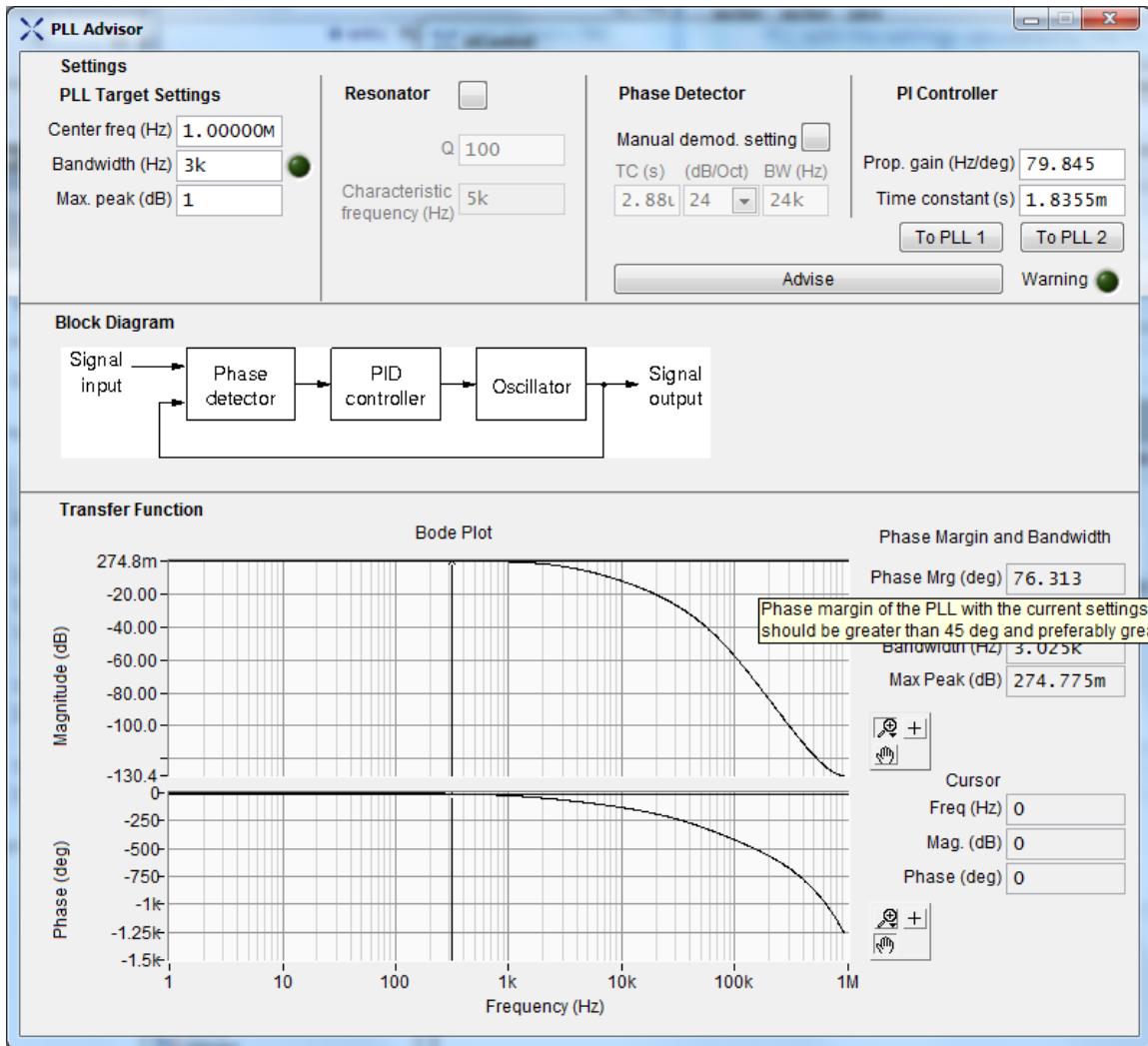


Figure 4.6. PLL Advisor

Table 4.6. PLL Advisor

Control/Tool	Options/Range	Description
Target center frequency	1 Hz to 50 MHz	defines the center frequency for the parameter estimation
Target bandwidth	16 Hz to 100 kHz	defines the measurement bandwidth for the parameter estimation
Target maximum overshoot	1 mdB to open	defines the maximum overshoot that is tolerated by the resonator
Resonator switch	OFF	the transfer function of the system is calculated using an internal look-up table simulating a response dependent solely on the center frequency (also ignores the target bandwidth)
	ON	a resonator with below mentioned characteristics is used for the estimation
Resonator Q factor	1 m to 1 T	defines the Q factor of the external resonator - the Q factor characterizes the bandwidth of the resonator relative to the center frequency $Q = f_R / BW$ - a higher quality factor indicates a lower

Control/Tool	Options/Range	Description
		energy loss compared to the stored energy in the resonator, which implies an oscillation with higher amplitudes, but a smaller range around the center frequency in which the oscillation is possible
Resonator characteristic frequency	-	indicates the characteristic frequency of the resonator $f_R = f_C / (2 * Q)$, representing the bandwidth of the resonator
PLL phase detector setting mode	OFF: automatic mode	calculates the phase detector settings based on the center frequency setting
	ON: manual mode	permits the user to define the own settings for the phase detector filter, and use the PLL Advisor to calculate the PID parameters
PLL phase detector filter TC	0.8 μ s to 580 s	sets the filter time constant of the phase detector for estimation
PLL phase detector filter order	1st to 8th	sets the filter order of the phase detector for estimation
PLL phase detector filter BW	80 μ Hz (filter order = 8) to 200 kHz (filter order = 1)	sets the filter bandwidth of the phase detector for estimation
PID proportional gain	10 mHz/deg to 10 kHz/deg	PID proportional characteristic for the transfer function calculation / estimation
PID integral time constant	100 μ s to 1 s	PID integral characteristic for the transfer function calculation / estimation
Advise button	[press once]	the PLL Advisor calculates the system transfer function based on the settings (see Bode plot), and calculates settings for the phase detector (if manual) and
Warning flag	-	the PLL Advisor was not able to calculate a satisfying solution - thus cross-check the result (example: bad overshoot, small phase margin)
To PLL 1 button	[press once]	copies the estimated phase detector and PID controller settings as active settings for PLL 1 - note: the settings have immediate effect
To PLL 2 button	[press once]	copies the estimated phase detector and PID controller settings as active settings for PLL 2 - note: the settings have immediate effect
Result phase margin	-	indicates the phase margin - a reasonable phase margin target is 65 degrees, though most systems will also operate well with 45 degrees
Result phase margin frequency	-	indicates the frequency where the phase margin is measured
Result bandwidth	-	indicates the 3 dB point of the closed loop transfer function
Result maximum peak	-	indicates the simulated maximum overshoot in dB
Transfer function Bode plot	-	represents the transfer function as estimated with the above settings of the resonator (if selected), the phase detector, and the PID

Control/Tool	Options/Range	Description
		controller - the Bode plot consists in magnitude (in dB) and phase (in degrees) of the transfer function of the complete system - the meaning of the magnitude relates how frequency modifications are transferred from the input to the output of the PLL
Bode plot cursor values	-	indicate the magnitude, phase, and frequency of the current cursor position

Note

The interpretation of the Bode plot is not within the scope of this document. Two comments regarding Bode plots are still to be mentioned: the overall stability of the system can be determined by the amount of overshoot that the system allows and by the phase margin the system has at the system bandwidth (3 dB point).

4.2.5. PID Settings Tab

Note

This tab appears only when the HF2LI-PID option has been purchased and activated. Customers can purchase this option at any time, whether when ordering their instrument or after delivery. It can be activated by the user or by Zurich Instruments via remote servicing. Some of the settings depend on the availability of other options.

Note

The HF2LI-PID option cannot be used at the same time as the HF2LI-RT option. If the PID option is installed on your instrument, then it is available after power-up. After downloading a RT program into the HF2 Instrument, the PID option is not available anymore. In order to re-activate the PID controller the user needs to either restart ziServer or to power cycle their HF2 Instrument.

Note

The modifications of some settings of the HF2LI-PID option modify the settings in other panels/ options.

Features:

- Four independent proportional, integral, derivative (PID) controllers
- Automatic P, I, and D parameter tuning for different system models (DUT)
- Bode plots to aid PID parameter tuning
- Simulated step response plots for the system model (DUT) and closed loop
- Output center and range setup (anti-windup)
- Eleven selectable input units for each controller
- Four selectable sources for the set point

- Four selectable output units for each controller
- Output to auxiliary output connectors
- Arithmetic processor for specific calculations
- Default output value for disabled controllers

Description:

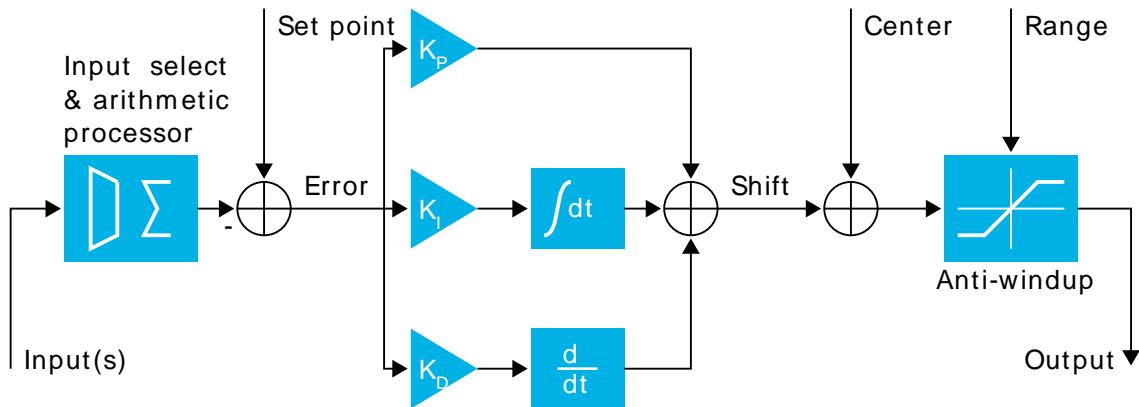


Figure 4.7. HF2LI-PID block diagram

The HF2LI-PID option serves to post-process a wide selection of input units in order to control a selected output by means of a proportional, integral, derivative controller. Each PID controller strives to reduce the Error that results from the subtraction of the input unit from the Set point. The Shift signal is then calculated according to the formula below and added to the Center. Before the output is generated, the anti-windup logic circuits makes sure that the Range is not exceeded at any time preventing illegal output ranges that could harm the following circuits.

$$\text{Shift} = K_P * \text{Error} + K_I * \int \text{Error} dt + K_D * \frac{d}{dt} \text{Error}$$

Equation 4.1. PID controller generic equation

The P, I, and D parameters are mapped as follows $K_P = P$, $K_I = I$, $K_D = D$.

The PID settings tab is divided into several sections labeled Input, Output, Control and DUT System Model. On the top right corner there is a rate indicator that provides an instant information of the PID output rate for all controllers. The rate decreases when more PID controllers are enabled, indicating that the same rate holds for all controllers. Although the bandwidth of a PID controller must be measured in action, it is possible to empirically calculate the bandwidth by dividing the output rate by 10, which is a conservative value.

Input:

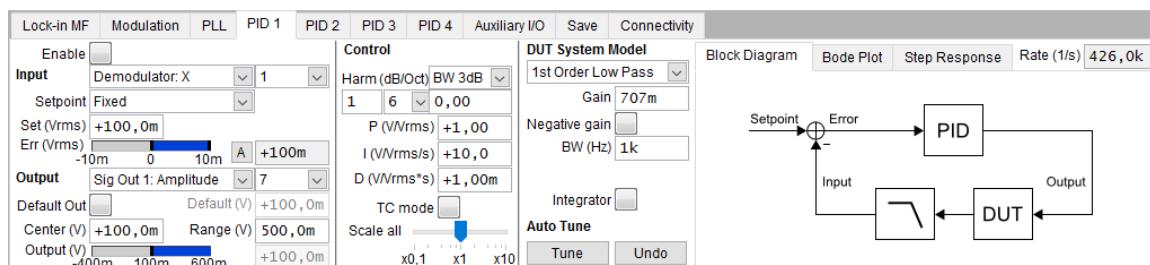


Figure 4.8. PID settings tab

Table 4.7. PID input settings

Control/Tool	Options/Range	Description
Input selection	Demodulator X, 1-6	defines the input unit for the PID controller as the output of a demodulator - for physical units see Table 4.12 .
	Demodulator Y, 1-6	
	Demodulator R, 1-6	
	Demodulator Θ, 1-6	
	Aux Input, 1-2	defines the input unit for the PID controller as the auxiliary inputs or the feedback value of the auxiliary outputs - for physical units see Table 4.12 .
	Aux Output (used as input), 1-4	
	AM Index, 1-2	defines the input unit to be the modulation index from an amplitude modulated signal (only available if the HF2LI-MOD option is available and active)
	Demodulator $ Z_{i+1} - Z_i $, 1-5	uses the arithmetic processor to combine the outputs of two consecutive demodulators, where Z_i is the complex signal, $Z_i = X_i + iY_i$ composed by the X_i and Y_i output of demodulator i
	Demodulator $ X_{i+1} - X_i $, 1-5	
	Demodulator $ Z_{i+1} - Z_i $, 1-5	
	Oscillator Frequency, 1-6	defines the input unit to be an oscillator frequency - note: the precision of the frequency is limited to 32-bit floating-point precision (~8 digits of precision) - e.g. at 500 Hz the resolution is approximately 20 mHz
Set point selection	Fixed	manual PID mode, sets a constant set point
	Toggle	toggle PID mode, toggles the PID set point between two fixed values at a rate of 1 Hz - this toggle mode is excellent to tune the dynamics of the controller settings
	Aux Input, 1-2	external PID mode, reads the set point from the auxiliary inputs
	PID N-1	cascaded PID mode, sets the set point of controller PID(n) using the output of the previous controller PID(n-1), more precisely PID(mod(n+2,4)+1))
Set	depends on input selection	defines a fixed set point - depending on the previous setting, there might be one, two, or no fields at all
Error range	-	displays the calculated PID error (Error = Set point - Input) as graphical range, it is possible to manually change the range by clicking onto the label inside ziControl
Auto button	[press once]	automatically changes the range of the graphical range display - does not change any PID setting
Error value	-	displays the calculated PID error (Error = Set point - Input) as numerical value

The three input units that make use of the arithmetic processor are explained hereafter. In fact the outputs of two consecutive demodulators are used to compute the unit that is used as PID controller input and subtracted from the set point. The available configurations serve as follows:

- Demodulator $|Z_{i+1}| - |Z_i|$: dual-frequency mode, and is equivalent to the notation $R_{i+1} - R_i$
- Demodulator $X_{i+1} - X_i$: Kelvin probe force microscopy
- Demodulator $|Z_{i+1} - Z_i|$: AM and FM sideband analyzer mode

When selecting one of these three input units, the selection range changes to a dual-digit notation, e.g. (1,2) meaning demodulator 1 and 2. Note the difference between the first and the third mode two PID inputs: The first subtracts the magnitude of demodulator $i+1$ from demodulator i , whereas the second subtracts the complex signals as a vector before calculating the magnitude of the result.

It is not the objective of this section to explain the purpose and application of these three input units. The Zurich Instruments blogs explain these modes instead.

Output:

Table 4.8. PID output settings

Control/Tool	Options/Range	Description
Output selection	Signal 1 amplitude, oscillator 1-8	defines the output unit of the PID controller to regulate the amplitude of Signal Output 1 - the related oscillator needs to be considered as well - for physical units, see Table 4.12
	Signal 2 amplitude, oscillator 1-8	defines the output unit of the PID controller to regulate the amplitude of Signal Output 2 - the related oscillator needs to be considered as well - for physical units, see Table 4.12
	Oscillator frequency, 1-6	defines the output unit of the PID controller to regulate the frequency of the defined oscillator - note: the precision of the frequency is limited to 32-bit floating-point precision (~8 digits of precision), e.g., at 500 Hz the resolution is approximately 20 mHz
	Aux output offset, 1-4	defines the output unit of the PID controller to regulate the offset setting of the defined auxiliary output
	DIO (digital 16 bit)	defines the output unit of the PID controller to regulate the DIO parallel port encode in int16 format
Default Out enable	OFF	the PID output is the last driven value when the PID controller is disabled
	ON	permits to define the default PID output when the PID controller is disabled
Default	$-\infty$ to $+\infty$	the default value that is written to the output if the PID controller is disabled and Default Out is enabled
Center	depends on input selection	adds an offset to the PID output
Range	0 to $+\infty$	defines the range of the PID output to prevent windup or to protect external hardware - the

Control/Tool	Options/Range	Description
		absolute limits of the output are [Center - Range, Center + Range] - the Range can be set to have negative output values - beware that in combination with some input units (e.g. negative values of R), this might have unexpected behavior
Output range	-	displays the PID output as graphical range - the calculated value is Output = Center + Shift, if the PID output reaches either the user-specified limits or the instruments hardware limits, the indicator will turn red
Output value	-	displays the PID output (Output = Center + Shift) as numerical value

Control:

Table 4.9. PID parameter settings

Control/Tool	Options/Range	Description
Filter Roll Off selection	1st: 6 dB/oct	sets the filter roll off (filter order) for the related demodulator
	2nd: 12 dB/oct	
	3rd: 18 dB/oct	
	4th: 24 dB/oct	
	5th: 30 dB/oct	
	6th: 36 dB/oct	
	7th: 42 dB/oct	
	8th: 48 dB/oct	
Filter Property Unit selection	BW 3 dB: 80 μ Hz (filter order = 8) to 200 kHz (filter order = 1)	defines the filter properties of the related demodulator displayed in either of these bandwidth or time constant units
	BW NEP: 90 μ Hz to 319 kHz	
	TC	
Harm	1, 2	sets the harmonic of the related demodulator
P (proportional gain)	$-\infty$ to $+\infty$	sets the proportional gain for the error signal $K_P = P$
I (integral gain)	$-\infty$ to $+\infty$	sets the integral gain for the error signal $K_I = I$
D (derivative gain)	$-\infty$ to $+\infty$	sets the derivative gain for the error signal $K_D = D$
TC mode control	OFF: PID mode	enables the determination of the gain parameters by the PID constants
	ON: TC mode	enables the determination of the gain parameters by the proportional gain $K_P = P$ and the time constants $T_I = P/I$ and $T_D = D/P$
Scale all slide bar	0.1 to 10	scales all gain parameters of the PID by a scalar factor between 0.1 and 10

Control/Tool	Options/Range	Description
Enable control	OFF	disables the PID controller - the input signals are tracked and the error signal is calculated, but no control loop is applied
	ON	enables the PID controller - the input signals are tracked, the error is calculated and the control loop is applied

DUT System Model:

All the settings in this section serve to define an efficient DUT model to be simulated together with the required bandwidth in order to determine optimal PID parameters. There is also a block diagram that shows the simulated units, and a frequency domain as well as a time domain analysis.

The block diagram serves to highlight the blocks that are simulated and displayed in the diagrams. This diagram actually depends on several settings in the PID tab, but also from settings in other tabs. The PID parameters are the result from the simulation and are read in the Control section. It is possible to modify the parameters and to immediately see the effects in the bode diagram and in the step response. The DUT model is defined with the settings from the table below. When a demodulator is selected as PID input, then the block diagram will display a low-pass filter which corresponds to the settings of the demodulator filters.

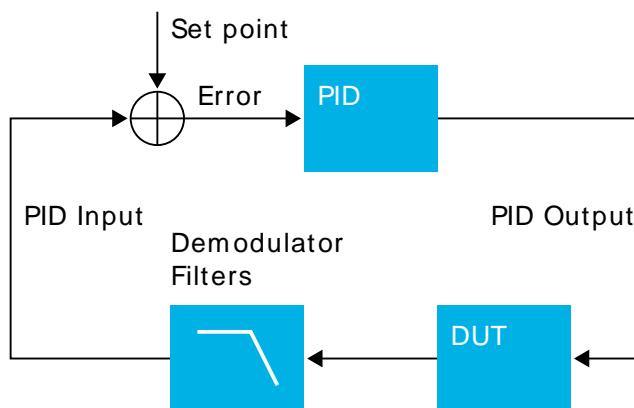


Figure 4.9. PID system diagram

Table 4.10. PID DUT model settings

Control/Tool	Options/Range	Description
DUT System Model selection	All Pass	models the DUT using an all pass filter, e.g., a feedback loop cable
	1st Order Low Pass	models the DUT using a first order low pass filter
	2nd Order Low Pass	models the DUT using a second order low pass filter
	AFM Cantilever Amplitude Control	models via AFM cantilever amplitude control
DUT System Model parameters	Gain (all models)	defines the gain applied in the plant models, positive gain parameters correspond to positive feedback, negative gain parameters correspond to negative feedback
	Negative Gain (all models)	inverts the gain defined in the Gain field (only applies to the resulting auto tune gains)

Control/Tool	Options/Range	Description
	BW (1st and 2nd order)	defines the characteristic bandwidth to use in the model [Hz]
	Damping (2nd order)	defines the damping used in the 2nd Order Low Pass model, with damping = $1/(2Q)$ and Q being the Q-factor - a critically damped system corresponds to damping of 1, or Q = 1/2
	Q (AFM cantilever)	defines the Q-factor used in the AFM Cantilever model
	F (AFM cantilever)	defines the frequency of the AFM cantilever used in the AFM cantilever model [Hz]
Integrator switch	OFF	does not use an additional integrator in the System (DUT) Model
	ON	uses an additional integrator in the System (DUT) Model - example use of this feature taking demodulator phase as PID input and driving oscillator frequency as PID output
Tune button	[press once]	calculates and applies optimized PID gain parameters for the PID controller
Undo button	[press once]	undoes the last Tune to the parameters used before pressing Tune

Table 4.11. PID system model simulation

Control/Tool	Options/Range	Description
Bode Plot tab	System (DUT)	plots the transfer function of the defined system DUT
	Demod Filter	plots the demodulator filter transfer function, when the PID input is selected to a demodulator output - this setting is grayed out when something else than a demodulator is selected
	System & Filter	plots the transfer function of the system DUT and the filter
	Open Loop	plots the open loop transfer function and makes an analysis of the gain margin (GM) and the phase margin (PM)
	Closed Loop	plots the closed loop transfer function and determines the effective system bandwidth and warns if there is a closed loop stability problem
Step Response tab	System (DUT)	plots the step response of the defined system DUT
	Closed Loop	plots the step response of the closed loop

Table 4.12. PID control units

Class	Signal	Unit
Input unit	Demodulator X value	[Input unit] _{RMS}
	Demodulator Y value	[Input unit] _{RMS}
	Demodulator R value	[Input unit] _{RMS}

Class	Signal	Unit
	Demodulator Θ value	°
	Auxiliary input	V
	Auxiliary output (used as input)	V
	Modulation index	In the range [0,1]
	Demodulator: $X_{n+1} - X_n$	[Input unit] _{RMS}
	Demodulator: $ \vec{z}_{n+1} - \vec{z}_n $	[Input unit] _{RMS}
	Oscillator frequency	Hz
Output unit	Signal output amplitude	V _{RMS}
	Oscillator frequency	Hz
	Auxiliary output	V
	DIO	TTL, encoded int16 format, referenced to least significant bit (LSB)
Control gain parameters	P	[Output unit] / [Input unit]
	I	[Output unit] / [Input unit] / s
	D	[Output unit] / [Input unit] * s
Control settings	Set point	[Input unit]
	Center	[Output unit]
	Range	[Output unit]
Calculated control parameters	Output	[Output unit]
	Error	[Input unit]

4.3. Other Settings

4.3.1. Real-time Settings Tab

Note

The real-time tab appears only when the HF2LI-RT / HF2IS-RT option has been purchased and activated. Customers can purchase the RT option at any time, whether when ordering their instrument or after delivery. This option can be activated by the user or by Zurich Instruments via remote servicing.

Features:

- Push button loading of real-time programs via the graphical user interface.
- Easy access to the embedded microprocessor's user registers to configure program parameters or view program output at run-time. Optional naming of user registers for easy identification, saving and opening of register names for persistent configuration across sessions.
- Displays real-time debugging information in the message log field box and/or allows the user to save the message log to file.
- Graphical CPU load indicator.

Description:

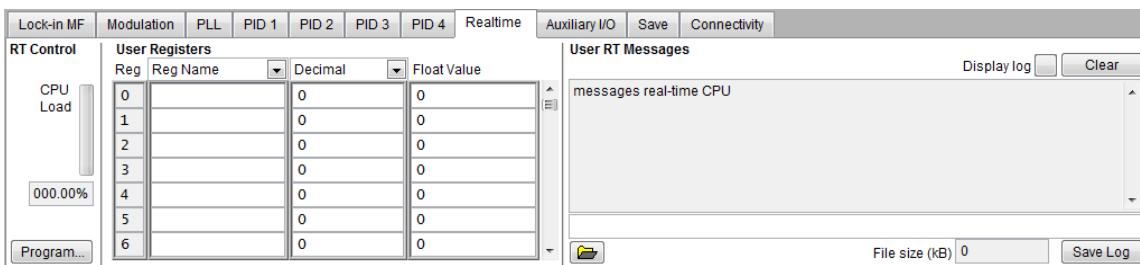


Figure 4.10. Real-time settings tab

Table 4.13. Real-time settings tab

Control/Tool	Options/Range	Description
CPU Load indicator	0% to 100%	indicates the momentary load of the embedded microprocessor running the real-time code
Program... push button	[press once]	selects a binary program to download to the HF2 Instrument; execution starts immediately thereafter
Reg Name pull-down menu	Reg Name	enters a name for the user register
	Save...	saves the user register names in an XML user register configuration file
	Open...	loads user register names saved in an XML user register configuration file
	Clear	clears entered user names. The values of the user register remain unaffected

Control/Tool	Options/Range	Description
Format pull-down menu	Decimal	selects the display format of the user register between decimal and hexadecimal
	Hexadecimal	
Float Value	IEEE754 32-bit floating-point number	view or edit a user register value as a 32-bit floating point number
Register fields 0-63	Register name & value	visualizes the processor register content - these fields are updated as the processor modifies the values - these fields are read/write: they permit to directly write values into the registers of the embedded processor. Each register can be optionally named for easy identification
User RT Messages	Read only	displays log messages printed via <code>zirTKPrintf()</code> from the real-time program running on the embedded processor.
Display log toggle button	OFF	update the message log field disabled
	ON	update the message log field enabled
Clear push button	[press once]	clears the message log field
Log file name edit box	File name	specifies a destination file to save real-time log messages
Folder push button	[press once]	opens a dialog to specify a destination file to save real-time log messages
File size (kB)	Read only	displays the current size of the saved log file
Save log push button	[press once]	saves the real-time log messages in the specified file

4.3.2. Auxiliary I/O Settings Tab

Features:

- Control for auxiliary output connectors
- Monitor of auxiliary input connectors
- Monitor and control of digital I/O connectors

Description:

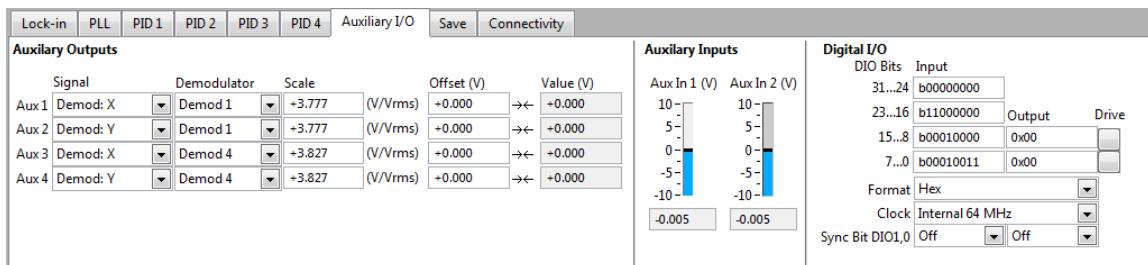


Figure 4.11. Auxiliary I/O settings tab

Table 4.14. Auxiliary I/O settings tab

Control/Tool	Options/Range	Description
Aux 1/2/3/4 output signal select	Manual (V)	set the signal that is provided on the Aux 1/2/3/4 connectors on the front panel of the instrument - the manual setting generates a DC value - the PLL: df selection is only available with the HF2LI-PLL option - the PID:Output selection is only available with the HF2LI-PID option
	X (V_{RMS})	
	Y (V_{RMS})	
	R (V_{RMS})	
	Theta (deg)	
	PLL 1: dF (Hz)	
	PLL 2: dF (Hz)	
	PID 1: Output (PID output unit)	
	PID 2: Output (PID output unit)	
	PID 3: Output (PID output unit)	
	PID 4: Output (PID output unit)	
Aux 1/2/3/4 output demodulator select	Demodulators	set the demodulator unit whose samples are being output to the related Aux output connector on the front panel of the instrument - note: this field is not available when the signal select is on manual or on PLL:dF or on PID:Output
Aux 1/2/3/4 output signal scale	-80 G to 80 G (V_{RMS}) for X, Y, R	set the scaling factor applied to the selection, consisting of a digital multiplication in real-time - the range of this field depends on the selected input range (see the following table) - note: this field is not available when the signal select is on manual
	-460 k to 460 k (V/deg) for Θ	
	-1.6 to 1.6 (V/Hz) for PLL:dF	
	-10^{37} to 10^{37} for PID:output	
Aux 1/2/3/4 output offset	-10 V to 10 V	set the DC offset value that is being added after the scaling of the selection
Aux 1/2/3/4 output value	[display]	instantaneous value at the corresponding auxiliary output connector
Aux In 1	-10 V to 10 V	indicates the current signal level on auxiliary input 1 (back panel)
Aux In 2	-10 V to 10 V	indicates the current signal level on auxiliary input 2 (back panel)
Digital I/O input bits 31...24	[input]	digital value in DIO input register bits 31 to 24
Digital I/O input bits 23...16	[input]	digital value in DIO input register bits 23 to 16
Digital I/O input bits 15...8	[input]	digital value in DIO input register bits 15 to 8
Digital I/O input bits 7...0	[input]	digital value in DIO input register bits 7 to 0

Control/Tool	Options/Range	Description
Digital I/O output bits 15...8	0x00 to 0xFF	digital value in DIO output register bits 15 to 8
Digital I/O output bits 7...0	0x00 to 0xFF	digital value in DIO output register bits 7 to 0
Digital I/O drive selector bits 15 to 8	OFF: no drive (input)	digital value at the DIO port bits 15 to 8 is not driven, high impedance, and the connectors can be used as inputs
	ON: drive (output)	digital value at the DIO port bits 15 to 8 is driven
Digital I/O drive selector bits 7 to 0	OFF: no drive (input)	digital value at the DIO port bits 7 to 0 is not driven, high impedance, and the connectors can be used as inputs
	ON: drive (output)	digital value at the DIO port bits 7 to 0 is driven
Digital I/O Hex/Bin	Hex	change between hexadecimal and binary representation
	Binary	
Digital I/O Clock	Internal 64 MHz	DIO input register is latched by internal 64 MHz clock
	Clk Pin 68	DIO input register is latched by signal on Pin 68 of the DIO port
Sync Bit DIO1,0	Off	DIO outputs 1 and 0 (BNC connectors) are not used for sync output and are free for other purposes
	Demod 1 to 8	reference signal of the selected demodulator is output on DIO 1 and 0 - note: there is a 166 ns delay between the sync and the front panel outputs (sync comes first) which leads to a relevant phase shift at high frequencies. To get an in phase sync, please make use of the WAVEFORM node setting and one of the HF front panel outputs

Table 4.15. Auxiliary output ranges

Input range setting	Scaling range for R (V/V _{RMS})	Scaling range for X and Y (V/V _{RMS})	Desired input full scale sensitivity (10 V full range output)
1 mV	80 G	75 G	1 nV to 1.5 V
10 mV	8 G	7.5 G	10 nV to 1.5 V
100 mV	760 M	750 M	100 nV to 1.5 V
1 V	83 M	71 M	1 μ V to 1.5 V
2.1 V	42 M	35 M	2 μ V to 1.5 V

4.3.3. Save Settings Tab

Features:

- Save of demodulated samples to host computer disk drive
- Load and save of instrument configurations

- File conversion utilities

Description:

The HF2 Instrument contains a default configuration for all settings when delivered to the customer. This default configuration is loaded every time the HF2 Instrument is powered up. It provides an initial safe state of the instrument so that externally connected setups are not damaged nor impacted. This default configuration cannot be modified by the user.

However, ziControl provides the capability to load and save user specific configurations by means of this tab. As the configuration files are saved on the host computer, the user benefits from an unlimited number of possible configurations that can be stored.

The configuration files are in text format, thus they can be conveniently edited by the user. The syntax is self-explanatory and is the same as described for the text interface in [Chapter Programming](#).

Saving demodulator samples to the hard disk can be done in text or binary format. When selecting text format, the data are stored as columns (without header) in a comma-separated values file. The contents of the columns are specified in the table "Save Field Information" below. Binary saving is faster and allows saving at higher sample rates, and minimizes CPU usage.

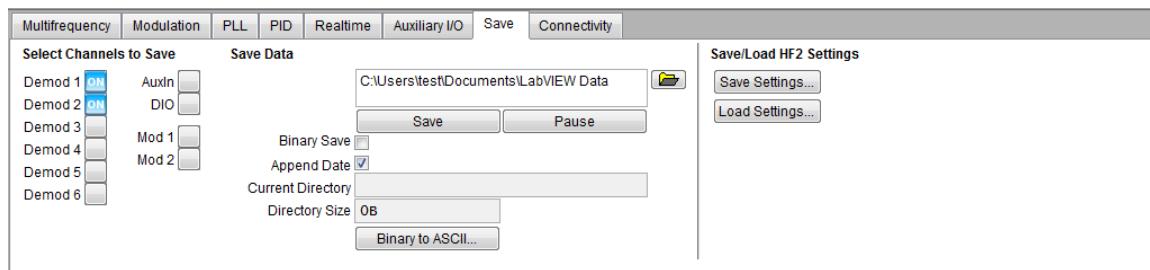


Figure 4.12. Save settings tab

Table 4.16. Save settings tab

Control/Tool	Options/Range	Description
Select channels to save	select Demodulators	set the demodulator outputs that are saved in CSV format to the specified directory on the host computer - one file is generated for each selected demodulator - refer to the next table to interpret the information regarding the data included in the files
	select Auxin	save data from the Auxiliary Inputs 1 and 2 to a separate file, at a maximum sampling rate of 400 kSa/s - note: the Auxiliary Inputs samples are also saved in the demodulator files at the readout rate of the demodulator - setting this switch generates large files
	select DIO	save data from the DIO port to a separate file, at a maximum sampling rate of 125 kSa/s - note: the DIO samples are also saved in the demodulator files at the readout rate of the demodulator - setting this switch may generate large files
	select Mod 1	save data from the AM/FM modulator 1 (requires related option to be activated)

Control/Tool	Options/Range	Description
	select Mod 2	save data from the AM/FM modulator 2 (requires related option to be activated)
Save directory	absolute path to the data save directory	set the trunk name for the save directory - note: you may browse to the target directory using the small icon at the top right
Binary save	OFF: ASCII save	-
	ON: binary save	
Append date to directory switch	OFF: no date	define whether a time-stamp is appended to the save directory name - this option allows to keep the same save path (trunk name) constant and to generate a unique directory name whenever a new save is started
	ON: append date	
Save button	OFF: stop the save	stop the save
	ON: start the save	start the save of demodulated samples to the directory defined in the save path field - pressing this button generates 1 file for each active demodulator
Pause button	OFF: no pause	pause the save in progress of demodulated data - this feature is useful to append several measurement sequences into the same files, thus reducing the number of generated files and directories - note: this button has no impact on the activity of the demodulators (measurement in progress)
	ON: save is paused	
Current directory	-	indicates the current save directory (useful in combination with the append date switch)
Directory size	-	size of the current save directory - this value is the sum of all saved files in a directory and is updated during a save in progress
Binary to ASCII button	-	indicates that the ziControl is not able to save all data to the specified directories, and that some data were discarded
Save Settings button	-	save current settings of the HF2
Load Settings button	-	load settings into the HF2

Table 4.17. Save field information

File	Data	Description
FreqX	Timestamp [s]	the time-stamp counter is a value in seconds that is initiated with power-up of the HF2 Instrument and continues to increment as long as it is powered - the expiration of the time-stamp is in the order of years
	Demodulator X output [V _{RMS}]	the X output of the related demodulator at the set output rate
	Demodulator Y output [V _{RMS}]	the Y output of the related demodulator at the set output rate
	Frequency [Hz]	the frequency of the demodulator

File	Data	Description
	DIO [decimal unsigned integer]	the value of digital I/O port
	Auxiliary Input 1 [V]	the voltage at the auxiliary input 1
	Auxiliary Input 2 [V]	the voltage at the auxiliary input 2

Note

As the time-stamp information is consistent between all demodulators with the same readout rate, the user can merge and compare data from the various files.

4.3.4. Connectivity Settings Tab

Features:

- HF2 Instrument connectivity on local network - ziServer connectivity
- Selection of active instrument (in case more than one instrument is connected to the selected ziServer)
- Overview of installed product options and activation of new options
- Software and hardware revision information

Description:

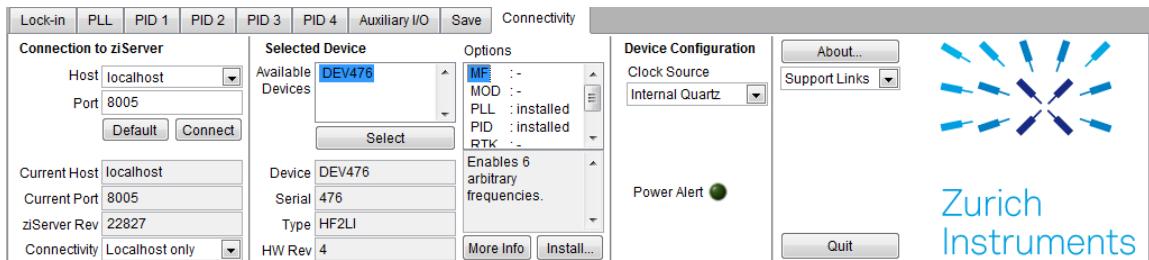


Figure 4.13. Connectivity settings tab

Table 4.18. Connectivity settings tab

Control/Tool	Options/Range	Description
Host	default: localhost, to be used when ziServer is running on the same host as ziControl	sets the host IP address where ziServer is running, and to which ziControl must connect in order to access the relevant HF2 Instrument
	range: any string recognized by your DNS	
Port	default: 8005	sets the port where ziServer is listening - the port number is given for a running server, and may be changed in the configuration files of ziServer
	range: 0 to 65535	
Default button	-	press this button to enter the default settings localhost:8005

Control/Tool	Options/Range	Description
Connect button	-	press this button to accept the entered host name and port
Current Host	-	information field regarding the connected host
Current Port	port number	information field regarding the connection port
ziServer Rev	revision number	information field regarding the revision of the connected server
Connectivity	Localhost only	the connected ziServer only accepts connectivity to the local host - remote clients are not able to access the local HF2 Instruments
	From everywhere	the connected ziServer accepts connectivity from any host on the LAN - all remote clients are able to access the local HF2 Instruments
Available Devices	select	this list shows all available HF2 Instruments on the selected server
Select button	-	press this button to select one of the available devices
Device	text	information field about the selected device
Serial	number	information field about the selected device
Type	text	HF2 Instrument type
HW Rev	text	hardware revision of the selected device
Installed options	MF	multi-frequency option is installed
	MOD	modulation option is installed
	PLL	phase-locked loop option is installed
	PID	PID option is installed
	RT	real-time option is installed
	UHS	ultra-high stability option is installed
More Info button	press	opens the product page on the Zurich Instruments website
Install options button	press	prompts a window that permits to enter a feature code to install additional features
Clock Source selector	Internal Quartz	selects the internal oscillator as clock reference
	Clock In 10 MHz	selects the external source for clock reference (external rubidium standard reference or atomic clock)
Power Alert	-	indicates that the supply voltage is too low (e.g. 100 V supply system) and therefore the instrument might provide incorrect measurements
About button	-	provides the information regarding the current version of ziControl
Support Links selector	User Manual	opens the installed version of the HF2 User Manual (this document)
	ZI Blogs	opens a browser window with access to the blog section of the Zurich Instruments website for a lot of online content

Control/Tool	Options/Range	Description
	ZI Support	opens a browser window with access to the support section of the Zurich Instruments website for support information
	ZI Software Updates	opens a browser window with access to the software update information of the Zurich Instruments website
	TeamViewer Download Win	permits to download the TeamViewer software for support in remote servicing (works also behind most firewalls, no need for software installation nor administrator rights)
Quit button	-	closes the ziControl application

4.3.5. Active Probes Settings Tab

The Active Probes settings tab adapts its content to the pre-amplifier connected to the HF2 Instrument. Whenever an active probe is connected with an RJ45 cable, this is automatically detected and a related screen appears on the ziControl tab. When the active probe is disconnected from the HF2, the panel on the tab disappears.

All active probes from Zurich Instruments are fully integrated inside ziControl.

HF2CA Current Amplifier

Features:

- Input impedance range from 10 V/A to 1 M V/A (R1, R2)
- Input mode differential or single-ended (Diff, Single)
- Input signal coupling mode (AC, DC)
- Output stage gain (G=1 or G=10)

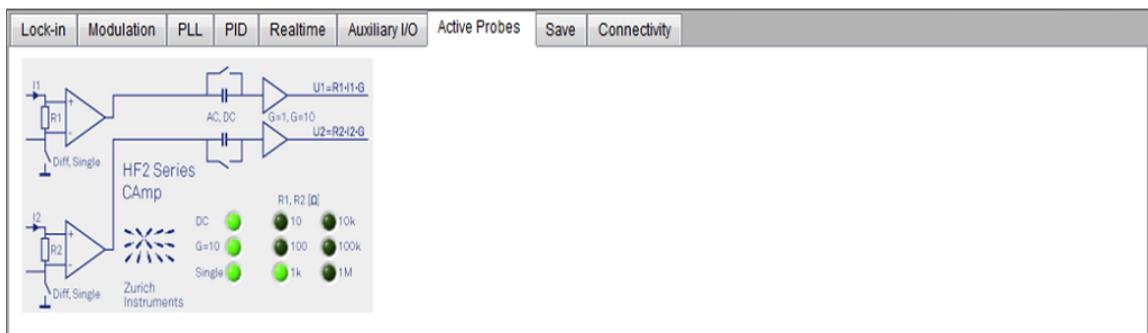


Figure 4.14. Active probes HF2CA tab

Additional HF2CA specification can be found in the [HF2CA Current Amplifier Data sheet](#).

HF2TA Current Amplifier

Features:

- Input offset +/- 10 V
- Transimpedance gain from 100 V/A to 100 M V/A (R1, R2)

- ─ Input signal coupling mode (AC, DC)
- ─ Addition gain (1, 10)
- ─ Total gain display ($R1 \times G$, $R2 \times G$)
- ─ Input Shield (GND, EXT Bias)
- ─ Auxiliary output +/- 10 V



Figure 4.15. Active probes HF2TA tab

Additional HF2TA specification can be found in the [HF2TA Current Amplifier Data sheet](#).

4.4. Tools Tabs

4.4.1. Numerical Tool

Note

The number of available demodulators depends on the purchased instrument and activated options, and whether the features are activated in the related settings tabs. Polar, Cartesian, and Noise tools are available to all users. The AM/FM measurements only if the HF2LI-MOD option has been purchased and is activated in the related settings tabs

Features:

- Real-time demodulator output analysis
- Graphical and numerical range indicators
- Polar and Cartesian formats
- AM/FM modulation information (optional feature)
- Impedance measurement (optional feature)
- Support for arbitrary input unit function

Description:

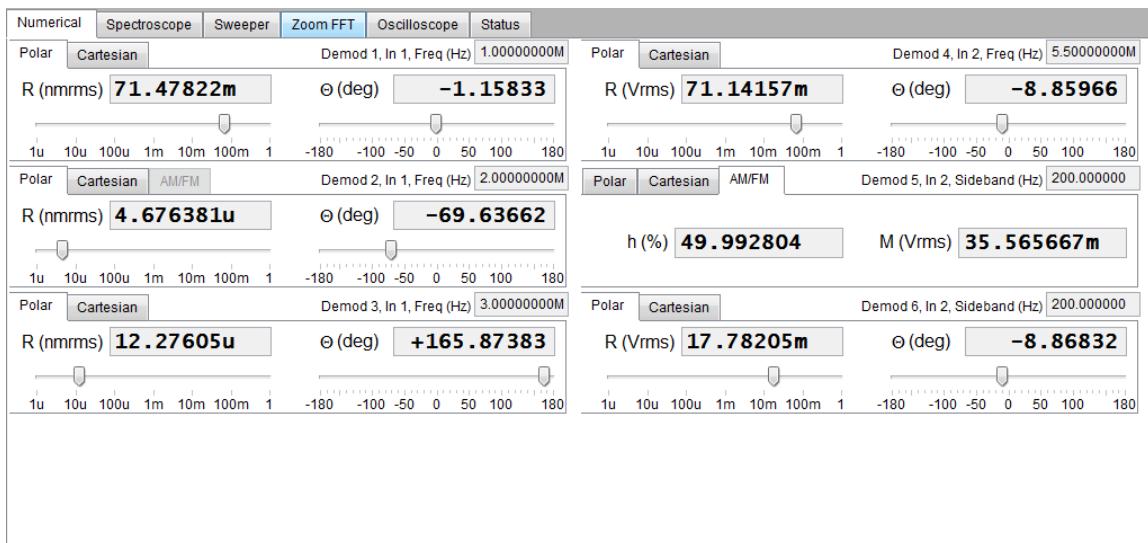


Figure 4.16. Numerical tool

Table 4.19. Polar and Cartesian tools

Control/Tool	Options/Range	Description
DEMOD 1 polar	-	press this tab to see polar (R, Θ) sample representation for the demodulator - the amplitude R is indicated in V_{RMS} while the angle Θ in degrees
DEMOD 1 Cartesian	-	press this tab to see Cartesian (X, Y) sample representation for the demodulator - the components X and Y are indicated in V_{RMS}

Control/Tool	Options/Range	Description
DEMOD 1 AM/FM	-	press this tab to quickly measure the modulation index and the modulation
DEMOD 1 frequency field	-	this field shows the current reference frequency of the demodulator
DEMOD 1 X, Y, R, Θ fields	-	these fields indicate the value of the data samples as output by the demodulator filters - the update rate of these fields is equivalent to the readout rate setting - the outputs of the demodulators are in 64-bit resolution, thus much higher than represented on the screen - to profit from the full sample resolution, please save the samples to a file (save settings tab)
All other demodulators have the same functionality as DEMOD 1		

Note

The following AM and FM tools descriptions are only valid for HF2LI models with HF2LI-MOD option activated.

The AM and FM tab is available in the tabs of demodulator 2 and demodulator 5 when the respective modulators are enabled in the modulations settings tab. The tab in demodulator 2 is for Mod 1, and the tab for demodulator 5 is for Mod 2.

Table 4.20. AM and FM tools

Control/Tool	Options/Range	Description
Modulation index (h %)	Amplitude modulation (AM)	this field shows the measured modulation index for amplitude modulation $h_{AM} = A_m / A_c$ (modulation amplitude divided by carrier amplitude)
	Frequency modulation (FM)	this field shows the measured modulation index for frequency modulation $h_{FM} = f_p / f_m$ (peak frequency divided by modulation frequency)
Modulation amplitude (V_{RMS})	Amplitude and frequency modulation	this field shows the accumulated amplitudes of the first sidebands (which are being demodulated)

4.4.2. Spectroscopic Tool

Features:

- Real-time demodulator monitoring over time in graphical format
- Independent scale setting for each demodulator measured amplitude and phase angle, auto-scale
- Polar and Cartesian data format
- 2 cursors for data analysis
- Support for arbitrary input unit function

Description:

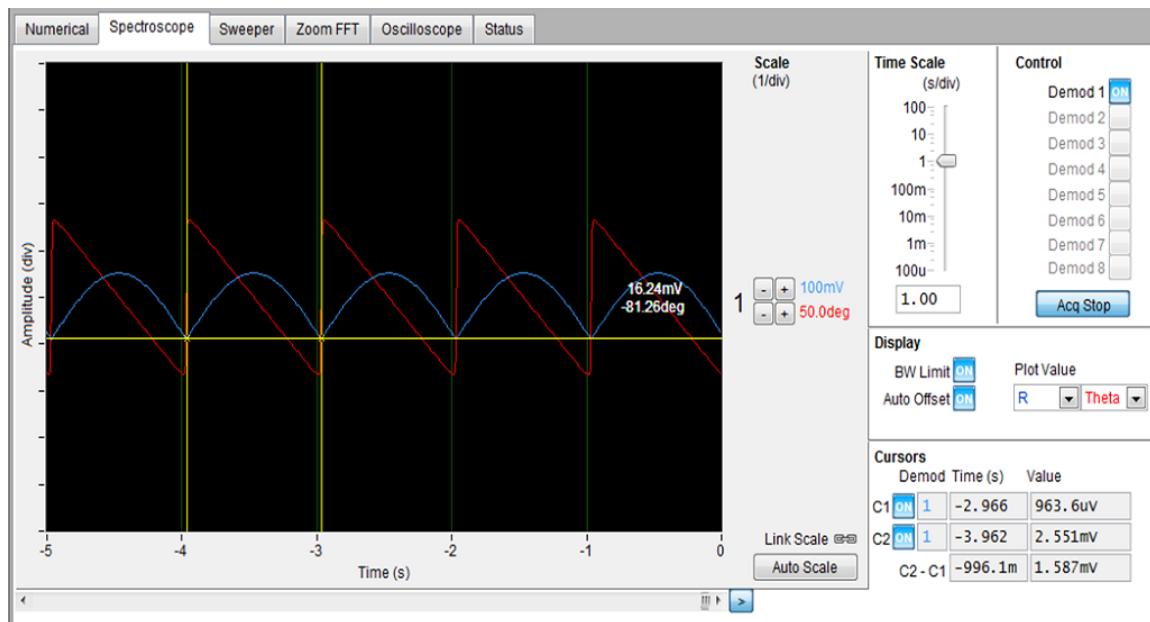


Figure 4.17. Spectroscopic tool

Table 4.21. Spectroscopic tool

Control/Tool	Options/Range	Description
Main spectroscope	-	shows a sample versus time plot - the last samples are plotted on the right, while older samples shift towards the left side - note that the horizontal axis always has 5 divisions, while the vertical axis has 10 divisions
Spectroscope time pan	[move slider]	move forward and backwards in time within the stored data
Spectroscope origin location (> button)	[press once]	forces the scope to stay right - the 0 seconds time always is fixed on the right hand side
Scale for X, Y, R, Θ display	V/DIV or degree/DIV	defines the voltage or degree per division that is displayed on vertical axis of the spectroscope screen for X/Y/R and Θ , respectively. The scale per division can be changed with no upper or lower bounds. Note: to change this value just press the +/- buttons or use the Auto Scale button
Link scale	[press once]	allows the manual vertical scaling to be done simultaneously for all demodulator plots
Auto scale button	[press once]	press once to adjust the vertical scale automatically
Scaling slider for time axis	100 μ s/DIV to 100 s/DIV	defines the time per division that is displayed on the horizontal axis - in order to change the value move the slider, or directly enter a value in the box close to the slider, or press cursor up/down when the focus is inside that box
Demodulator controls	OFF: demodulator output not plotted	define whether the demodulator outputs are plotted in the spectroscope window - the number

Control/Tool	Options/Range	Description
	ON: demodulator output plotted	of plotted curves changes the location of the individual curves - only the demodulators that are turned on in the settings tabs can be selected here (the others are grayed out)
Acquisition stop button	OFF: spectroscope plotting ongoing	activates the plotting of the selected frequencies on the spectroscope window- this button stops and starts the plot of all frequencies at the same time
	ON: spectroscope plotting stopped	
BW limiter button	OFF: limiter off	inserts a low-pass filter which reduces the noise from higher-frequency by averaging
	ON: limiter on	
Auto offset button	OFF: keep offset	signals are plotted with constant offset
	ON: auto / no offset	signals are plotted with no offset
Plot Value	X, Y, R, Θ & Freq	select values to be plotted from two drop-down lists
Cursor controls	OFF: remove cursor	press to add a cursor - the cursor can be moved with the left mouse button
	ON: add cursor	
Cursor indicators	-	indicates current cursor values

4.4.3. Sweeper Tool

Features:

- Full-featured parametric sweep tool
- Full HF2 Instrument frequency range supported with many sweep modes: single, continuous (run/stop), forward, backward, bi-directional
- Overlap display of previous sweep results with persistent display
- Normalization of sweep with calibration control
- Auto bandwidth, averaging, and display normalization
- Fundamental and harmonic sweep support
- Parametric sweeper: Frequency, Phase, Time constant, Output amplitude, Offset (Aux Out)
- Support for arbitrary input unit function

Description:

The sweeper tool uses an user-selected reference signal as the excitation voltage and measures the frequency and phase response with an user-selected demodulator. This flexibility in demodulator selection permits to drive the sample-under-test with one frequency and to perform the measurement at either the fundamental or the harmonic of the driven frequency. The selected demodulators for reference and signal input may not be used for other measurements during the sweep.

In addition to the frequency sweep, the sweeper tool can also be used to obtain response from parametric sweeps such as phase, time constant, output amplitude as well as offset from the auxiliary outputs.

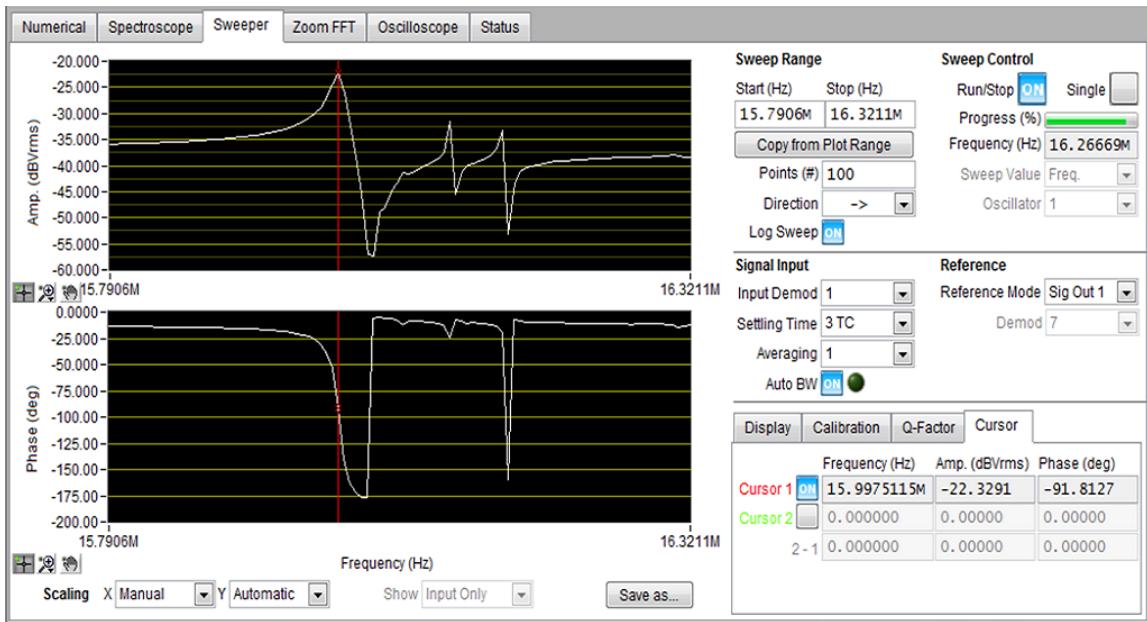


Figure 4.18. Sweeper tool

Table 4.22. Sweeper tool

Control/Tool	Options/Range	Description
Sweeper plot window	-	depicts the frequency response of the signal at signal input 1 or 2 - the measurement is made by placing a band-pass filter in discrete steps over the user-defined frequency range and simultaneously driving a sinusoidal signal at signal output 1 or 2 at the corresponding frequency
Sweeper Amp. label	User defined	default value is in Volt (V) - user defined unit can be enter in the Scale field under the Signal Inputs under the main lock-in control tab
Sweeper scale controls	Automatic	automatically fits the vertical and/or the horizontal scale to data (Automatic)
	Manual	scale is defined by the user
Show selection	Input Only Input / Ref Impedance Ref Only	plots the measured input signal only plots the input/ref quotient plots various equivalent impedance model (ghosted out for HF2LI; only available for HF2IS) plots the reference signal only
Sweep range Start	1 μ Hz to 50 MHz -90 to 90 deg 100 ps to 600 s -1 V to +1 V -10 V to +10 V	defines the starting point of the freq. sweep defines the starting point of the phase sweep defines the starting point of the TC sweep defines the starting point of the amplitude sweep defines the starting point of the aux offset sweep
Sweep range Stop	1 μ Hz to 100 MHz -90 deg to 90 deg 100 ps to 600 s	defines the end point of the freq. sweep defines the end point of the phase sweep defines the end point of the TC sweep

Control/Tool	Options/Range	Description
	-1 V to +1 V	defines the end point of the amplitude sweep
	-10 V to +10 V	defines the end point of the aux offset sweep
Copy from Plot Range button	[press once]	after zooming into the plot, press once to adopt the start and stop frequencies from the plot
Sweep range Points	2 to 1000000	number of frequency steps (point) at which the sweeper performs its measurements - high values require long times to finish one sweep
Sweep range Direction	<->	bi-directional sweep (low-to-high and high-to-low values)
	->	sweep from low to high values
	<-	sweep from high to low values
Log Sweep button	OFF: linear sweep	samples are equidistant - plot scale is linear
	ON: logarithmic sweep	samples are logarithmic distributed - plot scale is logarithmic
Sweep control Run/Stop button	OFF: stop	stop running continuous sweep
	ON: run	start continuous sweeping at the current frequency - after attaining the stop frequency, the sweeper restarts from the frequency depending on the sweep direction
Sweep control Single sweep button	OFF: stop	stop single sweep
	ON: start	start sweeping at the start frequency - after attaining the stop frequency, the sweeper stops
Sweep control Progress indicator	-	indicates the progress of the sweep
Sweep current value	-	indicates the current value during a sweep - the label changes depending on the type of parametric sweep being performed
Sweep value selection	Frequency	frequency sweep - oscillator selection 1 to 8 available
	Phase	phase sweep - output demodulator selection 1 to 6 available
	TC	time-constant sweep - output demodulator selection 1 to 6 available
	Amplitude	amplitude sweep - output demodulator selection 1 to 8 and signal output 1 to 2 available
	Aux Offset	offset sweep (Aux Out) - auxiliary output selection 1 to 4 available
	PID Setpoint	PID setpoint sweep - PID setpoint 1 to 4 selection available
Signal Input demodulator selection	1 to 6	sweeper uses the selected demodulator using its input choice, its oscillator choice, and its filter settings to perform the measurements of the sweep
Filter Settling time selection	5 TC eff	defines the sweeping time by filter settling time - for large bandwidths, the sweeping time is restricted by the software
	15 TC eff	
	30 TC eff	

Control/Tool	Options/Range	Description
Filter Averaging selection	1	defines the number of measurements taken at each point - the final value is calculated by averaging all measurements for the specific point
	2	
	4	
	8	
	16	
	32	
	64	
Filter Auto bandwidth control (active for frequency sweep)	OFF	manual bandwidth: the noise equivalent bandwidth (NEB) of the filter is defined by the settings of the chosen demodulator
	ON	auto bandwidth: the noise equivalent bandwidth is adjusted during the sweep to capture a larger part of the spectrum for logarithmic sweeps - this option has no effect for linear sweeps
Reference Mode selection	Off	reference mode is off
	Sig Out 1	sweeper uses signal output 1 as reference
	Sig Out 2	sweeper uses signal output 2 as reference
	Demod	sweeper uses one of the selected demodulators as reference
Reference Mode Demodulator selection	1 to 8	demodulator selection for reference mode (not available when Reference Mode is Off)
Display Coordinate system selection	Polar	selects the coordinate systems of the plot
	Polar (Log)	
	Cartesian	
	Nyquist	
Display Mapping selection	Linear	the Y scale of the plot is displayed with a linear unit
	dB	the Y scale of the plot is displayed with a logarithmic unit, being the ratio of the physical quantity (power and intensity) relative to a $1 \text{ V}_{\text{RMS}} / \text{V}_{\text{PK}}$ reference level
	dBm	the Y scale of the plot is displayed with the logarithmic unit, being the power ratio referenced to 1 mW (dissipation resistor of 50 Ohm)
Display Result Unit selection (available for Show: Input Only and Show: Ref Only)	V_{RMS}	the Y scale of the plot reads the signal RMS voltage
	V_{PK}	the Y scale of the plot reads the signal peak voltage, $\text{V}_{\text{PK}} = \sqrt{2} * \text{V}_{\text{RMS}}$
	V_{RMS}^2	the Y scale of the plot reads the signal RMS power (squared RMS voltage)
	V_{PK}^2	the Y scale of the plot reads the signal peak power (squared peak voltage), $\text{V}_{\text{PK}}^2 = 2 * \text{V}_{\text{RMS}}^2$
	$\text{V}_{\text{RMS}}/\sqrt{\text{Hz}}$	the Y scale of the plot reads the spectral voltage density, independent of the frequency resolution

Control/Tool	Options/Range	Description
		- this setting is useful for noise measurements, $V_{RMS}/\sqrt{Hz} = V_{RMS} / \sqrt{(\text{frequency resolution})}$
	V_{PK}/\sqrt{Hz}	the Y scale of the plot reads the spectral peak density, independent of the frequency resolution - this setting is useful for noise measurements, $V_{PK}/\sqrt{Hz} = V_{PK} / \sqrt{(\text{frequency resolution})}$
	V_{RMS}^2/Hz	the Y scale of the plot reads the spectral RMS power density, independent of the frequency resolution - this setting is useful for noise measurements, $V_{RMS}^2/Hz = V_{RMS} / (\text{frequency resolution})$
	V_{PK}^2/Hz	the Y scale of the plot reads the spectral peak power density, independent of the frequency resolution - this setting is useful for noise measurements, $V_{PK}^2/Hz = V_{PK} / (\text{frequency resolution})$
Display Unwrap Phase switch	OFF / ON	enables the unwrapping of the phase in polar coordinate system
Equivalent Model selection (only visible when Show Impedance is selected in HF2IS)	$ Z $, Phi (ohm)	display sweep result as absolute impedance and phase
	R, X (ohm)	display sweep result as real and imaginary impedance
	R//C	display sweep result as equivalent RC parallel circuit
	R, C	display sweep result as equivalent RC series circuit
	R, L	display sweep result as equivalent RL series circuit
	G, B	display sweep result as real and imaginary admittance
Persistent Display	Off	previous sweep tracking turned off
	Auto	allows to observe the differences between different plots
	Manual	allows to observe the differences between different plots and to memorize a plot of choice
Persistent Display Depth	1 to 100	number of traced sweeps (Note: each sweep will be displayed in a different color)
Persistent Display Persist	[press once]	memorizes current sweep (only active with History: Manual option)
Persistent Display Reset	[press once]	resets the history depth
Calibration Copy to Calib	[press once]	copies current sweep as the calibration reference
Calibration Control Amplitude button	[press once]	amplitude values will be calibrated
Calibration Control Phase button	[press once]	phase values will be calibrated

Control/Tool	Options/Range	Description
Calibration Save as... button	[press once]	saves the calibration setup to a file (XML format)
Calibration Load button	[press once]	loads the calibration setup from a file
Calibration selection	[drop-down menu]	allows to fast access the latest saved calibration setups
Q Factor	-	displays the estimated resonance Q from the frequency sweep
Save as... button	[press once]	generates a directory with the depicted sweeper plot as CSV and PNG files - a Readme.txt file including the description of the columns is saved along with the data - the user has the opportunity to define a directory name where the data is stored

Table 4.23. Sweeper tool plot scaling and cursor options

Control/Tool	Options/Range	Description
Plot X and Y scaling	Automatic	the X scale of the plot is calculated based on the FFT span, and Y scale of the plot is continuously adapted to the current demodulated samples
	Manual	the X scale and Y scale of the plot can be manually set - with manual setting, the cursors and zoom palette become available - the manual setting of the Y scaling can also be used to avoid the continuous changes in the scale limits
Plot cursor palette	[press once]	control permits to grab and move cursor point to the selected position
Plot zoom palette	[press once]	control permits to select one of 6 zoom modes including zoom in, zoom out, zoom X only, zoom Y only, and others
Cursor point control	2 to 1000000	defines the sample which the cursor is tracking
Cursor indicators	-	indicates current cursor values for X and Y scales
Cursor 2-1	-	indicates the difference between cursor 1 and 2

4.4.4. Zoom FFT Tool

Features:

- Fast, high-resolution FFT spectrum analyzer of demodulated data
- Variable center frequency, frequency resolution and frequency span
- Auto bandwidth, auto span (sampling rate)
- Choice of 4 different FFT window functions
- Continuous and block wise acquisition with different types of averaging
- Detailed noise power analysis
- Support for arbitrary input unit function

Description:

The Zoom FFT tool allows real-time FFT spectrum analysis on the complex samples ($X+jY$) output on each of the 6 demodulators. The Span of the calculated spectrum is equal to the Sampling rate (Hz) of the input Demodulator with a maximum value of 461 kHz. The resolution of the FFT spectra is determined by the duration recorded for each block of data, which is given by the ratio of the Span (Hz) and the number of Samples (#) recorded. With a maximum number of Samples (#) per block of 32.768 this leads for the largest span chosen still to a resolution of 14 Hz. When the frequency span is reduced to 112 Hz spectral features as close as 3.4 mHz can be resolved provided that the experimental setup is sufficiently stable over the recording time. Prior to calculating the complex FFT of the demodulator samples, one of the four different available window functions is applied to either optimize on dynamic range or resolution. The recommended default setting is using the Hann window which in general is considered to have a good trade off between both.

Technically, the center frequency of the FFT spectrum is always at zero when referred to the demodulator. However, taking the demodulator's reference oscillator into account the spectrum displayed can also be referred to the input signal by shifting it by the reference frequency, in the Zoom FFT tab indicated as Center frequency (Hz) of the FFT spectrum. Sometimes it is useful to scale the frequency axis accordingly by activating the Absolute freq button. Different spectral components on the input signal can then be attributed more easily to experiment parameters.

A basic signal analysis is performed where the signal at the center of the spectrum is determined and indicated as red curve in contrast to the noise and other spectral components drawn in white. For the Signal part of the spectrum a Noise Power Analysis is performed with all relevant values displayed in the lower right corner.

It is important to stress that the output of the demodulators are subject to low pass filtering and hence the always present noise floor will be naturally suppressed by this filter's frequency dependent transfer function. This behavior is indicated by the blue curve and labeled Fitted Noise. It resembles the calculated filter transfer function where the amplitude offset is fit with respect to the measured data. A very helpful feature is the Filter compensation which works by dividing the measurement data simply by the filter transfer function. This leads to a flat spectrum and allows for direct comparison of different amplitudes at various frequencies over the whole frequency span. Great care needs to be taken when activating the Filter Compensation that the SNR at the wings of the displayed spectrum is still sufficient. When using the auto setting button for the input sample rate one can be sure to be on the safe side here. This automatic algorithm targets to set the input sample rate to achieve a defined aliasing rejection rate of about 40 dB. This resembles the difference in dB between the filter transfer function in the center of the spectrum as compared to the very edge and can be simply measured from the highest point of the blue curve to the lowest points on the right and left borders of the FFT span while the Filter compensation is disabled. The FFT Advisor windows displays comments when settings chosen that lead to measurement artifact and misinterpretation of the data shown.

The noise panel provides useful information regarding the noise in the acquired demodulated signal. The noise tool inside ziControl always applies to demodulated samples. Consequently the noise figures depend on the bandwidth and the readout rate that is defined in the settings tabs. Measurements with large bandwidth collect more noise than measurements with small bandwidth.

Noise: $N = 1/n * \Sigma (x_{AVG} - x)$

SNR: $SNR = 10 \times \log (R_{RMS} / N_{RMS})$

Averaging can be enabled to reduce the fluctuations in the display, but do not impact the noise measurement accuracy.

The noise spectral densities are calculated using the noise equivalent power bandwidth (NEPBW). The NEPBW is the effective bandwidth considering the area below the transfer function of a low-

pass filter in the complete frequency spectrum. The NEPBW is actually larger than the typical 3 dB signal bandwidth.

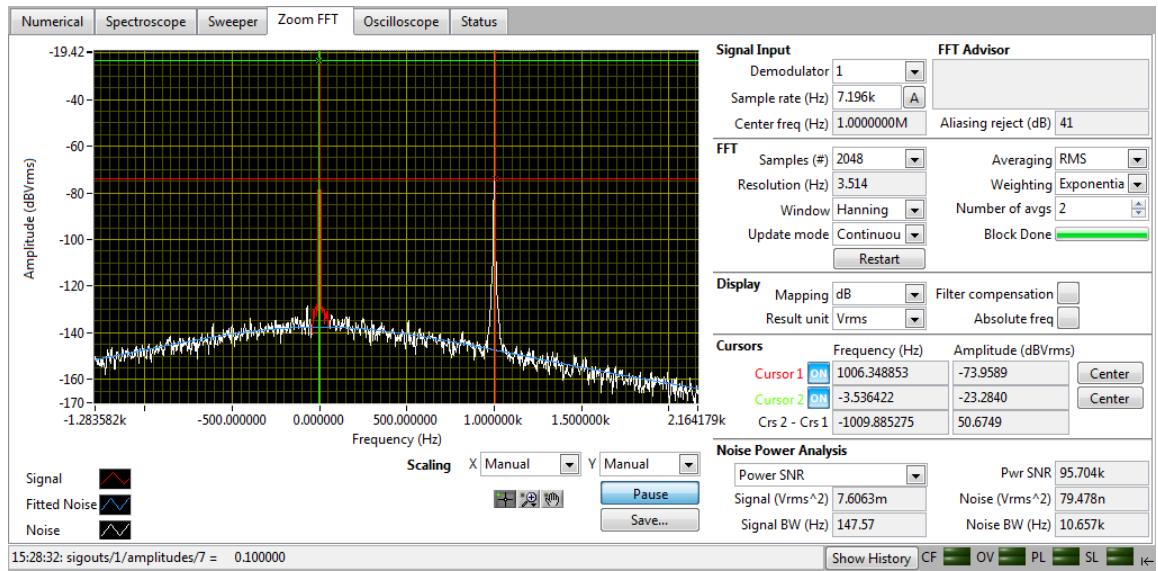


Figure 4.19. Zoom FFT tool

Table 4.24. Zoom FFT tool

Control/Tool	Options/Range	Description
FFT plot red line	Signal	the red plot line is the automatically determined signal component of the spectrum
FFT plot blue line	Fitted Noise	the blue plot line represents the filter transfer function of the selected demodulator fitted to the measured noise
FFT plot white line	Noise	the white plot line is the automatically determined noise component of the spectrum
Demodulator	1 to 6	determines the demodulator output that is taken for FFT spectrum analysis
Center frequency	-	indicates the center frequency of the selected demodulator - it is also the center frequency of the FFT span
FFT Advisor	-	provides information how to improve the quality of the spectrum plot and warnings
FFT Samples selection	256 512 1024 2048 4096 8192 16384 32768	defines the number of samples in one Block associated to the FFT span, and therefore also the frequency resolution - the number of selected samples together with the input sample rate determines the acquisition time for the FFT plot, $T_{ACQ} = \text{FFT_samples} / \text{signal_sample_rate}$
FFT Span (Hz)	0.22 Hz to 460 kHz	defines the demodulator output sample rate, equivalent to the FFT span

Control/Tool	Options/Range	Description
A = auto span	[press once]	performs an auto setting of the FFT span equivalent to the demodulator readout rate according to the settings of the demodulator filters (targets an aliasing rejection of 40 dB)
Resolution (Hz)	-	equivalent to the signal sample rate divided by the number of samples
FFT Window selection	Hann (default)	selection of different window functions for best signal representation
	Rectangle	
	Hamming	
	Blackman-Harris	
FFT Update Mode selection	Continuous	the FFT spectrum is updated approximately every 100 ms
	Blocks	a new FFT is calculated after a new block of data is retrieved - when this mode is selected, a Block Done indicator will be displayed on the right-hand side
FFT Averaging selection	Off	disables the averaging for the FFT plot
	RMS averaging	enables RMS averaging for the FFT plot - the displayed plot is calculated by the average of the past plots (number defined by selector below)
	Peak hold	enables peak averaging for the FFT plot - the displayed plot represents the peak values detected since the start of the series
FFT Weighting	Linear	instructs the FFT plot to perform an average that equally weights the past plots - the acquisition of new FFT spectra stops after the defined number of averages
	Exponential	instructs the FFT plot to perform an average that weights recent plots more than older plots and keeps infinite history
FFT Number of averages	0 to 100.000	determines the number of blocks that are averaged over time to produce the shown plot (has effect on both linear and exponential averaging)
Block Done indicator	-	indicates the progress of samples recorded for one block (only available for FFT Update Mode set to Blocks)
Averages Done indicator	-	indicates the progress of averaging (only shown for Linear averaging) - the acquisition of new FFT spectra stops after the defined number of averages
Restart button	[press once]	press once to restart averaging
Result mapping selection	Linear	the Y scale of the plot is displayed with a linear unit
	dB	the Y scale of the plot is displayed with a logarithmic unit, being the ratio of the physical quantity (power and intensity) relative to a $1 V_{\text{RMS}} / V_{\text{PK}}$ reference level

Control/Tool	Options/Range	Description
	dBm	the Y scale of the plot is displayed with the logarithmic unit, being the power ratio referenced to 1 mW (dissipation resistor of 50 Ohm)
Result unit selection	Unit _{RMS}	the Y scale of the plot reads the signal RMS
	Unit _{PK}	the Y scale of the plot reads the signal peak, Unit _{PK} = $\sqrt{2} * \text{Unit}_{\text{RMS}}$
	Unit _{RMS} ²	the Y scale of the plot reads the signal RMS power (squared RMS Unit)
	Unit _{PK} ²	the Y scale of the plot reads the signal peak power (squared peak Unit), Unit _{PK} ² = $2 * \text{Unit}_{\text{RMS}}^2$
	Unit _{RMS} /√Hz	the Y scale of the plot reads the spectral Unit density, independent of the frequency resolution - this setting is useful for noise measurements, Unit _{RMS} /√Hz = Unit _{RMS} / $\sqrt{(\text{frequency resolution})}$
	Unit _{RMS} ² /Hz	the Y scale of the plot reads the spectral RMS power density, independent of the frequency resolution - this setting is useful for noise measurements, Unit _{RMS} ² /Hz = Unit _{RMS} / (frequency resolution)
Filter compensation switch	OFF	the FFT plot does not compensate for the demodulator filter transfer function
	ON	the FFT plot compensates for the demodulator filter transfer function - this effect is small when the sample rate is in the same range as the demodulator bandwidth - the effect on the displayed plot is large when the signal sampling rate is much larger than the input signal rate
Absolute frequency switch	OFF	the X scale of the plot shows frequencies relative to the center of the FFT span which is labeled with 0 Hz (natural scaling)
	ON	the X scale of the plot is shifted by the demodulation frequency (Center freq) and refers to the frequencies applied to the Signal Input
Value selection	X, Y, R	selects the signal component that is taken for noise analysis
Value Mean (Unit _{RMS})	-	displays the average of the number of the samples in one FFT plot
Value Noise (Unit _{RMS})	-	displays the noise of the samples in one FFT plot
SNR	-	signal to noise ratio (SNR) calculated as the ratio between the value mean and the value noise
Value Noise Density (Unit _{RMS} /√Hz)	-	displays the noise of the samples in one FFT plot divided by the NEPBW of the demodulator
Pause button	OFF	continuous FFT plot
	ON	stopped FFT plot, the last calculated FFT plot is displayed and can be analyzed in detail (zoom and cursor palette)

Control/Tool	Options/Range	Description
Save ... button	[press once]	generates a directory with the depicted sweeper plot as CSV and PNG files - a Readme.txt file including the description of the columns is saved along with the data - the user has the opportunity to define a directory name where the data is stored

Table 4.25. Zoom FFT plot scaling and cursor options

Control/Tool	Options/Range	Description
Plot X and Y scaling	Automatic	the X scale of the plot is calculated based on the FFT span, and Y scale of the plot is continuously adapted to the current demodulated samples
	Manual	the X scale and Y scale of the plot can be manually set - with manual setting, the cursors and zoom palette become available - the manual setting of the Y scaling can also be used to avoid the continuous changes in the scale limits
Plot cursor palette	[press once]	control permits to grab and move cursor point to the selected position
Plot zoom palette	[press once]	control permits to select one of 6 zoom modes including zoom in, zoom out, zoom X only, zoom Y only, and others
Plot pan palette	[press once]	control permits to pan the plot with drag and drop - this is particularly useful for zoomed plots
Cursor 1 functions	ON	press to activate the cursor 1 - the plot must be paused in order to handle the cursor effectively
Cursor 2 functions	ON	press to activate the cursor 2 - the plot must be paused in order to handle the cursor effectively

4.4.5. Oscilloscope Tool

Features:

- Oscilloscope with 2048 samples memory for input and output signal monitoring
- 4 signal sources, up to 13 trigger sources, 16 different sampling rates, and 2 trigger methods
- Independent hold-off and trigger level settings
- Fast Fourier Transform (FFT), signal analysis, and sample histogram functions
- Support for arbitrary input unit function

Description:

The integrated oscilloscope with memory for 2048 samples provides a simple-to-use tool that allows to quickly check the signals at the inputs and outputs of the HF2 Instrument and to define the settings accordingly.

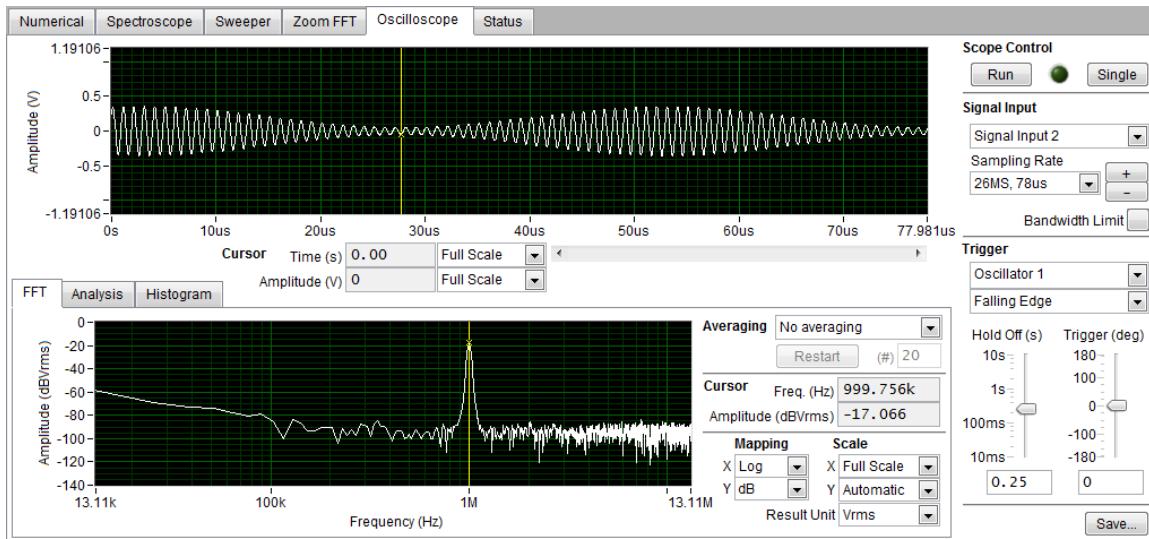


Figure 4.20. Oscilloscope tool with FFT tab

Table 4.26. Oscilloscope tool

Control/Tool	Options/Range	Description
Scope Control Run button	OFF / ON	activates the trigger of the oscilloscope for continuous oscilloscope waveforms
Trigger indicator	-	indicates that the oscilloscope has been triggered and that the display has been updated with a new waveform
Scope Control Single button	[press once]	activates the trigger of the oscilloscope for 1 single trigger event
Signal Input / oscilloscope source select	Signal Input 1	sets the oscilloscope to display the values sampled on Signal Input 1 after the HS-ADC
	Signal Input 2	sets the oscilloscope to display the values sampled on Signal Input 2 after the HS-ADC
	Signal Output 1	sets the oscilloscope to display the calculated values of Signal Output 1 before the HS-DAC - note: Add 1 does not impact this measured signal
	Signal Output 2	sets the oscilloscope to display the calculated values of Signal Output 2 before the HS-DAC - note: Add 2 does not impact this measured signal
Sampling Rate select	select	this control affects the sampling rate of the oscilloscope - changing the sampling rate impacts the frequencies that can be displayed without aliasing and the quality of the plot - thus this setting requires the user to consider the frequency components in his signal - this control also impacts the length in seconds of the displayed waveform (in Full Scale mode) - note: to change the value press the nearby increment/decrement buttons, or use the pull-down select
	210 MS, 10 us	105 MHz maximum frequency, 10 us full scale plot
	105 MS, 20 us	52 MHz maximum frequency, 20 us full scale plot

Control/Tool	Options/Range	Description
	53 MS, 39 us	21 MHz maximum frequency, 39 us full scale plot
	26 MS, 78 us	13 MHz maximum frequency, 78 us full scale plot
	13 MS, 160 us	6.5 MHz maximum frequency, 160 us full scale plot
	6.6 MS, 310 us	3.3 MHz maximum frequency, 320 us full scale plot
	3.3 MS, 620 us	1.6 MHz maximum frequency, 620 us full scale plot
	1.6 MS, 1.2 ms	800 kHz maximum frequency, 1.2 ms full scale plot
	820 kS, 2.5 ms	410 kHz maximum frequency, 2.5 ms full scale plot
	410 kS, 5 ms	205 kHz maximum frequency, 5 ms full scale plot
	205 kS, 10 ms	100 kHz maximum frequency, 10 ms full scale plot
	103 kS, 20 ms	51 kHz maximum frequency, 20 ms full scale plot
	51 kS, 40 ms	25 kHz maximum frequency, 40 ms full scale plot
	26 kS, 80 ms	13 kHz maximum frequency, 80 ms full scale plot
	13 kS, 160 ms	6.5 kHz maximum frequency, 160 ms full scale plot
	6.4 kS, 320 ms	3.2 kHz maximum frequency, 320 ms full scale plot
Bandwidth Limit switch	OFF	deactivates averaging permitting to plot also higher frequencies - the plot window may be disturbed by aliasing effects (high frequency components that are down-mixed)
	ON	activates averaging reducing aliasing due to frequency components that are higher than the Nyquist bandwidth of the set sampling frequency - this switch reduces high-frequent noise components
Trigger source select	Continuous	a new waveform is acquired and displayed after the hold off time - the trigger phase is ignored
	Signal Input 1	a new waveform is acquired and displayed when Signal Input 1 matches the trigger condition
	Signal Input 2	a new waveform is acquired and displayed when Signal Input 2 matches the trigger condition
	Signal Output 1	a new waveform is acquired and displayed when Signal Output 1 matches the trigger condition
	Signal Output 2	a new waveform is acquired and displayed when Signal Output 2 matches the trigger condition
	Oscillator 1, 2, 3, 4, 5, 6, (7, 8)	a new waveform is acquired and displayed when the oscillator for frequency 1, 2, 3, 4, 5, 6, (7, 8) matches the trigger condition
	DIO 0/1	a new waveform is acquired and displayed when DIO 0/1 matches the trigger condition

Control/Tool	Options/Range	Description
Trigger slope select	Falling Edge	sets the trigger slope (has no meaning for continuous trigger)
	Rising Edge	
Hold Off slider	1 ms to 10 s	determines the time during which the trigger is deactivated after being triggered - the user can also enter the hold-off time manually, and increment or decrement the value using the keyboard arrow keys
Trigger phase slider	real (V or degree)	determines the analog value where the trigger fires and an oscilloscope waveform is acquired - the user can also enter the trigger level manually, and increment or decrement the value using the keyboard arrow keys
Scale X mode select	Full Scale	adjusts the X-scale so that all acquired oscilloscope points are plotted
	Manual Scale	activates the zoom palette for manual zooming into parts of the window
Scale Y mode select	Full Scale	adjusts the Y-scale to the range setting of the selected input signal
	Auto Scale	adjusts the Y-scale automatically to 150% of the maximum detected input level
	Manual Scale	activates the zoom palette for manual zooming into parts of the window
Cursor X position	-	displays the current position of the cursor in seconds
Cursor Y position	-	displays the current position of the cursor in the unit that is selected for the oscilloscope source
Save button	[press]	save the current waveform into a comma-separated values (CSV) file

Table 4.27. Oscilloscope/FFT tab

Control/Tool	Options/Range	Description
Averaging select	No averaging	FFT performed on the 2048 samples of the current waveform
	RMS averaging	RMS averaging reduces signal fluctuations but not the noise floor - the noise floor is not reduced because RMS averaging averages the energy, or power, of the signal - RMS averaging also causes averaged RMS quantities of single-channel measurements to have zero phase
	Peak hold	peak hold averaging retains the peak levels of the averaged quantities - peak hold averaging is performed at each frequency line separately, retaining peak levels from one FFT record to the next
Cursor position X and Y	-	displays the cursor position in Hz for the X coordinate and for the displayed unit on the Y coordinate
Mapping X select	Linear	sets the X axis to linear mode

Control/Tool	Options/Range	Description
	Manual	sets the X axis to logarithmic mode
Mapping Y select	Linear	sets the Y axis to linear mode
	dB	sets the Y axis to logarithmic mode displaying dB
	dBm	sets the Y axis to logarithmic mode displaying dBm
Scale X mode	Full Scale	the X axis of the FFT diagram is adjusted automatically to the scope sampling rate
	Manual	the X axis of the FFT diagram can be specified manually in the toolbox that appears - the X scale can also be specified by clicking directly on the values on the X-axis
Scale Y mode	Automatic	the Y scale of the FFT diagram is adapted automatically
	Manual	the Y scale of the FFT diagram can be specified manually in the toolbox that appears - the Y scale can also be specified by clicking directly on the values on the Y-axis
Result Unit	Unit _{RMS}	RMS of selected oscilloscope unit
	Unit _{PK}	peak of selected oscilloscope unit
	Unit _{RMS} ²	RMS power of selected oscilloscope unit
	Unit _{PK} ²	peak power of selected oscilloscope unit
	Unit _{RMS} /√Hz	RMS density of selected oscilloscope unit
	Unit _{PK} /√Hz	peak density of selected oscilloscope unit
	Unit _{RMS} ² /Hz	RMS power density of selected oscilloscope unit
	Unit _{PK} ² /Hz	peak power density of selected oscilloscope unit

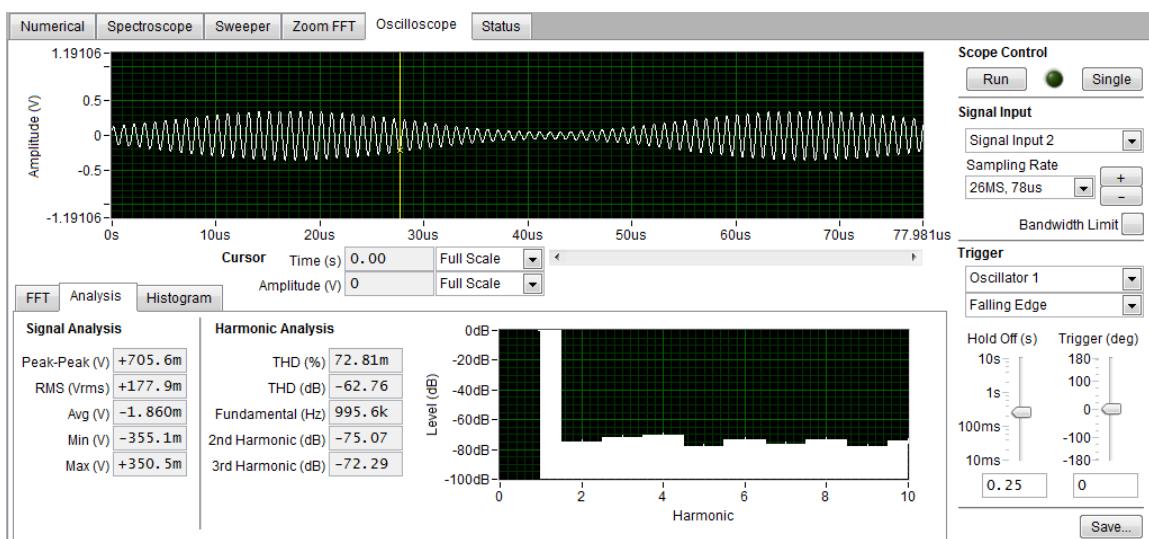


Figure 4.21. Oscilloscope tool with Analysis tab

Table 4.28. Oscilloscope/Analysis tab

Control/Tool	Options/Range	Description
Signal Peak-Peak value	-	maximum to minimum value of the samples in the displayed waveform
Signal RMS value	-	the root-mean-square of the samples in the displayed waveform
Signal Avg field	-	the average value of the samples in the displayed waveform
Signal Min field	-	the minimum value of the samples in the displayed waveform
Signal Max field	-	the maximum value of the samples in the displayed waveform
Total Harmonic Distortion (THD) field	-	the total harmonic distortion of the samples in the displayed waveform
Total Harmonic Distortion (THD) diagram	-	the total harmonic distortion diagram depicted with the harmonics in the X scale

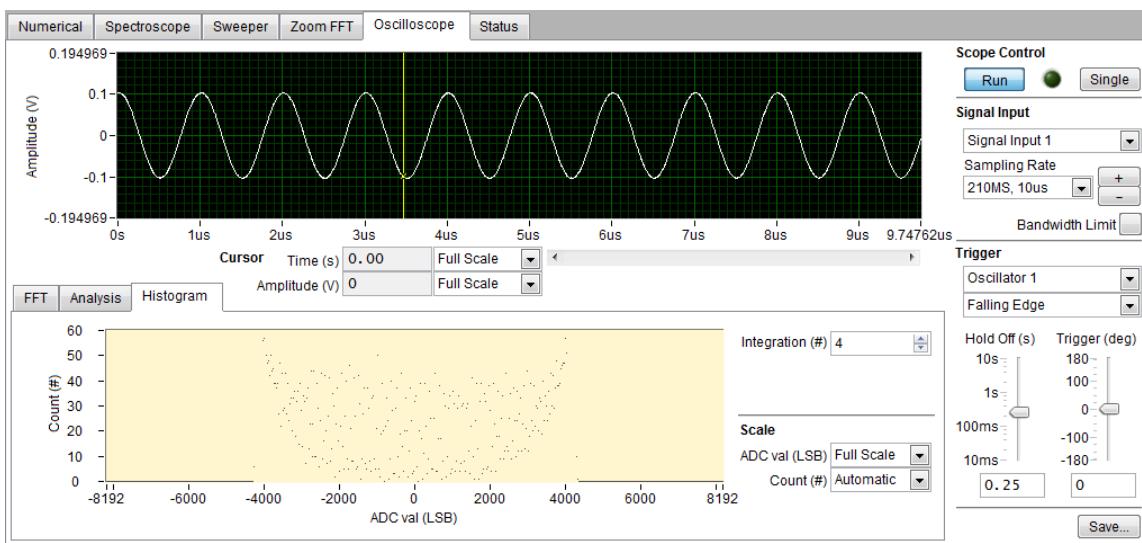


Figure 4.22. Oscilloscope tool with Histogram tab

Table 4.29. Oscilloscope/Histogram tab

Control/Tool	Options/Range	Description
Histogram display	-	shows the number of samples in the displayed waveforms with the value at the converter in the X scale
Integration select	1 to 256	defines the averaging on a number of waveforms, achieving a more stable histogram view
Scale X mode (ADC values)	Manual	makes the toolbox appear in order to set the X scale manually - select one scale label by clicking on it, and enter a new value - accept the new label by clicking somewhere in a neutral area of the GUI
	Full Scale	sets the X scale to full scale, equivalent to the resolution of the input ADC (from -8192 to 8192)

Control/Tool	Options/Range	Description
Scale Y mode (count values)	Automatic	sets the Y scale of the histogram display in automatic mode, so that the range is calculated depending on the actual waveform sample count
	Manual	makes the toolbox appear in order to set the Y scale manually - select one scale label by clicking on it, and enter a new value - accept the new label by clicking somewhere in a neutral area of the GUI

4.4.6. Status Tool and History Log

This section describes the features available in the status tab and in the history log section.

Features:

- First level instrument status information for problem solving (4 status indicators)
- Signal input level status, USB communication status
- Command history log

Description:

At the bottom-right area of the screen, 4 status indicators with 2 conditions each show notifications of the current status of the instrument. All status indicators indicate that a problem with the device is currently occurring or has occurred in the past. It is possible to reset the status indicators by pressing the return arrow symbol at the bottom-right. In case the status conditions existed in the past and are not true anymore, all indicators can be cleared at once. The clearing has not effect if the problem condition still exists: inspection of the current measurement conditions is required instead.

Warning

The data acquired when an error condition occurs may be faulty, incomplete, or inconsistent. Users should consider discarding the acquired data when a error condition has occurred during the measurement.

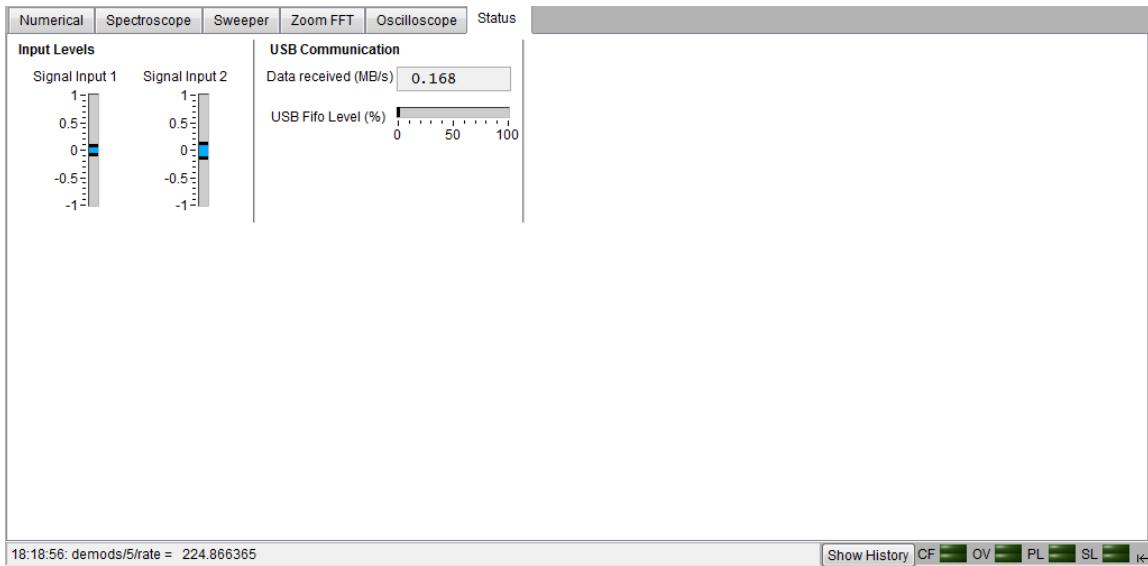


Figure 4.23. Status tool

The history log shows all commands that have been applied to the instrument since the start of the current ziControl session. There is a timestamp when the command has been performed and the command in the same syntax as for the text programming interface or for a LabVIEW program.

When clicking on the status line or on the Show History button, the complete history log opens and can be examined in detail. Copy and pasting of text parts is supported. Click again on the status line or the Hide History button to collapse the log.

The complete sequence of commands since the start of the ziControl session is also stored to a log file in the LabVIEW Data directory which is usually in this location `C:\Users\[USER]\Documents\LabVIEW Data` and the file name is called `com.zhiinst.ziControlStatusLog.txt`.

```

12:11:30: demods/0/timeconstant = 0.000020
12:11:30: OSCS/0/FREQ = 12742.749857
12:11:30: demods/0/timeconstant = 0.000012
12:11:30: OSCS/0/FREQ = 20691.380811
12:11:30: demods/0/timeconstant = 0.000007
12:11:30: OSCS/0/FREQ = 33598.182863
12:11:30: demods/0/timeconstant = 0.000005
12:11:31: OSCS/0/FREQ = 54555.947812
12:11:31: demods/0/timeconstant = 0.000003
12:11:31: OSCS/0/FREQ = 88586.679041
12:11:31: demods/0/timeconstant = 0.000002
12:11:31: OSCS/0/FREQ = 143844.988829
12:11:31: demods/0/timeconstant = 0.000001
12:11:31: OSCS/0/FREQ = 233572.146909
12:11:31: demods/0/timeconstant = 0.000001
12:11:31: OSCS/0/FREQ = 379269.019073
12:11:31: demods/0/timeconstant = 0.000000
12:11:31: OSCS/0/FREQ = 615848.211066
12:11:31: demods/0/timeconstant = 0.000000
12:11:31: OSCS/0/FREQ = 1000000.000000
12:12:46: /ZI/CONFIG/OPEN = 1.000000
12:12:48: /ZI/CONFIG/OPEN = 0.000000
15:47:45: oscs/0/freq = 2000000.000000
15:47:54: oscs/1/freq = 2500000.000000
15:47:59: DEMODS/2/OSCSELECT = 1.000000
15:48:16: DEMODS/0/timeconstant = 0.000692
15:48:21: DEMODS/1/HARMONIC = 2.000000
15:48:26: DEMODS/1/timeconstant = 0.006923
15:48:29: DEMODS/1/ORDER = 2.000000
15:48:32: Sigouts/1/ENABLES/4 = 1.000000

15:48:32: Sigouts/1/ENABLES/4 = 1.000000

```

Figure 4.24. History log

Table 4.30. Status tool

Control/Tool	Options/Range	Description
Input signal 1 level	-1 to +1	indicates in graphical format the current level at signal input 1 - the occurrence of an overshoot at signal input 1 lights the Input Over (OV) flag
Input signal 2 level	-1 to +1	indicates in graphical format the current level at signal input 2 - the occurrence of an overshoot at signal input 2 lights the Input Over (OV) flag
USB communication rate	[MB/s]	this level indicates the current communication throughput between the HF2 Instrument and the host computer - the rate is directly related to the readout rate of the demodulators - a level below 7 MB/s is suggested to avoid data loss
USB FIFO level	0% to 100%	the level indicates the current status of the USB FIFO buffer - a level above 100% during a short amount of time is equivalent to a package loss and the Package Loss (PL) flag is set - the FIFO level is dependent on many parameters, but in particular on the host computer and its operating system - higher performance computers are able to keep the FIFO level at a low level
CF (Clock fail)	Status	external clock is selected and does not work
	Occurred	clock fail has occurred in the past
OV (Input over voltage)	Status	the signals at Input 1 and/or Input 2 are overshooting the selected range and clipping of the samples occurs
	Occurred	input over voltage has occurred in the past
PL (Package loss)	Status	the USB communication is experiencing serious overflow and data packages are being discarded (samples and control) - this problem may be related to a high demodulator sample readout setting, however other reasons may also exist
	Occurred	package loss has occurred in the past
SL (Sample loss)	Status	the USB communication is experiencing serious demodulator sample losses - a sample loss may provoke or contribute to provoke a package loss - sample loss is less serious than package loss, however it may compromise the measurement integrity - a demodulator sample loss can be avoided by reducing the demodulator readout rate
	Occurred	sample loss has occurred in the past
Clear button	[press once]	clears all error flags

Chapter 5. Functional Description HF2IS

This section contains the detailed description of all panels of the graphical user interface (GUI) ziControl for the HF2IS product. This GUI is a LabVIEW based program that is delivered standard with all instruments.

On top of standard functionality like acquiring and saving data points this GUI provides a variety of measurement tools for the time and frequency domain. All of these features (and a few more) are also accessible by means of the programming interfaces described in [Chapter Programming](#).

5.1. Graphical User Interface Overview

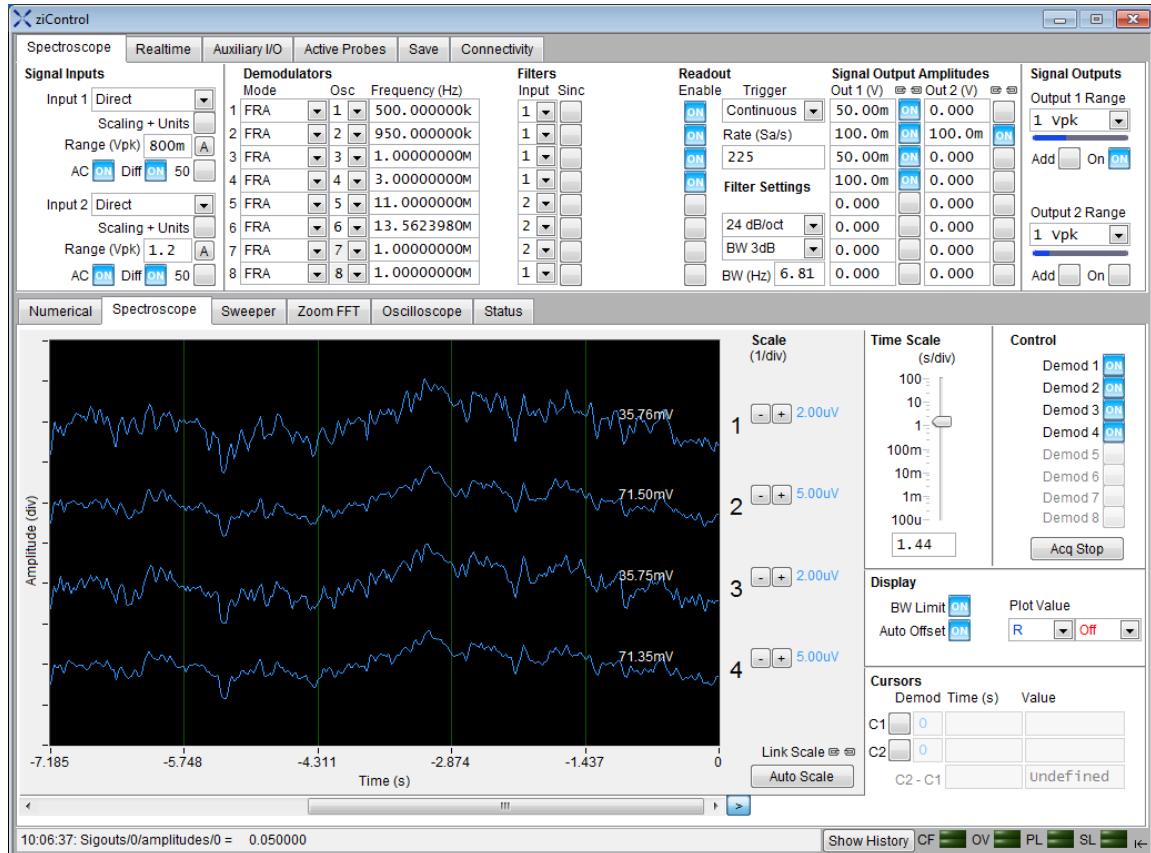


Figure 5.1. ziControl Overview

The GUI is divided into 2 sections as depicted in [Figure 5.1](#), each subdivided in a tab structure. The settings section on top is dedicated to display and control the main settings of the instrument, whereas the tools section provides additional measurement functionality. A status line on the lower end shows the command history which ziControl exchanged with the ziServer plus a couple of status flags on the right hand side.

The following tabs are available in the settings section:

- ─ Spectroscope
- ─ Real-time settings (optional feature)
- ─ Auxiliary I/O settings
- ─ Active probes settings (displayed only when external active probes are attached)
- ─ Save settings
- ─ Connectivity settings

These are the tabs in the tools section:

- ─ Numerical tool
- ─ Spectroscope tool
- ─ Frequency response sweeper tool
- ─ Zoom FFT tool

- Oscilloscope tool
- Status view

5.2. Settings Tabs

5.2.1. Spectroscope Tab

Note

Parts of this tab are ghosted when the HF2IS-MF multi-frequency option has not been purchased and is not activated.

Features:

- Control for 2 measurement units with 8 demodulators
- Control for 2 signal generators with up to 8 individual sinusoidal components
- Two and four terminal impedance measurement
- Continuous range setting for signal inputs and signal outputs
- Easy to use interface with many useful individual settings

Description:

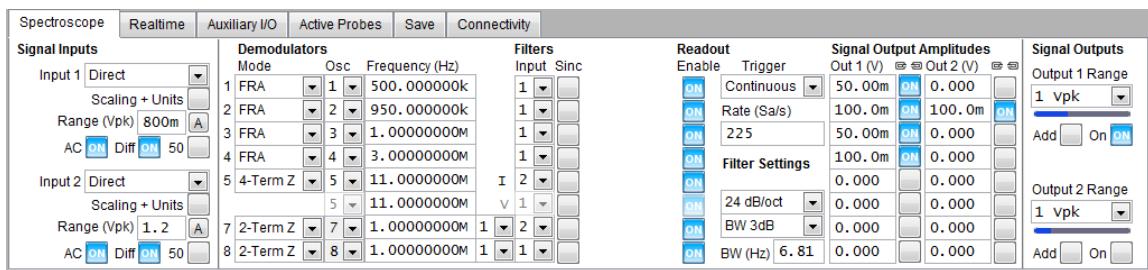


Figure 5.2. Spectroscope settings tab

Table 5.1. Spectroscope settings tab

Control/Tool	Options/Range	Description
Input 1/2 selection	Direct	defines the active probe that is connected to the input of the HF2 instrument - in case an HF2 Series pre-amplifier is connected then this selection will make the measurement account for external pre-amplification and current conversion - please make sure to define the correct pre-amplifier model, channel, and ZCtrl connectivity port (on the back panel of the instrument)
	HF2TA 0, Ch1	
	HF2TA 0, Ch2	
	HF2CA 0	
	HF2TA 1, Ch1	
	HF2TA 1, Ch2	
	HF2CA 1	
Scale + Unit enable	OFF / ON	enables arbitrary input scaling to define a scaling factor for all measurements regarding this input channel and also change the unit which is displayed throughout the tools and settings
Scale factor	numeric scaling factor	positive or negative real number to scale all measurement values attributed to that particular input; only appears when Scaling + Units is enabled

Control/Tool	Options/Range	Description
Unit acronym	three letter acronym	three letters can be set to define a unit, e.g. PSI; only appears when Scaling + Units is enabled
Range (unit)	1 mV to 2.1 V (or equivalent after unit + scale is applied)	sets the range of input 1/2 in fine increments within the available range - the value is the absolute range of the signal including a potential DC offset - note: the value inserted by the user may be approximated to the nearest value supported by the HF2; Scaling + Units is fully accounted for in this setting
A = Auto Range button	[press once]	press this button to automatically set the input range to two times the maximum amplitude of the input signal over a measured time of 100 ms
Signal input 1/2 AC switch	OFF: DC coupling	sets the input coupling for input 1/2 - an AC coupling inserts a high-pass filter at 1 kHz in the input path
	ON: AC coupling	
Signal input 1/2 differential switch	OFF: single-ended input	selects whether to connect to a single-ended signal (one cable) or to a differential signal (two cables)
	ON: differential input	
Signal input 1/2 50 Ω switch	OFF: 1 MΩ	sets the matching impedance for input 1/2
	ON: 50 Ω	
Demodulator 1-8 Mode selection	FRA	frequency response analyzer mode: the demodulator measures the RMS value of the selected unit
	2-Term Z	2 terminal impedance measurement mode: the demodulator measures impedance assuming a current is measured at the signal input and driven by the signal output voltage - note: this mode requires an external current amplifier to be connected between the DUT and the HF2 Instrument
	4-Term Z (demodulators 1,3,5 only)	4 terminal impedance measurement mode: the demodulator measures impedance assuming a current is measured at the signal input and driven by the signal output voltage - note: this mode requires an external current amplifier to be connected between the DUT and the HF2 Instrument - this mode also requires the use of two consecutive demodulators (1,2), (3,4), (5,6) - demodulators (7,8) cannot be used for 4-Term impedance measurement
Oscillator 1-6 selection	1 to 6	defines the active oscillator for each individual demodulator - note: the oscillator selection is shaded in case 4-Term Z mode is selected for the demodulator above
	7 and 8	oscillators 7 and 8 are hardwired to demodulators 7 and 8
Oscillator 1-8 Frequency selection	0 to 100 MHz	sets the frequency for the corresponding generator

Control/Tool	Options/Range	Description
Demodulator 1-8 Signal Output selection	1: the demodulator is connected to Signal Output 1	sets the Signal Output that is taken for impedance measurement - note: this selection only appears if 2-Term Z mode is selected
	1: the demodulator is connected to Signal Output 2	
Demodulator 1-8 Signal Input selection	1: the demodulator is connected to Signal Input 1	sets the Signal Input that is connected to each of the demodulators
	2: the demodulator is connected to Signal Input 2	
Sinc Filter 1-8 switch	OFF: Sinc filter disabled for corresponding demodulator	the Sinc filter is an additional filtering stage that permits to remove the omega and 2 times omega components - by using a large filter roll-off value, the user can effectively reduce the signal component at the second and third harmonics - for applications where large roll-off is not possible, the Sinc filter achieves the same effect - note: there are limitations regarding the maximum frequency and the frequency resolution allowed with Sinc filtering (see Table 4.2)
	ON: Sinc filter enabled for corresponding demodulator	
Demodulator 1/8 activity switch	OFF: demodulator inactive	sets the activity for the corresponding demodulator - when enabled demodulated samples are sent to the host computer at the rate defined in the readout rate field
	ON: demodulators active	
Trigger selection	Continuous	automatic internal trigger with the sample rate defined in the next control field
	DIO 0	samples are sent to the host computer depending on DIO 0 triggering
	DIO 1	samples are sent to the host computer depending on DIO 1 triggering
	DIO 0 or 1	samples are sent to the host computer depending on DIO 0 and 1 triggering
	Undefined	a complex trigger has been programmed to the HF2 Instrument by means of the programming interfaces - this mode cannot be selected by the user of ziControl, but is the result of a complex trigger definition - note: this is a read-only position
Trigger Mode selection	Rising Edge	1 data sample is sent to the host computer for a rising edge on the trigger input
	Falling Edge	1 data sample is sent to the host computer for a falling edge on the trigger input
	Both Edges	1 data sample is sent to the host computer for a rising edge and falling edge on the trigger input
	Gate High	data samples are sent to the host computer as long as the defined DIO 0/1 input is high - it is possible to determine the number of

Control/Tool	Options/Range	Description
		demodulated samples that are sent to the host computer after a trigger by setting the readout rate and modulating the pulse length on the DIO 0/1
	Gate Low	data samples are sent to the host computer as long as the defined DIO 0/1 input is low - it is possible to determine the number of demodulated samples that are sent to the host computer after a trigger by setting the readout rate and modulating the pulse length on the DIO 0/1
	Undefined	a complex trigger has been programmed to the HF2 Instrument by means of the programming interfaces - this mode cannot be selected by the user of ziControl, but is the result of a complex trigger definition - note: this is a read-only position
Readout sample rate	0.22 Hz to 460 kHz	sets the filter readout rate, equivalent to the demodulator samples (measured data points) that are sent to host computer per second - it is also the rate of data received by ziControl and saved to the computer hard disk - this setting has no impact on the sample rate on the physical auxiliary outputs - note: the value inserted by the user may be approximated to the nearest value supported by the HF2 (see Table 9.4)
Filter order select	1st: 6 dB/oct 2nd: 12 dB/oct 3rd: 18 dB/oct 4th: 24 dB/oct 5th: 30 dB/oct 6th: 36 dB/oct 7th: 42 dB/oct 8th: 48 dB/oct	sets the filter order for all active demodulators
Filter Notation selection	BW 3 dB: 80 μ Hz (filter order = 8) to 200 kHz (filter order = 1) BW NEP: 90 μ Hz to 319 kHz TC eff: 783 ns to 1900 s TC per order: 783 ns to 580 s	defines the filter property indication mode either in bandwidth or time constant notation - either of these differ by a scaling factor depending on the filter order - please refer to Table 10.1 for exact relation between TC, BW, and filter order
Filter setting	bandwidth or time constant	displays the filter setting according to the notation defined above - note: the value inserted by the user may be approximated to the nearest value supported by the Instrument

Control/Tool	Options/Range	Description
Signal Output 1/2 Amplitude 1-8	0 V to 10 V	sets the output voltage of the corresponding oscillator - the minimum setting is 0 V, the next level above zero depends on the Signal Output range - note: the value inserted by the user may be approximated to the nearest value supported by the Instrument
Signal Output 1/2 Amplitude Enable 1-8	OFF: oscillator output disabled	sets the output enable for the corresponding oscillator
	ON: oscillator output enabled	
Signal Output 1/2 range	$\pm 10 \text{ mV}$	sets the output range for output 1/2 - this select determines the maximum output peak to peak range - this setting will make sure that no peaks above the setting are generated at the output, independent from the amplitude settings (V) - the output signal is clipped if the amplitude is higher than the range - if the range is changed to a value smaller than V, then V is automatically reduced to the new range
	$\pm 100 \text{ mV}$	
	$\pm 1 \text{ V}$	
	$\pm 10 \text{ V}$	
Signal Output 1/2 Add switch	OFF: Add disabled	the Add input allows to add an external analog signal to the internally generated signal - when disabled, the signal at the Add input is ignored
	ON: Add enabled	
Signal Output 1/2 On switch	OFF: output disabled	sets the On switch for the related output - when the output is enabled, then the HF2 signal output is active and the blue LED on the front panel lights-up
	ON: output enabled	

Table 5.2. Filter order, TC, BW relation

Filter order	Bandwidth (BW)	BW at time constant TC=1 ms	BW at time constant TC=213 ms
1st: 6 dB/oct	$1.000/(2\pi\text{TC})$	159 Hz	0.75 Hz
2nd: 12 dB/oct	$0.644/(2\pi\text{TC})$	102 Hz	0.48 Hz
3rd: 18 dB/oct	$0.510/(2\pi\text{TC})$	81.2 Hz	0.38 Hz
4th: 24 dB/oct	$0.435/(2\pi\text{TC})$	69.2 Hz	0.33 Hz
5th: 30 dB/oct	$0.386/(2\pi\text{TC})$	61.4 Hz	0.29 Hz
6th: 36 dB/oct	$0.350/(2\pi\text{TC})$	55.7 Hz	0.26 Hz
7th: 42 dB/oct	$0.323/(2\pi\text{TC})$	51.4 Hz	0.24 Hz
8th: 48 dB/oct	$0.301/(2\pi\text{TC})$	47.9 Hz	0.22 Hz

5.3. Other Settings

5.3.1. Real-time Settings Tab

Note

The real-time tab appears only when the HF2LI-RT / HF2IS-RT option has been purchased and activated. Customers can purchase the RT option at any time, whether when ordering their instrument or after delivery. This option can be activated by the user or by Zurich Instruments via remote servicing.

Features:

- Push button loading of real-time programs via the graphical user interface.
- Easy access to the embedded microprocessor's user registers to configure program parameters or view program output at run-time. Optional naming of user registers for easy identification, saving and opening of register names for persistent configuration across sessions.
- Displays real-time debugging information in the message log field box and/or allows the user to save the message log to file.
- Graphical CPU load indicator.

Description:

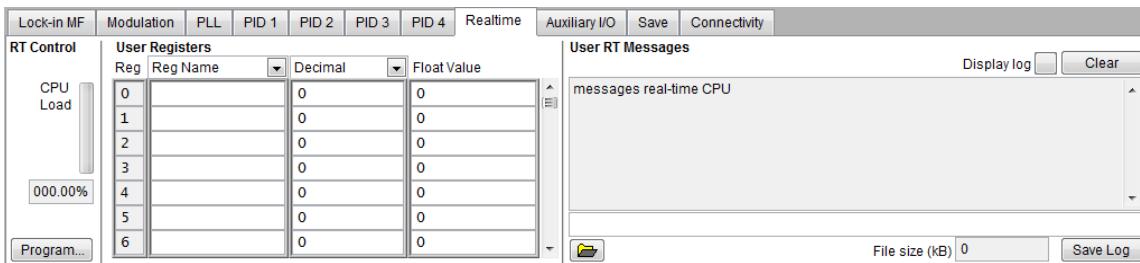


Figure 5.3. Real-time settings tab

Table 5.3. Real-time settings tab

Control/Tool	Options/Range	Description
CPU Load indicator	0% to 100%	indicates the momentary load of the embedded microprocessor running the real-time code
Program... push button	[press once]	selects a binary program to download to the HF2 Instrument; execution starts immediately thereafter
Reg Name pull-down menu	Reg Name	enters a name for the user register
	Save...	saves the user register names in an XML user register configuration file
	Open...	loads user register names saved in an XML user register configuration file
	Clear	clears entered user names. The values of the user register remain unaffected

Control/Tool	Options/Range	Description
Format pull-down menu	Decimal	selects the display format of the user register between decimal and hexadecimal
	Hexadecimal	
Float Value	IEEE754 32-bit floating-point number	view or edit a user register value as a 32-bit floating point number
Register fields 0-63	Register name & value	visualizes the processor register content - these fields are updated as the processor modifies the values - these fields are read/write: they permit to directly write values into the registers of the embedded processor. Each register can be optionally named for easy identification
User RT Messages	Read only	displays log messages printed via <code>zirTKPrintf()</code> from the real-time program running on the embedded processor.
Display log toggle button	OFF	update the message log field disabled
	ON	update the message log field enabled
Clear push button	[press once]	clears the message log field
Log file name edit box	File name	specifies a destination file to save real-time log messages
Folder push button	[press once]	opens a dialog to specify a destination file to save real-time log messages
File size (kB)	Read only	displays the current size of the saved log file
Save log push button	[press once]	saves the real-time log messages in the specified file

5.3.2. Auxiliary I/O Settings Tab

Features:

- Control for auxiliary output connectors
- Monitor of auxiliary input connectors
- Monitor and control of digital I/O connectors

Description:

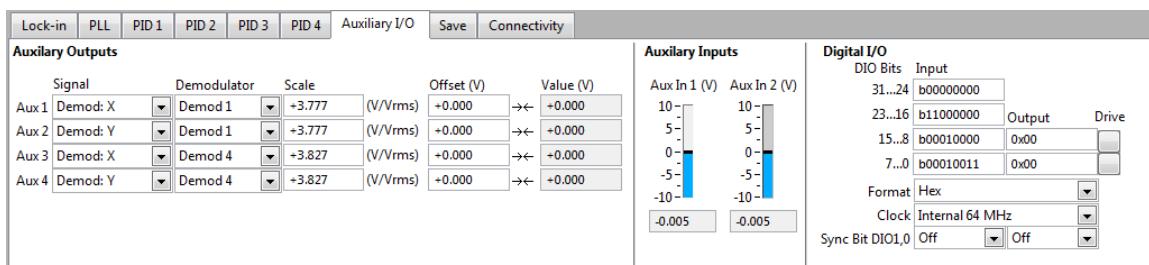


Figure 5.4. Auxiliary I/O settings tab

Table 5.4. Auxiliary I/O settings tab

Control/Tool	Options/Range	Description
Aux 1/2/3/4 output signal select	Manual (V)	set the signal that is provided on the Aux 1/2/3/4 connectors on the front panel of the instrument - the manual setting generates a DC value - the PLL: df selection is only available with the HF2LI-PLL option - the PID:Output selection is only available with the HF2LI-PID option
	X (V_{RMS})	
	Y (V_{RMS})	
	R (V_{RMS})	
	Theta (deg)	
	PLL 1: dF (Hz)	
	PLL 2: dF (Hz)	
	PID 1: Output (PID output unit)	
	PID 2: Output (PID output unit)	
	PID 3: Output (PID output unit)	
	PID 4: Output (PID output unit)	
Aux 1/2/3/4 output demodulator select	Demodulators	set the demodulator unit whose samples are being output to the related Aux output connector on the front panel of the instrument - note: this field is not available when the signal select is on manual or on PLL:dF or on PID:Output
Aux 1/2/3/4 output signal scale	-80 G to 80 G (V_{RMS}) for X, Y, R	set the scaling factor applied to the selection, consisting of a digital multiplication in real-time - the range of this field depends on the selected input range (see the following table) - note: this field is not available when the signal select is on manual
	-460 k to 460 k (V/deg) for Θ	
	-1.6 to 1.6 (V/Hz) for PLL:dF	
	-10^{37} to 10^{37} for PID:output	
Aux 1/2/3/4 output offset	-10 V to 10 V	set the DC offset value that is being added after the scaling of the selection
Aux 1/2/3/4 output value	[display]	instantaneous value at the corresponding auxiliary output connector
Aux In 1	-10 V to 10 V	indicates the current signal level on auxiliary input 1 (back panel)
Aux In 2	-10 V to 10 V	indicates the current signal level on auxiliary input 2 (back panel)
Digital I/O input bits 31...24	[input]	digital value in DIO input register bits 31 to 24
Digital I/O input bits 23...16	[input]	digital value in DIO input register bits 23 to 16
Digital I/O input bits 15...8	[input]	digital value in DIO input register bits 15 to 8
Digital I/O input bits 7...0	[input]	digital value in DIO input register bits 7 to 0

Control/Tool	Options/Range	Description
Digital I/O output bits 15...8	0x00 to 0xFF	digital value in DIO output register bits 15 to 8
Digital I/O output bits 7...0	0x00 to 0xFF	digital value in DIO output register bits 7 to 0
Digital I/O drive selector bits 15 to 8	OFF: no drive (input)	digital value at the DIO port bits 15 to 8 is not driven, high impedance, and the connectors can be used as inputs
	ON: drive (output)	digital value at the DIO port bits 15 to 8 is driven
Digital I/O drive selector bits 7 to 0	OFF: no drive (input)	digital value at the DIO port bits 7 to 0 is not driven, high impedance, and the connectors can be used as inputs
	ON: drive (output)	digital value at the DIO port bits 7 to 0 is driven
Digital I/O Hex/Bin	Hex	change between hexadecimal and binary representation
	Binary	
Digital I/O Clock	Internal 64 MHz	DIO input register is latched by internal 64 MHz clock
	Clk Pin 68	DIO input register is latched by signal on Pin 68 of the DIO port
Sync Bit DIO1,0	Off	DIO outputs 1 and 0 (BNC connectors) are not used for sync output and are free for other purposes
	Demod 1 to 8	reference signal of the selected demodulator is output on DIO 1 and 0 - note: there is a 166 ns delay between the sync and the front panel outputs (sync comes first) which leads to a relevant phase shift at high frequencies. To get an in phase sync, please make use of the WAVEFORM node setting and one of the HF front panel outputs

Table 5.5. Auxiliary output ranges

Input range setting	Scaling range for R (V/V _{RMS})	Scaling range for X and Y (V/V _{RMS})	Desired input full scale sensitivity (10 V full range output)
1 mV	80 G	75 G	1 nV to 1.5 V
10 mV	8 G	7.5 G	10 nV to 1.5 V
100 mV	760 M	750 M	100 nV to 1.5 V
1 V	83 M	71 M	1 μ V to 1.5 V
2.1 V	42 M	35 M	2 μ V to 1.5 V

5.3.3. Save Settings Tab

Features:

- Save of demodulated samples to host computer disk drive
- Load and save of instrument configurations

- File conversion utilities

Description:

The HF2 Instrument contains a default configuration for all settings when delivered to the customer. This default configuration is loaded every time the HF2 Instrument is powered up. It provides an initial safe state of the instrument so that externally connected setups are not damaged nor impacted. This default configuration cannot be modified by the user.

However, ziControl provides the capability to load and save user specific configurations by means of this tab. As the configuration files are saved on the host computer, the user benefits from an unlimited number of possible configurations that can be stored.

The configuration files are in text format, thus they can be conveniently edited by the user. The syntax is self-explanatory and is the same as described for the text interface in [Chapter Programming](#).

Saving demodulator samples to the hard disk can be done in text or binary format. When selecting text format, the data are stored as columns (without header) in a comma-separated values file. The contents of the columns are specified in the table "Save Field Information" below. Binary saving is faster and allows saving at higher sample rates, and minimizes CPU usage.

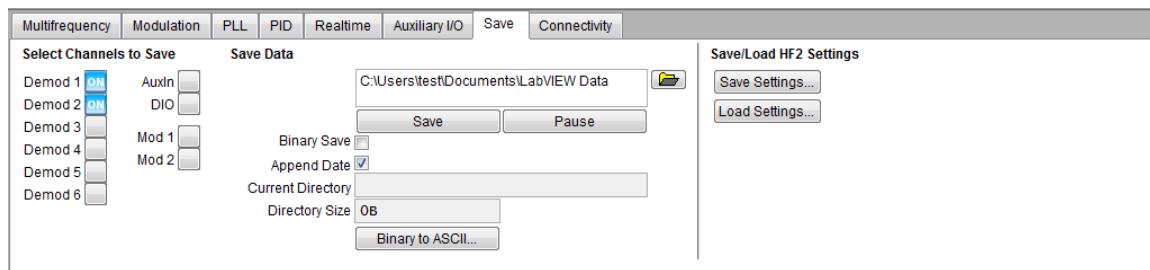


Figure 5.5. Save settings tab

Table 5.6. Save settings tab

Control/Tool	Options/Range	Description
Select channels to save	select Demodulators	set the demodulator outputs that are saved in CSV format to the specified directory on the host computer - one file is generated for each selected demodulator - refer to the next table to interpret the information regarding the data included in the files
	select Auxin	save data from the Auxiliary Inputs 1 and 2 to a separate file, at a maximum sampling rate of 400 kSa/s - note: the Auxiliary Inputs samples are also saved in the demodulator files at the readout rate of the demodulator - setting this switch generates large files
	select DIO	save data from the DIO port to a separate file, at a maximum sampling rate of 125 kSa/s - note: the DIO samples are also saved in the demodulator files at the readout rate of the demodulator - setting this switch may generate large files
	select Mod 1	save data from the AM/FM modulator 1 (requires related option to be activated)

Control/Tool	Options/Range	Description
	select Mod 2	save data from the AM/FM modulator 2 (requires related option to be activated)
Save directory	absolute path to the data save directory	set the trunk name for the save directory - note: you may browse to the target directory using the small icon at the top right
Binary save	OFF: ASCII save	-
	ON: binary save	
Append date to directory switch	OFF: no date	define whether a time-stamp is appended to the save directory name - this option allows to keep the same save path (trunk name) constant and to generate a unique directory name whenever a new save is started
	ON: append date	
Save button	OFF: stop the save	stop the save
	ON: start the save	start the save of demodulated samples to the directory defined in the save path field - pressing this button generates 1 file for each active demodulator
Pause button	OFF: no pause	pause the save in progress of demodulated data - this feature is useful to append several measurement sequences into the same files, thus reducing the number of generated files and directories - note: this button has no impact on the activity of the demodulators (measurement in progress)
	ON: save is paused	
Current directory	-	indicates the current save directory (useful in combination with the append date switch)
Directory size	-	size of the current save directory - this value is the sum of all saved files in a directory and is updated during a save in progress
Binary to ASCII button	-	indicates that the ziControl is not able to save all data to the specified directories, and that some data were discarded
Save Settings button	-	save current settings of the HF2
Load Settings button	-	load settings into the HF2

Table 5.7. Save field information

File	Data	Description
FreqX	Timestamp [s]	the time-stamp counter is a value in seconds that is initiated with power-up of the HF2 Instrument and continues to increment as long as it is powered - the expiration of the time-stamp is in the order of years
	Demodulator X output [V _{RMS}]	the X output of the related demodulator at the set output rate
	Demodulator Y output [V _{RMS}]	the Y output of the related demodulator at the set output rate
	Frequency [Hz]	the frequency of the demodulator

File	Data	Description
	DIO [decimal unsigned integer]	the value of digital I/O port
	Auxiliary Input 1 [V]	the voltage at the auxiliary input 1
	Auxiliary Input 2 [V]	the voltage at the auxiliary input 2

Note

As the time-stamp information is consistent between all demodulators with the same readout rate, the user can merge and compare data from the various files.

5.3.4. Connectivity Settings Tab

Features:

- HF2 Instrument connectivity on local network - ziServer connectivity
- Selection of active instrument (in case more than one instrument is connected to the selected ziServer)
- Overview of installed product options and activation of new options
- Software and hardware revision information

Description:

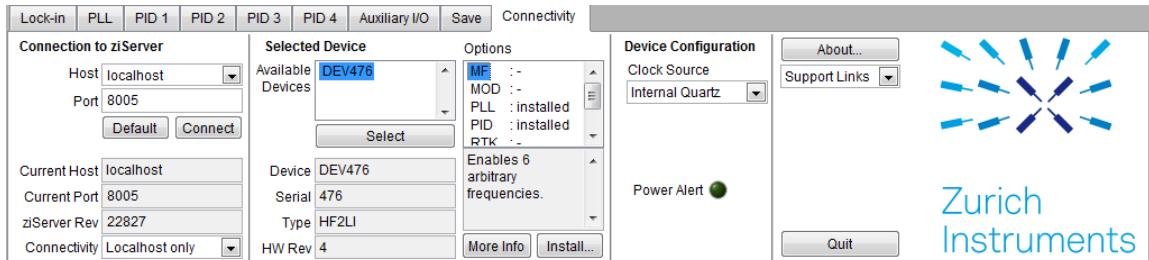


Figure 5.6. Connectivity settings tab

Table 5.8. Connectivity settings tab

Control/Tool	Options/Range	Description
Host	default: localhost, to be used when ziServer is running on the same host as ziControl	sets the host IP address where ziServer is running, and to which ziControl must connect in order to access the relevant HF2 Instrument
	range: any string recognized by your DNS	
Port	default: 8005	sets the port where ziServer is listening - the port number is given for a running server, and may be changed in the configuration files of ziServer
	range: 0 to 65535	
Default button	-	press this button to enter the default settings localhost:8005

Control/Tool	Options/Range	Description
Connect button	-	press this button to accept the entered host name and port
Current Host	-	information field regarding the connected host
Current Port	port number	information field regarding the connection port
ziServer Rev	revision number	information field regarding the revision of the connected server
Connectivity	Localhost only	the connected ziServer only accepts connectivity to the local host - remote clients are not able to access the local HF2 Instruments
	From everywhere	the connected ziServer accepts connectivity from any host on the LAN - all remote clients are able to access the local HF2 Instruments
Available Devices	select	this list shows all available HF2 Instruments on the selected server
Select button	-	press this button to select one of the available devices
Device	text	information field about the selected device
Serial	number	information field about the selected device
Type	text	HF2 Instrument type
HW Rev	text	hardware revision of the selected device
Installed options	MF	multi-frequency option is installed
	MOD	modulation option is installed
	PLL	phase-locked loop option is installed
	PID	PID option is installed
	RT	real-time option is installed
	UHS	ultra-high stability option is installed
More Info button	press	opens the product page on the Zurich Instruments website
Install options button	press	prompts a window that permits to enter a feature code to install additional features
Clock Source selector	Internal Quartz	selects the internal oscillator as clock reference
	Clock In 10 MHz	selects the external source for clock reference (external rubidium standard reference or atomic clock)
Power Alert	-	indicates that the supply voltage is too low (e.g. 100 V supply system) and therefore the instrument might provide incorrect measurements
About button	-	provides the information regarding the current version of ziControl
Support Links selector	User Manual	opens the installed version of the HF2 User Manual (this document)
	ZI Blogs	opens a browser window with access to the blog section of the Zurich Instruments website for a lot of online content

Control/Tool	Options/Range	Description
	ZI Support	opens a browser window with access to the support section of the Zurich Instruments website for support information
	ZI Software Updates	opens a browser window with access to the software update information of the Zurich Instruments website
	TeamViewer Download Win	permits to download the TeamViewer software for support in remote servicing (works also behind most firewalls, no need for software installation nor administrator rights)
Quit button	-	closes the ziControl application

5.3.5. Active Probes Settings Tab

The Active Probes settings tab adapts its content to the pre-amplifier connected to the HF2 Instrument. Whenever an active probe is connected with an RJ45 cable, this is automatically detected and a related screen appears on the ziControl tab. When the active probe is disconnected from the HF2, the panel on the tab disappears.

All active probes from Zurich Instruments are fully integrated inside ziControl.

HF2CA Current Amplifier

Features:

- Input impedance range from 10 V/A to 1 M V/A (R1, R2)
- Input mode differential or single-ended (Diff, Single)
- Input signal coupling mode (AC, DC)
- Output stage gain (G=1 or G=10)



Figure 5.7. Active probes HF2CA tab

Additional HF2CA specification can be found in the [HF2CA Current Amplifier Data sheet](#).

HF2TA Current Amplifier

Features:

- Input offset +/- 10 V
- Transimpedance gain from 100 V/A to 100 M V/A (R1, R2)

- ─ Input signal coupling mode (AC, DC)
- ─ Addition gain (1, 10)
- ─ Total gain display ($R1 \times G$, $R2 \times G$)
- ─ Input Shield (GND, EXT Bias)
- ─ Auxiliary output +/- 10 V



Figure 5.8. Active probes HF2TA tab

Additional HF2TA specification can be found in the [HF2TA Current Amplifier Data sheet](#).

5.4. Tools Tabs

The lower section of the ziControl Graphical User Interface contains tool tabs to assist and perform common measurement tasks with the HF2 Instrument. The [Numerical Tab](#) and [Sweeper Tab](#) have settings specific to the HF2IS and we refer the reader to the documentation in this section for these tabs. The other available tool tabs are identical to those available for the HF2LI and for these tabs we refer the reader to the relevant section in the HF2LI Functional Description:

- Spectroscope Tab - [Section 4.4.2](#),
- zoomFFT Tab - [Section 4.4.4](#),
- Oscilloscope Tab - [Section 4.4.5](#),
- Status Tab and History Log - [Section 4.4.6](#).

5.4.1. Numerical Tool

Note

The number of available demodulators depends on the purchased instrument and activated options, and whether the features are activated in the related settings tabs. Polar, Cartesian, and Z tools are available to all users.

Features:

- Real-time demodulator output analysis
- Graphical and numerical range indicators
- Polar, Cartesian and Z formats
- Impedance measurement
- Support for arbitrary input unit function

Description:

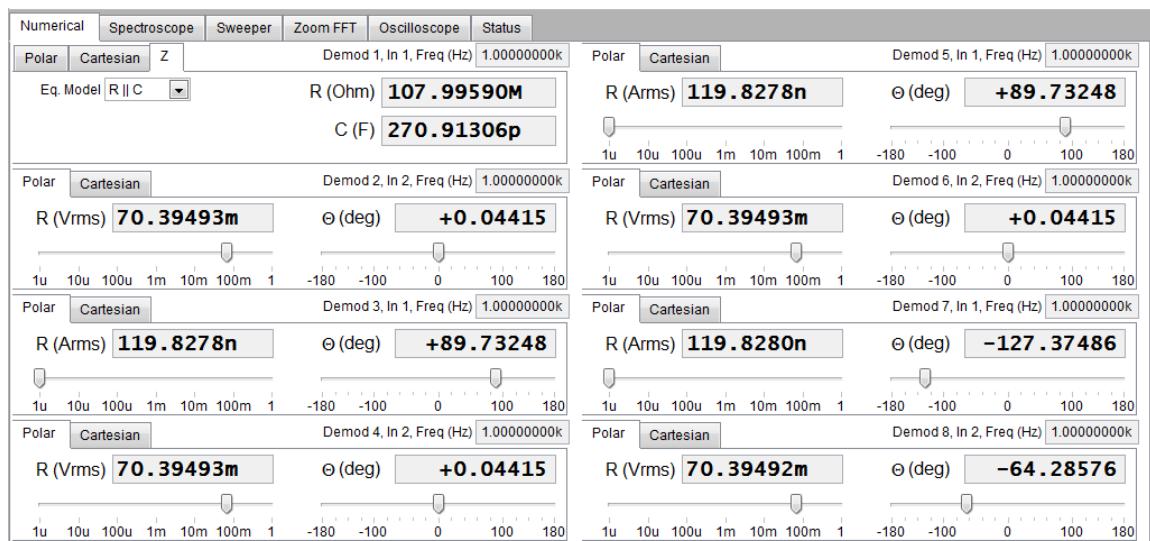


Figure 5.9. Numerical tool

Table 5.9. Polar and Cartesian tools

Control/Tool	Options/Range	Description
DEM0D 1 polar	-	press this tab to see polar (R, Θ) sample representation for the demodulator - the amplitude R is indicated in V_{RMS} while the angle Θ in degrees
DEM0D 1 Cartesian	-	press this tab to see Cartesian (X, Y) sample representation for the demodulator - the components X and Y are indicated in V_{RMS}
DEM0D 1 Z	-	press this tab to display impedance sample representation. The available representations are: <ul style="list-style-type: none"> - Z , Φ: impedance absolute value and phase - $\text{Re}(Z), \text{Im}(z)$: real and imaginary part of impedance - $R//C$: resistor in parallel with capacitor - R, C: resistor in series with capacitor - D, C: dissipation factor and capacitance - R, L: resistor in series with inductor - G, B: conductance and susceptance (i.e. inverse of Z)
DEM0D 1 frequency field	-	this field shows the current reference frequency of the demodulator
DEM0D 1 X, Y, R, Θ , Z fields	-	these fields indicate the value of the data samples as output by the demodulator filters - the update rate of these fields is equivalent to the readout rate setting - the outputs of the demodulators are in 64-bit resolution, thus much higher than represented on the screen - to profit from the full sample resolution, please save the samples to a file (save settings tab)
All other demodulators have the same functionality as DEM0D 1		

5.4.2. Spectroscope Tool

Note

The Spectroscope Tool available in ziControl for the HF2IS is identical for that of the HF2LI, please refer to [Section 4.4.2](#).

5.4.3. Sweeper Tool

Features:

- Full-featured parametric sweep tool
- Full HF2 instrument frequency range supported with many sweep modes: single, continuous (run/stop), forward, backward, bi-directional

- Different impedance models display
- Overlap display of previous sweep results with persistent display
- Normalization of sweep with calibration control
- Auto bandwidth, averaging, and display normalization
- Parametric sweeper: Frequency, Phase, Time constant, Output amplitude, Offset (Aux Out)
- Support for arbitrary input unit function

Description:

The sweeper tool uses an user-selected reference signal as the excitation voltage and measures the frequency and phase response with an user-selected demodulator. This flexibility in demodulator selection permits to drive the sample-under-test with one frequency and to perform the measurement at either the fundamental or the harmonic of the driven frequency. The selected demodulators for reference and signal input may not be used for other measurements during the sweep.

In addition to the frequency sweep, the sweeper tool can also be used to obtain response from parametric sweeps such as phase, time constant, output amplitude as well as offset from the auxiliary outputs.

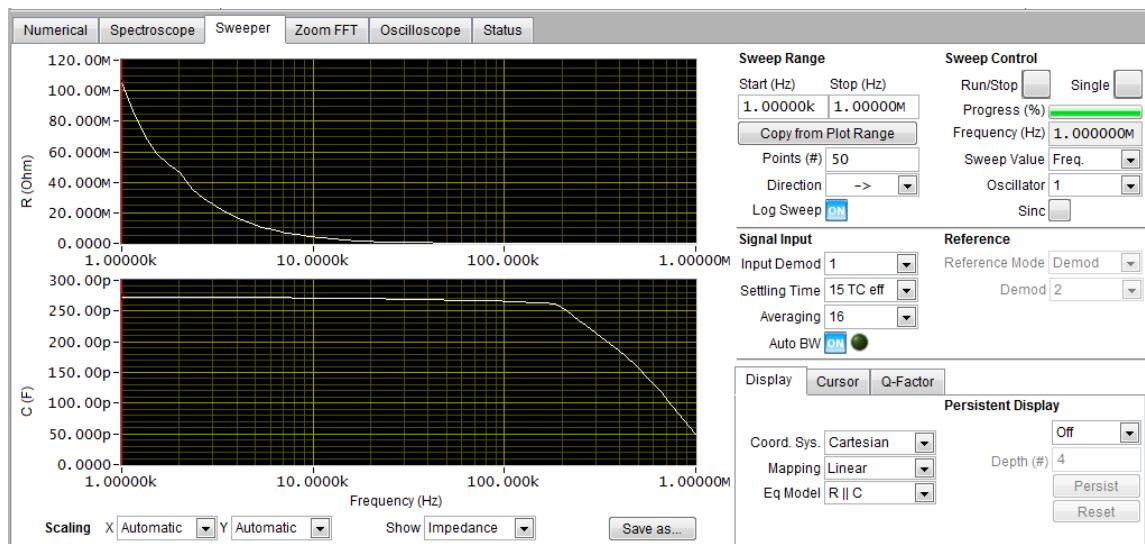


Figure 5.10. Sweeper tool

Table 5.10. Sweeper tool

Control/Tool	Options/Range	Description
Sweeper plot window	-	depicts the frequency response of the signal at signal input 1 or 2 - the measurement is made by placing a band-pass filter in discrete steps over the user-defined frequency range and simultaneously driving a sinusoidal signal at signal output 1 or 2 at the corresponding frequency
Sweeper Amp. label	User defined	default value is in Volt (V) - user defined unit can be enter in the Scale field under the Signal Inputs under the main lock-in control tab
Sweeper scale controls	Automatic	automatically fits the vertical and/or the horizontal scale to data (Automatic)

Control/Tool	Options/Range	Description
	Manual	scale is defined by the user
Show selection	Input Only	plots the measured input signal only
	Input / Ref	plots the input/ref quotient
	Impedance	plots various equivalent impedance model (only available for HF2IS)
	Ref Only	plots the reference signal only
Sweep range Start	1 μ Hz to 50 MHz	defines the starting point of the freq. sweep
	-90 to 90 deg	defines the starting point of the phase sweep
	100 ps to 600 s	defines the starting point of the TC sweep
	-1 V to +1 V	defines the starting point of the amplitude sweep
	-10 V to +10 V	defines the starting point of the aux offset sweep
Sweep range Stop	1 μ Hz to 100 MHz	defines the end point of the freq. sweep
	-90 deg to 90 deg	defines the end point of the phase sweep
	100 ps to 600 s	defines the end point of the TC sweep
	-1 V to +1 V	defines the end point of the amplitude sweep
	-10 V to +10 V	defines the end point of the aux offset sweep
Copy from Plot Range button	[press once]	after zooming into the plot, press once to adopt the start and stop frequencies from the plot
Sweep range Points	2 to 1000000	number of frequency steps (point) at which the sweeper performs its measurements - high values require long times to finish one sweep
Sweep range Direction	<->	bi-directional sweep (low-to-high and high-to-low values)
	->	sweep from low to high values
	<-	sweep from high to low values
Log Sweep button	OFF: linear sweep	samples are equidistant - plot scale is linear
	ON: logarithmic sweep	samples are logarithmic distributed - plot scale is logarithmic
Sweep control Run/Stop button	OFF: stop	stop running continuous sweep
	ON: run	start continuous sweeping at the current frequency - after attaining the stop frequency, the sweeper restarts from the frequency depending on the sweep direction
Sweep control Single sweep button	OFF: stop	stop single sweep
	ON: start	start sweeping at the start frequency - after attaining the stop frequency, the sweeper stops
Sweep control Progress indicator	-	indicates the progress of the sweep
Sweep current value	-	indicates the current value during a sweep - the label changes depending on the type of parametric sweep being performed
Sweep value selection	Frequency	frequency sweep - oscillator selection 1 to 8 available

Control/Tool	Options/Range	Description
	Phase	phase sweep - output demodulator selection 1 to 6 available
	TC	time-constant sweep - output demodulator selection 1 to 6 available
	Amplitude	amplitude sweep - output demodulator selection 1 to 8 and signal output 1 to 2 available
	Aux Offset	offset sweep (Aux Out) - auxiliary output selection 1 to 4 available
	PID Setpoint	PID setpoint sweep - PID setpoint 1 to 4 selection available
Signal Input demodulator selection	1 to 6	sweeper uses the selected demodulator using its input choice, its oscillator choice, and its filter settings to perform the measurements of the sweep
Filter Settling time selection	5 TC eff	defines the sweeping time by filter settling time - for large bandwidths, the sweeping time is restricted by the software
	15 TC eff	
	50 TC eff	
Filter Averaging selection	1	defines the number of measurements taken at each point - the final value is calculated by averaging all measurements for the specific point
	2	
	4	
	8	
	16	
	32	
	64	
Filter Auto bandwidth control (active for frequency sweep)	OFF	manual bandwidth: the noise equivalent bandwidth (NEB) of the filter is defined by the settings of the chosen demodulator
	ON	auto bandwidth: the noise equivalent bandwidth is adjusted during the sweep to capture a larger part of the spectrum for logarithmic sweeps - this option has no effect for linear sweeps
Reference Mode selection	Off	reference mode is off
	Sig Out 1	sweeper uses signal output 1 as reference
	Sig Out 2	sweeper uses signal output 2 as reference
	Demod	sweeper uses one of the selected demodulators as reference
Reference Mode Demod selection	1 to 8	demodulator selection for reference mode (not available when Reference Mode is Off)
Display Coordinate system selection	Polar	selects the coordinate systems of the plot
	Polar (Log)	
	Cartesian	
	Nyquist	
Display Mapping selection	Linear	the Y scale of the plot is displayed with a linear unit

Control/Tool	Options/Range	Description
	dB	the Y scale of the plot is displayed with a logarithmic unit, being the ratio of the physical quantity (power and intensity) relative to a $1 \text{ V}_{\text{RMS}} / \text{V}_{\text{PK}}$ reference level
	dBm	the Y scale of the plot is displayed with the logarithmic unit, being the power ratio referenced to 1 mW (dissipation resistor of 50 Ohm)
Display Result Unit selection (available for Show: Input Only and Show: Ref Only)	V_{RMS}	the Y scale of the plot reads the signal RMS voltage
	V_{PK}	the Y scale of the plot reads the signal peak voltage, $\text{V}_{\text{PK}} = \sqrt{2} * \text{V}_{\text{RMS}}$
	V_{RMS}^2	the Y scale of the plot reads the signal RMS power (squared RMS voltage)
	V_{PK}^2	the Y scale of the plot reads the signal peak power (squared peak voltage), $\text{V}_{\text{PK}}^2 = 2 * \text{V}_{\text{RMS}}^2$
	$\text{V}_{\text{RMS}}/\sqrt{\text{Hz}}$	the Y scale of the plot reads the spectral voltage density, independent of the frequency resolution - this setting is useful for noise measurements, $\text{V}_{\text{RMS}}/\sqrt{\text{Hz}} = \text{V}_{\text{RMS}} / \sqrt{(\text{frequency resolution})}$
	$\text{V}_{\text{PK}}/\sqrt{\text{Hz}}$	the Y scale of the plot reads the spectral peak density, independent of the frequency resolution - this setting is useful for noise measurements, $\text{V}_{\text{PK}}/\sqrt{\text{Hz}} = \text{V}_{\text{PK}} / \sqrt{(\text{frequency resolution})}$
	$\text{V}_{\text{RMS}}^2/\text{Hz}$	the Y scale of the plot reads the spectral RMS power density, independent of the frequency resolution - this setting is useful for noise measurements, $\text{V}_{\text{RMS}}^2/\text{Hz} = \text{V}_{\text{RMS}} / (\text{frequency resolution})$
	$\text{V}_{\text{PK}}^2/\text{Hz}$	the Y scale of the plot reads the spectral peak power density, independent of the frequency resolution - this setting is useful for noise measurements, $\text{V}_{\text{PK}}^2/\text{Hz} = \text{V}_{\text{PK}} / (\text{frequency resolution})$
Display Unwrap Phase switch	OFF / ON	enables the unwrapping of the phase in polar coordinate system
Equivalent Model selection (only visible when Show Impedance is selected in HF2IS)	$ \text{Z} $, Phi (ohm)	display sweep result as absolute impedance and phase
	R, X (ohm)	display sweep result as real and imaginary impedance
	R//C	display sweep result as equivalent RC parallel circuit
	R, C	display sweep result as equivalent RC series circuit
	R, L	display sweep result as equivalent RL series circuit
	G, B	display sweep result as real and imaginary admittance
Persistent Display	Off	previous sweep tracking turned off

Control/Tool	Options/Range	Description
	Auto	allows to observe the differences between different plots
	Manual	allows to observe the differences between different plots and to memorize a plot of choice
Persistent Display Depth	1 to 100	number of traced sweeps (Note: each sweep will be displayed in a different color)
Persistent Display Persist	[press once]	memorizes current sweep (only active with History: Manual option)
Persistent Display Reset	[press once]	resets the history depth
Q Factor	-	displays the estimated resonance Q from the frequency sweep
Save as... button	[press once]	generates a directory with the depicted sweeper plot as CSV and PNG files - a Readme.txt file including the description of the columns is saved along with the data - the user has the opportunity to define a directory name where the data is stored

Table 5.11. Sweeper tool plot scaling and cursor options

Control/Tool	Options/Range	Description
Plot X and Y scaling	Automatic	the X scale of the plot is calculated based on the FFT span, and Y scale of the plot is continuously adapted to the current demodulated samples
	Manual	the X scale and Y scale of the plot can be manually set - with manual setting, the cursors and zoom palette become available - the manual setting of the Y scaling can also be used to avoid the continuous changes in the scale limits
Plot cursor palette	[press once]	control permits to grab and move cursor point to the selected position
Plot zoom palette	[press once]	control permits to select one of 6 zoom modes including zoom in, zoom out, zoom X only, zoom Y only, and others
Cursor point control	2 to 1000000	defines the sample which the cursor is tracking
Cursor indicators	-	indicates current cursor values for X and Y scales
Cursor 2-1	-	indicates the difference between cursor 1 and 2

5.4.4. Zoom FFT Tool

Note

The Zoom FFT Tool available in ziControl for the HF2IS is identical for that of the HF2LI, please refer to [Section 4.4.4](#).

5.4.5. Oscilloscope Tool

Note

The Oscilloscope Tool available in ziControl for the HF2IS is identical for that of the HF2LI, please refer to [Section 4.4.5](#).

5.4.6. Status Tool and History Log

Note

The Status Tool and History Log available in ziControl for the HF2IS is identical for that of the HF2LI, please refer to [Section 4.4.6](#).

Chapter 6. Communication and Connectivity

This chapter describes the different possibilities to interface with an HF2 Instrument. The HF2 Series was designed with the concept that "the computer is the cockpit"; there are no controls on the front panel of the HF2 Instrument, instead the user has the freedom to configure and stream data from the instrument directly from their computer. The aim of this approach is to give the user the freedom to choose where they connect to, and how they control, their HF2 Instrument. The user can connect directly from a computer connected to the HF2 Instrument via USB or remotely from a different computer on the network, away from their experimental setup. Then, on either computer, the user can configure and retrieve data from their HF2 Instrument via a number of different interfaces, i.e. via ziControl and/or their own custom programs. In this way the user can decide which connectivity setup and combination of interfaces best suits their experimental setup and data processing needs.

We first provide an overview of how the user connects an HF2 Instrument to a PC in [Section 6.1](#) and then give an overview of how to quickly modify instrument settings using the text-based console in [Section 6.2](#). Finally, at the end of this chapter, we explain how to connect to an HF2 instrument over a public network, [Section 6.3](#).

Note

It is also possible to configure and obtain data from an HF2 Instrument via one of our APIs. Currently LabVIEW, Matlab, Python or C are available. These topics are covered in a separate document, The LabOne Programming Manual.

Note

New users could benefit by first familiarizing themselves with the instrument using ziControl, see [Chapter 3](#).

Note

Programming using the Real-time Option (ziRTK) is dealt with in [Section 8.2](#).

6.1. Instrument Connectivity Overview

The HF2 Series supports a server-based connectivity methodology for multi-user, multi-device operation. This means that it is possible to operate more than one HF2 Instrument from a single computer, that multiple users may access the same instrument, and that an instrument may be made available on a local area network. Server-based means that all communication between the user and the HF2 is via a computer program called a server, in our case ziServer. The ziServer program recognizes the device and manages all communication between the instrument and the host computer over the USB connection on one side, and the different available interfaces on the other side.

Before going into more detail, the terminology used in this chapter is explained.

- Host computer: The computer that is directly connected to the HF2 by USB. An HF2 can only be connected to one host computer, but to multiple remote computers on a local area network via ziServer running on the host.
- ziServer: A computer program that runs on the host computer and manages settings on, and data transfer to and from the HF2 by receiving commands from clients. It always has the most up-to-date configuration of the device and ensures that the configuration is synchronized between different clients.
- Remote computer: A computer, available on the same network as the host computer, that can communicate with the HF2 via the ziServer program running on the host.
- Client: A computer program that communicates with the HF2 via the server. The client can be running either on the host or the remote computer.
- API (Application Programming Interface): a collection of functions and data structures which enable communication between software components. In our case, the various APIs (e.g., LabVIEW, MATLAB®) for the HF2 provide functions to configure the device and receive measured experimental data.
- Interface: Either a client or an API.
- TCP/IP: Network communication protocols. In our case, ziServer communicates to the base API (ziAPI) using TCP/IP. This can happen either locally (entirely on the host computer) or between the host computer and remote computers.
- GUI (Graphical User Interface): A computer program that the user can operate via images as opposed to text-based commands.
- Modules: Software components that provide a unified interface to APIs to perform high-level common tasks such as sweeping data.

An overview of HF2 Instrument connectivity is shown in [Figure 6.1](#).

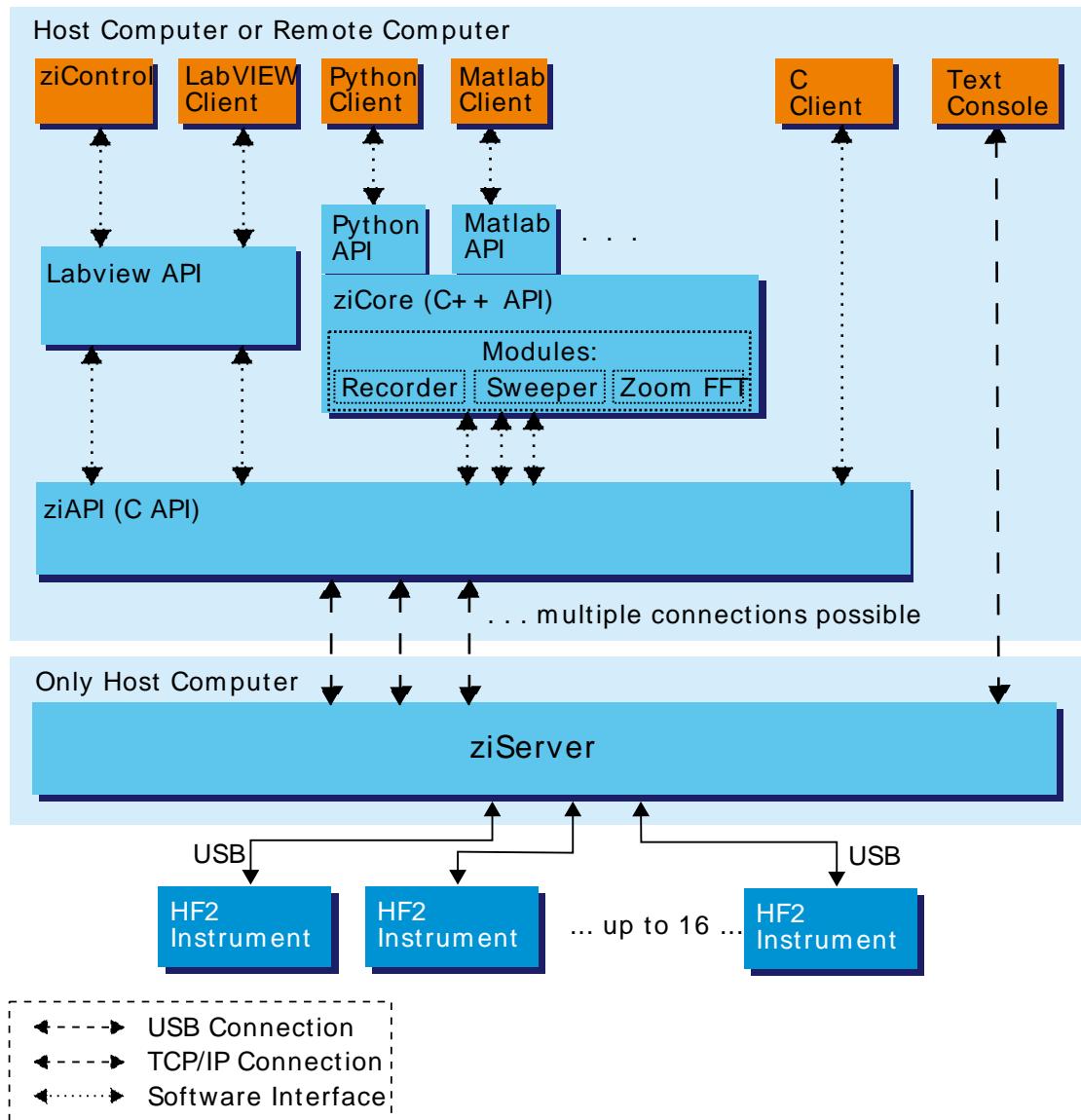


Figure 6.1. Instrument Connectivity

6.1.1. Physical Connectivity: Host and Remote Computers

In a commonly used configuration, the HF2 Instrument is connected to a host computer where both the server and the clients (denoted by the orange boxes in Figure 6.1) run. The ziServer program acts as bridge from the instrument to the various clients. For example, a user may use two clients in parallel: ziControl to configure the device and their own program created using the LabVIEW API to plot custom results streamed from the instrument. Both these clients communicate data via the same instance of ziServer and ziServer ensures that both clients are always updated with the current instrument configuration. Note however, that any combination of clients shown in Figure 6.1 may be used in parallel, limited only by the performance of the host computer and by the load from requests to ziServer. In this configuration, the top and bottom block of Figure 6.1 (denoted by the light blue box) are both running on the host computer.

Sometimes, the user wishes to use a client to control the HF2 on a remote computer. In this case, the software in the top block of Figure 6.1 runs on the remote computer, connecting via TCP/IP over the local area network to the instance of ziServer running on the host computer (which is connected to the HF2 via USB).

In total, there are three possibilities of physically connecting to an HF2 Instrument:

- On the host computer, i.e., all the software (ziServer, interfaces) is running on the same computer that is connected to the instrument via USB. This is the simplest and most common setup.
- On a remote computer connected to the host computer over a secure local area network. If a private network is available this is a simple setup, ziServer only needs to allow remote connections, see [the section called "Enabling a Remote Connection to ziServer"](#).
- On a remote computer connected to the host computer over a public, insecure network. This is a more advanced topic covered in [Section 6.3](#).

As you can now imagine, there are many possibilities to connect to an HF2 instrument. The following methods of connecting with HF2 Instruments are supported:

- Connection to and operation of an HF2 Instrument from multiple clients on different computers in parallel with automatic background update of instrument settings on all connected clients.
- Connection to and operation of up to 16 HF2 Instruments from a single host computer.
- Connection to and Operation of multiple remote HF2 Instruments that are connected on a TCP/IP LAN via a (or multiple) host computer(s), the number of which is limited by the performance of the remote computer. Note, there can only be one instance of ziServer running for one HF2 Instrument.

6.1.2. Software Connectivity: ziServer

The ziServer program provides a gateway to your HF2 Instrument from any of the programming interfaces described in this chapter. The ziServer program recognizes the device and manages all communication between the instrument and the host computer over the USB connection on one side, and the different available interfaces on the other side. Since ziServer is responsible for all communication to the instrument, it's important that only one instance of ziServer is running at any one time. This is how you can check that only one instance of ziServer is running, or is indeed running at all:

- Windows: Open Windows Task Manager with CTRL-SHIFT-ESC and check that both the processes `ziServer.exe` and `ziService.exe` are running.
- Linux: Either check manually that the process `ziServer` is running or alternatively use the `ziService` command

```
$ ziService status
```

in a terminal. You should see the output:

```
Status : ziServer is running.
```

Enabling a Remote Connection to ziServer

In order to enable connections to ziServer from a remote computer, the node `/zi/config/open` must be set to 1. To set this in ziControl go to the "Connectivity Tab" and under the "Connectivity to ziServer" section enable "From Anywhere" in the "Connectivity" setting.

6.1.3. Instrument Communication: The Node Hierarchy

In order to communicate with an HF2 Instrument via text-based commands, it is necessary to understand how the settings and measurement data of the instrument are accessed. All settings of the HF2 Instrument are organized in a file-system-like hierarchical structure. This means that it

is possible to plot a consistent tree of nodes, where the instrument settings are leaves of the tree. It is also possible to browse branches inside the tree as if the user were navigating in a file-system. This hierarchy is used, no matter which interface you use when performing measurements.

An example demonstrating the hierarchy is the representation of the first demodulator on the device, given by the node:

```
/devX/demods/0
```

which, as we've already noted, is very similar to a **path** on a computer's file-system. Note that, the top level of the path is the device that you are connected to. The demodulators are then given as a top-level **node** under your device-node and the node of the first demodulator is indexed by 0. This path represents a branch in the node hierarchy which, in this case, if we explore further, has the following nodes:

```
/devX/demods/0/adcselect
/devX/demods/0/order
/devX/demods/0/timeconstant
/devX/demods/0/rate
/devX/demods/0/trigger
/devX/demods/0/oscselect
/devX/demods/0/harmonic
/devX/demods/0/phaseshift
/devX/demods/0/sinc
/devX/demods/0/sample
```

These nodes are **leaves**, the most bottom-level nodes which represent a setting of an instrument or a field that can be read to retrieve measurement data. For example, `/devX/demods/0/adcselect` is the leaf that controls the setting corresponding to the choice of signal input for the first demodulator. To set the index of the signal input the user writes to this node. The leaf `/devX/demods/0/sample` is the leaf where the demodulator's output (timestamp, demodulated x-value, demodulated y-value) are written at the frequency specified by `/devX/demods/0/rate`. In order to obtain the demodulator output you read the values from this node by **polling** this node. Polling a node sends a request from the client to ziServer to obtain the data from the node at that particular point in time.

[Chapter 7](#) provides a full reference of nodes on HF2 Instruments and details which settings or measurement data they correspond to, whether they are read-only and, if they are writable, which values they may take (e.g., boolean, integer, floating point).

Note

The numbering on the front panel of the HF2 Instrument and the block numbering on ziControl generally start with 1, whereas the underlying instrument using the programming interfaces has a numbering notation starting with 0.

Note

A useful method to learn about paths in your HF2 Instrument is to look at the output of the history in the bottom of ziControl. The status line always shows the last applied command and you can view the entire history by clicking the "Show History" button. You will find paths like

```
/devx/sigins/0/ac = 1
```

after you switched on the AC mode for signal input 1, or

```
/devx/demods/1/rate =
7200.000000
```

after setting the readout rate of demodulator 2 to 7.2 kHz.

You can obtain a list of nodes available on your instrument as a text-file in ziControl by saving the instrument settings. Go to the "Save" tab in ziControl and in the "Save/Load HF2 Settings" click the "Save Settings..." button.

Note

We recommend that users who want to program their HF2 Instruments first familiarize themselves with the node hierarchy by browsing nodes via ziServer's text-based interface described in the next chapter. The text-based interface is an indispensable tool for HF2 programmers.

6.2. ziServer's Text-based Interface

The text-based interface is the simplest and most direct way of communicating with an HF2 Instrument and doesn't require any previous programming experience. Browsing the text interface physically happens within ziServer and since it makes use of TCP/IP sockets the user can also connect remotely over a network connection via telnet or ssh. In contrast to ziControl, this is a geeky way of using an HF2 Instrument.

After connecting to the text-based interface via telnet, you find yourself in a DOS or Unix terminal-like program, where you can browse instrument settings in the node hierarchy (Section 6.1.3). The terminal responds to known command syntax like `ls` (list all nodes in the current directory) and `cd` (select path to navigate in the directory hierarchy).

The text interface is a very powerful tool for users programming an HF2 Instrument with other interfaces such as Zurich Instrument's LabVIEW or MATLAB® API. It is a convenient way to verify the instrument's node paths and check that values have been set correctly by the interface you are actually programming with. It is also helpful for budding HF2 hackers who can use it to browse the node hierarchy and familiarize themselves with its structure.

Note

In theory, it would be possible to use the text-based interface to communicate with an HF2 Instrument from an arbitrary programming environment. However, this would require the implementation of a socket connection and a parser, and there is no exception handling should a command fail. Also, since it's a text interface, as opposed to a binary interface, data transfer is slower. Therefore, in general, we strongly encourage the user to instead use one of the existing binary interfaces documented later in this chapter as their primary programming interface.

6.2.1. Getting Started with the Text-based Interface

Preparation

The purpose of this section is to get quickly acquainted with the text interface to the ziServer. For this you will need to have installed LabOne (see Section 1.3) and have your HF2 Instrument connected to your host computer via USB. In order to access the text-based interface within ziServer, a telnet or SSH client providing a console is required.

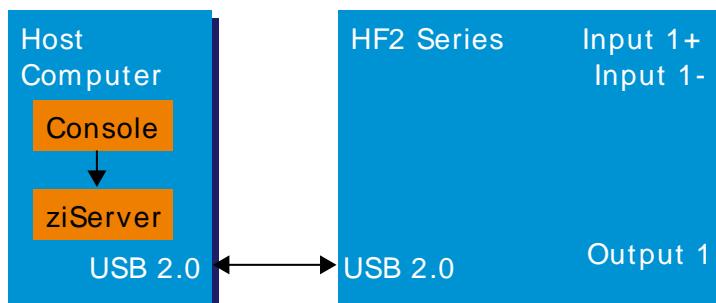


Figure 6.2. Setup for using the text-based interface

Connecting to ziServer on Windows

Zurich Instruments recommends to use the freeware PuTTY as a telnet client. PuTTY has to be configured with the following settings to connect with ziServer.

Table 6.1. PuTTY settings on Windows

Terminal category, Implicit CR in every LF	set
Session category, Host Name	localhost
Session category, Port	8005
Session category, Connection type	Telnet

Users connecting to a remote ziServer (a ziServer which is not running on the local machine, but on the host computer available on the LAN) have to configure the host name accordingly (e.g. computer.domain.com) after allowing remote connections to ziServer, see [Section 6.1.2](#).

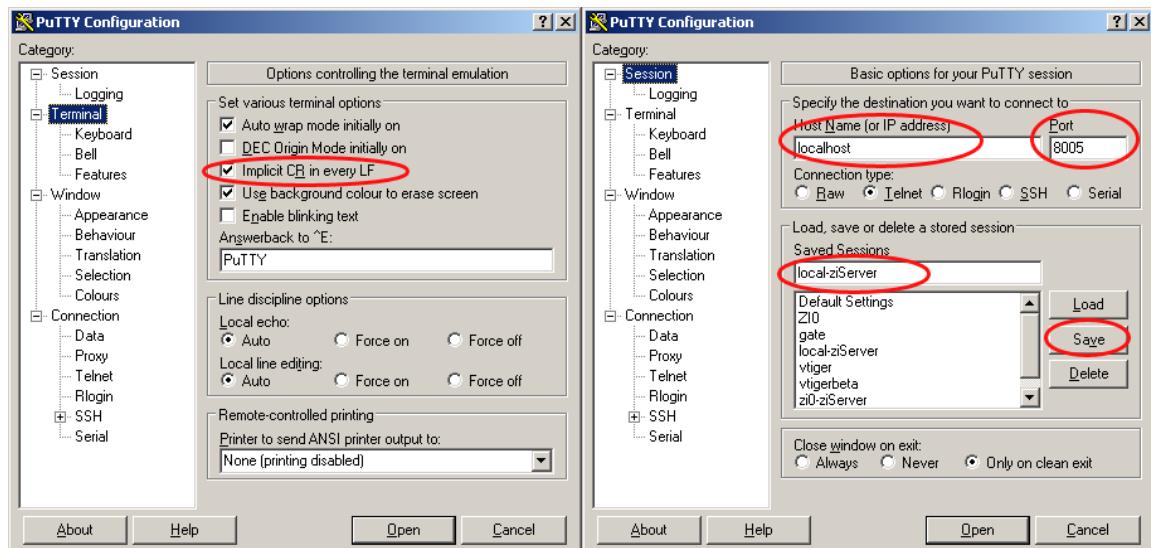


Figure 6.3. PuTTY configuration to connect to ziServer

Save the session settings with a suitable name, so that you can connect faster next time. After pressing the Open button, the following screen will appear: this message confirms successful connection to the ziServer. If the screen does not appear, or the text is missing, please check whether ziServer is running (Windows task manager, see [Section 6.1.2](#)) or check your PuTTY settings.

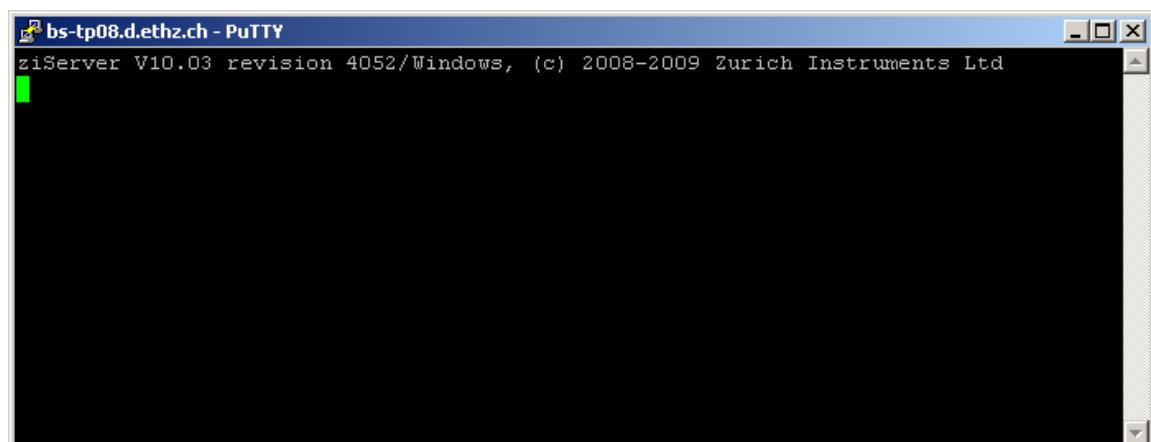


Figure 6.4. PuTTY successful ziServer connection

Connecting to ziServer on Linux

You may connect to a running ziServer from the host computer by invoking telnet in a shell:

```
user@zi:~$ telnet localhost 8005
Trying 127.0.0.1...
Connected to localhost.
Escape character is '^]'.
ziServer V17.12 revision 49524/Linux, (c) 2008-2017 Zurich Instruments AG
```

Or by using netcat:

```
user@zi:~$ nc localhost 8005
ziServer V17.12 revision 49524/Linux, (c) 2008-2017 Zurich Instruments AG
```

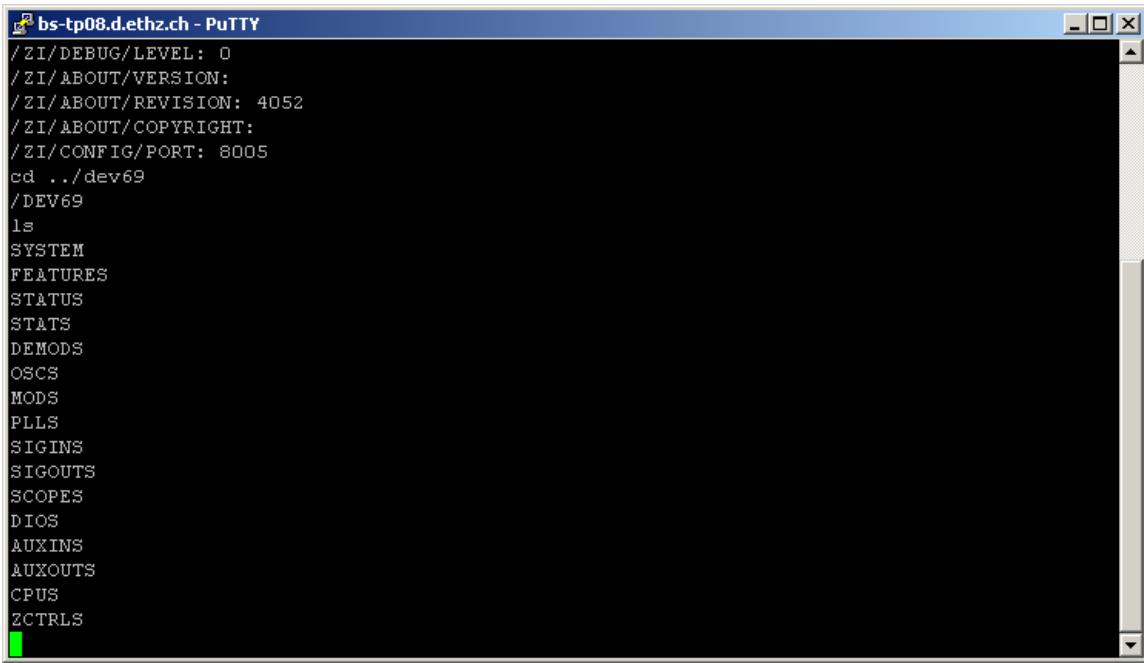
A Tour of the Text-based Interface

We start our tour with some basic commands. After successful connection, it's nice to see which instruments are connected to ziServer. An `ls` will do the job. This yields the information that we have a ZI node (the node for ziServer) and a DEVX node (denoting your HF2 Instrument). The DEVX is the serial number of the HF2 Instrument in front of you. Let's select the ziServer node with `cd zi`, list the nodes with `ls`, and then read all values of the node inside the `/ZI/` tree with `*//* ?`. Not very impressive so far.

```
bs-tp08.d.ethz.ch - PuTTY
ziServer V10.03 revision 4052/Windows, (c) 2008-2009 Zurich Instruments Ltd
ls
ZI
DEV69
cd zi
/ZI
ls
ABOUT
CONFIG
TREES
DEBUG
*//* ?
/ZI/CONFIG/OPEN: 0
/ZI/DEBUG/LEVEL: 0
/ZI/ABOUT/VERSION:
/ZI/ABOUT/REVISION: 4052
/ZI/ABOUT/COPYRIGHT:
/ZI/CONFIG/PORT: 8005
```

Figure 6.5. PuTTY tour: check server version

Let us move into the DEVX hierarchy by using the relative path `cd .../devx` (it's also possible to specify absolute paths, e.g., `cd /devx/` and investigate the structure of the node hierarchy with the `ls` command. This lists all the leaves inside of your device. Each leaf represents a setting that can be made inside of the instrument or a field that can be read to retrieve measurement data. The first level hierarchy inside the instrument is displayed in Figure 6.6.



```
/ZI/DEBUG/LEVEL: 0
/ZI/ABOUT/VERSION:
/ZI/ABOUT/REVISION: 4052
/ZI/ABOUT/COPYRIGHT:
/ZI/CONFIG/PORT: 8005
cd ../dev69
/DEV69
ls
SYSTEM
FEATURES
STATUS
STATS
DEMODS
OSCS
MODS
PLLS
SIGINS
SIGOUTS
SCOPES
DIOS
AUXINS
AUXOUTS
CPUS
ZCTRLS
```

Figure 6.6. PuTTY tour: first instrument hierarchy

This list gives a top-level insight into an HF2 Instrument showing its building blocks such as DEMODS (demodulators), OSCS (oscillators), SIGINS (signal inputs), SIGOUTS (signal outputs), SCOPES (oscilloscopes), AUXINS (auxiliary inputs), AUXOUTS (auxiliary outputs), CPUS (integrated processors), and so on. The branches and leaves that you see will depend on the options installed in your device: for instance, you will not see PLLS if you do not have the HF2PLL option installed.

It is time to dive into one branch of the instrument. Let us take oscillator 0: type `cd oscs`, then `ls` to see the branches at that level, then type `cd 0` to select the first oscillator, then list the leaves at that level, and use `* ?` to return the values of all leaves. We see for instance that `/DEVX/OSCS/0/FREQ` has a value of 2.5 MHz, see [Figure 6.7](#).

It is now possible to check that ziControl actually has the same value in the corresponding field. Note, that the block numbering notation inside of the GUI starts with 1, whereas the underlying instrument has a numbering notation starting with 0. It is also possible to change the frequency of the lock-in channel 1 inside of the GUI to 2.1 MHz, and then check the value inside the text interface by typing `* ?`. You notice that the settings changes are transparent to all clients connected to a ziServer. You can always rely on setting and data consistency.

```
bs-tp08.d.ethz.ch - PuTTY
SIGOUTS
SCOPES
DIOS
AUXINS
AUXOUTS
CPUS
ZCTRLS
cd oscs
/DEV69/OSCS
ls
0
1
2
3
4
5
cd 0
/DEV69/OSCS/0
ls
FREQ
* ?
/DEV69/OSCS/0/FREQ: 2500000.00000
* ?
/DEV69/OSCS/0/FREQ: 2100000.00000
```

Figure 6.7. PuTTY tour: leaves of an oscillator

Next, to change the value of the oscillator frequency, for instance to 4.5 MHz, type `freq 4500000`. The same effect can be achieved by using the absolute path `/DEV8/OSCS/0/FREQ 4500000`. Please note that the value in the GUI has changed from 2.1 MHz to 4.5 MHz in the meantime.

The wildcard symbol `*` can simplify life when many similar settings need to be made. Lets for instance check the frequency of all oscillators at once: type `cd ..`, and then `*/freq ?`, and then change all frequencies to 3.6 MHz with `*/freq 3600000`. This is where the text interface is becoming pretty powerful.

```
bs-tp08.d.ethz.ch - PuTTY
* ?
/DEV69/OSCS/0/FREQ: 2500000.00000
* ?
/DEV69/OSCS/0/FREQ: 2100000.00000
freq 4500000
/DEV69/OSCS/0/FREQ: 4500000.00000
/dev69/oscs/0/freq 4500000
/DEV69/OSCS/0/FREQ: 4500000.00000
cd ..
/DEV69/OSCS
*/freq ?
/DEV69/OSCS/4/FREQ: 1000000.00000
/DEV69/OSCS/2/FREQ: 1000000.00000
/DEV69/OSCS/1/FREQ: 2500001.00000
/DEV69/OSCS/5/FREQ: 1000000.00000
/DEV69/OSCS/0/FREQ: 4500000.00000
/DEV69/OSCS/3/FREQ: 1000000.00000
*/freq 3600000
/DEV69/OSCS/4/FREQ: 3600000.00000
/DEV69/OSCS/2/FREQ: 3600000.00000
/DEV69/OSCS/1/FREQ: 3600000.00000
/DEV69/OSCS/5/FREQ: 3600000.00000
/DEV69/OSCS/0/FREQ: 3600000.00000
/DEV69/OSCS/3/FREQ: 3600000.00000
```

Figure 6.8. PuTTY tour: using the wildcard symbol

One word on scripting. It is possible to manually compile several settings in a file using the syntax `path value`, then to copy-paste them into the terminal window. The sequence will be recognized by the ziServer and all defined settings will be made.

Note, another useful method to learn about the paths in your HF2 Instrument is to look at the bottom of ziControl after changing configuration (see this [Note](#)). The complete command history of a session is stored in the LabVIEW Data directory, file `com.zhinst.ziControlStatusLog.txt`.

This concludes getting started with text-based programming. Zurich Instruments hopes you found it useful, and hopes you are going to perform some tutorials in [Chapter 3](#). Thank you for measuring with Zurich Instruments.

6.2.2. Command Reference

Commands

The following lists all available commands in the text-based interface, it can be viewed in the interface by typing `help`.

```
Trying 127.0.0.1...
Connected to localhost.
Escape character is '^'.
ziServer V17.12 revision 49524/Linux, (c) 2008-2017 Zurich Instruments AG
help
```

```
ziServer V17.12 revision 49524/Linux, (c) 2008-2017 Zurich Instruments AG
```

All parameters from and to the device are organized in a tree structure. This simple interface allows you to interact with the device and use all its features based on an ascii-protocol.

The interface holds a current working node to which all paths you give are relative unless you begin a path with a `"/"`. You may use `.."` to reference a node one level higher in hierarchy. In all paths wildcards `"*"` may be used.

The following commands are recognized by the ascii-interface:

```
[path] [value] Sets the value of the node(s) at the specified path.
[path] ? Gets the value of the node(s) at the specified path.
sel|cd [path] Sets the working path according to the provided one.
sel|cd ? Prints the current working path.
ls [path] Lists all children available in the given path.
tr [path] Prints all children and children's children of the specified path as a
tree.
subs [path] Subscribes the nodes of the given path for event forwarding.
unsubs [path] Unsubscribes the nodes of the given path from event forwarding.
exit Terminates the running session
help Shows this page
```

Note

The text-based interface is case insensitive.

Nodes, Leaves and Paths

Every setting of the instrument is represented by a leaf as a terminal of a tree of nodes. There are also leaves which are not settings, but for instance used to retrieve data from the instrument. For each leaf there is a path and the related value.

```
path_list = path [path]
path = [/|/..|*]name[/name|*|**]
```

In the syntax above a name is a string, and the path is a list of names separated by a slash. If a path starts with a slash, it is an absolute path starting at the root of the hierarchy. The asterisk is a wildcard meaning all nodes at a given hierarchy, and two points in a row means one hierarchy higher.

Navigation and Trees

The navigation inside the text interface is performed with the `sel`/`cd`/`ls`/`tr` commands.

```
sel or cd [?|..|path]
ls [path]
tr [path]
info [path]
```

The command `cd ?` feedbacks the current path, `cd ..` moves up one tree level, `cd path` moves down one tree level. `sel` and `cd` are equivalent commands. `ls` lists the tree available on the current path, `ls path` lists the tree available on the specified path, `tr` lists the complete tree on the current path, `tr path` lists the complete tree on the specified path, `info` feedbacks the help string of the current path, and `info path` reports the help string of a given path.

Get and Set Node Values

The values of nodes are read and changed with the following syntax.

```
path ?
path_list value
```

The command `path ?` returns the value of path, `path value` sets the specified node to value, and `path_list value` sets several nodes to value. Some examples:

```
about/* ?           // return values of leaves at path
devx/demods/0/* ?
/zi/config/* ?

/devx/demods/0/adcselect ?    // return value at path
/devx/demods/0/adcselect 0    // set value of leaf

/devx/demods/0/adcselect /devx/demods/1/adcselect 1
                           // multiple set value
```

Subscriptions

The ziServer provides a mechanism to automatically send all changes to a leaf to a subscribed client. This mechanism efficiently informs a client whenever a setting or a data of the instrument has changed without the need of active polling. It is possible to subscribe to single leaves, or full trees.

When a value of a subscribed leaf changes, the updated value is sent to the client. Most often samples, error and status nodes are subscribed. If one needs to maintain a user interface, then this can be done using subscriptions.

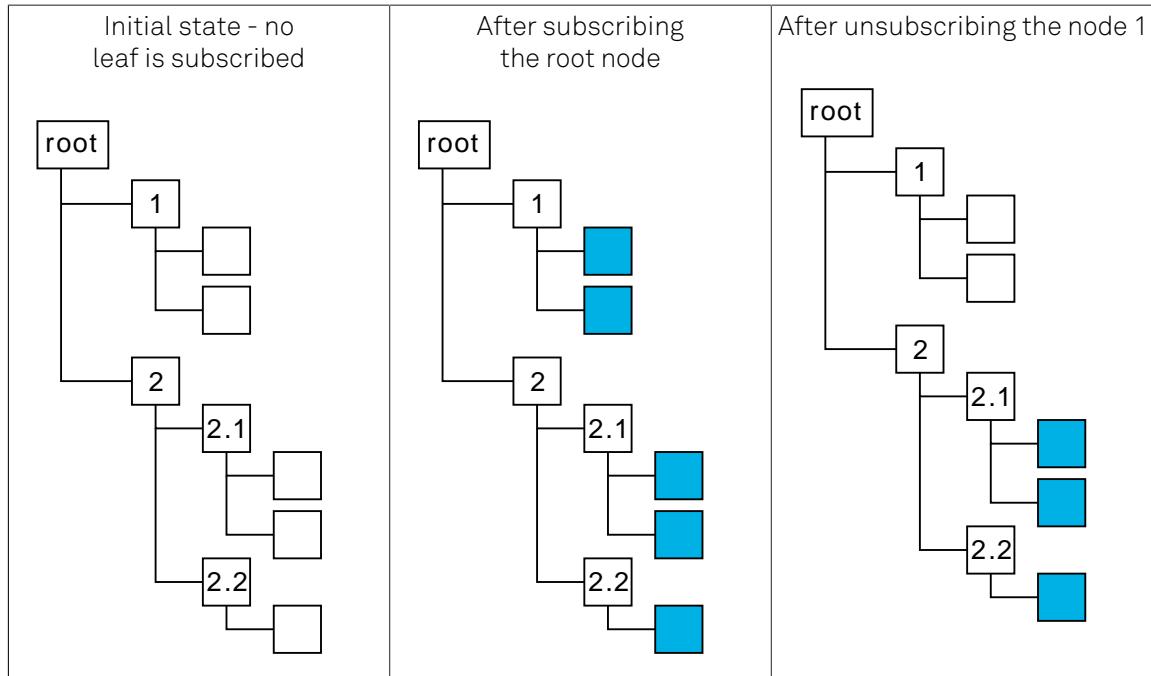
If you subscribe or unsubscribe from a node which is not a leaf, the subscription propagates to all nodes of the subtree. For example, you could first subscribe a subtree and then unsubscribe

specific nodes within this subtree and still receive events for all nodes except for unsubscribed ones.

```
subs path_list      // subscribe
unsubs path_list    // unsubscribe

path value          // return value for subscribed leaf
```

The following sequence illustrates subscribe and unsubscribe commands following each other, where turquoise leafs denote subscribed leafs.



The first image shows the initial state with no leaf subscribed. This state corresponds to a newly initiated ziServer session. After subscribing the root node, all leafs become subscribed. Then it is for instance possible to unsubscribe node 1 in order to leave just the leafs below node 2 subscribed.

Scripting

It is possible to prepare a sequence of commands in a text editor and copy-paste them into the terminal session. The console will send all commands to the ziServer and the server will interpret them one by one.

Note

Use the right-mouse button in order to copy-paste into a Windows Putty session.

```
cd /DEVX
SIGOUTS/*/ON 0
SIGOUTS/0/RANGE 1
SIGOUTS/0/AMPLITUDES/0 1
SIGOUTS/0/ENABLES/* 0
SIGOUTS/0/ENABLES/0 1
OSCS/0/FREQ 300000
SIGINS/0/RANGE 10
DEMODS/0/ORDER 2
DEMODS/0/RATE 1000
```

6.3. Connecting to ziServer over insecure or firewalled networks

If you want to connect to the ziServer over insecure, public networks like the public internet, you need to consider that the TCP/IP connection to the ziServer is unsecured. Also many firewalls will not allow traffic to port 8005. There are two common solutions to this problem. Either a VPN or a ssh port tunneling/forwarding. In this section ssh port tunneling/forwarding is described.

6.3.1. SSH port forwarding

You can use ssh to connect to a remote computer and use this connection to tunnel ziServer traffic between the local and remote computer.

To illustrate how port forwarding works, let us use an example. Suppose you have two buildings. In Lab #1, there is the lab with computers residing in the subnet 10.1.1.* and the HF2 is connected to one of these computers. At your Home, there are office computers residing in the subnet 10.2.2.*. The computers in Lab #1 are running the ziServer application that uses an unencrypted TCP/IP session to communicate data with, e.g., ziControl at your home. The firewall of the Lab and your Home might not permit this connection to be initiated. There are two kinds of port forwarding: local and remote forwarding. They are also called outgoing and incoming tunnels, respectively. Local port forwarding forwards traffic coming to a local port to a specified remote port. For example, all traffic coming to port 1234 on the client could be forwarded to port 8005 on the server (host).

The value of localhost is resolved after the Secure Shell connection has been established – so when defining local forwarding (outgoing tunnels), localhost refers to the server (remote host computer) you have connected to. Remote port forwarding does the opposite: it forwards traffic coming to a remote port to a specified local port. For example, all traffic coming to port 1234 on the server (host) could be forwarded to port 8005 on the client (localhost).

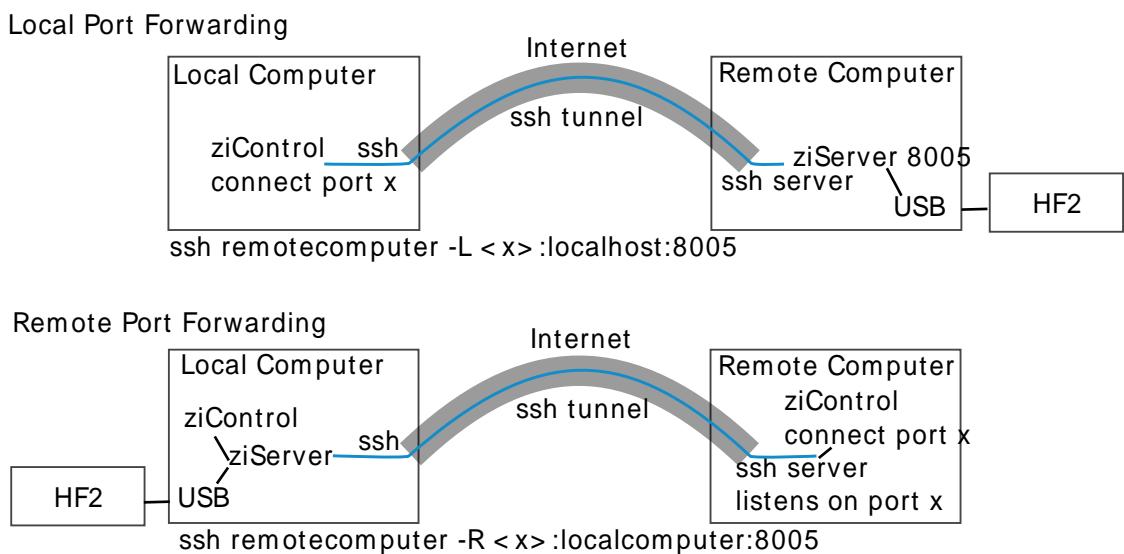


Figure 6.9. Secure connectivity

6.3.2. Local port forwarding

Accessing a service (in this example ziServer port TCP/8005) on a machine in the laboratory (10.1.1.*) from your machine at home (10.2.2.*), simply by connecting to the server `work.example.org` at work:

```
$ ssh user@work.example.org -L 10000:172.16.10.10:8005
```

We see the ziServer is available on the loop back interface only, listening on port TCP/10000:

```
$ netstat -tunelp | grep 10000  
tcp 0 0 127.0.0.1:10000 0.0.0.0:* LISTEN 1000 71679 12468/ssh
```

From your home machine, you should be able to connect to the machine at work:

```
$ telnet localhost 10000
```

By specifying localhost and port 10000 in ziControl you can connect with ziControl. Note that port 10000 is chosen arbitrarily.

6.3.3. Local port forward for anyone at home

If you want other people on your home subnet to be able to reach the machine at work by SSH, add the global option -g:

```
$ ssh user@work.example.org -L 10000:172.16.10.10:22 -g
```

We now see the service is available on all interfaces on your home computer (10.2.2.5), available for anyone to connect to on the local subnet:

```
$ netstat -tunelp | grep 10000 tcp 0 0 0.0.0.0:10000 0.0.0.0:* LISTEN  
1000 72265 12543/ssh
```

Anyone on your local subnet should be able to connect to the machine at work by doing this:

```
$ telnet 10.2.2.5 10000
```

By specifying host 10.2.2.5 and port 10000 in ziControl you can connect with ziControl.

6.3.4. Remote port forwarding

Giving access to a ziServer (port TCP/8005) on your home machine (10.2.2.5) to people at work:

```
$ ssh user@work.example.org -R 10000:10.2.2.5:8005
```

We see on our server at work (on the loop back interface on port TCP/10000) that we have access to our SSH server at home:

```
work.example.org$ netstat -tunelp | grep 10000 tcp 0 0 127.0.0.1:10000  
0.0.0.0:* LISTEN 0 73719534 3809/1
```

People logged in on the machine `work.example.org` now should be able to SSH into your home machine by doing:

```
work.example.org$ telnet localhost 10000
```

6.3.5. Remote port forwarding for anyone at work

If you want everybody on the subnet at work to be able to SSH into your home machine, there is no -g option for remote forward, so you need to change the SSH configuration of `work.example.org`, add to `sshd_config`:

```
GatewayPorts yes
```

Connect just as before:

```
home$ ssh user@work.example.org -R 10000:10.2.2.5:8005
```

Now, it is listening on all interfaces on the server at work:

```
work.example.org$ netstat -tunelp | grep 10000  tcp  0  0  0.0.0.0:10000
0.0.0.0:*  LISTEN  0  73721060  4426/1
```

Anyone at work can now connect to your home machine by SSH via the server:

```
anyone.example.org$ telnet work.example.org 10000
```

Chapter 7. Node Definitions

This chapter provides full information about the HF2 Instrument's settings and output. All settings (e.g., demodulator filter order) and results (e.g., demodulator output) are available to the user in a hierarchical tree structure which can be read from and written to. Having the settings of the instrument available to the user in a tree-like structure allows the user to program the instrument for readily repeating complicated experimental setups.

An overview of the node tree structure is provided in [Section 7.1](#) and detailed information about each node, such as whether it's readable or writable, is provided and in [Section 7.2](#).

Note

For an introduction to the node hierarchy see [Section 6.1.3](#) and to get familiar with reading from and writing to nodes in ziServer's text-based interface see [Section 6.2.1](#). See also the introduction to [Chapter 6](#) for an overview of the powerful means of programming the HF2 Instrument.

7.1. Overview

Table 7.1. Overview of the Node Tree

Node	Short Description
ZI	This node represents the instance of the ziServer your are connected to.
ABOUT	Node containing information about the server you are connected to.
VERSION	The version of this program.
REVISION	The revision of this program.
FWREVISION	The revision of the used firmware.
COPYRIGHT	The copyright string of this program.
DATASERVER	The name identifier of this data server.
CONFIG	Configuration data of the current instance of the server.
PORT	Configures the TCP/IP port on which the ziServer listens.
OPEN	Configures whether the ziServer should be open for connections from outside the local host.
TREES	Messages on tree changes.
MDS	Multi-device-synchronization.
GROUPS	Nodes for multi-device-synchronizations.
0...n	Nodes of a multi-device-synchronization group.
DEVICES	Devices in this group.
STATUS	Status of the synchronization in this group.
LOCKED	Group is locked.
KEEPALIVE	Acquire lock on this group.
CLOCKBASE	Provides timebase value for the server nodes
DEV0...n	This node represents a single device connected to the server.
CLOCKBASE	Provides clockbase value for the device
SYSTEM	Nodes providing system information and settings.
EXTCLK	Boolean value switching from internal to external clock.
HWREVISION	The revision of the main-board.
SYNCENABLE	Boolean value enabling multi-device timestamp synchronization over ZSync.

Node	Short Description
SYNCRESET	Boolean value activating timestamp reset over ZSync.
SYNCTIME	The timestamp to load when timestamp reset is activated.
ACTIVEINTERFACE	Node providing the active interface of the device.
PROPERTIES	Group of properties nodes.
MINFREQ	Minimum oscillator frequency of the device.
MAXFREQ	Maximum oscillator frequency of the device.
NEGATIVEFREQ	Device does support negative frequencies.
TIMEBASE	Time base of the device.
FREQRESOLUTION	Frequency resolution of the device.
MINTIMECONSTANT	Minimum filter time constant of the device.
MAXTIMECONSTANT	Maximum filter time constant of the device.
FEATURES	Node containing information on features of the device.
SERIAL	Node providing the serial number of the device.
DEVTYPE	Node providing a string about the type of device.
OPTIONS	Node giving information on enabled options.
CODE	Node providing a mechanism to write feature codes.
STATUS	Nodes providing status information from the device.
TIME	The current timestamp.
FLAGS	Node containing some status flags.
BINARY	A binary representation of all flags.
PLLLOCK	Flag indicating if the internal PLL for clock generation has locked.
DCMLOCK	Flag indicating if the internal digital clock manager (DCM) has locked.
FX2RX	Flag indicating if the device receives data via USB.
PKGLOSS	Flag indicating that the device lost data when sending via USB.
MIXERCLIP	Flags indicating that the internal mixer is clipping.
0...n	Flag indicating that this mixer-channel is clipping.
ADCCLIP	Flags indicating that the AD-converter is clipping.

Node	Short Description
0...n	Flag indicating that this ADC-channel is clipping.
SCOPESKIPPED	Flag indicating that scope data has been skipped.
DEMODSAMPLELOSS	Flag indicating that demodulator data has been lost.
FIFOLEVEL	Percentage of TX FIFO used.
ADCOMIN	The minimum value on Signal Input 1 (ADC0) during 100 ms.
ADCOMAX	The maximum value on Signal Input 1 (ADC0) during 100 ms.
ADC1MIN	The minimum value on Signal Input 2 (ADC1) during 100 ms.
ADC1MAX	The maximum value on Signal Input 2 (ADC1) during 100 ms.
ECHOWRITE	32bits written to this node will be echoed back via ECHOREAD node.
ECHOREAD	32bits written to ECHOWRITE node are echoed here.
STATS	Nodes providing statistical data about the device.
BYTESSENT	Total amount of bytes sent via USB.
BYTESRECEIVED	Total amount of bytes received via USB.
MEANPOLLCNT	Average poll-count.
MEANMSGCNT	Average message-count.
PHYSICAL	Group of nodes providing some physical information on the device.
1V2	Actual voltage of the 1.2 Volts supply.
1V8	Actual voltage of the 1.8 Volts supply.
2V5	Actual voltage of the 2.5 Volts supply.
3V3	Actual voltage of the 3.3 Volts supply.
5V0	Actual voltage of the 5.0 Volts supply.
TEMP	Actual temperature.
OVERTEMPERATURE	Too high FPGA temperature detected during session.
DEMOS	Demodulator nodes.
0...n	Nodes of a single demodulator
ADCSELECT	Selects the index of the signal input for the demodulator.
ORDER	Selects the order of the low-pass filter.

Node	Short Description
TIMECONSTANT	Sets the time constant of the low-pass filter.
RATE	The number of output values sent to the computer per second.
ENABLE	Enables the demodulator data stream.
TRIGGER	Sets the trigger- and gating-functionality of the demodulator.
OSCSELECT	Index of the oscillator used to demodulate the signal.
HARMONIC	The harmonic of the base frequency to be used.
FREQ	Frequency to of the demodulator.
PHASESHIFT	The phase shift of the demodulator.
SINC	Boolean value enabling Sinc filter functionality.
SAMPLE	Samples of the demodulator are given out at this node.
OSCS	Oscillator nodes.
0...n	Nodes of an oscillator.
FREQ	Frequency to of the oscillator.
MODS	Modulator option nodes.
0...n	Nodes of a Modulator.
ENABLE	Enables the modulation.
OUTPUT	Modulation output.
MODE	Modulation mode.
FREQDEVENABLE	In FM mode, enable peak deviation.
FREQDEV	In FM mode, set peak deviation value.
INDEX	In FM mode, set modulation index value. The modulation index equals peak deviation divided by modulation frequency.
RATE	The number of output values sent to the computer per second.
TRIGGER	Sets the trigger- and gating-functionality of the demodulator.
CARRIER	Group of carrier nodes.
INPUTSELECT	Signal Input for the carrier demodulation.
ORDER	Filter order for carrier demodulation.
TIMECONSTANT	Sets the time constant of the carrier low-pass filter.

Node	Short Description
OSCSELECT	Index of the oscillator used to demodulate the signal.
HARMONIC	Harmonic of the carrier frequency.
PHASESHIFT	The phase shift of the carrier demodulator.
ENABLE	Enables the carrier data stream.
AMPLITUDE	Carrier amplitude.
SIDEBANDS	Group of sideband nodes.
0...n	
MODE	Sideband selector.
INPUTSELECT	Signal Input for the sideband demodulation.
ORDER	Filter order for sideband demodulation.
TIMECONSTANT	Sets the time constant of the sideband low-pass filter.
OSCSELECT	Index of the oscillator used to demodulate the signal.
HARMONIC	Harmonic of the sideband frequency.
PHASESHIFT	The phase shift of the sideband demodulator.
ENABLE	Enables the sideband data stream.
AMPLITUDE	Sideband amplitude
SAMPLE	Modulation Samples.
PLLs	PLL nodes.
0...n	Nodes of a PLL.
ADCSELECT	Selects an input for the PLL.
AUTOCENTER	Switches auto-center.
FREQCENTER	Selects a center frequency.
FREQRANGE	Selects a frequency range for the PLL.
ENABLE	Enables the PLL.
ERROR	Error of the PLL.
AUTOTIMECONSTANT	Switches external time constant control.
TIMECONSTANT	The external time constant.
AUTOPID	Switches external PID.
P	Proportional gain of the PID controller.
I	Integral gain of the PID controller.
D	Derivative gain of the PID.

Node	Short Description
FREQDELTA	Frequency deviation from center frequency.
ADCTHRESHOLD	Threshold for edge detection.
AUXAVG	Delta frequency averaging control.
SETPOINT	The setpoint in degrees of the PLL.
HARMONIC	The harmonic of the base frequency to be used.
ORDER	Selects the order of the low-pass filter.
RATE	Update rate information.
OSCSELECT	Index of the oscillator used.
DEMODSELECT	Source demodulator.
LOCKED	Lock indicator for the PLL.
PIDS	PID nodes.
0...n	Nodes of a PID.
INPUT	Selects the input for the PID.
INPUTCHANNEL	If applicable, selects the channel of the selected INPUT.
OUTPUT	Selects the output for the PID.
OUTPUTCHANNEL	If applicable, selects the channel of the selected OUTPUT.
OUTPUTDEFAULTENABLE	If OUTPUTDEFAULTENABLE is set, the value specified by OUTPUTDEFAULT will be applied when the PID is switched off.
OUTPUTDEFAULT	If OUTPUTDEFAULTENABLE is set, this node specifies the value to be applied.
P	Proportional gain.
I	Proportional gain for integrator.
D	Proportional gain for differentiator.
SETPOINT	Target settle point.
SETPOINTSELECT	Set point selection.
MONITOROFFSET	Offset for the monitor output.
MONITORSCALE	Scale for the monitor output.
ERROR	Shows the error value.
CENTER	Sets the output center point.
RANGE	Sets the output range.
SHIFT	Shows the output shift.
ENABLE	Enable PID controller.

Node	Short Description
RATE	Control update rate.
TIPPROTECT	Contains nodes for configuring the TipProtect functionality.
ENABLE	Enable TipProtect for the PID controller.
PLL	Selects a PLL for TipProtect.
ACTIVE	Indicates whether TipProtect is active.
ACTIVETIMECONSTANT	Time constant when TipProtect is active.
ACTIVETHRESHOLD	Threshold for the active state.
INACTIVETIMECONSTANT	Time constant when TipProtect is inactive.
INACTIVETHRESHOLD	Threshold for the inactive state.
DEMOD	Contains nodes for configuring the demodulator settings of the PID.
ADCSELECT	Selects the index of the signal input for the demodulator.
ORDER	Selects the order of the low-pass filter.
TIMECONSTANT	Sets the time constant of the low-pass filter.
HARMONIC	The harmonic of the base frequency to be used.
SIGINS	Signal Input nodes.
0...n	Nodes of a signal input.
RANGE	Voltage range for the signal input.
AC	Boolean value setting for AC coupling of the Signal Input.
IMP50	Boolean value enabling 50 Ohm input impedance termination.
DIFF	Boolean value switching differential input mode.
SIGOUTS	Signal Output nodes.
0...n	Nodes of a Signal Output.
ON	Switches the output on and off.
ADD	Switches the output adder on and off.
RANGE	Selects the output range for the Signal Output.
OFFSET	Offset added to the Signal Output.
ENABLES	Switches for channels in the mixer.
0...n	Switches a channel in the mixer on and off.
AMPLITUDES	Amplitudes for channels in the mixer.

Node	Short Description
0...n	Fraction of the output range added to the output signal.
WAVEFORMS	
0...n	Waveforms for a channel in the mixer.
SCOPES	Scope nodes.
0...n	Nodes of a scope.
ENABLE	Enables the scope.
CHANNEL	Selects the channel for which scope data should be provided.
TRIGCHANNEL	Selects the channel which should be used as source for the scope's trigger.
BWLIMIT	The bandwidth-limit for the scope.
TRIGEDGE	Selects whether the scope should trigger on rising or falling edge.
TRIGLEVEL	Level at which a trigger is raised.
TRIGHOLDOFF	Time to wait for re-arming the trigger after one occurred.
TIME	Timescale of the scope wave (logarithmic decimation).
WAVE	Samples of scope-waveforms.
DIOS	DIO nodes.
0...n	Nodes of a DIO.
EXTCLK	Selects whether an external clock source should be used.
DECIMATION	Decimation for the sample rate of the DIO.
DRIVE	Selects if the outputs should be driven.
OUTPUT	Bits to output.
SYNCSELECT0	Source to output the sync signal on bit 0.
SYNCSELECT1	Source to output the sync signal on bit 1.
INPUT	Samples of the input.
AUXINS	Nodes of auxiliary inputs.
0...n	Node for an aux in.
AVERAGING	Averaging of the samples.
SAMPLE	Auxiliary input samples.
VALUES	Nodes of a auxins values.
0	Input 0 value.

Node	Short Description
1	Input 1 value.
AUXOUTS	Nodes of Auxiliary outputs.
0...n	Nodes of an Auxiliary output.
VALUE	Output value.
OUTPUTSELECT	Signal to be given out.
DEMODSELECT	Source demodulator.
SCALE	Scaling of the signal which is given out.
OFFSET	Value to be added to the output.
CPUS	Nodes for the real-time CPUs.
0...n	Node for one real-time CPU.
WORKLOAD	Usage of the processor-time.
PROGRAM	Node to write user programs to.
OUTPUT	Node containing the standard output stream written by the real time program.
USERREGS	General purpose registers to transfer data.
0...n	General purpose register.
ZCTRLS	Node containing connected ZCtrl devices.
0...n	ZCtrl instance
CAMP	A ZI current-amplifier connected to a ZCtrl port.
AVAILABLE	1 when HF2CA is connected to the corresponding ZCtrl port.
R	Chooses a value for the shunt-resistor.
GAIN	Switches between factor 1 and 10 gain.
DC	Switches between AC coupling and DC coupling.
SINGLEENDED	Switches between differential and single-ended input.
TAMP	A ZI transimpedance-amplifier connected to a ZCtrl port.
AVAILABLE	1 when HF2TA is connected to the corresponding ZCtrl port.
BIASOUT	Switches between internal and external bias.
EXTBIAS	Switches the external bias.
0...n	A Channel of a transimpedance amplifier.
CURRENTGAIN	Chooses a value for the current gain.

Node	Short Description
DC	Switches between AC and DC Mode.
VOLTAGEGAIN	Chooses a value for the voltage gain.
OFFSET	Adjust offset value.

7.2. Nodes

7.2.1. /ZI

This node represents the instance of the ziServer your are connected to.

7.2.2. /ZI/ABOUT

Node containing information about the server you are connected to.

7.2.3. /ZI/ABOUT/VERSION

The version of this program.

Write	-
Read	-
Setting	No

7.2.4. /ZI/ABOUT/REVISION

The revision of this program.

Write	-
Read	Integer Number
Setting	No

7.2.5. /ZI/ABOUT/FWREVISION

The revision of the used firmware.

Write	-
Read	Integer Number
Setting	No

7.2.6. /ZI/ABOUT/COPYRIGHT

The copyright string of this program.

Write	-
Read	-
Setting	No

7.2.7. /ZI/ABOUT/DATASERVER

The name identifier of this data server.

Write	-
Read	-

Setting	No
---------	----

7.2.8. /ZI/CONFIG

Configuration data of the current instance of the server.

7.2.9. /ZI/CONFIG/PORT

Configures the TCP/IP port on which the ziServer listens.

Write	Integer Number
Read	Integer Number
Setting	No
Default	8005
Range	1024 to 65535

7.2.10. /ZI/CONFIG/OPEN

Configures whether the ziServer should be open for connections from outside the local host.

Write	Integer Number				
Read	Integer Number				
Setting	No				
Unit	Boolean				
Default	0 (server only listens to localhost)				
Values	<table> <tr> <td>0</td> <td>server only listens to localhost</td> </tr> <tr> <td>1</td> <td>server is open for connections from outside</td> </tr> </table>	0	server only listens to localhost	1	server is open for connections from outside
0	server only listens to localhost				
1	server is open for connections from outside				

7.2.11. /ZI/TREES

Messages on tree changes.

Write	-
Read	-
Stream	-
Setting	No

Details

This node sends out a message every time a device is connected or disconnected or its tree has changed.

7.2.12. /ZI/MDS

Multi-device-synchronization.

7.2.13. /ZI/MDS/GROUPS

Nodes for multi-device-synchronizations.

7.2.14. /ZI/MDS/GROUPS/0...n

Nodes of a multi-device-synchronization group.

7.2.15. /ZI/MDS/GROUPS/0...n/DEVICES

Devices in this group.

Write	-
Read	-
Setting	No

7.2.16. /ZI/MDS/GROUPS/0...n/STATUS

Status of the synchronization in this group.

Write	Integer Number
Read	Integer Number
Setting	No
Default	0

7.2.17. /ZI/MDS/GROUPS/0...n/LOCKED

Group is locked.

Write	Integer Number
Read	Integer Number
Setting	No
Unit	Boolean
Default	No

7.2.18. /ZI/MDS/GROUPS/0...n/KEEPALIVE

Acquire lock on this group.

Write	Integer Number
Read	-
Setting	No
Unit	Boolean
Default	No

7.2.19. /ZI/CLOCKBASE

Provides timebase value for the server nodes

Write	-
Read	Integer Number
Setting	No

7.2.20. /DEV0...n

This node represents a single device connected to the server.

Note

The number of this node is the serial number of the connected device.

7.2.21. /DEV0...n/CLOCKBASE

Provides clockbase value for the device

Write	-
Read	Integer Number
Setting	No

7.2.22. /DEV0...n/SYSTEM

Nodes providing system information and settings.

7.2.23. /DEV0...n/SYSTEM/EXTCLK

Boolean value switching from internal to external clock.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Boolean
Default	0 (Internal clock)
Values	0 Internal clock 1 External clock

Details

When using external clock, make sure that a clock generator is connected to the Clock In connector.

7.2.24. /DEV0...n/SYSTEM/HWREVISION

The revision of the main-board.

Write	-
Read	Integer Number
Setting	No

7.2.25. /DEV0...n/SYSTEM/SYNCENABLE

Boolean value enabling multi-device timestamp synchronization over ZSync.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Default)	
Values	0	Default
	1	Timestamp synchronization enabled

Details

When synchronizing timestamps between devices make sure that an appropriate cable is connected between the ZSync ports of the master and slave devices.

7.2.26. /DEV0...n/SYSTEM/SYNCRESET

Boolean value activating timestamp reset over ZSync.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Default)	
Values	0	Default
	1	Timestamp reset activated

Details

When synchronizing timestamps between devices make sure that an appropriate cable is connected between the ZSync ports of the master and slave devices.

7.2.27. /DEV0...n/SYSTEM/SYNCTIME

The timestamp to load when timestamp reset is activated.

Write	-
Read	Decimal Number
Setting	No
Unit	s

7.2.28. /DEV0...n/SYSTEM/ACTIVEINTERFACE

Node providing the active interface of the device.

Write	-
Read	-
Setting	No

7.2.29. /DEV0...n/SYSTEM/PROPERTIES

Group of properties nodes.

7.2.30. /DEV0...n/SYSTEM/PROPERTIES/MINFREQ

Minimum oscillator frequency of the device.

7.2.31. /DEV0...n/SYSTEM/PROPERTIES/MAXFREQ

Maximum oscillator frequency of the device.

7.2.32. /DEV0...n/SYSTEM/PROPERTIES/NEGATIVEFREQ

Device does support negative frequencies.

7.2.33. /DEV0...n/SYSTEM/PROPERTIES/TIMEBASE

Time base of the device.

Write	-
Read	Decimal Number
Setting	No
Unit	s

7.2.34. /DEV0...n/SYSTEM/PROPERTIES/FREQRESOLUTION

Frequency resolution of the device.

Write	-
Read	Integer Number
Setting	No
Unit	bits

7.2.35. /DEV0...n/SYSTEM/PROPERTIES/MINTIMECONSTANT

Minimum filter time constant of the device.

Write	-
Read	Decimal Number
Setting	No
Unit	s

7.2.36. /DEV0...n/SYSTEM/PROPERTIES/MAXTIMECONSTANT

Maximum filter time constant of the device.

Write	-
Read	Decimal Number
Setting	No

Unit	s
------	---

7.2.37. /DEV0...n/FEATURES

Node containing information on features of the device.

7.2.38. /DEV0...n/FEATURES/SERIAL

Node providing the serial number of the device.

Write	-
Read	-
Setting	No

7.2.39. /DEV0...n/FEATURES/DEVTYPE

Node providing a string about the type of device.

Write	-
Read	-
Setting	No

7.2.40. /DEV0...n/FEATURES/OPTIONS

Node giving information on enabled options.

Write	-
Read	-
Setting	No

Details

Reading this node returns a string containing a newline-separated list of all installed options.

7.2.41. /DEV0...n/FEATURES/CODE

Node providing a mechanism to write feature codes.

Write	-
Read	-
Setting	No

7.2.42. /DEV0...n/STATUS

Nodes providing status information from the device.

7.2.43. /DEV0...n/STATUS/TIME

The current timestamp.

Write	-
-------	---

Read	Decimal Number
Setting	No
Unit	s

7.2.44. /DEV0...n/STATUS/FLAGS

Node containing some status flags.

7.2.45. /DEV0...n/STATUS/FLAGS/BINARY

A binary representation of all flags.

Write	-																														
Read	Integer Number																														
Setting	No																														
Unit	bit-coded																														
Values	<table> <tr> <td>b0 = 1</td> <td>PLL unlocked</td> </tr> <tr> <td>b1 = 2</td> <td>HF clock unlocked</td> </tr> <tr> <td>b2 = 4</td> <td>FX2 RX error</td> </tr> <tr> <td>b3 = 8</td> <td>Package loss</td> </tr> <tr> <td>b4 = 16</td> <td>Output 1 clipped</td> </tr> <tr> <td>b5 = 32</td> <td>Output 2 clipped</td> </tr> <tr> <td>b6 = 64</td> <td>Input 1 clipped</td> </tr> <tr> <td>b7 = 128</td> <td>Input 2 clipped</td> </tr> <tr> <td>b8 = 256</td> <td>Scope skipped a shot</td> </tr> <tr> <td>b9 = 512</td> <td>FX2 TX buffer almost full</td> </tr> <tr> <td>b10 = 1024</td> <td>0</td> </tr> <tr> <td>b11 = 2048</td> <td>PLL unlocked (version without de-bouncing)</td> </tr> <tr> <td>b12 = 4096</td> <td>FX2 TX package lost</td> </tr> <tr> <td>b20 = 1.048576E6</td> <td>PLL 1 locked</td> </tr> <tr> <td>b21 = 2.097152E6</td> <td>PLL 2 locked</td> </tr> </table>	b0 = 1	PLL unlocked	b1 = 2	HF clock unlocked	b2 = 4	FX2 RX error	b3 = 8	Package loss	b4 = 16	Output 1 clipped	b5 = 32	Output 2 clipped	b6 = 64	Input 1 clipped	b7 = 128	Input 2 clipped	b8 = 256	Scope skipped a shot	b9 = 512	FX2 TX buffer almost full	b10 = 1024	0	b11 = 2048	PLL unlocked (version without de-bouncing)	b12 = 4096	FX2 TX package lost	b20 = 1.048576E6	PLL 1 locked	b21 = 2.097152E6	PLL 2 locked
b0 = 1	PLL unlocked																														
b1 = 2	HF clock unlocked																														
b2 = 4	FX2 RX error																														
b3 = 8	Package loss																														
b4 = 16	Output 1 clipped																														
b5 = 32	Output 2 clipped																														
b6 = 64	Input 1 clipped																														
b7 = 128	Input 2 clipped																														
b8 = 256	Scope skipped a shot																														
b9 = 512	FX2 TX buffer almost full																														
b10 = 1024	0																														
b11 = 2048	PLL unlocked (version without de-bouncing)																														
b12 = 4096	FX2 TX package lost																														
b20 = 1.048576E6	PLL 1 locked																														
b21 = 2.097152E6	PLL 2 locked																														

Details

When multiple flags are set the values are or-ed.

7.2.46. /DEV0...n/STATUS/FLAGS/PLLLOCK

Flag indicating if the internal PLL for clock generation has locked.

Write	-				
Read	Integer Number				
Setting	No				
Unit	Boolean				
Values	<table> <tr> <td>0</td> <td>PLL locked</td> </tr> <tr> <td>1</td> <td>PLL not locked</td> </tr> </table>	0	PLL locked	1	PLL not locked
0	PLL locked				
1	PLL not locked				

7.2.47. /DEV0...n/STATUS/FLAGS/DCMLOCK

Flag indicating if the internal digital clock manager (DCM) has locked.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Values	0 DCM locked 1 DCM not locked

7.2.48. /DEV0...n/STATUS/FLAGS/FX2RX

Flag indicating if the device receives data via USB.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Values	0 Device receives 1 Device does not receive

7.2.49. /DEV0...n/STATUS/FLAGS/PKGLOSS

Flag indicating that the device lost data when sending via USB.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Values	0 no packet loss 1 packets are lost

7.2.50. /DEV0...n/STATUS/FLAGS/MIXERCLIP

Flags indicating that the internal mixer is clipping.

7.2.51. /DEV0...n/STATUS/FLAGS/MIXERCLIP/0...n

Flag indicating that this mixer-channel is clipping.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Values	0 no clipping

1	clipping
---	----------

7.2.52. /DEV0...n/STATUS/FLAGS/ADCCLIP

Flags indicating that the AD-converter is clipping.

7.2.53. /DEV0...n/STATUS/FLAGS/ADCCLIP/0...n

Flag indicating that this ADC-channel is clipping.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Values	0 no clipping 1 clipping

7.2.54. /DEV0...n/STATUS/FLAGS/SCOPESKIPPED

Flag indicating that scope data has been skipped.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Values	0 no data skipped 1 data skipped

Details

This happens when too much data is being sent over USB.

7.2.55. /DEV0...n/STATUS/FLAGS/DEMOSAMPLELOSS

Flag indicating that demodulator data has been lost.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Values	0 no demodulator data lost 1 demodulator data lost

7.2.56. /DEV0...n/STATUS/FIFOLEVEL

Percentage of TX FIFO used.

Write	-
-------	---

Read	Decimal Number
Setting	No
Unit	Percent
Range	0 to 100

7.2.57. /DEV0...n/STATUS/ADC0MIN

The minimum value on Signal Input 1 (ADC0) during 100 ms.

Write	-
Read	Integer Number
Setting	No
Range	-127 to 127

7.2.58. /DEV0...n/STATUS/ADC0MAX

The maximum value on Signal Input 1 (ADC0) during 100 ms.

Write	-
Read	Integer Number
Setting	No
Range	-127 to 127

7.2.59. /DEV0...n/STATUS/ADC1MIN

The minimum value on Signal Input 2 (ADC1) during 100 ms.

Write	-
Read	Integer Number
Setting	No
Range	-127 to 127

7.2.60. /DEV0...n/STATUS/ADC1MAX

The maximum value on Signal Input 2 (ADC1) during 100 ms.

Write	-
Read	Integer Number
Setting	No
Range	-127 to 127

7.2.61. /DEV0...n/STATUS/ECHOWRITE

32bits written to this node will be echoed back via ECHOREAD node.

Write	Integer Number
Read	-

Setting	No
---------	----

7.2.62. /DEV0...n/STATUS/ECHOREAD

32bits written to ECHOWRITE node are echoed here.

Write	-
Read	Integer Number
Setting	No

7.2.63. /DEV0...n/STATS

Nodes providing statistical data about the device.

7.2.64. /DEV0...n/STATS/BYTESSENT

Total amount of bytes sent via USB.

Write	-
Read	Integer Number
Setting	No
Unit	Bytes

7.2.65. /DEV0...n/STATS/BYTESRECEIVED

Total amount of bytes received via USB.

Write	-
Read	Integer Number
Setting	No
Unit	Bytes

7.2.66. /DEV0...n/STATS/MEANPOLLCNT

Average poll-count.

Write	-
Read	Decimal Number
Setting	No
Unit	Polls/Second

7.2.67. /DEV0...n/STATS/MEANMSGCNT

Average message-count.

Write	-
Read	Decimal Number
Setting	No

Unit	Messages/Second
------	-----------------

7.2.68. /DEV0...n/STATS/PHYSICAL

Group of nodes providing some physical information on the device.

7.2.69. /DEV0...n/STATS/PHYSICAL/1V2

Actual voltage of the 1.2 Volts supply.

Write	-
Read	Decimal Number
Setting	No
Unit	Volts

7.2.70. /DEV0...n/STATS/PHYSICAL/1V8

Actual voltage of the 1.8 Volts supply.

Write	-
Read	Decimal Number
Setting	No
Unit	Volts

7.2.71. /DEV0...n/STATS/PHYSICAL/2V5

Actual voltage of the 2.5 Volts supply.

Write	-
Read	Decimal Number
Setting	No
Unit	Volts

7.2.72. /DEV0...n/STATS/PHYSICAL/3V3

Actual voltage of the 3.3 Volts supply.

Write	-
Read	Decimal Number
Setting	No
Unit	Volts

7.2.73. /DEV0...n/STATS/PHYSICAL/5V0

Actual voltage of the 5.0 Volts supply.

Write	-
Read	Decimal Number

Setting	No
Unit	Volts

7.2.74. /DEV0...n/STATS/PHYSICAL/TEMP

Actual temperature.

Write	-
Read	Decimal Number
Setting	No
Unit	Degrees Celsius

7.2.75. /DEV0...n/STATS/PHYSICAL/OVERTEMPERATURE

Too high FPGA temperature detected during session.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Values	0 No overtemperature 1 Overtemperature detected

7.2.76. /DEVO...n/DEMOS

Demodulator nodes.

7.2.77. /DEV0...n/DEM0DS/0...n

Nodes of a single demodulator

7.2.78. /DEV0...n/DEM0DS/0...n/ADCSELECT

Selects the index of the signal input for the demodulator.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Index
Range	0 to 5
Values	0 Signal input 0
	1 Signal input 1
	2 Aux Input 0
	3 Aux Input 1
	4 DIO 0
	5 DIO 1

7.2.79. /DEV0...n/DEMODS/0...n/ORDER

Selects the order of the low-pass filter.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Order	
Default	4 (24 dB/oct slope)	
Range	1 to 8	
Values	1	6 dB/oct slope
	2	12 dB/oct slope
	3	18 dB/oct slope
	4	24 dB/oct slope
	5	30 dB/oct slope
	6	36 dB/oct slope
	7	42 dB/oct slope
	8	48 dB/oct slope

7.2.80. /DEV0...n/DEMODS/0...n/TIMECONSTANT

Sets the time constant of the low-pass filter.

Write	Decimal Number	
Read	Decimal Number	
Setting	Yes	
Unit	s	
Default	0.010164	

Details

The time constant is set for each stage of the low-pass filter. The total time constant and bandwidth depends on the selected order.

7.2.81. /DEV0...n/DEMODS/0...n/RATE

The number of output values sent to the computer per second.

Write	Decimal Number	
Read	Decimal Number	
Setting	Yes	
Unit	Hz	

7.2.82. /DEV0...n/DEMODS/0...n/ENABLE

Enables the demodulator data stream.

Write	Integer Number	

Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Demodulator stream off)	
Values	0	Demodulator stream off
	1	Demodulator stream on

7.2.83. /DEV0...n/DEMODS/0...n/trigger

Sets the trigger- and gating-functionality of the demodulator.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	bit-coded	
Values	b0 = 1	DIO0 rising edge
	b1 = 2	DIO0 falling edge
	b2 = 4	DIO1 rising edge
	b3 = 8	DIO1 falling edge
	b4 = 16	DIO0 high
	b5 = 32	DIO0 low
	b6 = 64	DIO1 high
	b7 = 128	DIO1 low

Details

The triggers are configured by the bits of an integer. When multiple bits/triggers are set, they are or-ed. If trigger is set to 0 then demodulator data is sent continuously.

7.2.84. /DEV0...n/DEMODS/0...n/OSCSELECT

Index of the oscillator used to demodulate the signal.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Index	

7.2.85. /DEV0...n/DEMODS/0...n/HARMONIC

The harmonic of the base frequency to be used.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Harmonic	

Default	1
Range	1 to 1023

Details

Selecting 1 chooses the fundamental frequency.

7.2.86. /DEV0...n/DEM0DS/0...n/FREQ

Frequency to of the demodulator.

Write	-
Read	Decimal Number
Setting	No
Unit	Hz
Default	1.0E6
Range	0 to 1.0E8

7.2.87. /DEV0...n/DEM0DS/0...n/PHASESHIFT

The phase shift of the demodulator.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	deg
Default	0
Range	-180 to 180

7.2.88. /DEV0...n/DEM0DS/0...n/SINC

Boolean value enabling Sinc filter functionality.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Boolean
Default	0 (Sinc filtering disabled)
Values	0 Sinc filtering disabled 1 Sinc filtering enabled

7.2.89. /DEV0...n/DEM0DS/0...n/SAMPLE

Samples of the demodulator are given out at this node.

Write	-
Read	-

Stream	-
Setting	No

7.2.90. /DEV0...n/OSCS

Oscillator nodes.

7.2.91. /DEV0...n/OSCS/0...n

Nodes of an oscillator.

7.2.92. /DEV0...n/OSCS/0...n/FREQ

Frequency to of the oscillator.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Hz
Default	1.0E6
Range	0 to 1.0E8

7.2.93. /DEV0...n/MODS

Modulator option nodes.

Details

These nodes are only visible when the MOD option is installed on the device.

7.2.94. /DEV0...n/MODS/0...n

Nodes of a Modulator.

Note

These nodes are only available with installed Modulation-Option.

7.2.95. /DEV0...n/MODS/0...n/ENABLE

Enables the modulation.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Boolean
Default	0 (Modulation Off)
Values	0 Modulation Off

1	Modulation On
---	---------------

7.2.96. /DEV0...n/MODS/0...n/OUTPUT

Modulation output.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0 (none)	
Values	0	none
	1	1
	2	2
	3	1 and 2

7.2.97. /DEV0...n/MODS/0...n/MODE

Modulation mode.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0 (Amplitude modulation)	
Values	0	Amplitude modulation
	1	Frequency modulation
	2	Manual

7.2.98. /DEV0...n/MODS/0...n/FREQDEVENABLE

In FM mode, enable peak deviation.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Peak deviation off)	
Values	0	Peak deviation off
	1	Peak deviation on

7.2.99. /DEV0...n/MODS/0...n/FREQDEV

In FM mode, set peak deviation value.

Write	Decimal Number	
Read	Decimal Number	
Setting	Yes	

Unit	V
------	---

7.2.100. /DEV0...n/MODS/0...n/INDEX

In FM mode, set modulation index value. The modulation index equals peak deviation divided by modulation frequency.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	

7.2.101. /DEV0...n/MODS/0...n/RATE

The number of output values sent to the computer per second.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Hz

7.2.102. /DEV0...n/MODS/0...n/TRIGGER

Sets the trigger- and gating-functionality of the demodulator.

Write	Integer Number																
Read	Integer Number																
Setting	Yes																
Unit	bit-coded																
Values	<table> <tr> <td>b0 = 1</td> <td>DIO0 rising edge</td> </tr> <tr> <td>b1 = 2</td> <td>DIO0 falling edge</td> </tr> <tr> <td>b2 = 4</td> <td>DIO1 rising edge</td> </tr> <tr> <td>b3 = 8</td> <td>DIO1 falling edge</td> </tr> <tr> <td>b4 = 16</td> <td>DIO0 high</td> </tr> <tr> <td>b5 = 32</td> <td>DIO0 low</td> </tr> <tr> <td>b6 = 64</td> <td>DIO1 high</td> </tr> <tr> <td>b7 = 128</td> <td>DIO1 low</td> </tr> </table>	b0 = 1	DIO0 rising edge	b1 = 2	DIO0 falling edge	b2 = 4	DIO1 rising edge	b3 = 8	DIO1 falling edge	b4 = 16	DIO0 high	b5 = 32	DIO0 low	b6 = 64	DIO1 high	b7 = 128	DIO1 low
b0 = 1	DIO0 rising edge																
b1 = 2	DIO0 falling edge																
b2 = 4	DIO1 rising edge																
b3 = 8	DIO1 falling edge																
b4 = 16	DIO0 high																
b5 = 32	DIO0 low																
b6 = 64	DIO1 high																
b7 = 128	DIO1 low																

7.2.103. /DEV0...n/MODS/0...n/CARRIER

Group of carrier nodes.

7.2.104. /DEV0...n/MODS/0...n/CARRIER/INPUTSELECT

Signal Input for the carrier demodulation.

Write	Integer Number
-------	----------------

Read	Integer Number	
Setting	Yes	
Default	0 (Sig In 1)	
Values	0	Sig In 1
	1	Sig In 2

7.2.105. /DEV0...n/MODS/0...n/CARRIER/ORDER

Filter order for carrier demodulation.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Order	
Default	4 (24 dB/oct slope)	
Range	1 to 8	
Values	1	6 dB/oct slope
	2	12 dB/oct slope
	3	18 dB/oct slope
	4	24 dB/oct slope
	5	30 dB/oct slope
	6	36 dB/oct slope
	7	42 dB/oct slope
	8	48 dB/oct slope

7.2.106. /DEV0...n/MODS/0...n/CARRIER/TIMECONSTANT

Sets the time constant of the carrier low-pass filter.

Write	Decimal Number	
Read	Decimal Number	
Setting	Yes	
Unit	s	

Details

The time constant is set for each stage of the low-pass filter. The total time constant and bandwidth depends on the selected order.

7.2.107. /DEV0...n/MODS/0...n/CARRIER/OSCSELECT

Index of the oscillator used to demodulate the signal.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	

7.2.108. /DEV0...n/MODS/0...n/CARRIER/HARMONIC

Harmonic of the carrier frequency.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Harmonic
Default	1
Range	1 to 1023

Details

Selecting 1 chooses the fundamental frequency.

7.2.109. /DEV0...n/MODS/0...n/CARRIER/PHASESHIFT

The phase shift of the carrier demodulator.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	deg
Default	0
Range	-180 to 180

7.2.110. /DEV0...n/MODS/0...n/CARRIER/ENABLE

Enables the carrier data stream.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Boolean
Default	0 (Demodulator stream off)
Values	0 Demodulator stream off 1 Demodulator stream on

7.2.111. /DEV0...n/MODS/0...n/CARRIER/AMPLITUDE

Carrier amplitude.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Gain

Range	-1 to 1
-------	---------

Details

Multiply this value with the range setting to obtain voltage in V.

7.2.112. /DEV0...n/MODS/0...n/SIDEBOARDS

Group of sideband nodes.

7.2.113. /DEV0...n/MODS/0...n/SIDEBOARDS/0...n

7.2.114. /DEV0...n/MODS/0...n/SIDEBOARDS/0...n/MODE

Sideband selector.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0 (Off)	
Values	0	Off
	1	C + M
	2	C - M

7.2.115. /DEV0...n/MODS/0...n/SIDEBOARDS/0...n/INPUTSELECT

Signal Input for the sideband demodulation.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0 (Sig In 1)	
Values	0	Sig In 1
	1	Sig In 2

7.2.116. /DEV0...n/MODS/0...n/SIDEBOARDS/0...n/ORDER

Filter order for sideband demodulation.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Order	
Default	4 (24 dB/oct slope)	
Range	1 to 8	
Values	1	6 dB/oct slope

2	12 dB/oct slope
3	18 dB/oct slope
4	24 dB/oct slope
5	30 dB/oct slope
6	36 dB/oct slope
7	42 dB/oct slope
8	48 dB/oct slope

7.2.117. /DEV0...n/MODS/0...n/SIDEBANDS/0...n/TIMECONSTANT

Sets the time constant of the sideband low-pass filter.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	s

Details

The time constant is set for each stage of the low-pass filter. The total time constant and bandwidth depends on the selected order.

7.2.118. /DEV0...n/MODS/0...n/SIDEBANDS/0...n/OSCSELECT

Index of the oscillator used to demodulate the signal.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Index

7.2.119. /DEV0...n/MODS/0...n/SIDEBANDS/0...n/HARMONIC

Harmonic of the sideband frequency.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Harmonic
Default	1
Range	1 to 1023

Details

Selecting 1 chooses the fundamental frequency.

7.2.120. /DEV0...n/MODS/0...n/SIDEBANDS/0...n/PHASESHIFT

The phase shift of the sideband demodulator.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	deg
Default	0
Range	-180 to 180

7.2.121. /DEV0...n/MODS/0...n/SIDEBANDS/0...n/ENABLE

Enables the sideband data stream.

Write	Integer Number				
Read	Integer Number				
Setting	Yes				
Unit	Boolean				
Default	0 (Demodulator stream off)				
Values	<table> <tr> <td>0</td> <td>Demodulator stream off</td> </tr> <tr> <td>1</td> <td>Demodulator stream on</td> </tr> </table>	0	Demodulator stream off	1	Demodulator stream on
0	Demodulator stream off				
1	Demodulator stream on				

7.2.122. /DEV0...n/MODS/0...n/SIDEBANDS/0...n/AMPLITUDE

Sideband amplitude

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Gain
Range	-1 to 1

Details

Multiply this value with the range setting to obtain voltage in V.

7.2.123. /DEV0...n/MODS/0...n/SAMPLE

Modulation Samples.

Write	-
Read	-
Stream	-

Setting	No
---------	----

7.2.124. /DEV0...n/PLLS

PLL nodes.

7.2.125. /DEV0...n/PLLS/0...n

Nodes of a PLL.

Note

These nodes are only available with installed PLL option.

7.2.126. /DEV0...n/PLLS/0...n/ADCSELECT

Selects an input for the PLL.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Index	
Range	0 to 5	
Values	0	Signal Input 1
	1	Signal Input 2
	2	Aux Input 1
	3	Aux Input 2
	4	DIO 0
	5	DIO 1

7.2.127. /DEV0...n/PLLS/0...n/AUTOCENTER

Switches auto-center.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Auto-center off)	
Values	0	Auto-center off
	1	Auto-center on

7.2.128. /DEV0...n/PLLS/0...n/FREQCENTER

Selects a center frequency.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Hz
Range	0 to 1.0E8

7.2.129. /DEV0...n/PLLS/0...n/FREQRANGE

Selects a frequency range for the PLL.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Hz
Range	0 to 1.0E8

7.2.130. /DEV0...n/PLLS/0...n/ENABLE

Enables the PLL.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Boolean
Default	0 (PLL off)
Values	0 PLL off 1 PLL on

7.2.131. /DEV0...n/PLLS/0...n/ERROR

Error of the PLL.

Write	-
Read	Decimal Number
Setting	No
Unit	deg
Range	-180 to 180

7.2.132. /DEV0...n/PLLS/0...n/AUTOTIMECONSTANT

Switches external time constant control.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Boolean

Default	0 (External time constant off)
Values	0 External time constant off
	1 External time constant on

7.2.133. /DEV0...n/PLLS/0...n/TIMECONSTANT

The external time constant.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	s
Default	0

7.2.134. /DEV0...n/PLLS/0...n/AUTOPID

Switches external PID.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Boolean
Default	0 (External PID off)
Values	0 External PID off
	1 External PID on

7.2.135. /DEV0...n/PLLS/0...n/P

Proportional gain of the PID controller.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Hz/deg
Default	0
Range	-36458.33 to 36458.33

7.2.136. /DEV0...n/PLLS/0...n/I

Integral gain of the PID controller.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Hz^2/deg
Default	0

Range	-8.1982529E6 to 8.19819035E6
-------	------------------------------

7.2.137. /DEV0...n/PLLS/0...n/D

Derivative gain of the PID.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	1/deg
Default	0
Range	-0.00002 to 0.00002

7.2.138. /DEV0...n/PLLS/0...n/FREQDELTA

Frequency deviation from center frequency.

Write	-
Read	Decimal Number
Setting	No
Unit	Hz

7.2.139. /DEV0...n/PLLS/0...n/ADCTHRESHOLD

Threshold for edge detection.

Write	Integer Number
Read	Integer Number
Setting	Yes
Default	100
Range	-4096 to 4095

Details

Full scale corresponds to -4096 and 4095.

7.2.140. /DEV0...n/PLLS/0...n/AUXAVG

Delta frequency averaging control.

Write	Integer Number
Read	Integer Number
Setting	Yes
Default	8
Range	0 to 128

7.2.141. /DEV0...n/PLLS/0...n/SETPOINT

The setpoint in degrees of the PLL.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	deg
Default	0
Range	-180 to 180

7.2.142. /DEV0...n/PLLS/0...n/HARMONIC

The harmonic of the base frequency to be used.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Harmonic
Default	1
Range	1 to 1023

Details

Selecting 1 chooses the fundamental frequency.

7.2.143. /DEV0...n/PLLS/0...n/ORDER

Selects the order of the low-pass filter.

Write	Integer Number																
Read	Integer Number																
Setting	Yes																
Unit	Order																
Default	4 (24 dB/oct slope)																
Range	1 to 8																
Values	<table> <tr> <td>1</td> <td>6 dB/oct slope</td> </tr> <tr> <td>2</td> <td>12 dB/oct slope</td> </tr> <tr> <td>3</td> <td>18 dB/oct slope</td> </tr> <tr> <td>4</td> <td>24 dB/oct slope</td> </tr> <tr> <td>5</td> <td>30 dB/oct slope</td> </tr> <tr> <td>6</td> <td>36 dB/oct slope</td> </tr> <tr> <td>7</td> <td>42 dB/oct slope</td> </tr> <tr> <td>8</td> <td>48 dB/oct slope</td> </tr> </table>	1	6 dB/oct slope	2	12 dB/oct slope	3	18 dB/oct slope	4	24 dB/oct slope	5	30 dB/oct slope	6	36 dB/oct slope	7	42 dB/oct slope	8	48 dB/oct slope
1	6 dB/oct slope																
2	12 dB/oct slope																
3	18 dB/oct slope																
4	24 dB/oct slope																
5	30 dB/oct slope																
6	36 dB/oct slope																
7	42 dB/oct slope																
8	48 dB/oct slope																

7.2.144. /DEV0...n/PLLS/0...n/RATE

Update rate information.

Write	-
-------	---

Read	Decimal Number
Setting	No
Unit	Samples/s

7.2.145. /DEV0...n/PLLS/0...n/OSCSELECT

Index of the oscillator used.

Write	-
Read	Integer Number
Setting	No
Unit	Index

7.2.146. /DEV0...n/PLLS/0...n/DEMODOSELECT

Source demodulator.

Write	-
Read	Integer Number
Setting	No
Unit	Index

7.2.147. /DEV0...n/PLLS/0...n/LOCKED

Lock indicator for the PLL.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Default	0 (PLL not locked)
Values	0 PLL not locked 1 PLL locked

7.2.148. /DEV0...n/PIDS

PID nodes.

7.2.149. /DEV0...n/PIDS/0...n

Nodes of a PID.

Note

These nodes are only available with installed PID option.

7.2.150. /DEV0...n/PIDS/0...n/INPUT

Selects the input for the PID.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0 (Demodulator X value [Vrms])	
Values	0	Demodulator X value [Vrms]
	1	Demodulator Y value [Vrms]
	2	Demodulator R value [Vrms]
	3	Demodulator Theta value [deg]
	4	Auxiliary Input [V]
	5	Auxiliary Output (as input) [V]
	6	Modulation Index [0, 1]
	7	Dual Frequency Tracking $ Z(n+1) - Z(n) $ [Vrms]
	8	Demodulator $x(n+1) - x(n)$ [Vrms]
	9	Demodulator $ z(n+1) - z(n) $ [Vrms]
	10	Oscillator Frequency [Hz]

7.2.151. /DEV0...n/PIDS/0...n/INPUTCHANNEL

If applicable, selects the channel of the selected INPUT.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0	

Details

Sets the input channel index for the selected INPUT, i.e., 0, 1, 2 etc.. The available channels depend on the input type.

7.2.152. /DEV0...n/PIDS/0...n/OUTPUT

Selects the output for the PID.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0 (Signal Output 1 [Vrms])	
Values	0	Signal Output 1 [Vrms]
	1	Signal Output 2 [Vrms]
	2	Oscillator frequency [Hz]
	3	Auxiliary Output (manual mode) [V]

4 DIO [5 Volt TTL]

7.2.153. /DEV0...n/PIDS/0...n/OUTPUTCHANNEL

If applicable, selects the channel of the selected OUTPUT.

Write	Integer Number
Read	Integer Number
Setting	Yes
Default	6

Details

Sets the input channel index for the selected OUTPUT, i.e., 0,1,2 etc.. The available channels depend on the output type

7.2.154. /DEV0...n/PIDS/0...n/OUTPUTDEFAULTENABLE

If OUTPUTDEFAULTENABLE is set, the value specified by OUTPUTDEFAULT will be applied when the PID is switched off.

Details

7.2.155. /DEV0...n/PIDS/0...n/OUTPUTDEFAULT

If OUTPUTDEFAULTENABLE is set, this node specifies the value to be applied.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Default	0.1

Details

7.2.156. /DEV0...n/PIDS/0...n/P

Proportional gain.

Write	Decimal Number
Read	Decimal Number
Setting	Yes

Unit	[OUTPUT Unit]/[INPUT Unit]
Default	1

Details

Sets the proportional gain for the error signal. Negative feedback corresponds to a negative gain.

7.2.157. /DEV0...n/PIDS/0...n/I

Proportional gain for integrator.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	[OUTPUT Unit]/[INPUT Unit]/s
Default	10

Details

Sets the proportional gain for the integrated (accumulated) error signal. Negative feedback corresponds to a negative gain.

7.2.158. /DEV0...n/PIDS/0...n/D

Proportional gain for differentiator.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	[OUTPUT Unit]/[INPUT Unit]*s
Default	0.001

Details

Sets the proportional gain for the differentiated error signal. Negative feedback corresponds to a negative gain.

7.2.159. /DEV0...n/PIDS/0...n/SETPOINT

Target settle point.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	[INPUT Unit]
Default	0.1

7.2.160. /DEV0...n/PIDS/0...n/SETPOINTSELECT

Set point selection.

Write	Integer Number
-------	----------------

Read	Integer Number	
Setting	Yes	
Default	0 (Manual Setpoint)	
Values	0	Manual Setpoint
	1	Auxiliary Input 1
	2	Auxiliary Input 2
	3	PID n

7.2.161. /DEV0...n/PIDS/0...n/MONITOROFFSET

Offset for the monitor output.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Default	0

7.2.162. /DEV0...n/PIDS/0...n/MONITORSCALE

Scale for the monitor output.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Default	1

7.2.163. /DEV0...n/PIDS/0...n/ERROR

Shows the error value.

Write	-
Read	Decimal Number
Setting	No
Unit	[OUTPUT Unit]

Details

The calculated error is : $\text{ERROR} = \text{SETPOINT} - \text{IN}$.

7.2.164. /DEV0...n/PIDS/0...n/CENTER

Sets the output center point.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	[OUTPUT Unit]

Default	0.1
---------	-----

7.2.165. /DEV0...n/PIDS/0...n/RANGE

Sets the output range.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	[OUTPUT Unit]
Default	0.5

Details

The limits for the output are : OUT = [CENTER - RANGE, CENTER + RANGE] with RANGE > 0.0.

7.2.166. /DEV0...n/PIDS/0...n/SHIFT

Shows the output shift.

Write	-
Read	Decimal Number
Setting	No
Unit	[OUTPUT Unit]
Default	0

Details

The calculated output value is : OUT = CENTER + SHIFT.

7.2.167. /DEV0...n/PIDS/0...n/ENABLE

Enable PID controller.

Write	Integer Number				
Read	Integer Number				
Setting	Yes				
Unit	Boolean				
Default	0 (OFF)				
Values	<table> <tr> <td>0</td> <td>OFF</td> </tr> <tr> <td>1</td> <td>ON</td> </tr> </table>	0	OFF	1	ON
0	OFF				
1	ON				

7.2.168. /DEV0...n/PIDS/0...n/RATE

Control update rate.

Write	-
Read	Decimal Number
Setting	No

Unit	Samples/s
------	-----------

7.2.169. /DEV0...n/PIDS/0...n/TIPPROTECT

Contains nodes for configuring the TipProtect functionality.

7.2.170. /DEV0...n/PIDS/0...n/TIPPROTECT/ENABLE

Enable TipProtect for the PID controller.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (OFF)	
Values	0	OFF
	1	ON

7.2.171. /DEV0...n/PIDS/0...n/TIPPROTECT/PLL

Selects a PLL for TipProtect.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0	

7.2.172. /DEV0...n/PIDS/0...n/TIPPROTECT/ACTIVE

Indicates whether TipProtect is active.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Default	0

7.2.173. /DEV0...n/PIDS/0...n/TIPPROTECT/ACTIVETIMECONSTANT

Time constant when TipProtect is active.

Write	Decimal Number	
Read	Decimal Number	
Setting	Yes	
Unit	s	
Default	0	

Range	0 to 1.0E8
-------	------------

Details

Time constant for low-pass filtering the PLL error² when TipProtect is active, i.e., when waiting to re-enable the PID controller.

7.2.174. /DEV0...n/PIDS/0...n/TIPPROTECT/ACTIVETHRESHOLD

Threshold for the active state.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	deg ²
Range	0 to 129600

Details

Threshold for PLL error² when TipProtect is active, i.e., when waiting to re-enable the PID controller.

7.2.175. /DEV0...n/PIDS/0...n/TIPPROTECT/INACTIVETIMECONSTANT

Time constant when TipProtect is inactive.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	s
Default	0
Range	0 to 1.0E8

Details

Time constant for low-pass filtering the PLL error² when TipProtect is inactive, i.e., when waiting to disable the PID controller.

7.2.176. /DEV0...n/PIDS/0...n/TIPPROTECT/INACTIVETHRESHOLD

Threshold for the inactive state.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	deg ²
Range	0 to 129600

Details

Threshold for PLL error² when TipProtect is inactive, i.e., when waiting to disable the PID controller.

7.2.177. /DEV0...n/PIDS/0...n/DEMOD

Contains nodes for configuring the demodulator settings of the PID.

7.2.178. /DEV0...n/PIDS/0...n/DEMOD/ADCSELECT

Selects the index of the signal input for the demodulator.

Write	Integer Number	
Read	Integer Number	
Setting	No	
Unit	Index	
Range	0 to 5	
Values	0	Signal input 0
	1	Signal input 1
	2	Aux Input 0
	3	Aux Input 1
	4	DIO 0
	5	DIO 1

7.2.179. /DEV0...n/PIDS/0...n/DEMOD/ORDER

Selects the order of the low-pass filter.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Order	
Default	4 (24 dB/oct slope)	
Range	1 to 8	
Values	1	6 dB/oct slope
	2	12 dB/oct slope
	3	18 dB/oct slope
	4	24 dB/oct slope
	5	30 dB/oct slope
	6	36 dB/oct slope
	7	42 dB/oct slope
	8	48 dB/oct slope

7.2.180. /DEV0...n/PIDS/0...n/DEMOD/TIMECONSTANT

Sets the time constant of the low-pass filter.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	s
Default	0.010164

Details

The time constant is set for each stage of the low-pass filter. The total time constant and bandwidth depends on the selected order.

7.2.181. /DEV0...n/PIDS/0...n/DEMOD/HARMONIC

The harmonic of the base frequency to be used.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Harmonic
Default	1
Range	1 to 1023

Details

Selecting 1 chooses the fundamental frequency.

7.2.182. /DEV0...n/SIGINS

Signal Input nodes.

7.2.183. /DEV0...n/SIGINS/0...n

Nodes of a signal input.

7.2.184. /DEV0...n/SIGINS/0...n/RANGE

Voltage range for the signal input.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	V
Default	1.2
Range	0.0001 to 2

7.2.185. /DEV0...n/SIGINS/0...n/AC

Boolean value setting for AC coupling of the Signal Input.

Write	Integer Number
-------	----------------

Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (DC coupling)	
Values	0	DC coupling
	1	AC coupling

7.2.186. /DEV0...n/SIGINS/0...n/IMP50

Boolean value enabling 50 Ohm input impedance termination.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (High impedance)	
Values	0	High impedance
	1	50 Ohm impedance

7.2.187. /DEV0...n/SIGINS/0...n/DIFF

Boolean value switching differential input mode.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Single-ended inputs)	
Values	0	Single-ended inputs
	1	Differential inputs

7.2.188. /DEV0...n/SIGOUTS

Signal Output nodes.

7.2.189. /DEV0...n/SIGOUTS/0...n

Nodes of a Signal Output.

7.2.190. /DEV0...n/SIGOUTS/0...n/ON

Switches the output on and off.

Write	Integer Number	
Read	Integer Number	

7.2.191. /DEV0...n/SIGOUTS/0...n/ADD

Switches the output adder on and off.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Adder off)	
Values	0	Adder off
	1	Adder on

7.2.192. /DEVO...n/SIGOUTS/0...n/RANGE

Selects the output range for the Signal Output.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	V
Default	1
Values	0.01
	0.01 V range
	0.1
	0.1 V range
Values	1
	1 V range
Values	10
	10 V range

7.2.193. /DEV0...n/SIGOUTS/0...n/OFFSET

Offset added to the Signal Output.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	Gain
Default	0
Range	-1 to 1

Details

Multiply this value with the range setting to obtain offset voltage in V.

7.2.194. /DEV0...n/SIGOUTS/0...n/ENABLES

Switches for channels in the mixer.

7.2.195. /DEV0...n/SIGOUTS/0...n/ENABLES/0...n

Switches a channel in the mixer on and off.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Values	0	Channel off (unconditionally)
	1	Channel on (unconditionally)
	2	Channel off (will be turned off on next change of sign from negative to positive)
	3	Channel on (will be turned on on next change of sign from negative to positive)

7.2.196. /DEV0...n/SIGOUTS/0...n/AMPLITUDES

Amplitudes for channels in the mixer.

7.2.197. /DEV0...n/SIGOUTS/0...n/AMPLITUDES/0...n

Fraction of the output range added to the output signal.

Write	Decimal Number	
Read	Decimal Number	
Setting	Yes	
Unit	Gain	
Range	-1 to 1	

Details

Multiply this value with the range setting to obtain voltage in V.

7.2.198. /DEV0...n/SIGOUTS/0...n/WAVEFORMS**7.2.199. /DEV0...n/SIGOUTS/0...n/WAVEFORMS/0...n**

Waveforms for a channel in the mixer.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0 (Sine)	
Values	0	Sine
	1	Square

Details

For hardware revisions 1.4 and lower, the output signal range for rectangular output is limited to 1 V.

7.2.200. /DEV0...n/SCOPES

Scope nodes.

7.2.201. /DEV0...n/SCOPES/0...n

Nodes of a scope.

7.2.202. /DEV0...n/SCOPES/0...n/ENABLE

Enables the scope.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Scope off)	
Values	0	Scope off
	1	Scope on

7.2.203. /DEV0...n/SCOPES/0...n/CHANNEL

Selects the channel for which scope data should be provided.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Index	
Default	0 (Signal Input 1)	
Range	0 to 3	
Values	0	Signal Input 1
	1	Signal Input 2
	2	Signal Output 1
	3	Signal Output 2

7.2.204. /DEV0...n/SCOPES/0...n/TRIGCHANNEL

Selects the channel which should be used as source for the scope's trigger.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Index	

Default	-1 (Off)	
Range	-2 to 11	
Values	-2	Continuous
	-1	Off
	0	Signal Input 1
	1	Signal Input 2
	2	Signal Output 1
	3	Signal Output 2
	4	Oscillator 1 phase
	5	Oscillator 2 phase
	6	Oscillator 3 phase
	7	Oscillator 4 phase
	8	Oscillator 5 phase
	9	Oscillator 6 phase
	0	Oscillator 7 phase
	11	Oscillator 8 phase
	12	DIO 0
	13	DIO 1

7.2.205. /DEV0...n/SCOPES/0...n/BWLIMIT

The bandwidth-limit for the scope.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (BW-limit off)	
Values	0	BW-limit off
	1	BW-limit on

7.2.206. /DEV0...n/SCOPES/0...n/TRIGEDGE

Selects whether the scope should trigger on rising or falling edge.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	1 (Rising edge)	
Values	0	Falling edge
	1	Rising edge

7.2.207. /DEV0...n/SCOPES/0...n/TRIGLEVEL

Level at which a trigger is raised.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	LSB
Default	0
Range	-32768 to 32767

Details

Full scale is covered by min and max values

7.2.208. /DEV0...n/SCOPES/0...n/TRIGHOLDOFF

Time to wait for re-arming the trigger after one occurred.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	s
Default	0.25
Range	0 to 65.535

7.2.209. /DEV0...n/SCOPES/0...n/TIME

Timescale of the scope wave (logarithmic decimation).

Write	Integer Number
Read	Integer Number
Setting	Yes
Default	0
Range	0 to 7

Details

Determines the decimation of the sample rate. The following formulas apply: span = $2^{\text{val}} * 10$ us, sample rate = 210 MSamples/ 2^{val}

7.2.210. /DEV0...n/SCOPES/0...n/WAVE

Samples of scope-waveforms.

Write	-
Read	-
Stream	-
Setting	No

7.2.211. /DEV0...n/DIOS

DIO nodes.

7.2.212. /DEV0...n/DIOS/0...n

Nodes of a DIO.

7.2.213. /DEV0...n/DIOS/0...n/EXTCLK

Selects whether an external clock source should be used.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Internal clock)	
Values	0	Internal clock
	1	External clock

Details

The external clock needs to be applied to the DIO connector when this node is set to 1.

7.2.214. /DEV0...n/DIOS/0...n/DECIMATION

Decimation for the sample rate of the DIO.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	256	
Range	0 to 65535	

7.2.215. /DEV0...n/DIOS/0...n/DRIVE

Selects if the outputs should be driven.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Default	0 (Drive off)	
Range	0 to 3	
Values	0	Drive off
	1	Drive lower 8 bits
	2	Drive higher 8 bits
	3	Drive all 16 bits

7.2.216. /DEV0...n/DIOS/0...n/OUTPUT

Bits to output.

Write	Integer Number
Read	Integer Number
Setting	Yes
Default	0
Range	0 to 65535

7.2.217. /DEV0...n/DIOS/0...n/SYNCSELECT0

Source to output the sync signal on bit 0.

Write	Integer Number
Read	Integer Number
Setting	Yes
Default	8
Range	0 to 8

7.2.218. /DEV0...n/DIOS/0...n/SYNCSELECT1

Source to output the sync signal on bit 1.

Write	Integer Number
Read	Integer Number
Setting	Yes
Default	8
Range	0 to 8

7.2.219. /DEV0...n/DIOS/0...n/INPUT

Samples of the input.

Write	-
Read	-
Stream	-
Setting	No

7.2.220. /DEV0...n/AUXINS

Nodes of auxiliary inputs.

7.2.221. /DEV0...n/AUXINS/0...n

Node for an aux in.

7.2.222. /DEV0...n/AUXINS/0...n/AVERAGING

Averaging of the samples.

Write	Integer Number
-------	----------------

Read	Integer Number
Setting	Yes
Default	256
Range	1 to 32768

7.2.223. /DEV0...n/AUXINS/0...n/SAMPLE

Auxiliary input samples.

Write	-
Read	-
Stream	-
Setting	No

7.2.224. /DEV0...n/AUXINS/0...n/VALUES

Nodes of a auxins values.

7.2.225. /DEV0...n/AUXINS/0...n/VALUES/0

Input 0 value.

Write	-
Read	Decimal Number
Setting	No
Unit	V
Range	-10 to 10

7.2.226. /DEV0...n/AUXINS/0...n/VALUES/1

Input 1 value.

Write	-
Read	Decimal Number
Setting	No
Unit	V
Range	-10 to 10

7.2.227. /DEV0...n/AUXOUTS

Nodes of Auxiliary outputs.

7.2.228. /DEV0...n/AUXOUTS/0...n

Nodes of an Auxiliary output.

7.2.229. /DEV0...n/AUXOUTS/0...n/VALUE

Output value.

Write	-
Read	Decimal Number
Setting	No
Unit	V
Range	-10 to 10

7.2.230. /DEV0...n/AUXOUTS/0...n/OUTPUTSELECT

Signal to be given out.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Values	-1	Manual
	0	X
	1	Y
	2	R
	3	Theta
	4	PLL 0 (with installed PLL option))
	4	PLL 1 (with installed PLL option))

7.2.231. /DEV0...n/AUXOUTS/0...n/DEMODSELECT

Source demodulator.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Index	
Default	0	

7.2.232. /DEV0...n/AUXOUTS/0...n/SCALE

Scaling of the signal which is given out.

Write	Decimal Number
Read	Decimal Number
Setting	Yes

7.2.233. /DEV0...n/AUXOUTS/0...n/OFFSET

Value to be added to the output.

Write	Decimal Number
Read	Decimal Number
Setting	Yes

Unit	V
Default	0
Range	-2560 to 2560

Details

The offset value is applied after scaling.

7.2.234. /DEV0...n/CPUS

Nodes for the real-time CPUs.

7.2.235. /DEV0...n/CPUS/0...n

Node for one real-time CPU.

Note

Only available with installed real-time option.

7.2.236. /DEV0...n/CPUS/0...n/WORKLOAD

Usage of the processor-time.

Write	-
Read	Decimal Number
Setting	No
Range	0 to 1

7.2.237. /DEV0...n/CPUS/0...n/PROGRAM

Node to write user programs to.

Write	-
Read	-
Setting	No

7.2.238. /DEV0...n/CPUS/0...n/OUTPUT

Node containing the standard output stream written by the real time program.

7.2.239. /DEV0...n/CPUS/0...n/USERREGS

General purpose registers to transfer data.

7.2.240. /DEV0...n/CPUS/0...n/USERREGS/0...n

General purpose register.

Write	Integer Number
-------	----------------

Read	Integer Number
Setting	Yes

7.2.241. /DEV0...n/ZCTRLS

Node containing connected ZCtrl devices.

Note

There has to be a device connected to either ZCtrl 1 or ZCtrl 2 on the backplane that children of this node appear.

7.2.242. /DEV0...n/ZCTRLS/0...n

ZCtrl instance

7.2.243. /DEV0...n/ZCTRLS/0...n/CAMP

A ZI current-amplifier connected to a ZCtrl port.

7.2.244. /DEV0...n/ZCTRLS/0...n/CAMP/AVAILABLE

1 when HF2CA is connected to the corresponding ZCtrl port.

Write	-				
Read	Integer Number				
Setting	No				
Unit	Boolean				
Default	0 (HF2CA is not connected)				
Values	<table> <tr> <td>0</td> <td>HF2CA is not connected</td> </tr> <tr> <td>1</td> <td>HF2CA is connected</td> </tr> </table>	0	HF2CA is not connected	1	HF2CA is connected
0	HF2CA is not connected				
1	HF2CA is connected				

7.2.245. /DEV0...n/ZCTRLS/0...n/CAMP/R

Chooses a value for the shunt-resistor.

Write	Integer Number										
Read	Integer Number										
Setting	Yes										
Unit	Ohm										
Default	0 (open, high ohmic)										
Values	<table> <tr> <td>0</td> <td>open, high ohmic</td> </tr> <tr> <td>10</td> <td>10 Ohm</td> </tr> <tr> <td>100</td> <td>100 Ohm</td> </tr> <tr> <td>1000</td> <td>1 kOhm</td> </tr> <tr> <td>10000</td> <td>10 kOhm</td> </tr> </table>	0	open, high ohmic	10	10 Ohm	100	100 Ohm	1000	1 kOhm	10000	10 kOhm
0	open, high ohmic										
10	10 Ohm										
100	100 Ohm										
1000	1 kOhm										
10000	10 kOhm										

	100000	100 kOhm
	1.0E6	1 MOhm

7.2.246. /DEV0...n/ZCTRLS/0...n/CAMP/GAIN

Switches between factor 1 and 10 gain.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Gain	
Default	1 (Factor 1 gain)	
Values	1	Factor 1 gain
	10	Factor 10 gain

7.2.247. /DEV0...n/ZCTRLS/0...n/CAMP/DC

Switches between AC coupling and DC coupling.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (AC coupling)	
Values	0	AC coupling
	1	DC coupling

7.2.248. /DEV0...n/ZCTRLS/0...n/CAMP/SINGLEENDED

Switches between differential and single-ended input.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	0 (Differential inputs)	
Values	0	Differential inputs
	1	Single-ended inputs

7.2.249. /DEV0...n/ZCTRLS/0...n/TAMP

A ZI transimpedance-amplifier connected to a ZCtrl port.

7.2.250. /DEV0...n/ZCTRLS/0...n/TAMP/AVAILABLE

1 when HF2TA is connected to the corresponding ZCtrl port.

Write	-
Read	Integer Number
Setting	No
Unit	Boolean
Default	0 (HF2TA is not connected)
Values	0 HF2TA is not connected 1 HF2TA is connected

7.2.251. /DEV0...n/ZCTRLS/0...n/TAMP/BIASOUT

Switches between internal and external bias.

Write	Decimal Number
Read	Decimal Number
Setting	Yes
Unit	V
Default	0

7.2.252. /DEV0...n/ZCTRLS/0...n/TAMP/EXTBIAS

Switches the external bias.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Boolean
Default	0 (External bias off)
Values	0 External bias off 1 External bias on

7.2.253. /DEV0...n/ZCTRLS/0...n/TAMP/0...n

A Channel of a transimpedance amplifier.

7.2.254. /DEV0...n/ZCTRLS/0...n/TAMP/0...n/CURRENTGAIN

Chooses a value for the current gain.

Write	Integer Number
Read	Integer Number
Setting	Yes
Unit	Gain
Default	1000 (Factor 1 k)
Values	100 Factor 100 1000 Factor 1 k

10000	Factor 10 k
100000	Factor 100 k
1.0E6	Factor 1 M
1.0E7	Factor 10 M
1.0E8	Factor 100 M

7.2.255. /DEV0...n/ZCTRLS/0...n/TAMP/0...n/DC

Switches between AC and DC Mode.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Boolean	
Default	1 (DC Mode)	
Values	0	AC Mode
	1	DC Mode

7.2.256. /DEV0...n/ZCTRLS/0...n/TAMP/0...n/VOLTAGEGAIN

Chooses a value for the voltage gain.

Write	Integer Number	
Read	Integer Number	
Setting	Yes	
Unit	Gain	
Default	1 (1 x)	
Values	1	1 x
	10	10 x

7.2.257. /DEV0...n/ZCTRLS/0...n/TAMP/0...n/OFFSET

Adjust offset value.

Write	Decimal Number	
Read	Decimal Number	
Setting	Yes	
Unit	V	
Default	0	

Chapter 8. Real-time Option

The Real-time option provides the capability to execute programs written in the C programming language on the RISC microprocessor of the HF2 Instrument with predictable latency and comes with an extensive programming environment.

This chapter describes:

- ─ Installation of the Real-time programming environment, in [Section 8.1](#). See [Section 8.1.1](#) and [Section 8.1.2](#) for the installation process on Windows and Linux, respectively. [Section 8.1.3](#) explains where to find the documentation in HTML format.
- ─ The Real-time Option programming reference guide which provides examples and lists the data structures and functions available in the API, in [Section 8.2](#).

Note

RT programming can be used only if the HF2LI-RT / HF2IS-RT option has been purchased and activated. This option is no further available for purchase from Zurich Instruments.

Note

The Real-time Option programming reference guide is also available as HTML. The HTML documentation is bundled with the Real-time installation zip-file available from the Zurich Instruments [download page](#).

8.1. Installation of the Real-time Development Environment

In this section we describe the installation process of the HF2's real-time development environment, see [Section 8.1.1](#) for Windows and [Section 8.1.2](#) for Linux. The real-time development environment is available from the Zurich Instruments [download page](#).

Note

The RT development environment does not include a special editor. Please use an editor of your choice, for example:

- [notepad++](#) or [PSPad](#) on Windows,
- emacs, vim, etc. on Linux.

8.1.1. Installation on Windows

Software Requirements

To use the compilation tools on Windows the RT development environment requires the 32-bit version of Cygwin which provides a Linux-like environment. Cygwin is free and open source software, for more details see the [Cygwin website](#). The only Cygwin package necessary is the `make` package. Installation of Cygwin is also detailed below.

Note

Even if your PC is natively 64-bit, the 32-bit version of Cygwin is required to run the compiler tools distributed with the RT development environment.

Installation Steps

1. Download the 32-bit version of [Cygwin](#) and run the Setup executable.

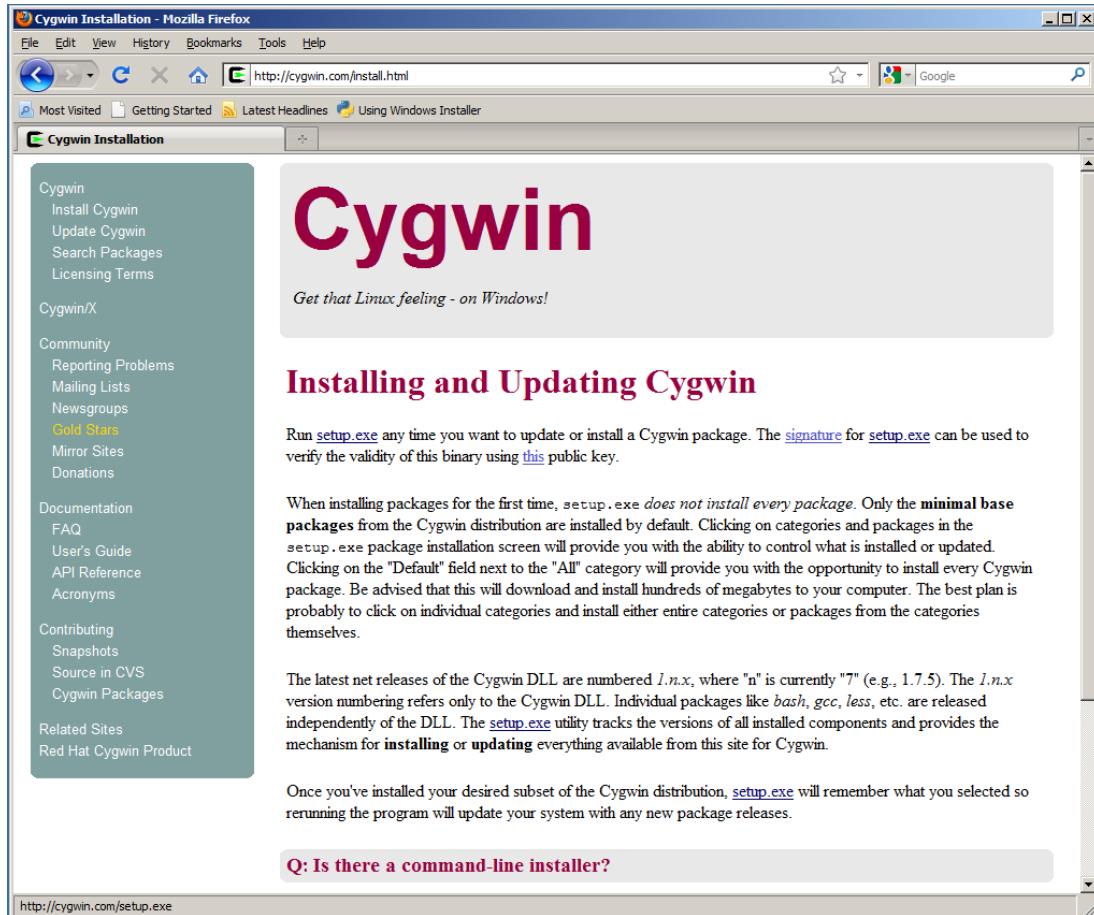


Figure 8.1. Cygwin website with install link

2. Go through the installation and, if possible, use default installation settings. There is one mandatory development package that must be installed in addition to the default installation. The package is called `make`. Select the package at the end of the installation. Select `devel`, then package `make` and select it in order to install it (see screenshots below).

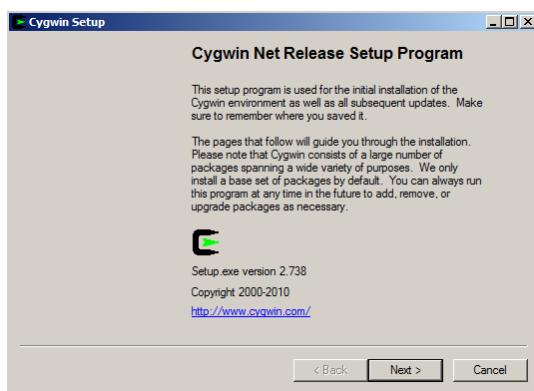
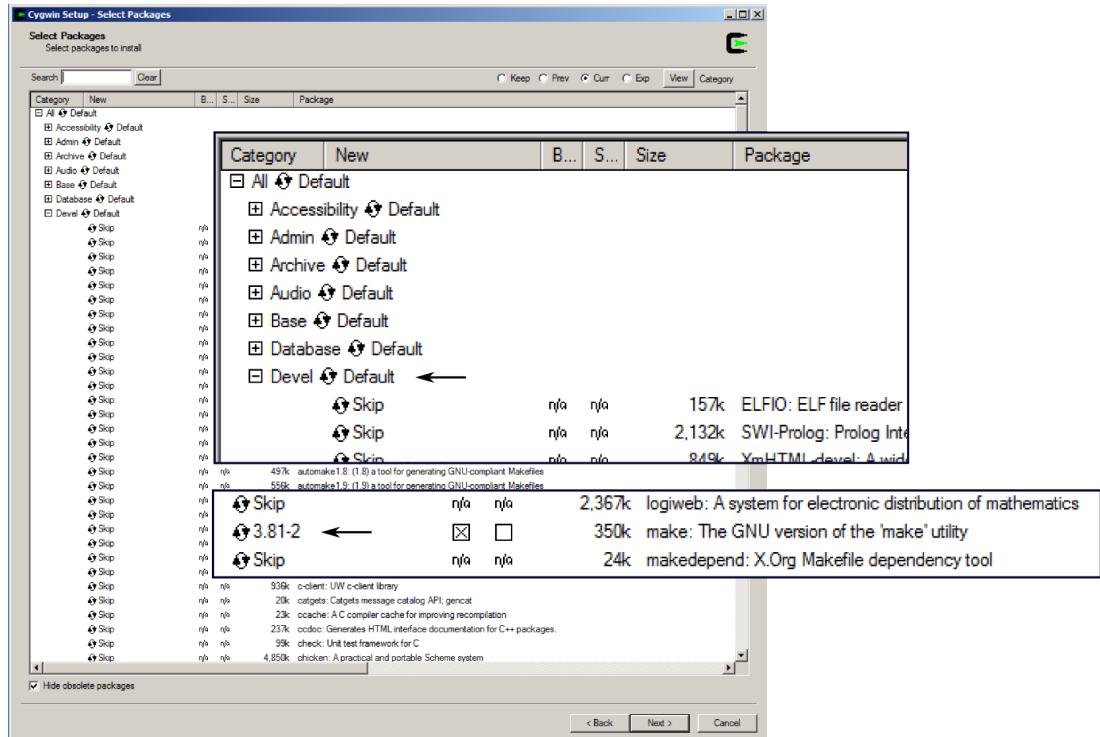


Figure 8.2. Cygwin installation



8.1.2. Installation on Linux

Software Requirements

Officially, only Ubuntu 10.04 LTS is supported, but it should be possible to run the tools on any recent Linux distribution. The program GNU make is required to compile the examples. Since the RT tools were compiled on a 32-bit architecture, you need the package `ia32_libs` installed on a 64-bit architecture in order to execute 32-bit programs on a 64-bit architecture. On a Debian-based system, both packages can be installed with:

```
sudo apt-get install ia32_libs make
```

Installation Steps

1. Before you begin with the ziRTK installation make sure that the development package "make" is installed.

```
sudo apt-get install make
```

2. Extract the ziRTK bundle in a temporary directory.

```
tar xzf ziRTK-[build number]-linux.tar.gz
```

3. Navigate into the extracted directory. The install script is called "install.sh".

```
cd ziRTK-Linux-[VERSION]
```

4. Run the install script with root rights and go through the guided installation.

```
sudo bash install.sh
```

Alternatively, you can also give executable rights to the install script and run it directly.

If possible, use default installation paths.

8.1.3. Accessing the Documentation

The developers of Zurich Instruments now happily recommend you to browse remaining examples and reference documentation in HTML format, which duplicates the following section of the user manual. You can find the HTML version of the documentation in

`[INSTALLPATH]/ziRTK/doc/html/index.html`,

which is typically found at

`C:\cygwin\usr\share\zi\ziRTK-XX.XX\doc\html\index.html`

on Windows or

`/opt/zi/ziRTK/doc/html/index.html`

on Linux.

8.2. Real-Time Option Reference Manual

This documentation is for the Zurich Instruments Real-time Option which enables users to write custom real-time programs that run on an HF2 Instrument.

The recommended way to browse this documentation is in HTML format, see the previous section for help to find it.

The documentation consists of:

- [getting started](#) which describes the necessary steps to compile and load a real-time program to an HF2 Instrument,
- a [simple example](#) that explains the structure of an RTK program,
- other [example programs](#) that demonstrate the most important concepts for controlling and configuring a real-time program,
- a [tips and tricks](#) section to get the best performance from your programs,
- a [brief description](#) of the most important development tools,
- an API Reference, that can either be browsed conceptually by module or as a complete [list](#) of available functions.

8.2.1. Getting Started

This quick-start section demonstrates how to:

- configure the real-time development environment,
- compile C source code,
- run tools on the binary to view memory usage,
- load the binary to the HF2 Instrument.

Please note that the exact shell output and the paths may differ from your installation.

Configuring the RT Development Environment

In order to use the development tools available in your RTK installation you have to configure your shell's `PATH` to include them. This can be done by sourcing the bash file included in the RTK installation directory, in your terminal type:

```
source [INSTALLPATH]/ziRTK-[VERSION]/settings.sh
```

Typically, in Windows (using Cygwin) `INSTALLPATH` is `/usr/share/` and in Linux `INSTALLPATH` is `/opt/zi/`.

In order to assert that the `PATH` has been set correctly, try locating the compiler `mb-gcc` with the `which` program and displaying its help message:

```
$ which mb-gcc
/usr/share/zi/ziRTK/tools/bin/mb-gcc.exe
$ mb-gcc --help
...
```

To automatically have the development tools available when you start a new bash shell you can add the statements in `settings.sh` to your own bash configuration file `.bashrc`, located in your home directory.

Note:

If you're using Windows you can find your `.bashrc` file (in a typical Cygwin installation) at
`C:\cygwin\home\[USERNAME]\.bashrc`
and the `settings.sh` file (typically) at
`C:\cygwin\usr\share\zi\ziRTK-[VERSION]\settings.sh`.

Compiling and Running the Examples

The Real-time option comes with a set of examples to demonstrate the main concepts of programming within the real-time environment. They are installed in your RTK example directory (by default `~/ziRTKExamples/`) during installation but can also be found in the examples directory of the ziRTK bundle downloaded from Zurich Instruments download page,.

There are two subdirectories in the RTK example directory (by default `ziRTKExamples`):

- `examples`, which contains some RT examples that are ready to compile and run,
- `skeleton`, a minimalistic example that can be used as a template for your own programs.

To compile and load an example, perform the following steps:

- Configure your path, as described above.
- In a terminal (under Windows, start Cygwin) navigate to a directory of one of the RTK examples, for example, [AuxInToAuxOut](#).
- Run `make` in the example directory by typing

```
make
```

This runs the `make` program using the `Makefile` found in the example's directory. The `Makefile` describes rules how the binary should be compiled from the source code. You should see output similar to:

```
$ make
mb-gcc -specs=/opt/zi/ziRTK/lib/microblaze/specs
-B/opt/zi/ziRTK/tools/bin/ -mno-xl-soft-mul -mxl-barrel-shift
-mxl-pattern-compare -mhard-float -mcpu=v7.10.b -O3
-fno-strict-aliasing -mxl-float-convert -mxl-float-sqrt
-fsingle-precision-constant -Winline -Wall -Wextra -std=gnu99
-I./. -I/opt/zi/ziRTK/include -c -o obj/AuxInToAuxOut.o
AuxInToAuxOut.c
mb-gcc -specs=/opt/zi/ziRTK/lib/microblaze/specs
-B/opt/zi/ziRTK/lib/microblaze/ -L/opt/zi/ziRTK/lib/microblaze
-T/opt/zi/ziRTK/lib/microblaze/linker_script_mb_standard.ld
-Wl,--as-needed -Wl,--start-group
/opt/zi/ziRTK/lib/microblaze/libziRTKmb.a obj/AuxInToAuxOut.o
-Wl,--end-group -Wl,--no-as-needed -lm -o AuxInToAuxOut.mem.elf
          Determining Size of ELF File ****
mb-size AuxInToAuxOut.mem.elf
      text      data      bss      dec      hex filename
  41650        372     3096    45118    b03e AuxInToAuxOut.mem.elf

          Generating mem file ****
data2mem -bd AuxInToAuxOut.mem.elf -d -o m AuxInToAuxOut.mem
```

Invoking the `make` command compiles, links, generates the binary `.mem`-file and runs some statistics on the binary. The next section tells you how to interpret the memory statistics.

To remove the generated files execute the command `make clean`:

```
$ make clean
rm -f obj/AuxInToAuxOut.o
```

```
rm -f AuxInToAuxOut.mem
rm -f AuxInToAuxOut.mem.elf
rm -f deps/AuxInToAuxOut.d
rm -rf obj
rm -rf deps
```

Analyze Memory Usage

The `ls -l` command lists files and reports information about them (like the size). Don't be shocked by the size of your elf file, the actual memory consumption is much smaller. Running `mb-size` on the generated ELF (Executable and Linkable Format) file informs you on the actual memory consumption.

```
$ ls -l AuxInToAuxOut.mem.elf
-rwxr-xr-x 1 user users 112957 2012-11-23 13:02 AuxInToAuxOut.mem.elf
$ mb-size AuxInToAuxOut.mem.elf
  text     data     bss     dec     hex filename
  41650     372    3096    45118    b03e AuxInToAuxOut.mem.elf
$ mb-strip AuxInToAuxOut.mem.elf
$ ls -l AuxInToAuxOut.mem.elf
-rwxr-xr-x 1 user users 43312 2012-11-23 13:03 AuxInToAuxOut.mem.elf
```

The output figures of `mb-size` are:

- text size of the program code. These are the actual commands. Note that single-value constants are often integrated in the text.
- data amount of data. These are constants like arrays of numbers and strings.
- bss the amount of data RAM that is going to be used uninitialized. This is needed for variables to compute with.
- dec the total memory usage in a decimal number
- hex the total memory usage in a hex number

To be sure that you do not include unneeded information, i.e. debugging information, in the binary you can use `mb-strip`. `mb-strip` strips all debugging information and symbol tables from an elf or object file. In this case you see that the file size is unaffected by running `mb-strip` and everything is OK.

You can find a short description of the other most important tools [here](#).

Loading the Binary to the HF2 Instrument

To load the compiled example to your HF2 Instrument on the command line run `make` specifying the target `prog`

```
make prog
```

This invokes the command line program `zirtkprog` to load the program.

To load the compiled example from ziControl select to "Real-time" tab and click the "Program" button. Note, however, the program has to be compiled from the command line.

8.2.2. Tips and Tricks

- Unless absolutely necessary, it is highly discouraged to use the `double` data type. The hardware floating-point unit only supports single-precision (`float`). Therefore using `double` will make your code ~10-times bigger and 10-times slower.
- Most maths functions exist in a `double` and a `float` variant. The `float` variants have an "f" appended. E.g. `cos (double x)` and `cosf (float x)`. For optimal performance, make sure

that you always use the float variants. In order to compare the timings of calling `sin()` versus `sinf()` for example, see the [SpeedTest example](#).

- Configure parameters of your real-time program at run-time to avoid hard-coding and recompiling your program, see the [UserRegs](#) and [MultipleParameters](#) examples.
- View log messages sent to the PC with the `ziRTKPrintf()` function in the Realtime tab of `ziControl` by enabling the Log messages button. Log messages can be viewed on the command line with the program `zirtkcat`.
- Use multiple triggers and test which trigger has fired for advanced control of your program, see the [MultipleTriggers example](#).
- Check that you're not dropping samples when using a sample-based trigger such as `ziRTKAddDemodSampleTrigger()` by ensuring that your program is occupying (slightly) less than 100% CPU. View CPU usage in the realtime tab of `ziControl` or via the node `/devX/cpus/0/workload`.
- Test how fast your `ziRTKLoop()` can run by calling it via a clock trigger (`ziRTKAddClockTrigger()`) with delay 0 and measuring its speed with code similar to that in the [SpeedTest example](#).
- Be aware that calling functions that change instrument settings, e.g., `ziRTKDIOSetOutput()`, or send data to the PC, e.g., `ziRTKPrintf()`, can cause a large amount of data to be sent between the real-time program and the PC. In extreme cases this can overload `ziServer` and cause the real-time program to stop running properly. In order to minimize this:
 - When possible use a "NoUpdate" version of the function, i.e., use `ziRTKDIOSetOutputNoUpdate()` instead of `ziRTKDIOSetOutput()`.
 - Avoid a large number of calls (at full loop speed) to `ziRTKPrintf()`, `ziRTKUserRegGet()` and `ziRTKUserRegSet()`.

8.2.3. Example Real-time Programs

The source code and Makefiles for the examples can be found either in the real-time examples directory that is created during the installation of RTK or in the "examples" folder of the RTK download bundle available from .

- Program structure: A simple program demonstrating `ziRTKInit()` and `ziRTKLoop()`
- User registers: Data transfer between the PC and the RT environment
- Triggers: Write values to an auxiliary output via a clock trigger
- Triggers: Control loop behavior with multiple triggers
- User registers: Control loop behavior and multiple program parameters with user registers
- Loop Structure: Record demodulator data upon a DIO trigger using a finite state machine
- Performance: Calculate the update rate of a real-time program

8.2.4. Program structure: A simple program demonstrating `ziRTKInit()` and `ziRTKLoop()`

In the real-time programming environment there are two functions available to the user which define how a real-time program runs: `ziRTKInit()` and `ziRTKLoop()`. The `main()` function is not directly available to the user, it is implemented elsewhere and manages low-level functionality (such as the triggers to `ziRTKLoop()`). The user must define both functions, although either function can be empty.

`ziRTKInit()` is called once upon loading the real-time program onto the HF2, it is used to initialize the program and HF2. It also defines which triggers should repeatedly cause `ziRTKLoop()` to be called. `ziRTKInit()` can configure `ziRTKLoop()` to run, for example, every time a new demodulator sample output is available (`ziRTKAddDemodSampleTrigger()`) or every time a trigger

signal is sent on the HF2's DIO (ziRTKAddDIOSampleTrigger()). ziRTKInit() can also specify that ziRTKLoop() be called with a fixed time delay (ziRTKAddClockTrigger()), or just as fast as possible (ziRTKAddClockTrigger() with delay 0), so that ziRTKLoop() just gets called back-to-back with minimal delay in between.

For help compiling the example see the [getting started section](#).

```
/* $Rev: 32629 $ $Date: 2015-09-23 14:40:54 +0200 (Wed, 23 Sep 2015) $  
*  
* Description:  
*  
* This example copies the values from auxiliary inputs 0 and 1  
* directly to the auxiliary outputs 0 and 1. ziRTKLoop() is defined  
* to be called every time a new auxiliary input sample arrives via  
* calling the function ziRTKAddAuxInSampleTrigger() to ziRTKInit().  
*  
* The averaging performed at the auxiliary input can be changed by  
* modifying the value of the 0th user register. This can be done in  
* the Real-time tab of ziControl.  
*  
*/  
  
#include <math.h>  
#include <ziRTK.h>  
  
AuxInSample Sample;  
float Ch0, Ch1;  
  
unsigned int Reg, TS=0, PrevTS=0;  
unsigned int Averaging = 10000;  
  
void ziRTKInit() {  
  
    ziRTKAddAuxInSampleTrigger( 0, NULL );  
    ziRTKAuxInSetAveraging( 0, Averaging );  
  
    // Set auxout 0 and 1 to manual  
    ziRTKAuxOutSetOutputSelect( 0, -1 );  
    ziRTKAuxOutSetOutputSelect( 1, -1 );  
  
}  
  
void ziRTKLoop() {  
  
    // Get sample from auxin  
    ziRTKAuxInGetSample( 0, &Sample );  
  
    ziRTKAuxInSampleGetValue( &Sample, 0, &Ch0 );  
    ziRTKAuxInSampleGetValue( &Sample, 1, &Ch1 );  
  
    ziRTKAuxOutSetOffset( 0, Ch0 );  
    ziRTKAuxOutSetOffset( 1, Ch1 );  
  
    ziRTKUserRegGet( 0, &Reg );  
  
    TS = ziRTKGetTimeStamp32();  
  
    if( Reg != Averaging ){  
        Averaging = Reg;  
        ziRTKAuxInSetAveraging( 0, Averaging );  
    }  
  
    //ziRTKPrintf("dT = %08u, UserReg0 = %u\n", ( TS - PrevTS ), Reg);  
  
    PrevTS = TS;  
}
```

8.2.5. User registers: Data transfer between the PC and the RT environment

```
/* $Rev: 34746 $ $Date: 2016-01-20 09:49:49 +0100 (Wed, 20 Jan 2016) $  
*  
* Description:  
*  
* This example demonstrates how to modify a parameter of a running  
* real-time program. This is achieved via so-called user registers  
* (0...63) which are dedicated 32-bit memory slots for a user to  
* transfer data between the PC and a real-time program.  
*  
* Here we get the R value from a demodulator and output it on  
* auxiliary output 0, scaled by a user-specified floating-point  
* factor obtained from user register 0.  
*  
* Test this program:  
*  
* Connect a feedback cable from signal input 0 to signal output 0 and  
* load the configuration file in this folder in the Save tab of  
* ziControl. In the Realtime tab in ziControl load the binary  
* UserRegs.mem, you should see the counter increment in the UserReg 1  
* in the "Decimal/Hex" field. Enable the log and modify the  
* scaleFactor parameter in the "Float Value" field of UserReg 0. You  
* can observe that the scaleFactor printed in the log is modified,  
* and, by going to the Auxiliary I/O Tab, the value of R written to  
* auxiliary output 0 is also modified accordingly.  
*  
* Note:  
*  
* Calling ziRTKLoop() at a very high frequency (e.g., by increasing  
* the demodulator's output rate) would cause a large amount of data  
* to be sent between the real-time program and the PC (due to  
* ziRTKUserRegGet(), ziRTKUserRegSet() and ziRTKPrintf()). In extreme  
* cases this can overload ziServer and cause the real-time program to  
* stop running properly.  
*/  
  
#include <math.h>  
#include <ziRTK.h>  
  
// helper to obtain floating point values from user registers  
typedef union {  
    unsigned int as_uint32;  
    float as_float;  
} union_uint32_float_t;  
  
unsigned int demodIndex = 0;  
unsigned int auxOutIndex = 0;  
  
void ziRTKInit() {  
  
    // Define a demodulator sample trigger  
    ziRTKAddDemodSampleTrigger( demodIndex, NULL );  
    // Use a slow demodulator output rate  
    ziRTKDemodSetRate( demodIndex, 10.0f );  
  
    // Set auxiliary output 0 to manual  
    ziRTKAuxOutSetOutputSelect( auxOutIndex, -1 );  
  
}  
  
DemodSample SampleD;  
float R = 0.0f;
```

```
float scaleFactor = 1.0f;
unsigned int counter = 0;

void ziRTKLoop() {

    // Update configuration. Note we have to do a reinterpret typecast
    // in order to retrieve a floating point value from a user
    // register. Necessary since ziRTKUserRegGet() retrieves an
    // unsigned integer but we want the floating-point number with the
    // same bit representation.
    union_uint32_float_t userreg;
    ziRTKUserRegGet( 0, &userreg.as_uint32 );
    scaleFactor = userreg.as_float;

    // Note if scaleFactor was an integer and not a floating point
    // value, it would be possible to just use:
    // unsigned int scaleFactor;
    // ziRTKUserRegGet( 0, &scaleFactor );

    // Get the demodulator R value
    ziRTKDemodGetSample( demodIndex, &SampleD );
    ziRTKDemodSampleGetCompR( &SampleD, &R );

    // Write the scaled value to the auxiliary output
    ziRTKAuxOutSetOffset( auxOutIndex, scaleFactor*R );

    // Also print the scaled value to the log
    ziRTKPrintf("\n R: %e, scaleFactor: %e\n", R, scaleFactor);

    // Write a counter to user register 1
    ziRTKUserRegSet( 1, counter );
    counter++;

}
```

8.2.6. Triggers: Write values to an auxiliary output via a clock trigger

```
/* $Rev: 937 $ $Date: 2009-01-09 20:19:12 +0100 (Fri, 09 Jan 2009) $ */
/*
 * Description:
 *
 * This example generates a sine wave (sinRet) on the auxiliary
 * output channel 0 and sets the frequency of oscillator 0 according
 * to fabs(sinRet)*1000000 with a fixed delay interval. The delay is
 * defined using a clock trigger in ziRTKInit().
 *
 * Note:
 *
 * Calling ziRTKLoop() at a very high frequency (e.g., by increasing
 * the demodulator's output rate) would cause a large amount of data
 * to be sent between the real-time program and the PC (due to
 * ziRTKAuxOutSetOffset()). In extreme cases this can overload
 * ziServer and cause the real-time program to stop running
 * properly. Please see the note below about the "NoUpdate" version of
 * ziRTKAuxOutSetOffset().
 *
 */

#include <math.h>
#include <ziRTK.h>

void ziRTKInit() {
```

```
// Define the delay for the clock trigger (in milliseconds)
unsigned int clockTriggerDelay = 200;
ziRTKAddClockTrigger( clockTriggerDelay, NULL );

// Set auxiliary output channel 0 to manual
ziRTKAuxOutSetOutputSelect( 1, -1 );

}

unsigned int counter = 0;
float phaseAcc = 0.;

void ziRTKLoop() {

    const float sinRet = sinf( phaseAcc );

    // Output on auxiliary outputs using with a 10 V amplitude
    // Auxiliary output range is -10 to 10 volts, scale accordingly
    ziRTKAuxOutSetOffset( 0, sinRet * 10. );
    // Note: if ziServer is getting overloaded by a lot of data
    // transfer, try using the no update version:
    // ziRTKAuxOutSetOffsetNoUpdate(), which sets the
    // values in hardware but doesn't communicate the value via USB to
    // the ziServer

    ziRTKOscSetFreq( 0, fabs( sinRet ) * 1000000 );

    // Accumulate phase
    phaseAcc += 0.001;
    if( phaseAcc > (float) 2. * 3.14159265 )
        phaseAcc-= (float) 2. * 3.14159265;

    // Say something every 1000 times
    if( counter % 1000 == 0 ) {
        ziRTKPrintf( "phaseAcc: %f, counter: %d \n",
                     phaseAcc, counter );
    }

    counter++;
}

}
```

8.2.7. Triggers: Control loop behavior with multiple triggers

```
/* $Rev: 32632 $ $Date: 2015-09-23 14:56:44 +0200 (Wed, 23 Sep 2015) $
 *
 * Description:
 *
 * This example demonstrates how to use multiple triggers in order to
 * perform two actions. The two actions are:
 *
 * 1. When a new sample is available from the demodulators ziRTKLoop()
 * obtains the demodulator's R and Theta values and writes them to the
 * first two auxiliary output channels.
 *
 * 2. When a new sample is available from the first auxiliary input
 * channel it writes the value to the digital output.
 *
 * Additionally, either the value of R and theta or the auxiliary
 * input sample is printed to the log. This be viewed either in the
 * log messages of ziControl's Realtime tab or in a terminal using the
 * program zirtkcat.
 *
 * This is achieved by adding demodulator and auxiliary input
 * triggers in ziRTKInit(). ziRTKLoop() checks which of the triggers
 * is active and performs the relevant action.
```

```
/*
 * Note: Calling ziRTKLoop() at a very high frequency (e.g., by using
 * a very high demodulator output rate) would cause a large amount of
 * data to be sent between the real-time program and the PC (due to
 * ziRTKPrintf()). In extreme cases this can overload ziServer and
 * cause the real-time program to stop running properly.
 */
#include <math.h>
#include <ziRTK.h>

ziTriggerId DemodTrigger;
ziTriggerId AuxInTrigger;

void ziRTKInit() {

    // Define when ziRTKLoop() should be called
    ziRTKAddDemodSampleTrigger( 0, &DemodTrigger );
    ziRTKAddAuxInSampleTrigger( 0, &AuxInTrigger );

    // Set a slow demodulator output rate
    ziRTKDemodSetRate( 0, 1 );

    // Set auxiliary output channels 0 and 1 to manual
    ziRTKAuxOutSetOutputSelect( 0, -1 );
    ziRTKAuxOutSetOutputSelect( 1, -1 );

    // Set the DIO to drive outputs
    ziRTKDIOSetDrive( 0, 3 );

}

DemodSample SampleD;

float R;
float Theta;

AuxInSample SampleAI;
float AuxInValue;

unsigned int IsTriggered;

void ziRTKLoop() {

    ziRTKWriteString( "Start of ziRTKLoop\n" );

    ziRTKTestTrigger( DemodTrigger, &IsTriggered );

    if( IsTriggered ) {
        // Retrieve the sample of demodulator 0
        ziRTKDemodGetSample( 0, &SampleD );

        // Retrieve the magnitude of the demodulator 0 sample.
        // All voltages in V
        ziRTKDemodSampleGetCompR( &SampleD, &R );

        // Retrieve the phase
        ziRTKDemodSampleGetTheta( &SampleD, &Theta );

        // Output the magnitude on stdout
        ziRTKPrintf( "Demod. R: %f\n", R );

        // Write R to on auxiliary output channel 0
        ziRTKAuxOutSetOffset( 0, R );
        // Write Theta on auxiliary output channel 1
        // Auxiliary output range is -10 to 10; scale Theta accordingly
    }
}
```

```
    ziRTKAuxOutSetOffset( 1, Theta / 18 );
}

ziRTKTestTrigger( AuxInTrigger, &IsTriggered );

if( IsTriggered ) {

    // Retrieve the sample from auxiliary input 0
    ziRTKAuxInGetSample( 0, &SampleAI );

    // Retrieve the value of the sample from channel 0
    ziRTKAuxInSampleGetValue( &SampleAI, 0, &AuxInValue );

    ziRTKPrintf( "AuxInValue: %f\n", AuxInValue );

    // Write the auxiliary output value to the digital output.
    // Auxiliary input range is -10 to 10; add 10 to obtain a
    // positive value.
    // Digital output range is 0 to 2^16; multiply by 2^16 to attain
    // an integer value in that range.
    ziRTKDIOSetOutput( 0, ( AuxInValue + 10 ) * 0x7fff );
}

}
```

8.2.8. User registers: Control loop behavior and multiple program parameters with user registers

```
/* $Rev: 34746 $ $Date: 2016-01-20 09:49:49 +0100 (Wed, 20 Jan 2016) $ */
/*
* Description:
*
* This program obtains demodulator 0's R value, scales and offsets it
* and writes it to an auxiliary output. ziRTKLoop() is called on a
* demod trigger. The scale, offset and auxiliary channel are all
* configurable via user registers. The mode (idle,compute,debug) of
* the program can also be controlled by a user register.
*
* Test this program:
*
* Connect a feedback cable from signal output 1 to signal input 1 and
* load the configuration file MultipleParameters.zicfg in this
* folder. You should obtain a near constant value on the R value of
* demodulator 0, it is this value we will write to the auxiliary
* outputs.
*
* Now go to the Realtime tab in ziControl and first load the xml file
* containing the user register names, RegisterNames.xml, by clicking
* on the "Reg Name" pull-down menu and selecting "Open...". You
* should see the first 5 user register names get populated.
*
* Now load the real-time program's binary file
* MultipleParameters.mem. The CONTROL register is used to define the
* program's behavior, if no bits of the CONTROL register are set,
* then the program is idle. The bits of CONTROL have the following
* meaning:
*
* - 1: perform a configuration update
* - 2: call the Compute() function (compute the average)
* - 3: show some debugging output
*
* Accordingly, setting the CONTROL register to the following values
* has the described affect:
* - control=0, idle
* - control=1, perform config update and then go idle
```

```
* - control=2, compute (average R and write to auxout)
* - control=3, perform config update once and then compute
* - control=6, compute and debug (write average R to R_AVERAGE
*           register)
* - control=7, perform config update once and then compute and debug
*           (write average R to R_AVERAGE register) See
*           ziRTKLoop() for more details.
*
*
* The program's CONTROL is initially set to 2 (compute bit is set),
* and we can see that the demodulator's R value is being written to
* auxiliary output channel 0 in the Auxiliary I/O Tab in ziControl.
*
* Now set the CONTROL register to 6, which runs the program in
* "compute and debug" mode, now you should see the average value of R
* update in the "DEBUG" register's "Float Value" field.
*
* The scale and offset used in the program can be modified in the
* corresponding register's "Float Value" field and the auxiliary
* output channel to be used can be specified in the AUXOUT_CHANNEL
* registers "Decimal" field. Send the configuration update to the
* program by setting the 1st bit of the CONTROL reg as described
* above.
*
*/
#include <math.h>
#include <ziRTK.h>

// Define which user registers hold which configuration
#define CONTROL_REG 0
#define AUXOUT_CHANNEL_REG 1
#define SCALE_REG 2
#define OFFSET_REG 3
#define DEBUG_REG 4

// helper to obtain floating point values from user registers
typedef union {
    unsigned int as_uint32;
    float as_float;
} union_uint32_float_t;

// Globals /////////////////////
// Program parameters
float Scale = 1.0f; // Scaling factor for the output
float Offset = 0.0f; // Offset for the output
unsigned int AuxOutChannel = 0;

// Helpers for debugging
// Write debugging output DebugPeriodSeconds times per second
float DebugPeriodSeconds = 0.5f;
unsigned int DebugPeriod = 0;

void ziRTKInit() {

    // Add a demod trigger for ziRTKLoop()
    ziRTKAddDemodSampleTrigger( 0, NULL );

    // Set auxout 0 to manual
    ziRTKAuxOutSetOutputSelect( AuxOutChannel, -1 );

    // Zero all user registers for tidiness
    for(int i=0;i<64;i++) {
        ziRTKUserRegSet( i, 0 );
    }

    // Initialise user registers that are used for program parameters
}
```

```
// with the initial values set in this program.
// Note that we have to use our union helper union_uint32_float_t to
// set floating-point values (see also UserRegs example).
unsigned int initialControl = 2;
ziRTKUserRegSet( CONTROL_REG, initialControl );
ziRTKUserRegSet( AUXOUT_CHANNEL_REG, AuxOutChannel );

union_uint32_float_t userreg;
userreg.as_float = Scale;
ziRTKUserRegSet( SCALE_REG, userreg.as_uint32 );
userreg.as_float = Offset;
ziRTKUserRegSet( OFFSET_REG, userreg.as_uint32 );

float demodRate = 0.0f;
ziRTKDemodGetRate( 0, &demodRate );
DebugPeriod = (unsigned int)(ceil(DebugPeriodSeconds*demodRate));

}

// Retrieve a configuration update from the user registers
inline void UpdateConfig(void) {

    // Note that we have to use our union helper union_uint32_float_t to
    // set floating-point values (see also UserRegs example).
    union_uint32_float_t userreg;

    ziRTKUserRegGet( SCALE_REG, &userreg.as_uint32 );
    Scale = userreg.as_float;

    ziRTKUserRegGet( OFFSET_REG, &userreg.as_uint32 );
    Offset = userreg.as_float;

    ziRTKUserRegGet( AUXOUT_CHANNEL_REG, &AuxOutChannel );
    // Set the auxiliary output channel to manual
    ziRTKAuxOutSetOutputSelect( AuxOutChannel, -1 );

    // Tell the user the config update via the log
    ziRTKPrintf(
        "\nUpdated config: AuxOutChannel: %d, Scale: %e, Offset: %e \n",
        AuxOutChannel, Scale, Offset );

    float demodRate = 0.0f;
    ziRTKDemodGetRate( 0, &demodRate );
    DebugPeriod = (unsigned int)(ceil(DebugPeriodSeconds*demodRate));

}

float result = 0.0f;

// Perform our computation on a new demod sample
inline void Compute(void) {

    // Get the R value of the first demodulator
    DemodSample SampleD;
    ziRTKDemodGetSample( 0, &SampleD );
    float R = 0.0f;
    ziRTKDemodSampleGetCompR( &SampleD, &R );

    // Perform a computation on R here..
    // R = ...

    // Scale and offset
    result = Scale*R + Offset;

    // Write the result to the auxiliary output
    ziRTKAuxOutSetOffsetNoUpdate( AuxOutChannel, result );

}
```

```
}

inline void Debug(void) {

    static unsigned int debugCount = 0;
    if( debugCount > DebugPeriod ) {
        // Write the current output to a user register
        union_uint32_float_t userreg;
        userreg.as_float = result;
        ziRTKUserRegSet( DEBUG_REG, userreg.as_uint32 );
        debugCount = 0;
    }
    debugCount++;
}

// The main loop
void ziRTKLoop() {

    // Get control register
    unsigned int control;
    ziRTKUserRegGet( CONTROL_REG, &control );

    // Update configuration?
    if (control & 0x0001) {
        UpdateConfig();
        ziRTKUserRegSet( CONTROL_REG, control & 0xffffffff );
    }

    // Compute?
    if (control & 0x0002) {
        Compute();
    }

    // Debug?
    if (control & 0x0004) {
        Debug();
    }
}
```

8.2.9. Loop Structure: Record demodulator data upon a DIO trigger using a finite state machine

```
/* $Rev: 34748 $ $Date: 2016-01-20 10:50:14 +0100 (Wed, 20 Jan 2016) $
 *
 * Description:
 *
 * Write a burst of demodulator data to an auxiliary output.
 *
 * Upon being triggered externally by a value written on the HF2's
 * digital input, this program writes RecordingTime milliseconds of
 * data from the demodulators to an auxiliary output.
 *
 * The recording behavior is implemented in ziRTKLoop() as a finite
 * state machine which acts on fresh demodulator samples as defined by
 * the demodulator trigger in ziRTKLoop().
 *
 * Test this program:
 *
 * After loading DemodRecorder.mem, go to the Auxiliary Outputs tab
 * in ziControl, enable the drive button of the lower Digital I/O bits
 * and change the lowest bit (DIO_PIN) of the input. In Auxiliary
 * Output 1's Value field you can see that the demodulator's magnitude
 * is written for a duration of RecordingTime milliseconds.
```

```
/*
 */

#include <math.h>
#include <ziRTK.h>

void ziRTKInit() {

    ziRTKAddDemodSampleTrigger( 0, NULL );

    // Set auxiliary output channel 0 to manual
    ziRTKAuxOutSetOutputSelect( 0, -1 );

    // Set DIO not to drive outputs
    ziRTKDIOSetDrive( 0, 0 );

}

// DIO pin which will be used as trigger source
// (may be set by the user)
#define DIO_PIN          0

// duration of the recording time in Milliseconds
// (may be set by the user)
const unsigned int RecordingTime = 2000;

// States of the finite state machine
#define STATE_IDLE        0
#define STATE_RECORDING   1
#define STATE_WAIT         2

// Initialize state of the finite state machine
unsigned int State = STATE_IDLE;

// Will hold the timestamp of the first sample in recording window
unsigned int StartTS = 0;

// Will be '1' if the trigger dio pin is in the active state
unsigned int TriggerPin = 0;

DemodSample SampleD;

DIOSample SampleDIO;
unsigned int DIOBits;

// Counter used for counting the samples being recorded
// just for testing!
unsigned int Count = 0;

void ziRTKLoop() {

    // Read the DIO sample
    ziRTKDIOGetSample( 0, &SampleDIO );
    ziRTKDIOSampleGetBits( &SampleDIO, &DIOBits );

    // Check if the pin is low (active state)
    TriggerPin = ( ( DIOBits & ( 1 << DIO_PIN ) ) == 0 ) ? 1 : 0;

    if( State == STATE_IDLE ) {

        ziRTKAuxOutSetOffset( 0, 0 );

        // Wait for the trigger pin going to active state
        if( TriggerPin == 1 )
            State = STATE_RECORDING;

        StartTS = 0;

    }

}
```

```
Count = 0;

}

if( State == STATE_RECORDING ) {
    // Record as long as the timestamp of the demodsample lies within
    // the recording window

    // Retrieve the sample from demodulator 0
    ziRTKDemodGetSample( 0, &SampleD );

    unsigned int CurrentTS;
    ziRTKDemodSampleGetTimeStamp32( &SampleD, &CurrentTS );

    if( StartTS == 0 ) {
        // Set the start timestamp if the current sample is the first
        // recorded sample ( StartTS == 0 )
        StartTS = CurrentTS;
    }
    else {
        // Check if the time recorded is bigger then the required
        // recording time
        // If so advance state to STATE_WAIT

        if( ( CurrentTS - StartTS )
            / ( ZIRTK_HF2_SAMPLERATE / 1000 ) > RecordingTime )
            State = STATE_WAIT;
    }
}

if( State == STATE_RECORDING ) {
    // Current timestamp lies within time window
    // (State has not been advanced in check)

    // Do something with the samples here!

    // For testing purposes printf the counter and increment it
    ziRTKPrintf("Count: %d\n", Count );
    Count++;

    float Mag;
    ziRTKDemodSampleGetCompR( &SampleD, &Mag );
    Mag = Mag * 10;           // Scale the Mag to 0 V ... 10 V
    if( Mag > 10 ) Mag = 10; // Clip to 10 V

    // Output on auxiliary output
    ziRTKAuxOutSetOffset( 0, Mag );
}

}

if( State == STATE_WAIT ) {

    ziRTKAuxOutSetOffset( 0, 0 );

    // Wait until the trigger pin returns to inactive state to return
    // to beginning
    if( TriggerPin == 0 )
        State = STATE_IDLE;

}

}
```

8.2.10. Performance: Calculate the update rate of a real-time program

```
/* $Rev: 34746 $ $Date: 2016-01-20 09:49:49 +0100 (Wed, 20 Jan 2016) $  
*  
* Description:  
*  
* This example shows how to check the update rate of ziRTKLoop().  
*  
* Test this program:  
*  
* Load the program SpeedTest.mem and the RegisterNames.xml file in  
* the Realtime tab of ziControl. The approximate time taken and  
* update rate of ziRTKLoop() is shown in the user registers 1 and 2.  
* Change the decimal value of the Control register to modify which  
* code is executed. We see that the floating point version of sin()  
* runs considerably faster.  
*/  
  
#include <math.h>  
#include "ziRTK.h"  
  
// Define user registers  
#define CONTROL_REG 0  
#define PERFORMANCE_DT_REG 1  
#define PERFORMANCE_RATE_REG 2  
  
// Helper for writing floats to user registers  
typedef union {  
    unsigned int as_uint;  
    float as_float;  
} union_uint_float;  
  
// Globals ////////////////////////////////  
  
// The delay (milliseconds) for the clock trigger.  
// Delay 0 results in continual triggering.  
unsigned int ziRTKLoopTimeDelay = 0;  
  
// Variables for performance monitoring:  
// Define how often we should calculate and update the performance  
unsigned int PerformanceRefreshPeriodSeconds = 1.0f;  
  
// Calculate and update performance after this many counts.  
// After the first performance calculation this is adjusted according  
// to PerformanceRefreshPeriodSeconds  
unsigned int PerformanceRefreshPeriod = 10000;  
  
// Counter to test when to calculate and update performance  
unsigned int PerformanceCount = 0UI;  
  
// Store the timestamp to calculate performance  
unsigned int PerformanceLastTs = 0UI;  
  
void ziRTKInit() {  
  
    // Define a clock trigger  
    const unsigned int timerDelayMicroSeconds = ziRTKLoopTimeDelay;  
    ziRTKAddClockTrigger( timerDelayMicroSeconds, NULL );  
  
    // Initialise RTK user registers to zero  
    for(unsigned int i=0;i<64;i++) {  
        ziRTKUserRegSet( i, 0 );  
    }  
}
```

```
}

PerformanceLastTs = ziRTKGetTimeStamp32();

}

inline void Performance(void) {

    if( PerformanceCount > PerformanceRefreshPeriod ) {

        // Get the current timestamp from the HF2 in ticks (integer)
        const unsigned int ts = ziRTKGetTimeStamp32();

        // Calculate the total time taken and convert it from ticks to
        // seconds by dividing by ZIRTK_HF2_SAMPLERATE
        const float dtPeriod =
            (float)(ts - PerformanceLastTs)/(float)(ZIRTK_HF2_SAMPLERATE);

        // Get the time taken for one loop
        const float dt = dtPeriod/PerformanceRefreshPeriod;
        // and calculate the rate
        const float rate = 1.0F/dt;

        // Adjust timing to run performance calculation approx every
        // PerformanceRefreshPeriodSeconds
        PerformanceRefreshPeriod = PerformanceRefreshPeriodSeconds/dt;

        // See example UserRegs for an explanation
        union_uint_float userreg;

        // Send rate and dt to the PC via user registers
        userreg.as_float = rate;
        ziRTKUserRegSet( PERFORMANCE_RATE_REG, userreg.as_uint );

        userreg.as_float = dt;
        ziRTKUserRegSet( PERFORMANCE_DT_REG, userreg.as_uint );

        PerformanceCount = 0UI;
        PerformanceLastTs = ziRTKGetTimeStamp32();

    }

    PerformanceCount++;

}

void ziRTKLoop(void) {

    // Get the control register which determines which function we want
    // to calculate
    unsigned int control = 0;
    ziRTKUserRegGet( CONTROL_REG, &control );

    // Declare volatile to prevent the compiler from performing
    // optimization that would distort our timings
    volatile float f = 0.0f;
    volatile float g = 1.23f;

    switch( control ) {
    case 0:
        // The normal version of sin()
        f = sin(g);
        break;
    default:
        // The floating point version of sin()
        f = sinf(g);
    }

}
```

```
// Check and calculate(?) the current performance of this program
Performance();

}
```

8.2.11. Development Tools

This section briefly documents the most important command-line tools delivered with the real-time programming environment. They can be found in

[INSTALLPATH]/ziRTK/tools/bin/.

For more information please see the Xilinx "Embedded System Tools Reference Manual".

- *zirtkprog* - loads binary real-time programs (.mem files) from the PC to an HF2 Instrument. Note, that it's also possible to load a program via the "Realtime" tab in ziControl.
- *zirtkcat* - displays the output ziRTKprintf of a real-time program running on an HF2 Instrument. Note, that it's also possible to view the output of a real-time program in the "Realtime" tab in ziControl.
- *mb-gcc* - the cross-compiler for C programs. It generates an .elf file from .c source files.
- *data2mem* - creates a .mem file from an .elf file.
- *mb-size* - displays size information of a .mem file.
- *mb-readelf* - displays detailed information about the .elf file.

8.2.12. Module Documentation

User Code

This section describes the two functions that you need to implement with your own code. The two functions are called by the RTK at the appropriate times.

Functions

- `void ziRTKInit (void)`
Declaration for the `ziRTKInit` function. This function has to be defined by the user.
- `void ziRTKLoop (void)`
Declaration for the loop function. This function has to be defined by the user.

Detailed Description

In `ziRTKInit`, you may initialize parameters to your needs and add triggers on specific samples or on a timer interval. After initializing, `ziRTKLoop` is executed each time one of the configured triggers is activated. Typically you will do some calculations on new data and output the results on the analog outputs, the digital IOs or send them to the host computer.

Function Documentation

ziRTKInit

void ziRTKInit (void)

Declaration for the ziRTKInit function. This function has to be defined by the user.

Used to initialize all settings for the current application e.g. add triggers or set initial values.

```
#include <ziRTK.h>

void ziRTKInit()
{
    // Add a clock trigger at an interval of 1 millisecond.
    ziRTKAddClockTrigger( 1000, NULL );

    // Set the rate of demodulator 0 to 1 Hz.
    ziRTKDemodSetRate( 0, 1 );

    // Write a "Hello" message to usb.
    ziRTKWriteString( "Hello" );

}
```

See Also:

[ziRTKLoop](#), [ziRTKAddClockTrigger](#), [ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddUserRegTrigger](#)

ziRTKLoop

void ziRTKLoop (void)

Declaration for the loop function. This function has to be defined by the user.

This function will be executed every time a trigger occurs. Use ziRTKTestTrigger to test which triggers caused the function to be executed.

```
#include <math.h>
#include <ziRTK.h>

// Output the Magnitude and Phase of demodulator 0 on auxiliary output 0 and
// auxiliary output 1.

DemodSample Sample;

void ziRTKLoop()
{
    //retrieve sample
    ziRTKDemodGetSample( 0, &Sample );

    //calculate magnitude
    float Mag;
    ziRTKDemodSampleGetCompR( &Sample, &Mag );

    //calculate phase
    float Phi;
    ziRTKDemodSampleGetTheta( &Sample, &Phi );

    //put the values on the misc analog outputs
    ziRTKAuxOutSetOffset( 0, Mag * 10 );
    ziRTKAuxOutSetOffset( 1, Phi * 10 );

}
```

See Also:

[ziRTKInit](#), [ziRTKTestTrigger](#)

General Functions

This sections describes the basic functions that provide information on the system.

Functions

- `float ziRTKGetTimeStamp ()`
Returns a the current timestamp in seconds.
- `unsigned int ziRTKGetTimeStamp32 ()`
Returns a the current timestamp in a 32bit unsigned int.
- `unsigned long long ziRTKGetTimeStamp64 ()`
Returns a the current timestamp in a 64 bit unsigned long long.
- `float ziRTKGetWorkload ()`
Get the current workload of the CPU.
- `void ziRTKUpdateHostPCOn (void)`
Enables updating of ziServer when a value changes.
- `void ziRTKUpdateHostPCOff (void)`
Disables updating of ziServer when a value changes.
- `unsigned char ziRTKGetUpdateHostPC (void)`
Enables updating of ziServer when a value changes.
- `void ziRTKPause (int Value)`
Pauses or resumes the updating of all signal-processing related parameters. Useful for synchronizing parameter updates with the RTK.

Function Documentation

ziRTKGetTimeStamp

`float ziRTKGetTimeStamp ()`

Returns a the current timestamp in seconds.

Returns:

the timestamp as a float

See Also:

[ziRTKGetTimeStamp32](#), [ziRTKGetTimeStamp64](#)

ziRTKGetTimeStamp32

unsigned int ziRTKGetTimeStamp32 ()

Returns a the current timestamp in a 32bit unsigned int.

Returns:

the timestamp as an unsigned int

See Also:

[ziRTKGetTimeStamp](#), [ziRTKGetTimeStamp64](#)

ziRTKGetTimeStamp64

unsigned long long ziRTKGetTimeStamp64 ()

Returns a the current timestamp in a 64 bit unsigned long long.

Returns:

the timestamp as an unsigned long long

See Also:

[ziRTKGetTimeStamp](#), [ziRTKGetTimeStamp32](#)

ziRTKGetWorkload

float ziRTKGetWorkload ()

Get the current workload of the CPU.

Returns:

the current workload as a float value (0 - 1)

ziRTKUpdateHostPCOn

void ziRTKUpdateHostPCOn (void)

Enables updating of ziServer when a value changes.

See Also:

[ziRTKUpdateHostPCOff](#), [ziRTKGetUpdateHostPC](#)

ziRTKUpdateHostPCOff

void ziRTKUpdateHostPCOff (void)

Disables updating of ziServer when a value changes.

See Also:

[ziRTKUpdateHostPCOn](#), [ziRTKGetUpdateHostPC](#)

ziRTKGetUpdateHostPC

unsigned char ziRTKGetUpdateHostPC (void)

Enables updating of ziServer when a value changes.

Returns:

- 0 when updating of ziServer is disabled
- 1 when updating of ziServer is enabled

See Also:

[ziRTKUpdateHostPCOn](#), [ziRTKUpdateHostPCOff](#)

ziRTKPause

void ziRTKPause (int Value)

Pauses or resumes the updating of all signal-processing related parameters. Useful for synchronizing parameter updates with the RTK.

Parameters:

[in] Value

when Value is not zero the updating is paused otherwise all updates are running

Triggers

A trigger calls user code contained in `ziRTKLoop` on defined occurrences. I.e., a trigger can be set up to call the `Loop()` function every time a new Sample from the Demodulator arrives or when a certain time has passed.

Functions

- `ZI_STATUS ziRTKAddDemodSampleTrigger (unsigned int Index, ziTriggerId* TriggerId)`
Add a trigger on a demod sample.
- `ZI_STATUS ziRTKAddDIOSampleTrigger (unsigned int Index, ziTriggerId* TriggerId)`
Add a trigger on a DIO sample.
- `ZI_STATUS ziRTKAddAuxInSampleTrigger (unsigned int Index, ziTriggerId* TriggerId)`
Add a trigger on an auxiliary input sample.
- `ZI_STATUS ziRTKAddClockTrigger (unsigned int USec, ziTriggerId* TriggerId)`
Add a trigger on a timer.
- `ZI_STATUS ziRTKAddUserRegTrigger (unsigned int Index, ziTriggerId* TriggerId)`
Add a trigger on a user-register.
- `ZI_STATUS ziRTKTestTrigger (ziTriggerId TriggerId, unsigned int* IsSet)`
Test if a trigger has been activated.

Detailed Description

Below is a simple program that creates two triggers and tests which trigger(s) fired via the function `ziRTKTestTrigger()`.

```
#include <math.h>
#include <zRTK.h>

ziTriggerId DemodTriggerId;
ziTriggerId ClockTriggerId;

unsigned int LoopCount = 0;

void ziRTKInit()
{
    // Define when ziRTKLoop() should be called trigger on samples of
    // demodulator 0.
    ziRTKAddDemodSampleTrigger( 0, &DemodTriggerId );

    // Add a trigger which fires every second.
    ziRTKAddClockTrigger( 1000000, &ClockTriggerId );
}

void ziRTKLoop()
{
```

```
unsigned int IsSet;

// Check if the demod-trigger has fired.
ziRTKTestTrigger( DemodTriggerId, &IsSet );

if( IsSet )
    ziRTKPrintf( "Demod Trigger (%u)!\n", LoopCount );

// Check if the clock-trigger has fired.
ziRTKTestTrigger( ClockTriggerId, &IsSet );

if( IsSet )
    ziRTKPrintf( "Clock Trigger (%u)!\n", LoopCount );

LoopCount++;
}
```

For other examples see:

- [ClockTrigger](#)
- [AuxInToAuxOut](#)
- [MultipleTriggers](#)

Function Documentation

ziRTKAddDemodSampleTrigger

ZI_STATUS ziRTKAddDemodSampleTrigger (unsigned int Index, ziTriggerId* TriggerId)

Add a trigger on a demod sample.

ziRTKAddDemodSampleTrigger adds a trigger on a demodulator sample.

Parameters:

[in] Index

index of the demodulator

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the index is out of range
- ZI_LIMIT when no more triggers are available

See Also:

[ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddClockTrigger](#),
[ziRTKAddUserRegTrigger](#), [ziRTKTestTrigger](#)

ziRTKAddDIOSampleTrigger

ZI_STATUS ziRTKAddDIOSampleTrigger (**unsigned int** Index, **ziTriggerId*** TriggerId)

Add a trigger on a DIO sample.

ziRTKAddDIOSampleTrigger adds a trigger on a DIO sample.

Parameters:

[in] Index

index of the DIO

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the index is out of range
- ZI_LIMIT when no more triggers are available

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddClockTrigger](#),
[ziRTKAddUserRegTrigger](#), [ziRTKTestTrigger](#)

ziRTKAddAuxInSampleTrigger

ZI_STATUS ziRTKAddAuxInSampleTrigger (**unsigned int** Index, **ziTriggerId*** TriggerId)

Add a trigger on an auxiliary input sample.

ziRTKAddAuxInSampleTrigger adds a trigger on an auxiliary input sample.

Parameters:

[in] Index

index of the auxiliary input

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the index is out of range
- ZI_LIMIT when no more triggers are available

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddClockTrigger](#),
[ziRTKAddUserRegTrigger](#), [ziRTKTestTrigger](#)

ziRTKAddClockTrigger

ZI_STATUS ziRTKAddClockTrigger (**unsigned int** USec, **ziTriggerId*** TriggerId)

Add a trigger on a timer.

ziRTKAddClockTrigger adds a trigger that occurs in an interval

Parameters:

[in] USec

Trigger interval in microseconds

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_LIMIT when no more triggers are available

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddUserRegTrigger](#), [ziRTKTestTrigger](#)

ziRTKAddUserRegTrigger

ZI_STATUS ziRTKAddUserRegTrigger (unsigned int Index, ziTriggerId* TriggerId)

Add a trigger on a user-register.

ziRTKAddUserRegTrigger adds a trigger that occurs on a change of a user-register

Parameters:

[in] Index

Index of the register (currently 0 - 15)

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_LIMIT no more triggers are available

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddClockTrigger](#), [ziRTKTestTrigger](#)

ziRTKTestTrigger

ZI_STATUS ziRTKTestTrigger (ziTriggerId TriggerId, unsigned int* IsSet)

Test if a trigger has been activated.

In case you have added more than one trigger, you can use this function in [ziRTKLoop](#) to determine which of the triggers were active

Parameters:

[in] TriggerId

the identifier for which the state should be returned

[out] IsSet

Pointer to an unsigned int which is not equal to zero, when the trigger is active, else zero

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when TriggerId is out of range

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddClockTrigger](#), [ziRTKAddUserRegTrigger](#)

Sending Data to the Host Computer

The ZI RTK features functions to send data via USB to a host computer running the ziBase drivers and utilities.

Functions

- [**ZI_STATUS** ziRTKPrintf \(char* Fmt, ... \)](#)
printf-compatible print to stdio. The output consists of a string which may include formatting directives.
- [**ZI_STATUS** ziRTKWriteString \(char* Str \)](#)
Write a zero-terminated string to stdio.
- [**ZI_STATUS** ziRTKWriteData \(unsigned char* Buffer, unsigned int Len \)](#)
Write a buffer to stdio.

Function Documentation

ziRTKPrintf

ZI_STATUS ziRTKPrintf (char* Fmt, ...)

printf-compatible print to stdio. The output consists of a string which may include formatting directives.

Parameters:

[in] Fmt

format string composed of zero or more formatting directives and ordinary characters

[in] ...

variable count of arguments being formatted and given out according to the format string

Returns:

- ZI_SUCCESS on success

This function description is based on the Unix man pages for printf.

The format string is composed of zero or more directives: ordinary characters (not %), which are copied unchanged to the output stream; and conversion specifications, each of which results in fetching zero or more subsequent arguments. Each conversion specification is introduced by the % character.

The arguments must correspond properly (after type promotion) with the conversion specifier. All integer values (signed and unsigned) have to be 32bit values, all floating point values have to be double but standard type promotion automatically converts float values to double. Pointer arguments also have to be 32bit wide, char-arrays as arguments are not supported.

After the %, the following appear in sequence:

- Zero or more of the following flags:
 - "0" (zero) Zero padding. For all conversions except n, the converted value is padded on the left with zeros rather than blanks. If a precision is given with a numeric conversion (d, i, o, u, i, x, and X), the 0 flag is ignored.
 - "-" A negative field width flag; the converted value is to be left adjusted on the field boundary. Except for n conversions, the converted value is padded on the right with blanks, rather than on the left with blanks or zeros. A - overrides a 0 if both are given.
 - " " (space) A blank should be left before a positive number produced by a signed conversion (a, A, d, e, E, f, F, g, G, or i).
 - "+" A sign must always be placed before a number produced by a signed conversion. A + overrides a space if both are used.
- An optional decimal digit string specifying a minimum field width. If the converted value has fewer characters than the field width, it will be padded with spaces on the left (or right, if the left-adjustment flag has been given) to fill out the field width.
- An optional precision, in the form of a period . followed by an optional digit string. If the digit string is omitted, the precision is taken as zero. This gives the minimum number of digits to appear for d, i, o, u, x, and X conversions, the number of digits to appear after the decimal-point for a, A, e, E, f, and F conversions, the maximum number of significant digits for g

and G conversions, or the maximum number of characters to be printed from a string for s conversions.

- A character that specifies the type of conversion to be applied. The conversion specifiers and their meanings are:

- d, i, o, u, x, X The int (or appropriate variant) argument is converted to signed decimal (d and i), unsigned octal (o), unsigned decimal (u), or unsigned hexadecimal (x and X) notation. The letters a, b, c, d, e, f are used for x conversions; the letters A, B, C, D, E, F are used for X conversions. The precision, if any, gives the minimum number of digits that must appear; if the converted value requires fewer digits, it is padded on the left with zeros.
- e, E The double argument is rounded and converted in the style [-]d.ddde+dd where there is one digit before the decimal-point character and the number of digits after it is equal to the precision; if the precision is missing, it is taken as 6; if the precision is zero, no decimal-point character appears. An E conversion uses the letter "E" (rather than "e") to introduce the exponent. The exponent always contains at least two digits; if the value is zero, the exponent is 00.

For a, A, e, E, f, F, g, and G conversions, positive and negative infinity are represented as inf and -inf respectively when using the lowercase conversion character, and INF and -INF respectively when using the uppercase conversion character. Similarly, NaN is represented as nan when using the lowercase conversion, and NAN when using the uppercase conversion.

- f, F The double argument is rounded and converted to decimal notation in the style [-]ddd.ddd, where the number of digits after the decimal-point character is equal to the precision specification. If the precision is missing, it is taken as 6; if the precision is explicitly zero, no decimal-point character appears. If a decimal point appears, at least one digit appears before it.
- g, G The double argument is converted in style f or e (or F or E for G conversions). The precision specifies the number of significant digits. If the precision is missing, 6 digits are given; if the precision is zero, it is treated as 1. Style e is used if the exponent from its conversion is less than -4 or greater than or equal to the precision. Trailing zeros are removed from the fractional part of the result; a decimal point appears only if it is followed by at least one digit.
- c The int argument is converted to an unsigned char, and the resulting character is written.
- p The void * pointer argument is printed in hexadecimal.
- n The number of characters written so far is stored into the integer indicated by the int * (or variant) pointer argument. No argument is converted.
- % A "%\" is written. No argument is converted. The complete conversion specification is "%\%".

Examples

print an integer followed by a newline:

```
int val = 1234;
ziRTKprintf( "value: %d\n", val );
```

print an integer as 8 digit hex with leading zeros followed by a newline:

```
int val = 1234;
ziRTKprintf( "value: %08x\n", val );
```

print a float followed by a newline:

```
float val = 1.234;
```

```
ziRTKprintf( "value: %f\n", val );
```

print a float and an integer followed by a newline:

```
float fval = 1.234;  
int ival = 5678;  
ziRTKprintf( "float: %f, int: %d\n", fval, ival );
```

See Also:

[ziRTKWriteString](#), [ziRTKWriteData](#)

ziRTKWriteString

ZI_STATUS ziRTKWriteString (char* Str)

Write a zero-terminated string to stdio.

Parameters:

[in] Str
pointer to the char array

Returns:

■ ZI_SUCCESS on success

See Also:

[ziRTKPrintf](#), [ziRTKWriteData](#)

ziRTKWriteData

ZI_STATUS ziRTKWriteData (**unsigned char*** Buffer, **unsigned int** Len)

Write a buffer to stdio.

Parameters:

[in] Buffer
pointer to data

[in] Len
Length of the data

Returns:

■ ZI_SUCCESS on success

See Also:

[ziRTKPrintf](#), [ziRTKWriteString](#)

Functions to read Feature Configuration

Enumerations

- `enum ZIRTK_OPTIONS { ZIRTK_OPTION_NONE,
ZIRTK_OPTION_MF, ZIRTK_OPTION_PLL,
ZIRTK_OPTION_MOD, ZIRTK_OPTION_RTK,
ZIRTK_OPTION_UHS, ZIRTK_OPTION_PID }`

- `enum ZIRTK_DEVTYPE { ZIRTK_DEVTYPE_DEFAULT,
ZIRTK_DEVTYPE_LI, ZIRTK_DEVTYPE_IS,
ZIRTK_DEVTYPE_UNKNOWN }`

Functions

- `ZI_STATUS ziRTKFeaturesGetOptions (ZIRTK_OPTIONS* Options)`
Read which options are enabled.

- `ZI_STATUS ziRTKFeaturesGetDevType (ZIRTK_DEVTYPE* DevType)`
Read the device type.

Enumeration Type Documentation

Enumerator:

- ZIRTK_OPTION_NONE
- ZIRTK_OPTION_MF
- ZIRTK_OPTION_PLL
- ZIRTK_OPTION_MOD
- ZIRTK_OPTION_RTK
- ZIRTK_OPTION_UHS
- ZIRTK_OPTION_PID

Enumerator:

- ZIRTK_DEVTYPE_DEFAULT
- ZIRTK_DEVTYPE_LI
- ZIRTK_DEVTYPE_IS
- ZIRTK_DEVTYPE_UNKNOWN

Function Documentation

ziRTKFeaturesGetOptions

ZI_STATUS ziRTKFeaturesGetOptions (ZIRTK_OPTIONS* Options)

Read which options are enabled.

Corresponding server paths:

- DEV123/FEATURES/OPTIONS

Parameters:

[out] Options

Pointer to a ZIRTK_OPTIONS to store the value in. The Values are or'ed together.

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Kits is NULL

ziRTKFeaturesGetDevType

ZI_STATUS ziRTKFeaturesGetDevType (**ZIRTK_DEVTYPE*** DevType)

Read the device type.

Corresponding server paths:

- DEV123/FEATURES/DEVTYPE

Parameters:

[out] DevType

Pointer to a ZIRTK_DEVTYPE to store the value in.

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when DevType is NULL

Functions concerning the device

Functions

- [ZI_STATUS ziRTKSystemGetExtClk \(int* ExtClk \)](#)
Gets whether the Clock source is internal or external.
- [ZI_STATUS ziRTKSystemSetExtClk \(int ExtClk \)](#)
Set Clock to internal or external mode.
- [ZI_STATUS ziRTKSystemGetHWRevision \(int* HWRevision \)](#)
Gets the revision-number of the hardware.

Function Documentation

ziRTKSystemGetExtClk

ZI_STATUS ziRTKSystemGetExtClk (int* ExtClk)

Gets whether the Clock source is internal or external.

Corresponding server paths:

- DEV123/SYSTEM/EXTCLK

Parameters:

[in] ExtClk

Pointer to an int to store the value in.

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when ExtClk is NULL

ziRTKSystemSetExtClk

ZI_STATUS ziRTKSystemSetExtClk (int ExtClk)

Set Clock to internal or external mode.

Corresponding server paths:

- DEV123/SYSTEM/EXTCLK

Parameters:

[in] ExtClk
int which contains the value.

Returns:

- ZI_SUCCESS on success

ziRTKSystemGetHWRevision

ZI_STATUS ziRTKSystemGetHWRevision (int* HWRevision)

Gets the revision-number of the hardware.

Corresponding server paths:

- DEV123/SYSTEM/HWREVISION

Parameters:

[in] HWRevision

Pointer to an int to store the value in.

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when HWRevision is NULL

User Registers

The user registers provide an easy way to pass information from the host computer to your RTK program and back. Up to 64 32 bit words can be set by the host computer and read by your RTK program.

Functions

- `unsigned int ziRTKUserRegGetCount ()`
get the count of the user registers.
- `ZI_STATUS ziRTKUserRegGet (unsigned char Index, unsigned int* Value)`
Read flags set by the host PC.
- `ZI_STATUS ziRTKUserRegSet (unsigned char Index, unsigned int Value)`
Write flags set by the host PC.

Function Documentation

ziRTKUserRegGetCount

`unsigned int ziRTKUserRegGetCount ()`

get the count of the user registers.

Returns:

the count of user registers

See Also:

[User Registers](#)

ziRTKUserRegGet

ZI_STATUS ziRTKUserRegGet (**unsigned char** Index, **unsigned int*** Value)

Read flags set by the host PC.

Corresponding server paths:

- DEV123/CPUS/0/USERREGS/0
- DEV123/CPUS/0/USERREGS/1
- ...

The user registers are set using ziServer. The provided function is to read those values.

Parameters:

[in] Index
index of the user registers-word (currently 0 - 15)

[in] Value
Pointer to an unsigned int to store the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the provided index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKUserRegSet](#), [User Registers](#)

ziRTKUserRegSet

ZI_STATUS ziRTKUserRegSet (**unsigned char** Index, **unsigned int** Value)

Write flags set by the host PC.

Corresponding server paths:

- DEV123/CPUS/0/USERREGS/0
- DEV123/CPUS/0/USERREGS/1
- ...

Parameters:

[in] Index
index of the user registers-word (currently 0 - 15)

[in] Value
the new value to write to the user register

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the provided index is out of range

See Also:

[ziRTKUserRegGet](#), [User Registers](#)

External Memory

The external memory provide extra data memory for applications that either need to log or process large amounts of data.

Functions

- `ZI_STATUS ziRTKExtMemGet (unsigned int Address,
unsigned int* Value)`
Read an address in the external memory.
- `ZI_STATUS ziRTKExtMemSet (unsigned int Address,
unsigned int Value)`
Write an address in the external memory.

Function Documentation

ziRTKExtMemGet

ZI_STATUS ziRTKExtMemGet (unsigned int Address, unsigned int* Value)

Read an address in the external memory.

Parameters:

[in] Address

the byte-aligned address to read (0x00000000 to 0x2FFFFFF)

[in] Value

Pointer to an unsigned int to store the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the provided index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKExtMemSet, External Memory](#)

ziRTKExtMemSet

ZI_STATUS ziRTKExtMemSet (unsigned int Address, unsigned int Value)

Write an address in the external memory.

Parameters:

[in] Address

the byte-aligned address to write (0x00000000 to 0x2FFFFFF)

[in] Value

the new value to write to the external memory

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the provided index is out of range

See Also:

[ziRTKExtMemGet, External Memory](#)

HF Signal Input

The functions in this section allow you to set the range and mode of the HF inputs.

Functions

- `unsigned int ziRTKSigInGetCount ()`
get the count of high speed signal inputs.
- `ZI_STATUS ziRTKSigInGetRange (unsigned int Index, float* Range)`
Get an input's range (V).
- `ZI_STATUS ziRTKSigInSetRange (unsigned int Index, float Range)`
Set an input's range(V).
- `ZI_STATUS ziRTKSigInGetAC (unsigned int Index, int* Value)`
Get an input's AC mode.
- `ZI_STATUS ziRTKSigInSetAC (unsigned int Index, int Value)`
Set an input's AC mode.
- `ZI_STATUS ziRTKSigInGetImp50 (unsigned int Index, int* Value)`
Get an input's 50 Ohm mode.
- `ZI_STATUS ziRTKSigInSetImp50 (unsigned int Index, int Value)`
Set an input's 50 Ohm mode.
- `ZI_STATUS ziRTKSigInGetDiff (unsigned int Index, int* Value)`
Get an input's differential mode.
- `ZI_STATUS ziRTKSigInSetDiff (unsigned int Index, int Value)`
Set an input's differential mode.

Function Documentation

ziRTKSigInGetCount

`unsigned int ziRTKSigInGetCount ()`

get the count of high speed signal inputs.

Returns:

The count of signal inputs

See Also:

[HF Signal Input](#)

ziRTKSigInGetRange

ZI_STATUS **ziRTKSigInGetRange** (**unsigned int** **Index**, **float*** **Range**)

Get an input's range (V).

Corresponding server paths:

- DEV123/SIGINS/0/RANGE
- DEV123/SIGINS/1/RANGE
- ...

Parameters:

[in] **Index**
index of the parameter

[out] **Range**
Pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Range is NULL

See Also:

[ziRTKSigInSetRange](#), HF Signal Input

ziRTKSigInSetRange

ZI_STATUS ziRTKSigInSetRange (**unsigned int** Index, **float** Range)

Set an input's range(V).

Corresponding server paths:

- DEV123/SIGINS/0/RANGE
- DEV123/SIGINS/1/RANGE
- ...

Parameters:

[in] Index
index of the parameter

[in] Range
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigInGetRange](#), HF Signal Input

ziRTKSigInGetAC

ZI_STATUS ziRTKSigInGetAC (**unsigned int** Index, **int*** Value)

Get an input's AC mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigInSetAC](#), [HF Signal Input](#)

ziRTKSigInSetAC

ZI_STATUS ziRTKSigInSetAC (unsigned int Index, int Value)

Set an input's AC mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigInGetAC](#), HF Signal Input

ziRTKSigInGetImp50

ZI_STATUS ziRTKSigInGetImp50 (**unsigned int** Index, **int*** Value)

Get an input's 50 Ohm mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigInSetImp50](#), HF Signal Input

ziRTKSigInSetImp50

ZI_STATUS ziRTKSigInSetImp50 (**unsigned int** Index, **int** Value)

Set an input's 50 Ohm mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigInGetImp50](#), HF Signal Input

ziRTKSigInGetDiff

ZI_STATUS ziRTKSigInGetDiff (unsigned int Index, int* Value)

Get an input's differential mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigInSetDiff](#), [HF Signal Input](#)

ziRTKSigInSetDiff

ZI_STATUS ziRTKSigInSetDiff (**unsigned int** Index, **int** Value)

Set an input's differential mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigInGetDiff](#), [HF Signal Input](#)

Oscillators

The functions in this section allow you to set the frequencies of the oscillators.

Functions

- `unsigned int ziRTK0scGetCount()`
get the count of oscillators.
- `ZI_STATUS ziRTK0scGetFreq(unsigned int Index, float* Freq)`
Get an oscillator's frequency.
- `ZI_STATUS ziRTK0scSetFreq(unsigned int Index, float Freq)`
Set an oscillator's frequency.

Function Documentation

ziRTKOscGetCount

`unsigned int ziRTKOscGetCount ()`

get the count of oscillators.

Returns:

The count of oscillators

See Also:

[Oscillators](#)

ziRTKOscGetFreq**ZI_STATUS ziRTKOscGetFreq (unsigned int Index, float* Freq)**

Get an oscillator's frequency.

Corresponding server paths:

- DEV123/OSCS/0/FREQ
- DEV123/OSCS/1/FREQ
- ...

Parameters:

[in] Index
index of the parameter

[out] Freq
Pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Freq is NULL

See Also:

[ziRTKOscSetFreq](#), [Oscillators](#)

ziRTKOscSetFreq

ZI_STATUS ziRTKOscSetFreq (**unsigned int** Index, **float** Freq)

Set an oscillator's frequency.

Corresponding server paths:

- DEV123/OSCS/0/FREQ
- DEV123/OSCS/1/FREQ
- ...

Parameters:

[in] Index
index of the parameter

[in] Freq
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKOscGetFreq](#), Oscillators

Demodulator

The functions in this section allow you to configure the demodulators.

Data Structures

- `struct DemodSample`
type for holding a sample of a demodulator.

Functions

- `unsigned int ziRTKDemodGetCount ()`
get the count of demodulators.
- `ZI_STATUS ziRTKDemodGetADCSelect (unsigned int Index, int* ADCSelect)`
Get a demodulator's adcselect.
- `ZI_STATUS ziRTKDemodSetADCSelect (unsigned int Index, int ADCSelect)`
Set a demodulator's ADC select.
- `ZI_STATUS ziRTKDemodGetOscSelect (unsigned int Index, int* OscSelect)`
Get a demodulator's oscillator select.
- `ZI_STATUS ziRTKDemodSetOscSelect (unsigned int Index, int OscSelect)`
Set a demodulator's oscillator select.
- `ZI_STATUS ziRTKDemodGetOrder (unsigned int Index, int* Order)`
Get a demodulator's filter order.
- `ZI_STATUS ziRTKDemodSetOrder (unsigned int Index, int Order)`
Set a demodulator's filter order.
- `ZI_STATUS ziRTKDemodGetHarmonic (unsigned int Index, int* Harmonic)`
Get a demodulator's harmonic.
- `ZI_STATUS ziRTKDemodSetHarmonic (unsigned int Index, int Harmonic)`
Set a demodulator's harmonic.
- `ZI_STATUS ziRTKDemodGetRate (unsigned int Index, float* Rate)`
Get a demodulator's rate.
- `ZI_STATUS ziRTKDemodSetRate (unsigned int Index, float Rate)`
Set a demodulator's rate.
- `ZI_STATUS ziRTKDemodGetTrigger (unsigned int Index, unsigned int* Trigger)`

- Get a demodulator's trigger- and gating-settings.
- `ZI_STATUS ziRTKDemodSetTrigger (unsigned int Index, unsigned int Trigger)`
Set a demodulator's trigger- and gating-settings.
- `ZI_STATUS ziRTKDemodGetPhaseShift (unsigned int Index, float* PhaseShift)`
Get a demodulator's phaseshift.
- `ZI_STATUS ziRTKDemodSetPhaseShift (unsigned int Index, float PhaseShift)`
Set a demodulator's phaseshift.
- `ZI_STATUS ziRTKDemodGetTimeConstant (unsigned int Index, float* TimeConstant)`
Get a demodulator's timeconstant.
- `ZI_STATUS ziRTKDemodSetTimeConstant (unsigned int Index, float TimeConstant)`
Set a demodulator's timeconstant.
- `ZI_STATUS ziRTKDemodGetSinc (unsigned int Index, unsigned int* Value)`
Get a demodulator's sinc.
- `ZI_STATUS ziRTKDemodSetSinc (unsigned int Index, unsigned int Value)`
Set a demodulator's sinc.
- `ZI_STATUS ziRTKDemodGetSample (unsigned int Index, DemodSample* Sample)`
Retrieve a demodulator's sample.
- `ZI_STATUS ziRTKDemodSampleGetTimeStamp (DemodSample* Sample, float* Seconds)`
Returns the timestamp of a `DemodSample` in Seconds.
- `ZI_STATUS ziRTKDemodSampleGetTimeStamp32 (DemodSample* Sample, unsigned int* TS)`
Returns the timestamp of a `DemodSample` as 32bit unsigned int.
- `ZI_STATUS ziRTKDemodSampleGetTimeStamp64 (DemodSample* Sample, unsigned long long* TS)`
Returns the timestamp of a `DemodSample` as 64 bit unsigned long long.
- `ZI_STATUS ziRTKDemodSampleGetX (DemodSample* Sample, float* X)`
Returns the X-Value of a `DemodSample` as float.
- `ZI_STATUS ziRTKDemodSampleGetX32 (DemodSample* Sample, unsigned int* X)`
Returns the X-Value of a `DemodSample` as unsigned int.

- **ZI_STATUS** ziRTKDemodSampleGetX64 (**DemodSample*** Sample, unsigned long long* X)
Returns the X-Value of a **DemodSample** as unsigned long long.
- **ZI_STATUS** ziRTKDemodSampleGetCompX (**DemodSample*** Sample, float* X)
Returns the compensated X-Value of a **DemodSample**.
- **ZI_STATUS** ziRTKDemodSampleGetY (**DemodSample*** Sample, float* Y)
Returns the Y-Value of a **DemodSample** as float.
- **ZI_STATUS** ziRTKDemodSampleGetY32 (**DemodSample*** Sample, unsigned int* Y)
Returns the Y-Value of a **DemodSample** as unsigned int.
- **ZI_STATUS** ziRTKDemodSampleGetY64 (**DemodSample*** Sample, unsigned long long* Y)
Returns the Y-Value of a **DemodSample** as unsigned long long.
- **ZI_STATUS** ziRTKDemodSampleGetCompY (**DemodSample*** Sample, float* Y)
Returns the compensated Y-Value of a **DemodSample**.
- **ZI_STATUS** ziRTKDemodSampleGetCompXY (**DemodSample*** Sample, float* X, float* Y)
Returns the compensated X-Value and the compensated Y-Value of a **DemodSample**.
- **ZI_STATUS** ziRTKDemodSampleGetR (**DemodSample*** Sample, float* R)
Returns the Radius-Value of a **DemodSample**.
- **ZI_STATUS** ziRTKDemodSampleGetCompR (**DemodSample*** Sample, float* R)
Returns the compensated Radius-Value of a **DemodSample**.
- **ZI_STATUS** ziRTKDemodSampleGetTheta (**DemodSample*** Sample, float* Theta)
Returns the Theta-Value of a **DemodSample**.

Data Structure Documentation

struct DemodSample

type for holding a sample of a demodulator.

```
#include "ziRTK.h"

typedef struct DemodSample {
    TS_t TS;
    Val_t X;
    Val_t Y;
    unsigned int Reserved[3];
} DemodSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- Val_t X
X-Value of the sample where index 0 is the lower word and index 1 is the upper word.
- Val_t Y
Y-Value of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Reserved
reserved for internal usage

Detailed Description

See Also:

[ziRTKDemodGetSample](#)

Function Documentation

ziRTKDemodGetCount

`unsigned int ziRTKDemodGetCount ()`

get the count of demodulators.

Returns:

The count of demodulators

See Also:

[Demodulator](#)

ziRTKDemodGetADCSelect

ZI_STATUS ziRTKDemodGetADCSelect (unsigned int Index, int* ADCSelect)

Get a demodulator's adcselect.

Corresponding server paths:

- DEV123/DEMOS/0/ADCSELECT
- DEV123/DEMOS/1/ADCSELECT
- ...

Note:

ADCSelect is only available with installed Multi-frequency Option.

Parameters:

[in] Index
index of the Parameter

[out] ADCSelect
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when ADCSelect is NULL

See Also:

[ziRTKDemodSetADCSelect](#), [Demodulator](#)

ziRTKDemodSetADCSelect

ZI_STATUS ziRTKDemodSetADCSelect (**unsigned int** Index, **int** ADCSelect)

Set a demodulator's ADC select.

Corresponding server paths:

- DEV123/DEM0DS/0/ADCSELECT
- DEV123/DEM0DS/1/ADCSELECT
- ...

Note:

ADCSelect is only available with installed Multi-frequency Option.

Parameters:

[in] Index
index of the Parameter

[in] ADCSelect
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetADCSelect](#), [Demodulator](#)

ziRTKDemodGetOscSelect

ZI_STATUS ziRTKDemodGetOscSelect (**unsigned int** Index, **int*** OscSelect)

Get a demodulator's oscillator select.

Corresponding server paths:

- DEV123/DEMODS/0/OSCSELECT
- DEV123/DEMODS/1/OSCSELECT
- ...

Note:

OscSelect is only available with installed Multi-frequency Option.

Parameters:

[in] Index
index of the Parameter

[out] OscSelect
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when ADCSelect is NULL

See Also:

[ziRTKDemodSetOscSelect](#), [Demodulator](#)

ziRTKDemodSetOscSelect

ZI_STATUS ziRTKDemodSetOscSelect (**unsigned int** Index, **int** OscSelect)

Set a demodulator's oscillator select.

Corresponding server paths:

- DEV123/DEMODS/0/OSCSELECT
- DEV123/DEMODS/1/OSCSELECT
- ...

Note:

OscSelect is only available when the Multi-frequency Option is installed.

Parameters:

[in] Index
index of the Parameter

[in] OscSelect
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetOscSelect](#), Demodulator

ziRTKDemodGetOrder

ZI_STATUS ziRTKDemodGetOrder (**unsigned int** Index, **int*** Order)

Get a demodulator's filter order.

Corresponding server paths:

- DEV123/DEMODS/0/ORDER
- DEV123/DEMODS/1/ORDER
- ...

Parameters:

[in] Index
index of the Parameter

[out] Order
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Order is NULL

See Also:

[ziRTKDemodSetOrder](#), Demodulator

ziRTKDemodSetOrder

ZI_STATUS ziRTKDemodSetOrder (**unsigned int** Index, **int** Order)

Set a demodulator's filter order.

Corresponding server paths:

- DEV123/DEMODS/0/ORDER
- DEV123/DEMODS/1/ORDER
- ...

Parameters:

[in] Index
index of the Parameter

[in] Order
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetOrder](#), Demodulator

ziRTKDemodGetHarmonic

ZI_STATUS ziRTKDemodGetHarmonic (**unsigned int** Index, **int*** Harmonic)

Get a demodulator's harmonic.

Corresponding server paths:

- DEV123/DEMOS/0/ORDER
- DEV123/DEMOS/1/ORDER
- ...

Parameters:

[in] Index
index of the Parameter

[out] Harmonic
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Harmonic is NULL

See Also:

[ziRTKDemodSetHarmonic](#), Demodulator

ziRTKDemodSetHarmonic

ZI_STATUS ziRTKDemodSetHarmonic (**unsigned int** Index, **int** Harmonic)

Set a demodulator's harmonic.

Corresponding server paths:

- DEV123/DEMOS/0/ORDER
- DEV123/DEMOS/1/ORDER
- ...

Parameters:

[in] Index
index of the Parameter

[in] Harmonic
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetHarmonic](#), Demodulator

ziRTKDemodGetRate**ZI_STATUS ziRTKDemodGetRate (unsigned int Index, float* Rate)**

Get a demodulator's rate.

Corresponding server paths:

- DEV123/DEMODS/0/RATE
- DEV123/DEMODS/1/RATE
- ...

Parameters:

[in] Index
index of the Parameter

[out] Rate
pointer to a float to write the value in (in Hertz)

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Rate is NULL

See Also:

[ziRTKDemodSetRate](#), [Demodulator](#)

ziRTKDemodSetRate

ZI_STATUS ziRTKDemodSetRate (**unsigned int** Index, **float** Rate)

Set a demodulator's rate.

Corresponding server paths:

- DEV123/DEMODS/0/RATE
- DEV123/DEMODS/1/RATE
- ...

Parameters:

[in] Index
index of the parameter

[in] Rate
float providing the value (in Hertz)

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetRate](#), Demodulator

ziRTKDemodGetTrigger**ZI_STATUS ziRTKDemodGetTrigger (unsigned int Index, unsigned int* Trigger)**

Get a demodulator's trigger- and gating-settings.

Corresponding server paths:

- DEV123/DEMOS/0/trigger
- DEV123/DEMOS/1/trigger
- ...

Parameters:

[in] Index
index of the Parameter

[out] Trigger
pointer to an unsigned int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Trigger is NULL

See Also:

[ziRTKDemodSetTrigger](#), [Demodulator](#)

ziRTKDemodSetTrigger

ZI_STATUS ziRTKDemodSetTrigger (**unsigned int** Index, **unsigned int** Trigger)

Set a demodulator's trigger- and gating-settings.

Corresponding server paths:

- DEV123/DEMODS/0/TRIGGER
- DEV123/DEMODS/1/TRIGGER
- ...

Parameters:

[in] Index
index of the parameter

[in] Trigger
unsigned int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetTrigger](#), Demodulator

ziRTKDemodGetPhaseShift

ZI_STATUS ziRTKDemodGetPhaseShift (**unsigned int** Index, **float*** PhaseShift)

Get a demodulator's phaseshift.

Corresponding server paths:

- DEV123/DEMODS/0/PHASESHIFT
- DEV123/DEMODS/1/PHASESHIFT
- ...

Parameters:

[in] Index
index of the Parameter

[out] PhaseShift
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when PhaseShift is NULL

See Also:

[ziRTKDemodSetPhaseShift](#), Demodulator

ziRTKDemodSetPhaseShift

ZI_STATUS ziRTKDemodSetPhaseShift (**unsigned int** Index, **float** PhaseShift)

Set a demodulator's phaseshift.

Corresponding server paths:

- DEV123/DEMOS/0/PHASESHIFT
- DEV123/DEMOS/1/PHASESHIFT
- ...

Parameters:

[in] Index
index of the parameter

[in] PhaseShift
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetPhaseShift](#), Demodulator

ziRTKDemodGetTimeConstant**ZI_STATUS ziRTKDemodGetTimeConstant (unsigned int Index, float* TimeConstant)**

Get a demodulator's timeconstant.

Corresponding server paths:

- DEV123/DEMODS/0/TIMECONSTANT
- DEV123/DEMODS/1/TIMECONSTANT
- ...

Parameters:

[in] Index
index of the Parameter

[out] TimeConstant
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when TimeConstant is NULL

See Also:

[ziRTKDemodSetTimeConstant](#), [Demodulator](#)

ziRTKDemodSetTimeConstant

ZI_STATUS ziRTKDemodSetTimeConstant (**unsigned int** Index, **float** TimeConstant)

Set a demodulator's timeconstant.

Corresponding server paths:

- DEV123/DEMODS/0/TIMECONSTANT
- DEV123/DEMODS/1/TIMECONSTANT
- ...

Parameters:

[in] Index
index of the parameter

[in] TimeConstant
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetTimeConstant](#), Demodulator

ziRTKDemodGetSinc

ZI_STATUS ziRTKDemodGetSinc (**unsigned int** Index, **unsigned int*** Value)

Get a demodulator's sinc.

Corresponding server paths:

- DEV123/DEMODY/0/SINC
- DEV123/DEMODY/1/SINC
- ...

Parameters:

[in] Index
index of the Parameter

[out] Value
pointer to an unsigned int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when TimeConstant is NULL

See Also:

[ziRTKDemodSetSinc](#), [Demodulator](#)

ziRTKDemodSetSinc

ZI_STATUS ziRTKDemodSetSinc (**unsigned int** Index, **unsigned int** Value)

Set a demodulator's sinc.

Corresponding server paths:

- DEV123/DEMODS/0/SINC
- DEV123/DEMODS/1/SINC
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
unsigned int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetSinc](#), Demodulator

ziRTKDemodGetSample

ZI_STATUS ziRTKDemodGetSample (**unsigned int** Index, **DemodSample*** Sample)

Retrieve a demodulator's sample.

Corresponding server paths:

- DEV123/DEMOS/0/SAMPLE
- DEV123/DEMOS/1/SAMPLE
- ...

Parameters:

[in] Index
index of the parameter

[out] Sample
DemodSample to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when the Sample is NULL

See Also:

[Demodulator](#)

ziRTKDemodSampleGetTimeStamp

ZI_STATUS ziRTKDemodSampleGetTimeStamp (**DemodSample*** Sample, **float*** Seconds)

Returns the timestamp of a [DemodSample](#) in Seconds.

Parameters:

[in] Sample
the sample to extract the value from

[out] Seconds
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Seconds is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetTimeStamp32](#), [ziRTKDemodSampleGetTimeStamp64](#), [Demodulator](#)

ziRTKDemodSampleGetTimeStamp32

ZI_STATUS ziRTKDemodSampleGetTimeStamp32 (**DemodSample*** Sample, **unsigned int*** TS)

Returns the timestamp of a [DemodSample](#) as 32bit unsigned int.

Note:

you may also read TS.TS32[ZIRTK_LSB_INDEX] of a [DemodSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetTimeStamp](#), [ziRTKDemodSampleGetTimeStamp64](#), [Demodulator](#)

ziRTKDemodSampleGetTimeStamp64

ZI_STATUS ziRTKDemodSampleGetTimeStamp64 (**DemodSample*** Sample, **unsigned long long*** TS)

Returns the timestamp of a [DemodSample](#) as 64 bit unsigned long long.

Note:

you may also read TS.TS64 of a [DemodSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetTimeStamp](#), [ziRTKDemodSampleGetTimeStamp32](#), [Demodulator](#)

ziRTKDemodSampleGetX

ZI_STATUS ziRTKDemodSampleGetX (**DemodSample*** Sample, **float*** X)

Returns the X-Value of a DemodSample as float.

Parameters:

[in] Sample
the sample to extract the value from

[out] X
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#), [ziRTKDemodSampleGetCompX](#),
[ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetX32

ZI_STATUS ziRTKDemodSampleGetX32 (**DemodSample*** Sample, **unsigned int*** X)

Returns the X-Value of a **DemodSample** as unsigned int.

Note:

you may also read X.Val32[ZIRTK_MSB_INDEX] of a **DemodSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] X

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX64](#), [ziRTKDemodSampleGetCompX](#),
[ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetX64

ZI_STATUS ziRTKDemodSampleGetX64 (**DemodSample*** Sample, **unsigned long long*** X)

Returns the X-Value of a **DemodSample** as **unsigned long long**.

Note:

you may also read X.Val64 of a **DemodSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] X

pointer to an **unsigned long long** in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetCompX](#),
[ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetCompX

ZI_STATUS ziRTKDemodSampleGetCompX (**DemodSample*** Sample, **float*** X)

Returns the compensated X-Value of a **DemodSample**.

Parameters:

[in] Sample
the sample to extract the value from

[out] X
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetY

ZI_STATUS ziRTKDemodSampleGetY (**DemodSample*** Sample, **float*** Y)

Returns the Y-Value of a DemodSample as float.

Parameters:

[in] Sample

the sample to extract the value from

[out] Y

pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Y is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetCompX](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetY32

ZI_STATUS ziRTKDemodSampleGetY32 (**DemodSample*** Sample, **unsigned int*** Y)

Returns the Y-Value of a **DemodSample** as unsigned int.

Note:

you may also read Y.Val32[ZIERTK_MSB_INDEX] of a **DemodSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] Y

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Y is NULL or Sample is NULL

See Also:

ziRTKDemodSampleGetX, ziRTKDemodSampleGetX32, ziRTKDemodSampleGetX64,
ziRTKDemodSampleGetCompX, ziRTKDemodSampleGetY, ziRTKDemodSampleGetY64,
ziRTKDemodSampleGetCompY, ziRTKDemodSampleGetCompXY, Demodulator

ziRTKDemodSampleGetY64

ZI_STATUS ziRTKDemodSampleGetY64 (**DemodSample*** Sample, **unsigned long long*** Y)

Returns the Y-Value of a **DemodSample** as **unsigned long long**.

Note:

you may also read Y.Val64 of a **DemodSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] Y

pointer to an **unsigned long long** in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Y is NULL or Sample is NULL

See Also:

ziRTKDemodSampleGetX, ziRTKDemodSampleGetX32, ziRTKDemodSampleGetX64,
ziRTKDemodSampleGetCompX, ziRTKDemodSampleGetY, ziRTKDemodSampleGetY32,
ziRTKDemodSampleGetCompY, ziRTKDemodSampleGetCompXY, Demodulator

ziRTKDemodSampleGetCompY**ZI_STATUS ziRTKDemodSampleGetCompY (DemodSample* Sample, float* Y)**

Returns the compensated Y-Value of a DemodSample.

Parameters:

[in] Sample

the sample to extract the value from

[out] Y

pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Y is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetCompX](#), [ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#),
[ziRTKDemodSampleGetY64](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetCompXY

ZI_STATUS **ziRTKDemodSampleGetCompXY (DemodSample* Sample, float* X, float* Y)**

Returns the compensated X-Value and the compensated Y-Value of a DemodSample.

Parameters:

[in] Sample
the sample to extract the value from

[out] X
pointer to a float in which the X-value will be returned

[out] Y
pointer to a float in which the Y-value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Y is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetCompX](#), [ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#),
[ziRTKDemodSampleGetY64](#), [ziRTKDemodSampleGetCompY](#), Demodulator

ziRTKDemodSampleGetR

ZI_STATUS ziRTKDemodSampleGetR (**DemodSample*** Sample, **float*** R)

Returns the Radius-Value of a **DemodSample**.

Parameters:

[in] Sample
the sample to extract the value from

[out] R
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when R is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetCompR](#), [ziRTKDemodSampleGetTheta](#), [Demodulator](#)

ziRTKDemodSampleGetCompR

ZI_STATUS ziRTKDemodSampleGetCompR (**DemodSample*** Sample, **float*** R)

Returns the compensated Radius-Value of a DemodSample.

Parameters:

[in] Sample
the sample to extract the value from

[out] R
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when R is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetR](#), [ziRTKDemodSampleGetTheta](#), [Demodulator](#)

ziRTKDemodSampleGetTheta

ZI_STATUS ziRTKDemodSampleGetTheta (**DemodSample*** Sample, **float*** Theta)

Returns the Theta-Value of a [DemodSample](#).

Parameters:

[in] Sample
the sample to extract the value from

[out] Theta
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Theta is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetR](#), [ziRTKDemodSampleGetCompR](#), [Demodulator](#)

HF Signal Output

The functions in this section allow you to set the range and the amplitude of the HF signal output.

Functions

- `unsigned int ziRTKSigOutGetCount ()`
get the count of HF Signal outputs.
- `ZI_STATUS ziRTKSigOutGetRange (unsigned int Index, float* Range)`
Get an output's range (V).
- `ZI_STATUS ziRTKSigOutSetRange (unsigned int Index, float Range)`
Set an output's range (V).
- `unsigned int ziRTKSigOutGetChannelCount ()`
get the count of HF Signal output Mixer Channels.
- `ZI_STATUS ziRTKSigOutGetEnable (unsigned int Index, unsigned int Channel, int* Value)`
Get an output's mixer enable.
- `ZI_STATUS ziRTKSigOutSetEnable (unsigned int Index, unsigned int Channel, int Value)`
Set an output's mixer enable.
- `ZI_STATUS ziRTKSigOutGetAmplitude (unsigned int Index, unsigned int Channel, float* Value)`
Get an output's mixer amplitude.
- `ZI_STATUS ziRTKSigOutSetAmplitude (unsigned int Index, unsigned int Channel, float Value)`
Set an output's mixer amplitude.
- `ZI_STATUS ziRTKSigOutGetOn (unsigned int Index, int* Value)`
Get an output's on-switch.
- `ZI_STATUS ziRTKSigOutSetOn (unsigned int Index, int Value)`
Set an output's on-switch.
- `ZI_STATUS ziRTKSigOutGetAdd (unsigned int Index, int* Value)`
Get an output's add mode.
- `ZI_STATUS ziRTKSigOutSetAdd (unsigned int Index, int Value)`
Set an output's add mode.

Function Documentation

ziRTKSigOutGetCount

`unsigned int ziRTKSigOutGetCount ()`

get the count of HF Signal outputs.

Returns:

The count of the signal outputs.

See Also:

[HF Signal Output](#)

ziRTKSigOutGetRange

ZI_STATUS ziRTKSigOutGetRange (**unsigned int** Index, **float*** Range)

Get an output's range (V).

returns an output's range.

Corresponding server paths:

- DEV123/SIGOUTS/0/RANGE
- DEV123/SIGOUTS/1/RANGE
- ...

Parameters:

[in] Index
index of the parameter

[out] Range
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Range is NULL

See Also:

[ziRTKSigOutSetRange](#), HF Signal Output

ziRTKSigOutSetRange

ZI_STATUS **ziRTKSigOutSetRange** (**unsigned int** **Index**, **float** **Range**)

Set an output's range (V).

sets an output's range.

Corresponding server paths:

- DEV123/SIGOUTS/0/RANGE
- DEV123/SIGOUTS/1/RANGE
- ...

Parameters:

[in] **Index**
index of the parameter

[in] **Range**
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKSigOutGetRange](#), HF Signal Output

ziRTKSigOutGetChannelCount

unsigned int ziRTKSigOutGetChannelCount ()

get the count of HF Signal output Mixer Channels.

Returns:

The count of mixer channels

See Also:

[HF Signal Output](#)

ziRTKSigOutGetEnable

ZI_STATUS **ziRTKSigOutGetEnable** (**unsigned int** **Index**, **unsigned int** **Channel**, **int*** **Value**)

Get an output's mixer enable.

returns an output's mixer enable.

Corresponding server paths:

- DEV123/SIGOUTS/0/ENABLES/0
- DEV123/SIGOUTS/0/ENABLES/1
- ...
- DEV123/SIGOUTS/1/ENABLES/0
- DEV123/SIGOUTS/1/ENABLES/1
- ...

Parameters:

[in] **Index**
index of the signal output

[in] **Channel**
index of the mixer-channel

[out] **Value**
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Value is NULL
- ZI_OUTOFRANGE when Index is out of range or channel is out of range

See Also:

[ziRTKSigOutSetEnable](#), HF Signal Output

ziRTKSigOutSetEnable

ZI_STATUS **ziRTKSigOutSetEnable** (**unsigned int Index, unsigned int Channel, int Value**)

Set an output's mixer enable.

sets an output's mixer enable.

Corresponding server paths:

- DEV123/SIGOUTS/0/ENABLES/0
- DEV123/SIGOUTS/0/ENABLES/1
- ...
- DEV123/SIGOUTS/1/ENABLES/0
- DEV123/SIGOUTS/1/ENABLES/1
- ...

Parameters:

[in] Index
index of the signal output

[in] Channel
index of the mixer-channel

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range or channel is out of range

See Also:

[ziRTKSigOutGetEnable](#), [HF Signal Output](#)

ziRTKSigOutGetAmplitude

ZI_STATUS **ziRTKSigOutGetAmplitude** (**unsigned int** **Index**, **unsigned int** **Channel**, **float*** **Value**)

Get an output's mixer amplitude.

returns an output's mixer amplitude.

Corresponding server paths:

- DEV123/SIGOUTS/0/AMPLITUDES/0
- DEV123/SIGOUTS/0/AMPLITUDES/1
- ...
- DEV123/SIGOUTS/1/AMPLITUDES/0
- DEV123/SIGOUTS/1/AMPLITUDES/1
- ...

Parameters:

[in] **Index**
index of the signal output

[in] **Channel**
index of the mixer-channel

[out] **Value**
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Value is NULL
- ZI_OUTOFRANGE when Index is out of range or channel is out of range

See Also:

[ziRTKSigOutSetAmplitude](#), [HF Signal Output](#)

ziRTKSigOutSetAmplitude**ZI_STATUS ziRTKSigOutSetAmplitude (unsigned int Index, unsigned int Channel, float Value)**

Set an output's mixer amplitude.

sets an output's mixer amplitude.

Corresponding server paths:

- DEV123/SIGOUTS/0/AMPLITUDES/0
- DEV123/SIGOUTS/0/AMPLITUDES/1
- ...
- DEV123/SIGOUTS/1/AMPLITUDES/0
- DEV123/SIGOUTS/1/AMPLITUDES/1
- ...

Parameters:

[in] Index
index of the signal output

[in] Channel
index of the mixer-channel

[in] Value
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range or channel is out of range

See Also:

[ziRTKSigOutGetAmplitude](#), HF Signal Output

ziRTKSigOutGetOn

ZI_STATUS ziRTKSigOutGetOn (unsigned int Index, int* Value)

Get an output's on-switch.

Corresponding server paths:

- DEV123/SIGOUTS/0/ON
- DEV123/SIGOUTS/1/ON
- ...

Parameters:

[in] Index

index of the parameter

[out] Value

Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigOutSetOn](#), [HF Signal Output](#)

ziRTKSigOutSetOn

ZI_STATUS ziRTKSigOutSetOn (unsigned int Index, int Value)

Set an output's on-switch.

Corresponding server paths:

- DEV123/SIGOUTS/0/ON
- DEV123/SIGOUTS/1/ON
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigOutGetOn](#), [HF Signal Output](#)

ziRTKSigOutGetAdd

ZI_STATUS ziRTKSigOutGetAdd (**unsigned int** Index, **int*** Value)

Get an output's add mode.

Corresponding server paths:

- DEV123/SIGOUTS/0/ON
- DEV123/SIGOUTS/1/ON
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigOutSetAdd](#), HF Signal Output

ziRTKSigOutSetAdd

ZI_STATUS ziRTKSigOutSetAdd (**unsigned int** Index, **int** Value)

Set an output's add mode.

Corresponding server paths:

- DEV123/SIGOUTS/0/ON
- DEV123/SIGOUTS/1/ON
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigOutGetAdd](#), [HF Signal Output](#)

Auxiliary Analog Outputs

Functions

- `unsigned int ziRTKAuxOutGetCount ()`
get the count of auxiliary analog outputs.
- `ZI_STATUS ziRTKAuxOutGetValue (unsigned int Index, float* Value)`
Get a auxiliary outputs value as float (V).
- `ZI_STATUS ziRTKAuxOutGetOutputSelect (unsigned int Index, int* OutputSelect)`
Get an auxiliary outputs outputselect.
- `ZI_STATUS ziRTKAuxOutSetOutputSelect (unsigned int Index, int OutputSelect)`
Set an auxiliary output's outputselect.
- `ZI_STATUS ziRTKAuxOutGetDemodSelect (unsigned int Index, int* DemodSelect)`
Get an auxiliary output's demodselect.
- `ZI_STATUS ziRTKAuxOutSetDemodSelect (unsigned int Index, int DemodSelect)`
Set an auxiliary output's demodselect.
- `ZI_STATUS ziRTKAuxOutGetOffset (unsigned int Index, float* Offset)`
Get an auxiliary output's offset.
- `ZI_STATUS ziRTKAuxOutSetOffset (unsigned int Index, float Offset)`
Set an auxiliary output's offset.
- `ZI_STATUS ziRTKAuxOutSetOffsetNoUpdate (unsigned int Index, float Offset)`
Set an auxiliary output's offset without updating ziServer.
- `ZI_STATUS ziRTKAuxOutSetOffsetInt (unsigned int Index, int Offset)`
Set an auxiliary output's offset as integer.
- `ZI_STATUS ziRTKAuxOutSetOffsetIntNoUpdate (unsigned int Index, int Offset)`
Set an auxiliary output's offset as integer without updating ziServer.
- `ZI_STATUS ziRTKAuxOutGetScale (unsigned int Index, float* Scale)`
Get an auxiliary output's scale.
- `ZI_STATUS ziRTKAuxOutSetScale (unsigned int Index, float Scale)`
Set an auxiliary output's scale.

Detailed Description

The functions in this section allow you to set voltages on the auxiliary analog outputs.

Function Documentation

ziRTKAuxOutGetCount

`unsigned int ziRTKAuxOutGetCount ()`

get the count of auxiliary analog outputs.

Returns:

The count of auxiliary outputs

See Also:

[Auxiliary Analog Outputs](#)

ziRTKAuxOutGetValue

ZI_STATUS ziRTKAuxOutGetValue (unsigned int Index, float* Value)

Get a auxiliary outputs value as float (V).

Corresponding server paths:

- DEV123/AUXOUTS/0/VALUE
- DEV123/AUXOUTS/1/VALUE
- ...

Parameters:

[in] Index
index of the Parameter

[out] Value
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[Auxiliary Analog Outputs](#)

ziRTKAuxOutGetOutputSelect

ZI_STATUS ziRTKAuxOutGetOutputSelect (**unsigned int** Index, **int*** OutputSelect)

Get an auxiliary outputs outputselect.

Corresponding server paths:

- DEV123/AUXOUTS/0/OUTPUTSELECT
- DEV123/AUXOUTS/1/OUTPUTSELECT
- ...

Parameters:

[in] Index
index of the Parameter

[out] OutputSelect
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when OutputSelect is NULL

See Also:

[ziRTKAuxOutSetOutputSelect](#), Auxiliary Analog Outputs

ziRTKAuxOutSetOutputSelect

ZI_STATUS ziRTKAuxOutSetOutputSelect (**unsigned int** Index, **int** OutputSelect)

Set an auxiliary output's outputselect.

Corresponding server paths:

- DEV123/AUXOUTS/0/OUTPUTSELECT
- DEV123/AUXOUTS/1/OUTPUTSELECT
- ...

Parameters:

[in] Index
index of the Parameter

[out] OutputSelect
an int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutGetOutputSelect](#), Auxiliary Analog Outputs

ziRTKAuxOutGetDemodSelect

ZI_STATUS ziRTKAuxOutGetDemodSelect (**unsigned int** Index, **int*** DemodSelect)

Get an auxiliary output's demodselect.

Corresponding server paths:

- DEV123/AUXOUTS/0/DEMODOSELECT
- DEV123/AUXOUTS/1/DEMODOSELECT
- ...

Parameters:

[in] Index
index of the Parameter

[out] DemodSelect
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when DemodSelect is NULL

See Also:

[ziRTKAuxOutSetDemodSelect](#), [Auxiliary Analog Outputs](#)

ziRTKAuxOutSetDemodSelect

ZI_STATUS ziRTKAuxOutSetDemodSelect (**unsigned int** Index, **int** DemodSelect)

Set an auxiliary output's demodselect.

Corresponding server paths:

- DEV123/AUXOUTS/0/DEMODOSELECT
- DEV123/AUXOUTS/1/DEMODOSELECT
- ...

Parameters:

[in] Index
index of the Parameter

[out] DemodSelect
an int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutGetDemodSelect](#), Auxiliary Analog Outputs

ziRTKAuxOutGetOffset

ZI_STATUS ziRTKAuxOutGetOffset (**unsigned int** Index, **float*** Offset)

Get an auxiliary output's offset.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] Index
index of the Parameter

[out] Offset
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Offset is NULL

See Also:

[ziRTKAuxOutSetOffset](#), [ziRTKAuxOutSetOffsetNoUpdate](#), [ziRTKAuxOutSetOffsetInt](#),
[ziRTKAuxOutSetOffsetIntNoUpdate](#), [Auxiliary Analog Outputs](#)

ziRTKAuxOutSetOffset

ZI_STATUS ziRTKAuxOutSetOffset (**unsigned int** Index, **float** Offset)

Set an auxiliary output's offset.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] Index
index of the Parameter

[out] Offset
a float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutSetOffsetNoUpdate](#), [ziRTKAuxOutSetOffsetInt](#), [ziRTKAuxOutSetOffsetIntNoUpdate](#),
[ziRTKAuxOutGetOffset](#), Auxiliary Analog Outputs

ziRTKAuxOutSetOffsetNoUpdate

ZI_STATUS **ziRTKAuxOutSetOffsetNoUpdate (unsigned int Index, float Offset)**

Set an auxiliary output's offset without updating ziServer.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] Index
index of the Parameter

[out] Offset
a float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutSetOffset](#), [ziRTKAuxOutSetOffsetInt](#), [ziRTKAuxOutSetOffsetIntNoUpdate](#),
[ziRTKAuxOutGetOffset](#), Auxiliary Analog Outputs

ziRTKAuxOutSetOffsetInt

ZI_STATUS **ziRTKAuxOutSetOffsetInt** (**unsigned int** **Index**, **int** **Offset**)

Set an auxiliary output's offset as integer.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] **Index**
index of the Parameter

[out] **Offset**
an integer providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutSetOffsetInt32](#), [ziRTKAuxOutSetOffset](#), [ziRTKAuxOutSetOffsetNoUpdate](#),
[ziRTKAuxOutGetOffset](#), [Auxiliary Analog Outputs](#)

ziRTKAuxOutSetOffsetIntNoUpdate

ZI_STATUS ziRTKAuxOutSetOffsetIntNoUpdate (**unsigned int** Index, **int** Offset)

Set an auxiliary output's offset as integer without updating ziServer.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] Index
index of the Parameter

[out] Offset
an integer providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

ziRTKAuxOutSetOffsetInt, ziRTKAuxOutSetOffset, ziRTKAuxOutSetOffsetNoUpdate,
ziRTKAuxOutGetOffset, Auxiliary Analog Outputs

ziRTKAuxOutGetScale

ZI_STATUS ziRTKAuxOutGetScale (**unsigned int** Index, **float*** Scale)

Get an auxiliary output's scale.

Corresponding server paths:

- DEV123/AUXOUTS/0/SCALE
- DEV123/AUXOUTS/1/SCALE
- ...

Parameters:

[in] Index
index of the Parameter

[out] Scale
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Scale is NULL

See Also:

[ziRTKAuxOutSetScale](#), Auxiliary Analog Outputs

ziRTKAuxOutSetScale

ZI_STATUS ziRTKAuxOutSetScale (**unsigned int** Index, **float** Scale)

Set an auxiliary output's scale.

Corresponding server paths:

- DEV123/AUXOUTS/0/SCALE
- DEV123/AUXOUTS/1/SCALE
- ...

Parameters:

[in] Index
index of the Parameter

[out] Scale
a float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutGetScale](#), Auxiliary Analog Outputs

Auxiliary Analog Inputs

The functions in this section read signals from the 2 auxiliary analog inputs.

Data Structures

- `struct AuxInSample`
type for holding a sample of auxiliary input

Functions

- `unsigned int ziRTKAuxInGetCount ()`
get the count of Auxiliary Analog Inputs.
- `ZI_STATUS ziRTKAuxInGetAveraging (unsigned int Index, int* Averaging)`
Get a auxiliary input's averaging/sample rate.
- `ZI_STATUS ziRTKAuxInSetAveraging (unsigned int Index, int Averaging)`
Set an auxiliary output's averaging sample rate.
- `ZI_STATUS ziRTKAuxInGetSample (unsigned int Index, AuxInSample* Sample)`
retrieve a auxiliary input sample
- `ZI_STATUS ziRTKAuxInSampleGetTimeStamp (AuxInSample* Sample, float* Seconds)`
Returns the timestamp of an `AuxInSample` in Seconds.
- `ZI_STATUS ziRTKAuxInSampleGetTimeStamp32 (AuxInSample* Sample, unsigned int* TS)`
Returns the timestamp of an `AuxInSample` as 32bit unsigned int.
- `ZI_STATUS ziRTKAuxInSampleGetTimeStamp64 (AuxInSample* Sample, unsigned long long* TS)`
Returns the timestamp of an `AuxInSample` as 64 bit unsigned long long.
- `ZI_STATUS ziRTKAuxInSampleGetValue (AuxInSample* Sample, unsigned int Channel, float* Value)`
Returns a Value of a `AuxInSample`.
- `ZI_STATUS ziRTKAuxInSampleGetValue16 (AuxInSample* Sample, unsigned int Channel, short* Value)`
Returns a Value of a `AuxInSample` as short.

Data Structure Documentation

struct AuxInSample

type for holding a sample of auxiliary input

```
#include "ziRTK.h"

typedef struct AuxInSample {
    TS_t TS;
    unsigned int Values[1];
} AuxInSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Values
Values of the sample.

Detailed Description

See Also:

[ziRTKAuxInGetSample](#)

Function Documentation

ziRTKAuxInGetCount

`unsigned int ziRTKAuxInGetCount ()`

get the count of Auxiliary Analog Inputs.

Returns:

The count of auxiliary inputs

See Also:

[Auxiliary Analog Inputs](#)

ziRTKAuxInGetAveraging

ZI_STATUS ziRTKAuxInGetAveraging (**unsigned int** Index, **int*** Averaging)

Get a auxiliary input's averaging/sample rate.

Corresponding server path:

- DEV123/AUXINS/0/AVERAGING

See [Aux Analog Inputs](#) for details.

Parameters:

[in] Index
index of the Parameter

[out] Averaging
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Averaging is NULL

See Also:

[ziRTKAuxInSetAveraging](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInSetAveraging

ZI_STATUS ziRTKAuxInSetAveraging (**unsigned int** Index, **int** Averaging)

Set an auxiliary output's averaging sample rate.

Corresponding server path:

- DEV123/AUXINS/0/AVERAGING

See [Aux Analog Inputs](#) for details.

Parameters:

[in] Index
index of the Parameter

[out] Averaging
value to set

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxInGetAveraging](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInGetSample

ZI_STATUS ziRTKAuxInGetSample (**unsigned int** Index, **AuxInSample*** Sample)

retrieve a auxiliary input sample

Corresponding server path:

■ DEV123/AUXINS/0/SAMPLE

Parameters:

[in] Index

index of the Parameter

[in] Sample

AuxInSample to write the value in.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Sample is NULL

See Also:

[Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetTimeStamp

ZI_STATUS **ziRTKAuxInSampleGetTimeStamp** (**AuxInSample*** Sample, **float*** Seconds)

Returns the timestamp of an [AuxInSample](#) in Seconds.

Parameters:

[in] Sample

the sample to extract the value from

[out] Seconds

pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Seconds is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetTimeStamp32](#), [ziRTKAuxInSampleGetTimeStamp64](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetTimeStamp32

ZI_STATUS ziRTKAuxInSampleGetTimeStamp32 (**AuxInSample*** Sample, **unsigned int*** TS)

Returns the timestamp of an [AuxInSample](#) as 32bit unsigned int.

Note:

you may also read TS.TS32[ZIRTK_LSB_INDEX] of a [AuxInSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetTimeStamp](#), [ziRTKAuxInSampleGetTimeStamp64](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetTimeStamp64

ZI_STATUS ziRTKAuxInSampleGetTimeStamp64 ([AuxInSample](#)* Sample, **unsigned long long*** TS)

Returns the timestamp of an [AuxInSample](#) as 64 bit unsigned long long.

Note:

you may also read TS.TS64 of a [AuxInSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned long long in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetTimeStamp](#), [ziRTKAuxInSampleGetTimeStamp32](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetValue

ZI_STATUS ziRTKAuxInSampleGetValue (**AuxInSample*** Sample, **unsigned int** Channel, **float*** Value)

Returns a Value of a [AuxInSample](#).

Parameters:

[in] Sample
the sample to extract the value from

[in] Channel
the channel to retrieve the value from

[out] Value
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when channel is out of range
- ZI_NULLPTR when Value is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetValue16](#), Auxiliary Analog Inputs

ziRTKAuxInSampleGetValue16

ZI_STATUS ziRTKAuxInSampleGetValue16 (**AuxInSample*** Sample, **unsigned int** Channel, **short*** Value)

Returns a Value of a **AuxInSample** as short.

Parameters:

[in] Sample
the sample to extract the value from

[in] Channel
the channel to retrieve the value from

[out] Value
pointer to a short in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when channel is out of range
- ZI_NULLPTR when Value is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetValue](#), [Auxiliary Analog Inputs](#)

Digital Input/Output

The functions in this section allow you to read signals from and write signals to the 32 DIO lines.

Data Structures

- `struct DIOSample`
type for holding a sample of dio

Functions

- `unsigned int ziRTKDIOGetCount()`
get the count of Digital IO.
- `ZI_STATUS ziRTKDIOGetExtClk(unsigned int Index, int* ExtClk)`
Get a DIO's External-Clock setting.
- `ZI_STATUS ziRTKDIOSetExtClk(unsigned int Index, int ExtClk)`
Set a DIO's External-Clock setting.
- `ZI_STATUS ziRTKDIOGetDecimation(unsigned int Index, int* Decimation)`
Get a DIO's Decimation.
- `ZI_STATUS ziRTKDIOSetDecimation(unsigned int Index, int Decimation)`
Set a DIO's Decimation.
- `ZI_STATUS ziRTKDIOGetDrive(unsigned int Index, int* Drive)`
Get a DIO's Drive.
- `ZI_STATUS ziRTKDIOSetDrive(unsigned int Index, int Drive)`
Set a DIO's Drive.
- `ZI_STATUS ziRTKDIOGetOutput(unsigned int Index, unsigned int* Output)`
Get a DIO's Output.
- `ZI_STATUS ziRTKDIOSetOutput(unsigned int Index, unsigned int Output)`
Set a DIO's Output.
- `ZI_STATUS ziRTKDIOSetOutputNoUpdate(unsigned int Index, unsigned int Output)`
Set a DIO's Output without updating ziServer.
- `ZI_STATUS ziRTKDIOGetSample(unsigned int Index, DIOSample* Sample)`
retrieve a dio sample
- `ZI_STATUS ziRTKDIOSampleGetTimeStamp(DIOSample* Sample, float* Seconds)`
Returns the timestamp of a `DIOSample` in Seconds.

- **ZI_STATUS** ziRTKDIOSampleGetTimeStamp32 (**DIOSample*** Sample, unsigned int* TS)
Returns the timestamp of a **DIOSample** as 32bit unsigned int.
- **ZI_STATUS** ziRTKDIOSampleGetTimeStamp64 (**DIOSample*** Sample, unsigned long long* TS)
Returns the timestamp of a **DIOSample** as 64 bit unsigned long long.
- **ZI_STATUS** ziRTKDIOSampleGetBits (**DIOSample*** Sample, unsigned int* Bits)
Returns the Bits of a **DIOSample**.

Data Structure Documentation

struct DIOSample

type for holding a sample of dio

```
#include "ziRTK.h"

typedef struct DIOSample {
    TS_t TS;
    unsigned int Bits[1];
} DIOSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Bits
values of the sample

Detailed Description

See Also:

[ziRTKDIOGetSample](#)

Function Documentation

ziRTKDIOGetCount

`unsigned int ziRTKDIOGetCount ()`

get the count of Digital IO.

Returns:

The count of the DIO

See Also:

[Digital Input/Output](#)

ziRTKDIOGetExtClk

ZI_STATUS ziRTKDIOGetExtClk (unsigned int Index, int* ExtClk)

Get a DIO's External-Clock setting.

Corresponding server path:

■ DEV123/DIOS/0/EXTCLK

Parameters:

[in] Index
index of the Parameter

[out] ExtClk
pointer to an int to write the value in

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range
■ ZI_NULLPTR when ExtClk is NULL

See Also:

[ziRTKDIOSetExtClk](#), Digital Input/Output

ziRTKDIOSetExtClk

ZI_STATUS **ziRTKDIOSetExtClk** (**unsigned int Index, int ExtClk**)

Set a DIO's External-Clock setting.

Corresponding server path:

■ DEV123/DIOS/0/EXTCLK

Parameters:

[in] Index
index of the Parameter

[in] ExtClk
int providing the value

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOGetExtClk](#), [Digital Input/Output](#)

ziRTKDIOGetDecimation

ZI_STATUS ziRTKDIOGetDecimation (**unsigned int** Index, **int*** Decimation)

Get a DIO's Decimation.

Corresponding server path:

■ DEV123/DIOS/0/DECIMATION

Parameters:

[in] Index
index of the Parameter

[out] Decimation
pointer to an int to write the value in

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range
■ ZI_NULLPTR when Decimation is NULL

See Also:

[ziRTKDIOSetDecimation](#), [Digital Input/Output](#)

ziRTKDIOSetDecimation

ZI_STATUS ziRTKDIOSetDecimation (**unsigned int** Index, **int** Decimation)

Set a DIO's Decimation.

Corresponding server path:

■ DEV123/DIOS/0/DECIMATION

Parameters:

[in] Index
index of the Parameter

[in] Decimation
int providing the value

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOGetDecimation](#), [Digital Input/Output](#)

ziRTKDIOGetDrive

ZI_STATUS ziRTKDIOGetDrive (unsigned int Index, int* Drive)

Get a DIO's Drive.

Corresponding server path:

■ DEV123/DIOS/0/DRIVE

Parameters:

[in] Index
index of the Parameter

[out] Drive
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Drive is NULL

See Also:

[ziRTKDIOSetDrive](#), [Digital Input/Output](#)

ziRTKDIOSetDrive

ZI_STATUS ziRTKDIOSetDrive (unsigned int Index, int Drive)

Set a DIO's Drive.

Corresponding server path:

■ DEV123/DIOS/0/DRIVE

Parameters:

[in] Index
index of the Parameter

[in] Drive
int providing the value

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOGetDrive](#), [Digital Input/Output](#)

ziRTKDIOGetOutput

ZI_STATUS ziRTKDIOGetOutput (unsigned int Index, unsigned int* Output)

Get a DIO's Output.

Corresponding server path:

■ DEV123/DIOS/0/OUTPUT

Parameters:

[in] Index
index of the Parameter

[out] Output
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Output is NULL

See Also:

[ziRTKDIOSetOutput](#), Digital Input/Output

ziRTKDIOSetOutput

ZI_STATUS ziRTKDIOSetOutput (unsigned int Index, unsigned int Output)

Set a DIO's Output.

Corresponding server path:

■ DEV123/DIOS/0/OUTPUT

Parameters:

[in] Index
index of the Parameter

[in] Output
unsigned int providing the value

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOGetOutput](#), [ziRTKDIOSetOutputNoUpdate](#), [Digital Input/Output](#)

ziRTKDIOSetOutputNoUpdate

ZI_STATUS ziRTKDIOSetOutputNoUpdate (**unsigned int** Index, **unsigned int** Output)

Set a DIO's Output without updating ziServer.

Corresponding server path:

- DEV123/DIOS/0/OUTPUT

Parameters:

[in] Index
index of the Parameter

[in] Output
unsigned int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOSetOutput](#), [Digital Input/Output](#)

ziRTKDIOGetSample

ZI_STATUS ziRTKDIOGetSample (**unsigned int** Index, **DIOSample*** Sample)

retrieve a dio sample

Corresponding server path:

■ DEV123/DIOS/0/INPUT

Parameters:

[in] Index

index of the Parameter

[in] Sample

DIOSample to write the value in.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Sample is NULL

See Also:

[Digital Input/Output](#)

ziRTKDIOSampleGetTimeStamp

ZI_STATUS ziRTKDIOSampleGetTimeStamp (**DIOSample*** Sample, **float*** Seconds)

Returns the timestamp of a **DIOSample** in Seconds.

Parameters:

[in] Sample
the sample to extract the value from

[out] Seconds
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Seconds is NULL or Sample is NULL

See Also:

[ziRTKDIOGetSample](#), [ziRTKDIOSampleGetTimeStamp32](#), [ziRTKDIOSampleGetTimeStamp64](#),
Digital Input/Output

ziRTKDIOSampleGetTimeStamp32

ZI_STATUS ziRTKDIOSampleGetTimeStamp32 (**DIOSample*** Sample, **unsigned int*** TS)

Returns the timestamp of a **DIOSample** as 32bit unsigned int.

Note:

you may also read TS.TS32[ZIRTK_LSB_INDEX] of a **DIOSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKDIOSampleGetTimeStamp](#), [ziRTKDIOSampleGetTimeStamp64](#),
[Digital Input/Output](#)

ziRTKDIOSampleGetTimeStamp64

ZI_STATUS ziRTKDIOSampleGetTimeStamp64 (**DIOSample*** Sample, **unsigned long long*** TS)

Returns the timestamp of a [DIOSample](#) as 64 bit unsigned long long.

Note:

you may also read TS.TS64 of a [DIOSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned long long in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKDIOGetSample](#), [ziRTKDIOSampleGetTimeStamp](#), [ziRTKDIOSampleGetTimeStamp32](#),
Digital Input/Output

ziRTKDIOSampleGetBits

ZI_STATUS ziRTKDIOSampleGetBits (**DIOSample*** Sample, **unsigned int*** Bits)

Returns the Bits of a [DIOSample](#).

Note:

you may also read Bits[0] of a [DIOSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] Bits

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Bits is NULL or Sample is NULL

See Also:

[ziRTKDIOGetSample](#), [Digital Input/Output](#)

PLL

The functions in this section allow you change (some) PLL settings.

Functions

- `ZI_STATUS ziRTKPLLSetADCSelect (unsigned int Index, int ADCSelect)`
Set the ADCSELECT value of a PLL.

Function Documentation

ziRTKPLLSetADCSelect

ZI_STATUS ziRTKPLLSetADCSelect (unsigned int Index, int ADCSelect)

Set the ADCSELECT value of a PLL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[PLL](#)

8.2.13. Data Structure Documentation

struct AuxInSample

type for holding a sample of auxiliary input

```
#include "ziRTK.h"

typedef struct AuxInSample {
    TS_t TS;
    unsigned int Values[1];
} AuxInSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Values
Values of the sample.

Detailed Description

See Also:

[ziRTKAuxInGetSample](#)

struct DemodSample

type for holding a sample of a demodulator.

```
#include "ziRTK.h"

typedef struct DemodSample {
    TS_t TS;
    Val_t X;
    Val_t Y;
    unsigned int Reserved[3];
} DemodSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- Val_t X
X-Value of the sample where index 0 is the lower word and index 1 is the upper word.
- Val_t Y
Y-Value of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Reserved
reserved for internal usage

Detailed Description

See Also:

[ziRTKDemodGetSample](#)

struct DIOSample

type for holding a sample of dio

```
#include "ziRTK.h"

typedef struct DIOSample {
    TS_t TS;
    unsigned int Bits[1];
} DIOSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Bits
values of the sample

Detailed Description

See Also:

[ziRTKDIOGetSample](#)

union TS_t

```
typedef union TS_t {
    unsigned int TS32[2];
    unsigned long long TS64;
} TS_t;
```

Data Fields

- unsigned int TS32
- unsigned long long TS64

union Val_t

```
typedef union Val_t {  
    unsigned int Val32[2];  
    unsigned long long Val64;  
} Val_t;
```

Data Fields

- unsigned int Val32
- unsigned long long Val64

8.2.14. File Documentation

File ziRTK.h

Header File for the Zurich Instruments Ltd. Real-time Option running on an embedded CPU.

```
#include "stddef.h"
```

Data Structures

- union `TS_t`
- union `Val_t`
- struct `DemodSample`
type for holding a sample of a demodulator.
- struct `AuxInSample`
type for holding a sample of auxiliary input
- struct `DIOSample`
type for holding a sample of dio

Enumerations

- enum `ZI_STATUS` { `ZI_SUCCESS`, `ZI_OUTOFRANGE`,
`ZI_NULLPTR`, `ZI_LIMIT`, `ZI_MAX_STATUS` }
The `ZI_STATUS` enum is used as the general return value for ziRTK functions.
- enum `ZIRTK_OPTIONS` { `ZIRTK_OPTION_NONE`,
`ZIRTK_OPTION_MF`, `ZIRTK_OPTION_PLL`,
`ZIRTK_OPTION_MOD`, `ZIRTK_OPTION_RTK`,
`ZIRTK_OPTION_UHS`, `ZIRTK_OPTION_PID` }
- enum `ZIRTK_DEVTYPE` { `ZIRTK_DEVTYPE_DEFAULT`,
`ZIRTK_DEVTYPE_LI`, `ZIRTK_DEVTYPE_IS`,
`ZIRTK_DEVTYPE_UNKNOWN` }

Functions

- `void ziRTKInit (void)`
Declaration for the ziRTKInit function. This function has to be defined by the user.
- `void ziRTKLoop (void)`
Declaration for the loop function. This function has to be defined by the user.
- `float ziRTKGetTimeStamp ()`

- Returns a the current timestamp in seconds.
- `unsigned int ziRTKGetTimeStamp32 ()`
Returns a the current timestamp in a 32bit unsigned int.
 - `unsigned long long ziRTKGetTimeStamp64 ()`
Returns a the current timestamp in a 64 bit unsigned long long.
 - `float ziRTKGetWorkload ()`
Get the current workload of the CPU.
 - `void ziRTKUpdateHostPCOn (void)`
Enables updating of ziServer when a value changes.
 - `void ziRTKUpdateHostPCOff (void)`
Disables updating of ziServer when a value changes.
 - `unsigned char ziRTKGetUpdateHostPC (void)`
Enables updating of ziServer when a value changes.
 - `void ziRTKPause (int Value)`
Pauses or resumes the updating of all signal-processing related parameters. Useful for synchronizing parameter updates with the RTK.
 - `ZI_STATUS ziRTKAddDemodSampleTrigger (unsigned int Index, ziTriggerId* TriggerId)`
Add a trigger on a demod sample.
 - `ZI_STATUS ziRTKAddDIOSampleTrigger (unsigned int Index, ziTriggerId* TriggerId)`
Add a trigger on a DIO sample.
 - `ZI_STATUS ziRTKAddAuxInSampleTrigger (unsigned int Index, ziTriggerId* TriggerId)`
Add a trigger on an auxiliary input sample.
 - `ZI_STATUS ziRTKAddClockTrigger (unsigned int USec, ziTriggerId* TriggerId)`
Add a trigger on a timer.
 - `ZI_STATUS ziRTKAddUserRegTrigger (unsigned int Index, ziTriggerId* TriggerId)`
Add a trigger on a user-register.
 - `ZI_STATUS ziRTKTestTrigger (ziTriggerId TriggerId, unsigned int* IsSet)`
Test if a trigger has been activated.
 - `ZI_STATUS ziRTKPrintf (char* Fmt, ...)`
printf-compatible print to stdio. The output consists of a string which may include formatting directives.
 - `ZI_STATUS ziRTKWriteString (char* Str)`

Write a zero-terminated string to stdio.

- **ZI_STATUS** ziRTKWriteData (unsigned char* Buffer, unsigned int Len)
Write a buffer to stdio.
- **ZI_STATUS** ziRTKFeaturesGetOptions (ZIRTK_OPTIONS* Options)
Read which options are enabled.
- **ZI_STATUS** ziRTKFeaturesGetDevType (ZIRTK_DEVTYPE* DevType)
Read the device type.
- **ZI_STATUS** ziRTKSystemGetExtClk (int* ExtClk)
Gets whether the Clock source is internal or external.
- **ZI_STATUS** ziRTKSystemSetExtClk (int ExtClk)
Set Clock to internal or external mode.
- **ZI_STATUS** ziRTKSystemGetHWRevision (int* HWRevision)
Gets the revision-number of the hardware.
- unsigned int ziRTKUserRegGetCount ()
get the count of the user registers.
- **ZI_STATUS** ziRTKUserRegGet (unsigned char Index, unsigned int* Value)
Read flags set by the host PC.
- **ZI_STATUS** ziRTKUserRegSet (unsigned char Index, unsigned int Value)
Write flags set by the host PC.
- **ZI_STATUS** ziRTKExtMemGet (unsigned int Address, unsigned int* Value)
Read an address in the external memory.
- **ZI_STATUS** ziRTKExtMemSet (unsigned int Address, unsigned int Value)
Write an address in the external memory.
- unsigned int ziRTKSigInGetCount ()
get the count of high speed signal inputs.
- **ZI_STATUS** ziRTKSigInGetRange (unsigned int Index, float* Range)
Get an input's range (V).
- **ZI_STATUS** ziRTKSigInSetRange (unsigned int Index, float Range)
Set an input's range(V).
- **ZI_STATUS** ziRTKSigInGetAC (unsigned int Index, int* Value)
Get an input's AC mode.

- **ZI_STATUS** `ziRTKSigInSetAC (unsigned int Index, int Value)`
Set an input's AC mode.
- **ZI_STATUS** `ziRTKSigInGetImp50 (unsigned int Index, int* Value)`
Get an input's 50 Ohm mode.
- **ZI_STATUS** `ziRTKSigInSetImp50 (unsigned int Index, int Value)`
Set an input's 50 Ohm mode.
- **ZI_STATUS** `ziRTKSigInGetDiff (unsigned int Index, int* Value)`
Get an input's differential mode.
- **ZI_STATUS** `ziRTKSigInSetDiff (unsigned int Index, int Value)`
Set an input's differential mode.
- `unsigned int ziRTKOscGetCount ()`
get the count of oscillators.
- **ZI_STATUS** `ziRTKOscGetFreq (unsigned int Index, float* Freq)`
Get an oscillator's frequency.
- **ZI_STATUS** `ziRTKOscSetFreq (unsigned int Index, float Freq)`
Set an oscillator's frequency.
- `unsigned int ziRTKDemodGetCount ()`
get the count of demodulators.
- **ZI_STATUS** `ziRTKDemodGetADCSelect (unsigned int Index, int* ADCSelect)`
Get a demodulator's adcselect.
- **ZI_STATUS** `ziRTKDemodSetADCSelect (unsigned int Index, int ADCSelect)`
Set a demodulator's ADC select.
- **ZI_STATUS** `ziRTKDemodGetOscSelect (unsigned int Index, int* OscSelect)`
Get a demodulator's oscillator select.
- **ZI_STATUS** `ziRTKDemodSetOscSelect (unsigned int Index, int OscSelect)`
Set a demodulator's oscillator select.
- **ZI_STATUS** `ziRTKDemodGetOrder (unsigned int Index, int* Order)`
Get a demodulator's filter order.
- **ZI_STATUS** `ziRTKDemodSetOrder (unsigned int Index, int Order)`
Set a demodulator's filter order.
- **ZI_STATUS** `ziRTKDemodGetHarmonic (unsigned int Index, int* Harmonic)`

Get a demodulator's harmonic.

- **ZI_STATUS** `ziRTKDemodSetHarmonic (unsigned int Index, int Harmonic)`
Set a demodulator's harmonic.
- **ZI_STATUS** `ziRTKDemodGetRate (unsigned int Index, float* Rate)`
Get a demodulator's rate.
- **ZI_STATUS** `ziRTKDemodSetRate (unsigned int Index, float Rate)`
Set a demodulator's rate.
- **ZI_STATUS** `ziRTKDemodGetTrigger (unsigned int Index, unsigned int* Trigger)`
Get a demodulator's trigger- and gating-settings.
- **ZI_STATUS** `ziRTKDemodSetTrigger (unsigned int Index, unsigned int Trigger)`
Set a demodulator's trigger- and gating-settings.
- **ZI_STATUS** `ziRTKDemodGetPhaseShift (unsigned int Index, float* PhaseShift)`
Get a demodulator's phaseshift.
- **ZI_STATUS** `ziRTKDemodSetPhaseShift (unsigned int Index, float PhaseShift)`
Set a demodulator's phaseshift.
- **ZI_STATUS** `ziRTKDemodGetTimeConstant (unsigned int Index, float* TimeConstant)`
Get a demodulator's timeconstant.
- **ZI_STATUS** `ziRTKDemodSetTimeConstant (unsigned int Index, float TimeConstant)`
Set a demodulator's timeconstant.
- **ZI_STATUS** `ziRTKDemodGetSinc (unsigned int Index, unsigned int* Value)`
Get a demodulator's sinc.
- **ZI_STATUS** `ziRTKDemodSetSinc (unsigned int Index, unsigned int Value)`
Set a demodulator's sinc.
- **ZI_STATUS** `ziRTKDemodGetSample (unsigned int Index, DemodSample* Sample)`
Retrieve a demodulator's sample.
- **ZI_STATUS** `ziRTKDemodSampleGetTimeStamp (DemodSample* Sample, float* Seconds)`
Returns the timestamp of a `DemodSample` in Seconds.
- **ZI_STATUS** `ziRTKDemodSampleGetTimeStamp32 (DemodSample* Sample, unsigned int* TS)`

- Returns the timestamp of a [DemodSample](#) as 32bit unsigned int.
- [ZI_STATUS ziRTKDemodSampleGetTimeStamp64 \(DemodSample* Sample, unsigned long long* TS \)](#)
Returns the timestamp of a [DemodSample](#) as 64 bit unsigned long long.
 - [ZI_STATUS ziRTKDemodSampleGetX \(DemodSample* Sample, float* X \)](#)
Returns the X-Value of a [DemodSample](#) as float.
 - [ZI_STATUS ziRTKDemodSampleGetX32 \(DemodSample* Sample, unsigned int* X \)](#)
Returns the X-Value of a [DemodSample](#) as unsigned int.
 - [ZI_STATUS ziRTKDemodSampleGetX64 \(DemodSample* Sample, unsigned long long* X \)](#)
Returns the X-Value of a [DemodSample](#) as unsigned long long.
 - [ZI_STATUS ziRTKDemodSampleGetCompX \(DemodSample* Sample, float* X \)](#)
Returns the compensated X-Value of a [DemodSample](#).
 - [ZI_STATUS ziRTKDemodSampleGetY \(DemodSample* Sample, float* Y \)](#)
Returns the Y-Value of a [DemodSample](#) as float.
 - [ZI_STATUS ziRTKDemodSampleGetY32 \(DemodSample* Sample, unsigned int* Y \)](#)
Returns the Y-Value of a [DemodSample](#) as unsigned int.
 - [ZI_STATUS ziRTKDemodSampleGetY64 \(DemodSample* Sample, unsigned long long* Y \)](#)
Returns the Y-Value of a [DemodSample](#) as unsigned long long.
 - [ZI_STATUS ziRTKDemodSampleGetCompY \(DemodSample* Sample, float* Y \)](#)
Returns the compensated Y-Value of a [DemodSample](#).
 - [ZI_STATUS ziRTKDemodSampleGetCompXY \(DemodSample* Sample, float* X, float* Y \)](#)
Returns the compensated X-Value and the compensated Y-Value of a [DemodSample](#).
 - [ZI_STATUS ziRTKDemodSampleGetR \(DemodSample* Sample, float* R \)](#)
Returns the Radius-Value of a [DemodSample](#).
 - [ZI_STATUS ziRTKDemodSampleGetCompR \(DemodSample* Sample, float* R \)](#)
Returns the compensated Radius-Value of a [DemodSample](#).
 - [ZI_STATUS ziRTKDemodSampleGetTheta \(DemodSample* Sample, float* Theta \)](#)

Returns the Theta-Value of a [DemodSample](#).

- `unsigned int ziRTKSigOutGetCount ()`
get the count of HF Signal outputs.
- `ZI_STATUS ziRTKSigOutGetRange (unsigned int Index, float* Range)`
Get an output's range (V).
- `ZI_STATUS ziRTKSigOutSetRange (unsigned int Index, float Range)`
Set an output's range (V).
- `unsigned int ziRTKSigOutGetChannelCount ()`
get the count of HF Signal output Mixer Channels.
- `ZI_STATUS ziRTKSigOutGetEnable (unsigned int Index, unsigned int Channel, int* Value)`
Get an output's mixer enable.
- `ZI_STATUS ziRTKSigOutSetEnable (unsigned int Index, unsigned int Channel, int Value)`
Set an output's mixer enable.
- `ZI_STATUS ziRTKSigOutGetAmplitude (unsigned int Index, unsigned int Channel, float* Value)`
Get an output's mixer amplitude.
- `ZI_STATUS ziRTKSigOutSetAmplitude (unsigned int Index, unsigned int Channel, float Value)`
Set an output's mixer amplitude.
- `ZI_STATUS ziRTKSigOutGetOn (unsigned int Index, int* Value)`
Get an output's on-switch.
- `ZI_STATUS ziRTKSigOutSetOn (unsigned int Index, int Value)`
Set an output's on-switch.
- `ZI_STATUS ziRTKSigOutGetAdd (unsigned int Index, int* Value)`
Get an output's add mode.
- `ZI_STATUS ziRTKSigOutSetAdd (unsigned int Index, int Value)`
Set an output's add mode.
- `unsigned int ziRTKAuxOutGetCount ()`
get the count of auxiliary analog outputs.
- `ZI_STATUS ziRTKAuxOutGetValue (unsigned int Index, float* Value)`
Get a auxiliary outputs value as float (V).
- `ZI_STATUS ziRTKAuxOutGetOutputSelect (unsigned int Index, int* OutputSelect)`

Get an auxiliary outputs outputselect.

- ─ **ZI_STATUS** `ziRTKAuxOutSetOutputSelect (unsigned int Index, int OutputSelect)`
Set an auxiliary output's outputselect.
- ─ **ZI_STATUS** `ziRTKAuxOutGetDemodSelect (unsigned int Index, int* DemodSelect)`
Get an auxiliary output's demodselect.
- ─ **ZI_STATUS** `ziRTKAuxOutSetDemodSelect (unsigned int Index, int DemodSelect)`
Set an auxiliary output's demodselect.
- ─ **ZI_STATUS** `ziRTKAuxOutGetOffset (unsigned int Index, float* Offset)`
Get an auxiliary output's offset.
- ─ **ZI_STATUS** `ziRTKAuxOutSetOffset (unsigned int Index, float Offset)`
Set an auxiliary output's offset.
- ─ **ZI_STATUS** `ziRTKAuxOutSetOffsetNoUpdate (unsigned int Index, float Offset)`
Set an auxiliary output's offset without updating ziServer.
- ─ **ZI_STATUS** `ziRTKAuxOutSetOffsetInt (unsigned int Index, int Offset)`
Set an auxiliary output's offset as integer.
- ─ **ZI_STATUS** `ziRTKAuxOutSetOffsetIntNoUpdate (unsigned int Index, int Offset)`
Set an auxiliary output's offset as integer without updating ziServer.
- ─ **ZI_STATUS** `ziRTKAuxOutGetScale (unsigned int Index, float* Scale)`
Get an auxiliary output's scale.
- ─ **ZI_STATUS** `ziRTKAuxOutSetScale (unsigned int Index, float Scale)`
Set an auxiliary output's scale.
- ─ `unsigned int ziRTKAuxInGetCount ()`
get the count of Auxiliary Analog Inputs.
- ─ **ZI_STATUS** `ziRTKAuxInGetAveraging (unsigned int Index, int* Averaging)`
Get a auxiliary input's averaging/sample rate.
- ─ **ZI_STATUS** `ziRTKAuxInSetAveraging (unsigned int Index, int Averaging)`
Set an auxiliary output's averaging sample rate.
- ─ **ZI_STATUS** `ziRTKAuxInGetSample (unsigned int Index, AuxInSample* Sample)`

- retrieve a auxiliary input sample
- **ZI_STATUS** `ziRTKAuxInSampleGetTimeStamp (AuxInSample* Sample, float* Seconds)`
Returns the timestamp of an [AuxInSample](#) in Seconds.
- **ZI_STATUS** `ziRTKAuxInSampleGetTimeStamp32 (AuxInSample* Sample, unsigned int* TS)`
Returns the timestamp of an [AuxInSample](#) as 32bit unsigned int.
- **ZI_STATUS** `ziRTKAuxInSampleGetTimeStamp64 (AuxInSample* Sample, unsigned long long* TS)`
Returns the timestamp of an [AuxInSample](#) as 64 bit unsigned long long.
- **ZI_STATUS** `ziRTKAuxInSampleGetValue (AuxInSample* Sample, unsigned int Channel, float* Value)`
Returns a Value of a [AuxInSample](#).
- **ZI_STATUS** `ziRTKAuxInSampleGetValue16 (AuxInSample* Sample, unsigned int Channel, short* Value)`
Returns a Value of a [AuxInSample](#) as short.
- `unsigned int ziRTKDIOGetCount ()`
get the count of Digital IO.
- **ZI_STATUS** `ziRTKDIOGetExtClk (unsigned int Index, int* ExtClk)`
Get a DIO's External-Clock setting.
- **ZI_STATUS** `ziRTKDIOSetExtClk (unsigned int Index, int ExtClk)`
Set a DIO's External-Clock setting.
- **ZI_STATUS** `ziRTKDIOGetDecimation (unsigned int Index, int* Decimation)`
Get a DIO's Decimation.
- **ZI_STATUS** `ziRTKDIOSetDecimation (unsigned int Index, int Decimation)`
Set a DIO's Decimation.
- **ZI_STATUS** `ziRTKDIOGetDrive (unsigned int Index, int* Drive)`
Get a DIO's Drive.
- **ZI_STATUS** `ziRTKDIOSetDrive (unsigned int Index, int Drive)`
Set a DIO's Drive.
- **ZI_STATUS** `ziRTKDIOGetOutput (unsigned int Index, unsigned int* Output)`
Get a DIO's Output.
- **ZI_STATUS** `ziRTKDIOSetOutput (unsigned int Index, unsigned int Output)`
Set a DIO's Output.

- **ZI_STATUS** `ziRTKDIOSetOutputNoUpdate (unsigned int Index, unsigned int Output)`
Set a DIO's Output without updating ziServer.
- **ZI_STATUS** `ziRTKDIOGetSample (unsigned int Index, DIOSample* Sample)`
retrieve a dio sample
- **ZI_STATUS** `ziRTKDIOSampleGetTimeStamp (DIOSample* Sample, float* Seconds)`
Returns the timestamp of a `DIOSample` in Seconds.
- **ZI_STATUS** `ziRTKDIOSampleGetTimeStamp32 (DIOSample* Sample, unsigned int* TS)`
Returns the timestamp of a `DIOSample` as 32bit unsigned int.
- **ZI_STATUS** `ziRTKDIOSampleGetTimeStamp64 (DIOSample* Sample, unsigned long long* TS)`
Returns the timestamp of a `DIOSample` as 64 bit unsigned long long.
- **ZI_STATUS** `ziRTKDIOSampleGetBits (DIOSample* Sample, unsigned int* Bits)`
Returns the Bits of a `DIOSample`.
- **ZI_STATUS** `ziRTKPLLSetADCSelect (unsigned int Index, int ADCSelect)`
Set the ADCSELECT value of a PLL.

Data Structure Documentation

union TS_t

```
typedef union TS_t {
    unsigned int TS32[2];
    unsigned long long TS64;
} TS_t;
```

Data Fields

- unsigned int TS32
- unsigned long long TS64

union Val_t

```
typedef union Val_t {  
    unsigned int Val32[2];  
    unsigned long long Val64;  
} Val_t;
```

Data Fields

- unsigned int Val32
- unsigned long long Val64

struct DemodSample

type for holding a sample of a demodulator.

```
#include "ziRTK.h"

typedef struct DemodSample {
    TS_t TS;
    Val_t X;
    Val_t Y;
    unsigned int Reserved[3];
} DemodSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- Val_t X
X-Value of the sample where index 0 is the lower word and index 1 is the upper word.
- Val_t Y
Y-Value of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Reserved
reserved for internal usage

Detailed Description**See Also:**

[ziRTKDemodGetSample](#)

struct AuxInSample

type for holding a sample of auxiliary input

```
#include "ziRTK.h"

typedef struct AuxInSample {
    TS_t TS;
    unsigned int Values[1];
} AuxInSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Values
Values of the sample.

Detailed Description**See Also:**

[ziRTKAuxInGetSample](#)

struct DIOSample

type for holding a sample of dio

```
#include "ziRTK.h"

typedef struct DIOSample {
    TS_t TS;
    unsigned int Bits[1];
} DIOSample;
```

Data Fields

- TS_t TS
Timestamp of the sample where index 0 is the lower word and index 1 is the upper word.
- unsigned int Bits
values of the sample

Detailed Description**See Also:**

[ziRTKDIOGetSample](#)

Enumeration Type Documentation

The ZI_STATUS enum is used as the general return value for ziRTK functions.

Enumerator:

- ZI_SUCCESS
Success (no error)
- ZI_OUTOFRANGE
a provided parameter (normally an index) is out of range
- ZI_NULLPTR
a provided parameter is NULL
- ZI_LIMIT
a limit has been reached
- ZI_MAX_STATUS

Enumerator:

- ZIRTK_OPTION_NONE
- ZIRTK_OPTION_MF
- ZIRTK_OPTION_PLL
- ZIRTK_OPTION_MOD
- ZIRTK_OPTION_RTK
- ZIRTK_OPTION_UHS
- ZIRTK_OPTION_PID

Enumerator:

- ZIRTK_DEVTYPE_DEFAULT
- ZIRTK_DEVTYPE_LI
- ZIRTK_DEVTYPE_IS
- ZIRTK_DEVTYPE_UNKNOWN

Function Documentation

ziRTKInit

void ziRTKInit (void)

Declaration for the ziRTKInit function. This function has to be defined by the user.

Used to initialize all settings for the current application e.g. add triggers or set initial values.

```
#include <ziRTK.h>

void ziRTKInit()
{
    // Add a clock trigger at an interval of 1 millisecond.
    ziRTKAddClockTrigger( 1000, NULL );

    // Set the rate of demodulator 0 to 1 Hz.
    ziRTKDemodSetRate( 0, 1 );

    // Write a "Hello" message to usb.
    ziRTKWriteString( "Hello" );

}
```

See Also:

[ziRTKLoop](#), [ziRTKAddClockTrigger](#), [ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddUserRegTrigger](#)

ziRTKLoop

void ziRTKLoop (void)

Declaration for the loop function. This function has to be defined by the user.

This function will be executed every time a trigger occurs. Use ziRTKTestTrigger to test which triggers caused the function to be executed.

```
#include <math.h>
#include <ziRTK.h>

// Output the Magnitude and Phase of demodulator 0 on auxiliary output 0 and
// auxiliary output 1.

DemodSample Sample;

void ziRTKLoop()
{
    //retrieve sample
    ziRTKDemodGetSample( 0, &Sample );

    //calculate magnitude
    float Mag;
    ziRTKDemodSampleGetCompR( &Sample, &Mag );

    //calculate phase
    float Phi;
    ziRTKDemodSampleGetTheta( &Sample, &Phi );

    //put the values on the misc analog outputs
    ziRTKAuxOutSetOffset( 0, Mag * 10 );
    ziRTKAuxOutSetOffset( 1, Phi * 10 );

}
```

See Also:

[ziRTKInit](#), [ziRTKTestTrigger](#)

ziRTKGetTimeStamp

float ziRTKGetTimeStamp ()

Returns a the current timestamp in seconds.

Returns:

the timestamp as a float

See Also:

[ziRTKGetTimeStamp32](#), [ziRTKGetTimeStamp64](#)

ziRTKGetTimeStamp32

unsigned int ziRTKGetTimeStamp32 ()

Returns a the current timestamp in a 32bit unsigned int.

Returns:

the timestamp as an unsigned int

See Also:

[ziRTKGetTimeStamp](#), [ziRTKGetTimeStamp64](#)

ziRTKGetTimeStamp64

`unsigned long long ziRTKGetTimeStamp64 ()`

Returns a the current timestamp in a 64 bit unsigned long long.

Returns:

the timestamp as an unsigned long long

See Also:

[ziRTKGetTimeStamp](#), [ziRTKGetTimeStamp32](#)

ziRTKGetWorkload

float ziRTKGetWorkload ()

Get the current workload of the CPU.

Returns:

the current workload as a float value (0 - 1)

ziRTKUpdateHostPCOn

void ziRTKUpdateHostPCOn (void)

Enables updating of ziServer when a value changes.

See Also:

[ziRTKUpdateHostPCOff](#), [ziRTKGetUpdateHostPC](#)

ziRTKUpdateHostPCOff

void ziRTKUpdateHostPCOff (void)

Disables updating of ziServer when a value changes.

See Also:

[ziRTKUpdateHostPCOn](#), [ziRTKGetUpdateHostPC](#)

ziRTKGetUpdateHostPC

unsigned char ziRTKGetUpdateHostPC (void)

Enables updating of ziServer when a value changes.

Returns:

- 0 when updating of ziServer is disabled
- 1 when updating of ziServer is enabled

See Also:

[ziRTKUpdateHostPCOn](#), [ziRTKUpdateHostPCOff](#)

ziRTKPause

void ziRTKPause (int Value)

Pauses or resumes the updating of all signal-processing related parameters. Useful for synchronizing parameter updates with the RTK.

Parameters:

[in] Value

when Value is not zero the updating is paused otherwise all updates are running

ziRTKAddDemodSampleTrigger

ZI_STATUS ziRTKAddDemodSampleTrigger (unsigned int Index, ziTriggerId* TriggerId)

Add a trigger on a demod sample.

ziRTKAddDemodSampleTrigger adds a trigger on a demodulator sample.

Parameters:

[in] Index

index of the demodulator

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the index is out of range
- ZI_LIMIT when no more triggers are available

See Also:

[ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddClockTrigger](#),
[ziRTKAddUserRegTrigger](#), [ziRTKTestTrigger](#)

ziRTKAddDIOSampleTrigger

ZI_STATUS ziRTKAddDIOSampleTrigger (**unsigned int** Index, **ziTriggerId*** TriggerId)

Add a trigger on a DIO sample.

ziRTKAddDIOSampleTrigger adds a trigger on a DIO sample.

Parameters:

[in] Index

index of the DIO

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the index is out of range
- ZI_LIMIT when no more triggers are available

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddClockTrigger](#),
[ziRTKAddUserRegTrigger](#), [ziRTKTestTrigger](#)

ziRTKAddAuxInSampleTrigger

ZI_STATUS ziRTKAddAuxInSampleTrigger (**unsigned int** Index, **ziTriggerId*** TriggerId)

Add a trigger on an auxiliary input sample.

ziRTKAddAuxInSampleTrigger adds a trigger on an auxiliary input sample.

Parameters:

[in] Index

index of the auxiliary input

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the index is out of range
- ZI_LIMIT when no more triggers are available

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddClockTrigger](#),
[ziRTKAddUserRegTrigger](#), [ziRTKTestTrigger](#)

ziRTKAddClockTrigger

ZI_STATUS ziRTKAddClockTrigger (**unsigned int** USec, **ziTriggerId*** TriggerId)

Add a trigger on a timer.

ziRTKAddClockTrigger adds a trigger that occurs in an interval

Parameters:

[in] USec

Trigger interval in microseconds

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_LIMIT when no more triggers are available

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddUserRegTrigger](#), [ziRTKTestTrigger](#)

ziRTKAddUserRegTrigger

ZI_STATUS ziRTKAddUserRegTrigger (unsigned int Index, ziTriggerId* TriggerId)

Add a trigger on a user-register.

ziRTKAddUserRegTrigger adds a trigger that occurs on a change of a user-register

Parameters:

[in] Index

Index of the register (currently 0 - 15)

[out] TriggerId

Pointer to a ziTriggerId returning the id of the newly added trigger. If not used pass NULL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_LIMIT no more triggers are available

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddClockTrigger](#), [ziRTKTestTrigger](#)

ziRTKTestTrigger

ZI_STATUS ziRTKTestTrigger (ziTriggerId TriggerId, unsigned int* IsSet)

Test if a trigger has been activated.

In case you have added more than one trigger, you can use this function in [ziRTKLoop](#) to determine which of the triggers were active

Parameters:

[in] TriggerId

the identifier for which the state should be returned

[out] IsSet

Pointer to an unsigned int which is not equal to zero, when the trigger is active, else zero

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when TriggerId is out of range

See Also:

[ziRTKAddDemodSampleTrigger](#), [ziRTKAddDIOSampleTrigger](#), [ziRTKAddAuxInSampleTrigger](#), [ziRTKAddClockTrigger](#), [ziRTKAddUserRegTrigger](#)

ziRTKPrintf**ZI_STATUS ziRTKPrintf (char* Fmt, ...)**

printf-compatible print to stdio. The output consists of a string which may include formatting directives.

Parameters:

[in] Fmt

format string composed of zero or more formatting directives and ordinary characters

[in] ...

variable count of arguments being formatted and given out according to the format string

Returns:

- ZI_SUCCESS on success

This function description is based on the Unix man pages for printf.

The format string is composed of zero or more directives: ordinary characters (not %), which are copied unchanged to the output stream; and conversion specifications, each of which results in fetching zero or more subsequent arguments. Each conversion specification is introduced by the % character.

The arguments must correspond properly (after type promotion) with the conversion specifier. All integer values (signed and unsigned) have to be 32bit values, all floating point values have to be double but standard type promotion automatically converts float values to double. Pointer arguments also have to be 32bit wide, char-arrays as arguments are not supported.

After the %, the following appear in sequence:

- Zero or more of the following flags:
 - "0" (zero) Zero padding. For all conversions except n, the converted value is padded on the left with zeros rather than blanks. If a precision is given with a numeric conversion (d, i, o, u, x, and X), the 0 flag is ignored.
 - "-" A negative field width flag; the converted value is to be left adjusted on the field boundary. Except for n conversions, the converted value is padded on the right with blanks, rather than on the left with blanks or zeros. A - overrides a 0 if both are given.
 - " " (space) A blank should be left before a positive number produced by a signed conversion (a, A, d, e, E, f, F, g, G, or i).
 - "+" A sign must always be placed before a number produced by a signed conversion. A + overrides a space if both are used.
- An optional decimal digit string specifying a minimum field width. If the converted value has fewer characters than the field width, it will be padded with spaces on the left (or right, if the left-adjustment flag has been given) to fill out the field width.
- An optional precision, in the form of a period . followed by an optional digit string. If the digit string is omitted, the precision is taken as zero. This gives the minimum number of digits to appear for d, i, o, u, x, and X conversions, the number of digits to appear after the decimal-point for a, A, e, E, f, and F conversions, the maximum number of significant digits for g and G conversions, or the maximum number of characters to be printed from a string for s conversions.

- A character that specifies the type of conversion to be applied. The conversion specifiers and their meanings are:
 - **d, i, o, u, x, X** The int (or appropriate variant) argument is converted to signed decimal (d and i), unsigned octal (o), unsigned decimal (u), or unsigned hexadecimal (x and X) notation. The letters a, b, c, d, e, f are used for x conversions; the letters A, B, C, D, E, F are used for X conversions. The precision, if any, gives the minimum number of digits that must appear; if the converted value requires fewer digits, it is padded on the left with zeros.
 - **e, E** The double argument is rounded and converted in the style [-]d.ddde+-dd where there is one digit before the decimal-point character and the number of digits after it is equal to the precision; if the precision is missing, it is taken as 6; if the precision is zero, no decimal-point character appears. An E conversion uses the letter "E" (rather than "e") to introduce the exponent. The exponent always contains at least two digits; if the value is zero, the exponent is 00.

For a, A, e, E, f, F, g, and G conversions, positive and negative infinity are represented as inf and -inf respectively when using the lowercase conversion character, and INF and -INF respectively when using the uppercase conversion character. Similarly, NaN is represented as nan when using the lowercase conversion, and NAN when using the uppercase conversion.

- **f, F** The double argument is rounded and converted to decimal notation in the style [-]ddd.ddd, where the number of digits after the decimal-point character is equal to the precision specification. If the precision is missing, it is taken as 6; if the precision is explicitly zero, no decimal-point character appears. If a decimal point appears, at least one digit appears before it.
- **g, G** The double argument is converted in style f or e (or F or E for G conversions). The precision specifies the number of significant digits. If the precision is missing, 6 digits are given; if the precision is zero, it is treated as 1. Style e is used if the exponent from its conversion is less than -4 or greater than or equal to the precision. Trailing zeros are removed from the fractional part of the result; a decimal point appears only if it is followed by at least one digit.
- **c** The int argument is converted to an unsigned char, and the resulting character is written.
- **p** The void * pointer argument is printed in hexadecimal.
- **n** The number of characters written so far is stored into the integer indicated by the int * (or variant) pointer argument. No argument is converted.
- **%** A "%" is written. No argument is converted. The complete conversion specification is "%\%".

Examples

print an integer followed by a newline:

```
int val = 1234;
ziRTKPrintf( "value: %d\n", val );
```

print an integer as 8 digit hex with leading zeros followed by a newline:

```
int val = 1234;
ziRTKPrintf( "value: %08x\n", val );
```

print a float followed by a newline:

```
float val = 1.234;
ziRTKPrintf( "value: %f\n", val );
```

print a float and an integer followed by a newline:

```
float fval = 1.234;
int ival = 5678;
ziRTKPrintf( "float: %f, int: %d\n", fval, ival );
```

See Also:

[ziRTKWriteString](#), [ziRTKWriteData](#)

ziRTKWriteString

ZI_STATUS ziRTKWriteString (**char*** Str)

Write a zero-terminated string to stdio.

Parameters:

[in] Str
pointer to the char array

Returns:

■ ZI_SUCCESS on success

See Also:

[ziRTKPrintf](#), [ziRTKWriteData](#)

ziRTKWriteData

ZI_STATUS ziRTKWriteData (**unsigned char*** Buffer, **unsigned int** Len)

Write a buffer to stdio.

Parameters:

[in] Buffer
pointer to data

[in] Len
Length of the data

Returns:

■ ZI_SUCCESS on success

See Also:

[ziRTKPrintf](#), [ziRTKWriteString](#)

ziRTKFeaturesGetOptions

ZI_STATUS ziRTKFeaturesGetOptions (**ZIRTK_OPTIONS*** Options)

Read which options are enabled.

Corresponding server paths:

- DEV123/FEATURES/OPTIONS

Parameters:

[out] Options

Pointer to a ZIRTK_OPTIONS to store the value in. The Values are or'ed together.

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Kits is NULL

ziRTKFeaturesGetDevType

ZI_STATUS ziRTKFeaturesGetDevType (**ZIRTK_DEVTYPE*** DevType)

Read the device type.

Corresponding server paths:

- DEV123/FEATURES/DEVTYPE

Parameters:

[out] DevType

Pointer to a ZIRTK_DEVTYPE to store the value in.

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when DevType is NULL

ziRTKSystemGetExtClk

ZI_STATUS ziRTKSystemGetExtClk (**int*** ExtClk)

Gets whether the Clock source is internal or external.

Corresponding server paths:

- DEV123/SYSTEM/EXTCLK

Parameters:

[in] ExtClk

Pointer to an int to store the value in.

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when ExtClk is NULL

ziRTKSystemSetExtClk

ZI_STATUS ziRTKSystemSetExtClk (int ExtClk)

Set Clock to internal or external mode.

Corresponding server paths:

- DEV123/SYSTEM/EXTCLK

Parameters:

[in] ExtClk
int which contains the value.

Returns:

- ZI_SUCCESS on success

ziRTKSystemGetHWRevision

ZI_STATUS ziRTKSystemGetHWRevision (int* HWRevision)

Gets the revision-number of the hardware.

Corresponding server paths:

- DEV123/SYSTEM/HWREVISION

Parameters:

[in] HWRevision

Pointer to an int to store the value in.

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when HWRevision is NULL

ziRTKUserRegGetCount

unsigned int ziRTKUserRegGetCount ()

get the count of the user registers.

Returns:

the count of user registers

See Also:

[User Registers](#)

ziRTKUserRegGet

ZI_STATUS ziRTKUserRegGet (**unsigned char** Index, **unsigned int*** Value)

Read flags set by the host PC.

Corresponding server paths:

- DEV123/CPUS/0/USERREGS/0
- DEV123/CPUS/0/USERREGS/1
- ...

The user registers are set using ziServer. The provided function is to read those values.

Parameters:

[in] Index
index of the user registers-word (currently 0 - 15)

[in] Value
Pointer to an unsigned int to store the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the provided index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKUserRegSet](#), [User Registers](#)

ziRTKUserRegSet

ZI_STATUS ziRTKUserRegSet (**unsigned char** Index, **unsigned int** Value)

Write flags set by the host PC.

Corresponding server paths:

- DEV123/CPUS/0/USERREGS/0
- DEV123/CPUS/0/USERREGS/1
- ...

Parameters:

[in] Index
index of the user registers-word (currently 0 - 15)

[in] Value
the new value to write to the user register

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the provided index is out of range

See Also:

[ziRTKUserRegGet](#), [User Registers](#)

ziRTKExtMemGet

ZI_STATUS ziRTKExtMemGet (**unsigned int** Address, **unsigned int*** Value)

Read an address in the external memory.

Parameters:

[in] Address

the byte-aligned address to read (0x00000000 to 0x2FFFFFF)

[in] Value

Pointer to an unsigned int to store the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the provided index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKExtMemSet](#), [External Memory](#)

ziRTKExtMemSet

ZI_STATUS ziRTKExtMemSet (unsigned int Address, unsigned int Value)

Write an address in the external memory.

Parameters:

[in] Address

the byte-aligned address to write (0x00000000 to 0x2FFFFFF)

[in] Value

the new value to write to the external memory

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when the provided index is out of range

See Also:

[ziRTKExtMemGet, External Memory](#)

ziRTKSigInGetCount

unsigned int ziRTKSigInGetCount ()

get the count of high speed signal inputs.

Returns:

The count of signal inputs

See Also:

[HF Signal Input](#)

ziRTKSigInGetRange**ZI_STATUS** ziRTKSigInGetRange (**unsigned int** Index, **float*** Range)

Get an input's range (V).

Corresponding server paths:

- DEV123/SIGINS/0/RANGE
- DEV123/SIGINS/1/RANGE
- ...

Parameters:

[in] Index
index of the parameter

[out] Range
Pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Range is NULL

See Also:

[ziRTKSigInSetRange](#), HF Signal Input

ziRTKSigInSetRange**ZI_STATUS ziRTKSigInSetRange (unsigned int Index, float Range)**

Set an input's range(V).

Corresponding server paths:

- DEV123/SIGINS/0/RANGE
- DEV123/SIGINS/1/RANGE
- ...

Parameters:

[in] Index
index of the parameter

[in] Range
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigInGetRange](#), HF Signal Input

ziRTKSigInGetAC

ZI_STATUS ziRTKSigInGetAC (**unsigned int** Index, **int*** Value)

Get an input's AC mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigInSetAC](#), [HF Signal Input](#)

ziRTKSigInSetAC

ZI_STATUS ziRTKSigInSetAC (unsigned int Index, int Value)

Set an input's AC mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigInGetAC](#), HF Signal Input

ziRTKSigInGetImp50

ZI_STATUS ziRTKSigInGetImp50 (**unsigned int** Index, **int*** Value)

Get an input's 50 Ohm mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigInSetImp50](#), HF Signal Input

ziRTKSigInSetImp50

ZI_STATUS ziRTKSigInSetImp50 (**unsigned int** Index, **int** Value)

Set an input's 50 Ohm mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigInGetImp50](#), HF Signal Input

ziRTKSigInGetDiff**ZI_STATUS** ziRTKSigInGetDiff (unsigned int Index, int* Value)

Get an input's differential mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigInSetDiff](#), [HF Signal Input](#)

ziRTKSigInSetDiff

ZI_STATUS **ziRTKSigInSetDiff** (**unsigned int** Index, **int** Value)

Set an input's differential mode.

Corresponding server paths:

- DEV123/SIGINS/0/AC
- DEV123/SIGINS/1/AC
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigInGetDiff](#), [HF Signal Input](#)

ziRTKOscGetCount

unsigned int ziRTKOscGetCount ()

get the count of oscillators.

Returns:

The count of oscillators

See Also:

[Oscillators](#)

ziRTKOscGetFreq**ZI_STATUS ziRTKOscGetFreq (unsigned int Index, float* Freq)**

Get an oscillator's frequency.

Corresponding server paths:

- DEV123/OSCS/0/FREQ
- DEV123/OSCS/1/FREQ
- ...

Parameters:

[in] Index
index of the parameter

[out] Freq
Pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Freq is NULL

See Also:

[ziRTKOscSetFreq](#), [Oscillators](#)

ziRTKOscSetFreq

ZI_STATUS ziRTKOscSetFreq (unsigned int Index, float Freq)

Set an oscillator's frequency.

Corresponding server paths:

- DEV123/OSCS/0/FREQ
- DEV123/OSCS/1/FREQ
- ...

Parameters:

[in] Index
index of the parameter

[in] Freq
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKOscGetFreq](#), Oscillators

ziRTKDemodGetCount

unsigned int ziRTKDemodGetCount ()

get the count of demodulators.

Returns:

The count of demodulators

See Also:

[Demodulator](#)

ziRTKDemodGetADCSelect

ZI_STATUS ziRTKDemodGetADCSelect (unsigned int Index, int* ADCSelect)

Get a demodulator's adcselect.

Corresponding server paths:

- DEV123/DEMOS/0/ADCSELECT
- DEV123/DEMOS/1/ADCSELECT
- ...

Note:

ADCSelect is only available with installed Multi-frequency Option.

Parameters:

[in] Index
index of the Parameter

[out] ADCSelect
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when ADCSelect is NULL

See Also:

[ziRTKDemodSetADCSelect](#), [Demodulator](#)

ziRTKDemodSetADCSelect

ZI_STATUS ziRTKDemodSetADCSelect (**unsigned int** Index, **int** ADCSelect)

Set a demodulator's ADC select.

Corresponding server paths:

- DEV123/DEM0DS/0/ADCSELECT
- DEV123/DEM0DS/1/ADCSELECT
- ...

Note:

ADCSelect is only available with installed Multi-frequency Option.

Parameters:

[in] Index
index of the Parameter

[in] ADCSelect
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetADCSelect](#), [Demodulator](#)

ziRTKDemodGetOscSelect

ZI_STATUS ziRTKDemodGetOscSelect (**unsigned int** Index, **int*** OscSelect)

Get a demodulator's oscillator select.

Corresponding server paths:

- DEV123/DEMODS/0/OSCSELECT
- DEV123/DEMODS/1/OSCSELECT
- ...

Note:

OscSelect is only available with installed Multi-frequency Option.

Parameters:

[in] Index
index of the Parameter

[out] OscSelect
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when ADCSelect is NULL

See Also:

[ziRTKDemodSetOscSelect](#), [Demodulator](#)

ziRTKDemodSetOscSelect

ZI_STATUS **ziRTKDemodSetOscSelect** (**unsigned int** **Index**, **int** **OscSelect**)

Set a demodulator's oscillator select.

Corresponding server paths:

- DEV123/DEMODS/0/OSCSELECT
- DEV123/DEMODS/1/OSCSELECT
- ...

Note:

OscSelect is only available when the Multi-frequency Option is installed.

Parameters:

[in] **Index**
index of the Parameter

[in] **OscSelect**
int providing the value

Returns:

- **ZI_SUCCESS** on success
- **ZI_OUTOFRANGE** when index is out of range

See Also:

[ziRTKDemodGetOscSelect](#), Demodulator

ziRTKDemodGetOrder

ZI_STATUS ziRTKDemodGetOrder (**unsigned int** Index, **int*** Order)

Get a demodulator's filter order.

Corresponding server paths:

- DEV123/DEMODS/0/ORDER
- DEV123/DEMODS/1/ORDER
- ...

Parameters:

[in] Index
index of the Parameter

[out] Order
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Order is NULL

See Also:

[ziRTKDemodSetOrder](#), Demodulator

ziRTKDemodSetOrder

ZI_STATUS ziRTKDemodSetOrder (**unsigned int** Index, **int** Order)

Set a demodulator's filter order.

Corresponding server paths:

- DEV123/DEMODS/0/ORDER
- DEV123/DEMODS/1/ORDER
- ...

Parameters:

[in] Index
index of the Parameter

[in] Order
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetOrder](#), Demodulator

ziRTKDemodGetHarmonic

ZI_STATUS ziRTKDemodGetHarmonic (**unsigned int** Index, **int*** Harmonic)

Get a demodulator's harmonic.

Corresponding server paths:

- DEV123/DEMOS/0/ORDER
- DEV123/DEMOS/1/ORDER
- ...

Parameters:

[in] Index
index of the Parameter

[out] Harmonic
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Harmonic is NULL

See Also:

[ziRTKDemodSetHarmonic](#), Demodulator

ziRTKDemodSetHarmonic

ZI_STATUS ziRTKDemodSetHarmonic (**unsigned int** Index, **int** Harmonic)

Set a demodulator's harmonic.

Corresponding server paths:

- DEV123/DEMOS/0/ORDER
- DEV123/DEMOS/1/ORDER
- ...

Parameters:

[in] Index
index of the Parameter

[in] Harmonic
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetHarmonic](#), Demodulator

ziRTKDemodGetRate

ZI_STATUS ziRTKDemodGetRate (**unsigned int** Index, **float*** Rate)

Get a demodulator's rate.

Corresponding server paths:

- DEV123/DEMODS/0/RATE
- DEV123/DEMODS/1/RATE
- ...

Parameters:

[in] Index
index of the Parameter

[out] Rate
pointer to a float to write the value in (in Hertz)

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Rate is NULL

See Also:

[ziRTKDemodSetRate](#), [Demodulator](#)

ziRTKDemodSetRate

ZI_STATUS ziRTKDemodSetRate (**unsigned int** Index, **float** Rate)

Set a demodulator's rate.

Corresponding server paths:

- DEV123/DEMODS/0/RATE
- DEV123/DEMODS/1/RATE
- ...

Parameters:

[in] Index
index of the parameter

[in] Rate
float providing the value (in Hertz)

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetRate](#), Demodulator

ziRTKDemodGetTrigger**ZI_STATUS ziRTKDemodGetTrigger (unsigned int Index, unsigned int* Trigger)**

Get a demodulator's trigger- and gating-settings.

Corresponding server paths:

- DEV123/DEMODS/0/trigger
- DEV123/DEMODS/1/trigger
- ...

Parameters:

[in] Index
index of the Parameter

[out] Trigger
pointer to an unsigned int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Trigger is NULL

See Also:

[ziRTKDemodSetTrigger](#), [Demodulator](#)

ziRTKDemodSetTrigger

ZI_STATUS ziRTKDemodSetTrigger (**unsigned int** Index, **unsigned int** Trigger)

Set a demodulator's trigger- and gating-settings.

Corresponding server paths:

- DEV123/DEMODS/0/TRIGGER
- DEV123/DEMODS/1/TRIGGER
- ...

Parameters:

[in] Index
index of the parameter

[in] Trigger
unsigned int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetTrigger](#), Demodulator

ziRTKDemodGetPhaseShift

ZI_STATUS ziRTKDemodGetPhaseShift (unsigned int Index, float* PhaseShift)

Get a demodulator's phaseshift.

Corresponding server paths:

- DEV123/DEMODS/0/PHASESHIFT
- DEV123/DEMODS/1/PHASESHIFT
- ...

Parameters:

[in] Index
index of the Parameter

[out] PhaseShift
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when PhaseShift is NULL

See Also:

[ziRTKDemodSetPhaseShift](#), Demodulator

ziRTKDemodSetPhaseShift

ZI_STATUS ziRTKDemodSetPhaseShift (**unsigned int** Index, **float** PhaseShift)

Set a demodulator's phaseshift.

Corresponding server paths:

- DEV123/DEMODS/0/PHASESHIFT
- DEV123/DEMODS/1/PHASESHIFT
- ...

Parameters:

[in] Index
index of the parameter

[in] PhaseShift
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetPhaseShift](#), Demodulator

ziRTKDemodGetTimeConstant**ZI_STATUS ziRTKDemodGetTimeConstant (unsigned int Index, float* TimeConstant)**

Get a demodulator's timeconstant.

Corresponding server paths:

- DEV123/DEMODS/0/TIMECONSTANT
- DEV123/DEMODS/1/TIMECONSTANT
- ...

Parameters:

[in] Index
index of the Parameter

[out] TimeConstant
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when TimeConstant is NULL

See Also:

[ziRTKDemodSetTimeConstant](#), [Demodulator](#)

ziRTKDemodSetTimeConstant

ZI_STATUS ziRTKDemodSetTimeConstant (**unsigned int** Index, **float** TimeConstant)

Set a demodulator's timeconstant.

Corresponding server paths:

- DEV123/DEMODS/0/TIMECONSTANT
- DEV123/DEMODS/1/TIMECONSTANT
- ...

Parameters:

[in] Index
index of the parameter

[in] TimeConstant
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetTimeConstant](#), Demodulator

ziRTKDemodGetSinc

ZI_STATUS ziRTKDemodGetSinc (**unsigned int** Index, **unsigned int*** Value)

Get a demodulator's sinc.

Corresponding server paths:

- DEV123/DEMODY/0/SINC
- DEV123/DEMODY/1/SINC
- ...

Parameters:

[in] Index
index of the Parameter

[out] Value
pointer to an unsigned int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when TimeConstant is NULL

See Also:

[ziRTKDemodSetSinc](#), [Demodulator](#)

ziRTKDemodSetSinc

ZI_STATUS ziRTKDemodSetSinc (**unsigned int** Index, **unsigned int** Value)

Set a demodulator's sinc.

Corresponding server paths:

- DEV123/DEMODS/0/SINC
- DEV123/DEMODS/1/SINC
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
unsigned int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKDemodGetSinc](#), Demodulator

ziRTKDemodGetSample

ZI_STATUS ziRTKDemodGetSample (**unsigned int** Index, **DemodSample*** Sample)

Retrieve a demodulator's sample.

Corresponding server paths:

- DEV123/DEMOS/0/SAMPLE
- DEV123/DEMOS/1/SAMPLE
- ...

Parameters:

[in] Index
index of the parameter

[out] Sample
DemodSample to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when the Sample is NULL

See Also:

[Demodulator](#)

ziRTKDemodSampleGetTimeStamp

ZI_STATUS ziRTKDemodSampleGetTimeStamp (**DemodSample*** Sample, **float*** Seconds)

Returns the timestamp of a [DemodSample](#) in Seconds.

Parameters:

[in] Sample
the sample to extract the value from

[out] Seconds
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Seconds is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetTimeStamp32](#), [ziRTKDemodSampleGetTimeStamp64](#), [Demodulator](#)

ziRTKDemodSampleGetTimeStamp32

ZI_STATUS ziRTKDemodSampleGetTimeStamp32 (**DemodSample*** Sample, **unsigned int*** TS)

Returns the timestamp of a [DemodSample](#) as 32bit unsigned int.

Note:

you may also read TS.TS32[ZIRTK_LSB_INDEX] of a [DemodSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetTimeStamp](#), [ziRTKDemodSampleGetTimeStamp64](#), [Demodulator](#)

ziRTKDemodSampleGetTimeStamp64

ZI_STATUS ziRTKDemodSampleGetTimeStamp64 (**DemodSample*** Sample, **unsigned long long*** TS)

Returns the timestamp of a [DemodSample](#) as 64 bit unsigned long long.

Note:

you may also read TS.TS64 of a [DemodSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetTimeStamp](#), [ziRTKDemodSampleGetTimeStamp32](#), [Demodulator](#)

ziRTKDemodSampleGetX

ZI_STATUS ziRTKDemodSampleGetX (**DemodSample*** Sample, **float*** X)

Returns the X-Value of a DemodSample as float.

Parameters:

[in] Sample
the sample to extract the value from

[out] X
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#), [ziRTKDemodSampleGetCompX](#),
[ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetX32

ZI_STATUS ziRTKDemodSampleGetX32 (**DemodSample*** Sample, **unsigned int*** X)

Returns the X-Value of a **DemodSample** as unsigned int.

Note:

you may also read X.Val32[ZIRTK_MSB_INDEX] of a **DemodSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] X

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX64](#), [ziRTKDemodSampleGetCompX](#),
[ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetX64

ZI_STATUS ziRTKDemodSampleGetX64 (**DemodSample*** Sample, **unsigned long long*** X)

Returns the X-Value of a **DemodSample** as **unsigned long long**.

Note:

you may also read X.Val64 of a **DemodSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] X

pointer to an **unsigned long long** in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetCompX](#),
[ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetCompX

ZI_STATUS ziRTKDemodSampleGetCompX (**DemodSample*** Sample, **float*** X)

Returns the compensated X-Value of a **DemodSample**.

Parameters:

[in] Sample
the sample to extract the value from

[out] X
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetY

ZI_STATUS ziRTKDemodSampleGetY (**DemodSample*** Sample, **float*** Y)

Returns the Y-Value of a DemodSample as float.

Parameters:

[in] Sample
the sample to extract the value from

[out] Y
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Y is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetCompX](#), [ziRTKDemodSampleGetY32](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetY32

ZI_STATUS ziRTKDemodSampleGetY32 (**DemodSample*** Sample, **unsigned int*** Y)

Returns the Y-Value of a **DemodSample** as unsigned int.

Note:

you may also read Y.Val32[ZIERTK_MSB_INDEX] of a **DemodSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] Y

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Y is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetCompX](#), [ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY64](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetY64

ZI_STATUS ziRTKDemodSampleGetY64 (**DemodSample*** Sample, **unsigned long long*** Y)

Returns the Y-Value of a **DemodSample** as **unsigned long long**.

Note:

you may also read Y.Val64 of a **DemodSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] Y

pointer to an **unsigned long long** in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Y is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetCompX](#), [ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#),
[ziRTKDemodSampleGetCompY](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetCompY**ZI_STATUS ziRTKDemodSampleGetCompY (DemodSample* Sample, float* Y)**

Returns the compensated Y-Value of a DemodSample.

Parameters:

[in] Sample

the sample to extract the value from

[out] Y

pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Y is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetCompX](#), [ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#),
[ziRTKDemodSampleGetY64](#), [ziRTKDemodSampleGetCompXY](#), [Demodulator](#)

ziRTKDemodSampleGetCompXY

ZI_STATUS **ziRTKDemodSampleGetCompXY** (**DemodSample*** **Sample**, **float*** **X**, **float*** **Y**)

Returns the compensated X-Value and the compensated Y-Value of a DemodSample.

Parameters:

[in] **Sample**

the sample to extract the value from

[out] **X**

pointer to a float in which the X-value will be returned

[out] **Y**

pointer to a float in which the Y-value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when X is NULL or Y is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetX](#), [ziRTKDemodSampleGetX32](#), [ziRTKDemodSampleGetX64](#),
[ziRTKDemodSampleGetCompX](#), [ziRTKDemodSampleGetY](#), [ziRTKDemodSampleGetY32](#),
[ziRTKDemodSampleGetY64](#), [ziRTKDemodSampleGetCompY](#), [Demodulator](#)

ziRTKDemodSampleGetR

ZI_STATUS ziRTKDemodSampleGetR (**DemodSample*** Sample, **float*** R)

Returns the Radius-Value of a **DemodSample**.

Parameters:

[in] Sample
the sample to extract the value from

[out] R
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when R is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetCompR](#), [ziRTKDemodSampleGetTheta](#), [Demodulator](#)

ziRTKDemodSampleGetCompR

ZI_STATUS ziRTKDemodSampleGetCompR (**DemodSample*** Sample, **float*** R)

Returns the compensated Radius-Value of a DemodSample.

Parameters:

[in] Sample
the sample to extract the value from

[out] R
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when R is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetR](#), [ziRTKDemodSampleGetTheta](#), Demodulator

ziRTKDemodSampleGetTheta

ZI_STATUS ziRTKDemodSampleGetTheta (**DemodSample*** Sample, **float*** Theta)

Returns the Theta-Value of a [DemodSample](#).

Parameters:

[in] Sample
the sample to extract the value from

[out] Theta
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Theta is NULL or Sample is NULL

See Also:

[ziRTKDemodSampleGetR](#), [ziRTKDemodSampleGetCompR](#), [Demodulator](#)

ziRTKSigOutGetCount

unsigned int ziRTKSigOutGetCount ()

get the count of HF Signal outputs.

Returns:

The count of the signal outputs.

See Also:

[HF Signal Output](#)

ziRTKSigOutGetRange

ZI_STATUS ziRTKSigOutGetRange (**unsigned int** Index, **float*** Range)

Get an output's range (V).

returns an output's range.

Corresponding server paths:

- DEV123/SIGOUTS/0/RANGE
- DEV123/SIGOUTS/1/RANGE
- ...

Parameters:

[in] Index
index of the parameter

[out] Range
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range
- ZI_NULLPTR when Range is NULL

See Also:

[ziRTKSigOutSetRange](#), HF Signal Output

ziRTKSigOutSetRange

ZI_STATUS **ziRTKSigOutSetRange** (**unsigned int** **Index**, **float** **Range**)

Set an output's range (V).

sets an output's range.

Corresponding server paths:

- DEV123/SIGOUTS/0/RANGE
- DEV123/SIGOUTS/1/RANGE
- ...

Parameters:

[in] **Index**
index of the parameter

[in] **Range**
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when index is out of range

See Also:

[ziRTKSigOutGetRange](#), HF Signal Output

ziRTKSigOutGetChannelCount

unsigned int ziRTKSigOutGetChannelCount ()

get the count of HF Signal output Mixer Channels.

Returns:

The count of mixer channels

See Also:

[HF Signal Output](#)

ziRTKSigOutGetEnable

ZI_STATUS **ziRTKSigOutGetEnable** (**unsigned int** **Index**, **unsigned int** **Channel**, **int*** **Value**)

Get an output's mixer enable.

returns an output's mixer enable.

Corresponding server paths:

- DEV123/SIGOUTS/0/ENABLES/0
- DEV123/SIGOUTS/0/ENABLES/1
- ...
- DEV123/SIGOUTS/1/ENABLES/0
- DEV123/SIGOUTS/1/ENABLES/1
- ...

Parameters:

[in] **Index**
index of the signal output

[in] **Channel**
index of the mixer-channel

[out] **Value**
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Value is NULL
- ZI_OUTOFRANGE when Index is out of range or channel is out of range

See Also:

[ziRTKSigOutSetEnable](#), [HF Signal Output](#)

ziRTKSigOutSetEnable

ZI_STATUS **ziRTKSigOutSetEnable** (**unsigned int Index, unsigned int Channel, int Value**)

Set an output's mixer enable.

sets an output's mixer enable.

Corresponding server paths:

- DEV123/SIGOUTS/0/ENABLES/0
- DEV123/SIGOUTS/0/ENABLES/1
- ...
- DEV123/SIGOUTS/1/ENABLES/0
- DEV123/SIGOUTS/1/ENABLES/1
- ...

Parameters:

[in] Index
index of the signal output

[in] Channel
index of the mixer-channel

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range or channel is out of range

See Also:

[ziRTKSigOutGetEnable](#), [HF Signal Output](#)

ziRTKSigOutGetAmplitude

ZI_STATUS **ziRTKSigOutGetAmplitude** (**unsigned int** **Index**, **unsigned int** **Channel**, **float*** **Value**)

Get an output's mixer amplitude.

returns an output's mixer amplitude.

Corresponding server paths:

- DEV123/SIGOUTS/0/AMPLITUDES/0
- DEV123/SIGOUTS/0/AMPLITUDES/1
- ...
- DEV123/SIGOUTS/1/AMPLITUDES/0
- DEV123/SIGOUTS/1/AMPLITUDES/1
- ...

Parameters:

[in] **Index**
index of the signal output

[in] **Channel**
index of the mixer-channel

[out] **Value**
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Value is NULL
- ZI_OUTOFRANGE when Index is out of range or channel is out of range

See Also:

[ziRTKSigOutSetAmplitude](#), [HF Signal Output](#)

ziRTKSigOutSetAmplitude

ZI_STATUS **ziRTKSigOutSetAmplitude** (**unsigned int** **Index**, **unsigned int** **Channel**, **float** **Value**)

Set an output's mixer amplitude.

sets an output's mixer amplitude.

Corresponding server paths:

- DEV123/SIGOUTS/0/AMPLITUDES/0
- DEV123/SIGOUTS/0/AMPLITUDES/1
- ...
- DEV123/SIGOUTS/1/AMPLITUDES/0
- DEV123/SIGOUTS/1/AMPLITUDES/1
- ...

Parameters:

[in] Index
index of the signal output

[in] Channel
index of the mixer-channel

[in] Value
float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range or channel is out of range

See Also:

[ziRTKSigOutGetAmplitude](#), HF Signal Output

ziRTKSigOutGetOn

ZI_STATUS ziRTKSigOutGetOn (unsigned int Index, int* Value)

Get an output's on-switch.

Corresponding server paths:

- DEV123/SIGOUTS/0/ON
- DEV123/SIGOUTS/1/ON
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigOutSetOn](#), [HF Signal Output](#)

ziRTKSigOutSetOn

ZI_STATUS ziRTKSigOutSetOn (unsigned int Index, int Value)

Set an output's on-switch.

Corresponding server paths:

- DEV123/SIGOUTS/0/ON
- DEV123/SIGOUTS/1/ON
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigOutGetOn](#), [HF Signal Output](#)

ziRTKSigOutGetAdd

ZI_STATUS ziRTKSigOutGetAdd (unsigned int Index, int* Value)

Get an output's add mode.

Corresponding server paths:

- DEV123/SIGOUTS/0/ON
- DEV123/SIGOUTS/1/ON
- ...

Parameters:

[in] Index
index of the parameter

[out] Value
Pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[ziRTKSigOutSetAdd](#), HF Signal Output

ziRTKSigOutSetAdd

ZI_STATUS ziRTKSigOutSetAdd (**unsigned int** Index, **int** Value)

Set an output's add mode.

Corresponding server paths:

- DEV123/SIGOUTS/0/ON
- DEV123/SIGOUTS/1/ON
- ...

Parameters:

[in] Index
index of the parameter

[in] Value
int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKSigOutGetAdd](#), [HF Signal Output](#)

ziRTKAuxOutGetCount

unsigned int ziRTKAuxOutGetCount ()

get the count of auxiliary analog outputs.

Returns:

The count of auxiliary outputs

See Also:

[Auxiliary Analog Outputs](#)

ziRTKAuxOutGetValue

ZI_STATUS ziRTKAuxOutGetValue (unsigned int Index, float* Value)

Get a auxiliary outputs value as float (V).

Corresponding server paths:

- DEV123/AUXOUTS/0/VALUE
- DEV123/AUXOUTS/1/VALUE
- ...

Parameters:

[in] Index
index of the Parameter

[out] Value
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Value is NULL

See Also:

[Auxiliary Analog Outputs](#)

ziRTKAuxOutGetOutputSelect

ZI_STATUS ziRTKAuxOutGetOutputSelect (**unsigned int** Index, **int*** OutputSelect)

Get an auxiliary outputs outputselect.

Corresponding server paths:

- DEV123/AUXOUTS/0/OUTPUTSELECT
- DEV123/AUXOUTS/1/OUTPUTSELECT
- ...

Parameters:

[in] Index
index of the Parameter

[out] OutputSelect
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when OutputSelect is NULL

See Also:

[ziRTKAuxOutSetOutputSelect](#), Auxiliary Analog Outputs

ziRTKAuxOutSetOutputSelect

ZI_STATUS ziRTKAuxOutSetOutputSelect (**unsigned int** Index, **int** OutputSelect)

Set an auxiliary output's outputselect.

Corresponding server paths:

- DEV123/AUXOUTS/0/OUTPUTSELECT
- DEV123/AUXOUTS/1/OUTPUTSELECT
- ...

Parameters:

[in] Index
index of the Parameter

[out] OutputSelect
an int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutGetOutputSelect](#), Auxiliary Analog Outputs

ziRTKAuxOutGetDemodSelect

ZI_STATUS ziRTKAuxOutGetDemodSelect (**unsigned int** Index, **int*** DemodSelect)

Get an auxiliary output's demodselect.

Corresponding server paths:

- DEV123/AUXOUTS/0/DEMODOSELECT
- DEV123/AUXOUTS/1/DEMODOSELECT
- ...

Parameters:

[in] Index
index of the Parameter

[out] DemodSelect
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when DemodSelect is NULL

See Also:

[ziRTKAuxOutSetDemodSelect](#), [Auxiliary Analog Outputs](#)

ziRTKAuxOutSetDemodSelect

ZI_STATUS **ziRTKAuxOutSetDemodSelect** (**unsigned int Index, int DemodSelect**)

Set an auxiliary output's demodselect.

Corresponding server paths:

- DEV123/AUXOUTS/0/DEMODOSELECT
- DEV123/AUXOUTS/1/DEMODOSELECT
- ...

Parameters:

[in] Index
index of the Parameter

[out] DemodSelect
an int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutGetDemodSelect](#), Auxiliary Analog Outputs

ziRTKAuxOutGetOffset

ZI_STATUS ziRTKAuxOutGetOffset (**unsigned int** Index, **float*** Offset)

Get an auxiliary output's offset.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] Index
index of the Parameter

[out] Offset
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Offset is NULL

See Also:

[ziRTKAuxOutSetOffset](#), [ziRTKAuxOutSetOffsetNoUpdate](#), [ziRTKAuxOutSetOffsetInt](#),
[ziRTKAuxOutSetOffsetIntNoUpdate](#), [Auxiliary Analog Outputs](#)

ziRTKAuxOutSetOffset

ZI_STATUS **ziRTKAuxOutSetOffset (unsigned int Index, float Offset)**

Set an auxiliary output's offset.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] Index
index of the Parameter

[out] Offset
a float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutSetOffsetNoUpdate](#), [ziRTKAuxOutSetOffsetInt](#), [ziRTKAuxOutSetOffsetIntNoUpdate](#),
[ziRTKAuxOutGetOffset](#), Auxiliary Analog Outputs

ziRTKAuxOutSetOffsetNoUpdate

ZI_STATUS **ziRTKAuxOutSetOffsetNoUpdate** (**unsigned int Index, float Offset**)

Set an auxiliary output's offset without updating ziServer.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] Index
index of the Parameter

[out] Offset
a float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutSetOffset](#), [ziRTKAuxOutSetOffsetInt](#), [ziRTKAuxOutSetOffsetIntNoUpdate](#),
[ziRTKAuxOutGetOffset](#), Auxiliary Analog Outputs

ziRTKAuxOutSetOffsetInt

ZI_STATUS **ziRTKAuxOutSetOffsetInt** (**unsigned int** **Index**, **int** **Offset**)

Set an auxiliary output's offset as integer.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] **Index**
index of the Parameter

[out] **Offset**
an integer providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutSetOffsetInt32](#), [ziRTKAuxOutSetOffset](#), [ziRTKAuxOutSetOffsetNoUpdate](#),
[ziRTKAuxOutGetOffset](#), [Auxiliary Analog Outputs](#)

ziRTKAuxOutSetOffsetIntNoUpdate**ZI_STATUS ziRTKAuxOutSetOffsetIntNoUpdate (unsigned int Index, int Offset)**

Set an auxiliary output's offset as integer without updating ziServer.

Corresponding server paths:

- DEV123/AUXOUTS/0/OFFSET
- DEV123/AUXOUTS/1/OFFSET
- ...

Parameters:

[in] Index
index of the Parameter

[out] Offset
an integer providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutSetOffsetInt](#), [ziRTKAuxOutSetOffset](#), [ziRTKAuxOutSetOffsetNoUpdate](#),
[ziRTKAuxOutGetOffset](#), Auxiliary Analog Outputs

ziRTKAuxOutGetScale

ZI_STATUS ziRTKAuxOutGetScale (**unsigned int** Index, **float*** Scale)

Get an auxiliary output's scale.

Corresponding server paths:

- DEV123/AUXOUTS/0/SCALE
- DEV123/AUXOUTS/1/SCALE
- ...

Parameters:

[in] Index
index of the Parameter

[out] Scale
pointer to a float to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Scale is NULL

See Also:

[ziRTKAuxOutSetScale](#), Auxiliary Analog Outputs

ziRTKAuxOutSetScale

ZI_STATUS ziRTKAuxOutSetScale (**unsigned int** Index, **float** Scale)

Set an auxiliary output's scale.

Corresponding server paths:

- DEV123/AUXOUTS/0/SCALE
- DEV123/AUXOUTS/1/SCALE
- ...

Parameters:

[in] Index
index of the Parameter

[out] Scale
a float providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxOutGetScale](#), Auxiliary Analog Outputs

ziRTKAuxInGetCount

unsigned int ziRTKAuxInGetCount ()

get the count of Auxiliary Analog Inputs.

Returns:

The count of auxiliary inputs

See Also:

[Auxiliary Analog Inputs](#)

ziRTKAuxInGetAveraging

ZI_STATUS ziRTKAuxInGetAveraging (**unsigned int** Index, **int*** Averaging)

Get a auxiliary input's averaging/sample rate.

Corresponding server path:

- DEV123/AUXINS/0/AVERAGING

See [Aux Analog Inputs](#) for details.

Parameters:

[in] Index
index of the Parameter

[out] Averaging
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Averaging is NULL

See Also:

[ziRTKAuxInSetAveraging](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInSetAveraging

ZI_STATUS ziRTKAuxInSetAveraging (**unsigned int** Index, **int** Averaging)

Set an auxiliary output's averaging sample rate.

Corresponding server path:

- DEV123/AUXINS/0/AVERAGING

See [Aux Analog Inputs](#) for details.

Parameters:

[in] Index
index of the Parameter

[out] Averaging
value to set

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKAuxInGetAveraging](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInGetSample

ZI_STATUS ziRTKAuxInGetSample (**unsigned int** Index, **AuxInSample*** Sample)

retrieve a auxiliary input sample

Corresponding server path:

■ DEV123/AUXINS/0/SAMPLE

Parameters:

[in] Index

index of the Parameter

[in] Sample

AuxInSample to write the value in.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Sample is NULL

See Also:

[Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetTimeStamp**ZI_STATUS ziRTKAuxInSampleGetTimeStamp ([AuxInSample*](#) Sample, [float*](#) Seconds)**

Returns the timestamp of an [AuxInSample](#) in Seconds.

Parameters:

[in] Sample

the sample to extract the value from

[out] Seconds

pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Seconds is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetTimeStamp32](#), [ziRTKAuxInSampleGetTimeStamp64](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetTimeStamp32

ZI_STATUS ziRTKAuxInSampleGetTimeStamp32 (**AuxInSample*** Sample, **unsigned int*** TS)

Returns the timestamp of an [AuxInSample](#) as 32bit unsigned int.

Note:

you may also read TS.TS32[ZIRTK_LSB_INDEX] of a [AuxInSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetTimeStamp](#), [ziRTKAuxInSampleGetTimeStamp64](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetTimeStamp64

ZI_STATUS ziRTKAuxInSampleGetTimeStamp64 ([AuxInSample](#)* Sample, **unsigned long long*** TS)

Returns the timestamp of an [AuxInSample](#) as 64 bit unsigned long long.

Note:

you may also read TS.TS64 of a [AuxInSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned long long in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetTimeStamp](#), [ziRTKAuxInSampleGetTimeStamp32](#), [Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetValue

ZI_STATUS **ziRTKAuxInSampleGetValue** (**AuxInSample*** Sample, **unsigned int** Channel, **float*** Value)

Returns a Value of a **AuxInSample**.

Parameters:

[in] Sample
the sample to extract the value from

[in] Channel
the channel to retrieve the value from

[out] Value
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when channel is out of range
- ZI_NULLPTR when Value is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetValue16, Auxiliary Analog Inputs](#)

ziRTKAuxInSampleGetValue16

ZI_STATUS ziRTKAuxInSampleGetValue16 (**AuxInSample*** Sample, **unsigned int** Channel, **short*** Value)

Returns a Value of a **AuxInSample** as short.

Parameters:

[in] Sample
the sample to extract the value from

[in] Channel
the channel to retrieve the value from

[out] Value
pointer to a short in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when channel is out of range
- ZI_NULLPTR when Value is NULL or Sample is NULL

See Also:

[ziRTKAuxInSampleGetValue](#), [Auxiliary Analog Inputs](#)

ziRTKDIOGetCount

unsigned int ziRTKDIOGetCount ()

get the count of Digital IO.

Returns:

The count of the DIO

See Also:

[Digital Input/Output](#)

ziRTKDIOGetExtClk

ZI_STATUS ziRTKDIOGetExtClk (unsigned int Index, int* ExtClk)

Get a DIO's External-Clock setting.

Corresponding server path:

■ DEV123/DIOS/0/EXTCLK

Parameters:

[in] Index
index of the Parameter

[out] ExtClk
pointer to an int to write the value in

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range
■ ZI_NULLPTR when ExtClk is NULL

See Also:

[ziRTKDIOSetExtClk](#), Digital Input/Output

ziRTKDIOSetExtClk

ZI_STATUS **ziRTKDIOSetExtClk** (**unsigned int Index, int ExtClk**)

Set a DIO's External-Clock setting.

Corresponding server path:

■ DEV123/DIOS/0/EXTCLK

Parameters:

[in] Index
index of the Parameter

[in] ExtClk
int providing the value

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOGetExtClk](#), [Digital Input/Output](#)

ziRTKDIOGetDecimation

ZI_STATUS ziRTKDIOGetDecimation (**unsigned int** Index, **int*** Decimation)

Get a DIO's Decimation.

Corresponding server path:

■ DEV123/DIOS/0/DECIMATION

Parameters:

[in] Index
index of the Parameter

[out] Decimation
pointer to an int to write the value in

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range
■ ZI_NULLPTR when Decimation is NULL

See Also:

[ziRTKDIOSetDecimation](#), [Digital Input/Output](#)

ziRTKDIOSetDecimation

ZI_STATUS ziRTKDIOSetDecimation (**unsigned int** Index, **int** Decimation)

Set a DIO's Decimation.

Corresponding server path:

■ DEV123/DIOS/0/DECIMATION

Parameters:

[in] Index
index of the Parameter

[in] Decimation
int providing the value

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOGetDecimation](#), [Digital Input/Output](#)

ziRTKDIOGetDrive

ZI_STATUS ziRTKDIOGetDrive (unsigned int Index, int* Drive)

Get a DIO's Drive.

Corresponding server path:

■ DEV123/DIOS/0/DRIVE

Parameters:

[in] Index
index of the Parameter

[out] Drive
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Drive is NULL

See Also:

[ziRTKDIOSetDrive](#), [Digital Input/Output](#)

ziRTKDIOSetDrive

ZI_STATUS ziRTKDIOSetDrive (unsigned int Index, int Drive)

Set a DIO's Drive.

Corresponding server path:

■ DEV123/DIOS/0/DRIVE

Parameters:

[in] Index
index of the Parameter

[in] Drive
int providing the value

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOGetDrive](#), [Digital Input/Output](#)

ziRTKDIOGetOutput

ZI_STATUS ziRTKDIOGetOutput (unsigned int Index, unsigned int* Output)

Get a DIO's Output.

Corresponding server path:

— DEV123/DIOS/0/OUTPUT

Parameters:

[in] Index
index of the Parameter

[out] Output
pointer to an int to write the value in

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Output is NULL

See Also:

[ziRTKDIOSetOutput](#), Digital Input/Output

ziRTKDIOSetOutput

ZI_STATUS ziRTKDIOSetOutput (**unsigned int** Index, **unsigned int** Output)

Set a DIO's Output.

Corresponding server path:

■ DEV123/DIOS/0/OUTPUT

Parameters:

[in] Index
index of the Parameter

[in] Output
unsigned int providing the value

Returns:

■ ZI_SUCCESS on success
■ ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOGetOutput](#), [ziRTKDIOSetOutputNoUpdate](#), [Digital Input/Output](#)

ziRTKDIOSetOutputNoUpdate

ZI_STATUS ziRTKDIOSetOutputNoUpdate (**unsigned int** Index, **unsigned int** Output)

Set a DIO's Output without updating ziServer.

Corresponding server path:

- DEV123/DIOS/0/OUTPUT

Parameters:

[in] Index
index of the Parameter

[in] Output
unsigned int providing the value

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[ziRTKDIOSetOutput](#), [Digital Input/Output](#)

ziRTKDIOGetSample

ZI_STATUS ziRTKDIOGetSample (**unsigned int** Index, **DIOSample*** Sample)

retrieve a dio sample

Corresponding server path:

■ DEV123/DIOS/0/INPUT

Parameters:

[in] Index

index of the Parameter

[in] Sample

DIOSample to write the value in.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range
- ZI_NULLPTR when Sample is NULL

See Also:

[Digital Input/Output](#)

ziRTKDIOSampleGetTimeStamp

ZI_STATUS ziRTKDIOSampleGetTimeStamp (**DIOSample*** Sample, **float*** Seconds)

Returns the timestamp of a **DIOSample** in Seconds.

Parameters:

[in] Sample
the sample to extract the value from

[out] Seconds
pointer to a float in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Seconds is NULL or Sample is NULL

See Also:

[ziRTKDIOGetSample](#), [ziRTKDIOSampleGetTimeStamp32](#), [ziRTKDIOSampleGetTimeStamp64](#),
Digital Input/Output

ziRTKDIOSampleGetTimeStamp32

ZI_STATUS ziRTKDIOSampleGetTimeStamp32 (**DIOSample*** Sample, **unsigned int*** TS)

Returns the timestamp of a **DIOSample** as 32bit unsigned int.

Note:

you may also read TS.TS32[ZIRTK_LSB_INDEX] of a **DIOSample** to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKDIOSampleGetTimeStamp](#), [ziRTKDIOSampleGetTimeStamp64](#),
[Digital Input/Output](#)

ziRTKDIOSampleGetTimeStamp64

ZI_STATUS ziRTKDIOSampleGetTimeStamp64 (**DIOSample*** Sample, **unsigned long long*** TS)

Returns the timestamp of a [DIOSample](#) as 64 bit unsigned long long.

Note:

you may also read TS.TS64 of a [DIOSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] TS

pointer to an unsigned long long in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when TS is NULL or Sample is NULL

See Also:

[ziRTKDIOGetSample](#), [ziRTKDIOSampleGetTimeStamp](#), [ziRTKDIOSampleGetTimeStamp32](#),
Digital Input/Output

ziRTKDIOSampleGetBits

ZI_STATUS ziRTKDIOSampleGetBits (**DIOSample*** Sample, **unsigned int*** Bits)

Returns the Bits of a [DIOSample](#).

Note:

you may also read Bits[0] of a [DIOSample](#) to get the same result

Parameters:

[in] Sample

the sample to extract the value from

[out] Bits

pointer to an unsigned int in which the value will be returned

Returns:

- ZI_SUCCESS on success
- ZI_NULLPTR when Bits is NULL or Sample is NULL

See Also:

[ziRTKDIOGetSample](#), [Digital Input/Output](#)

ziRTKPLLSetADCSelect

ZI_STATUS ziRTKPLLSetADCSelect (**unsigned int** Index, **int** ADCSelect)

Set the ADCSELECT value of a PLL.

Returns:

- ZI_SUCCESS on success
- ZI_OUTOFRANGE when Index is out of range

See Also:

[PLL](#)

Chapter 9. Specifications

Important

Unless otherwise stated, all specifications apply after 30 minutes of instrument warm-up.

Important

Important changes in the specification parameters are explicitly noted in the revision history of this document.

9.1. General Specifications

Table 9.1. General and storage

Parameter	min	typ	max
storage temperature	-25 °C	-	65 °C
storage relative humidity (non-condensing)	-	-	95%
operating temperature	5 °C	-	40 °C
operating relative humidity (non-condensing)	-	-	90%
specification temperature	18 °C	-	28 °C
power consumption	-	-	60 W
operating environment	IEC61010, indoor location, installation category II, pollution degree 2		
operating altitude	up to 2000 meters		
power supply AC line	110–120/220–240 V, 50/60 Hz		
power supply Japan	requires external 100 V to 110 V transformer (50/60 Hz) for operation according to specification		
dimensions with handles and feet	45 x 34 x 10 cm, 17.7 x 13.6 x 4.0 inch, 19 inch rack compatible		
weight	6.2 kg		
recommended calibration interval	2 years		

Table 9.2. Maximum ratings

Parameter	min	typ	max
damage threshold HF inputs (Input 1, Input 2)	-5 V	-	5 V
damage threshold HF outputs (Output 1, Output 2)	-12 V	-	12 V
damage threshold Add inputs (Add 1, Add 2)	-12 V	-	12 V
damage threshold Sync output (Sync 1, Sync 2)	-12 V	-	12 V
damage threshold auxiliary outputs	-12 V	-	12 V
damage threshold auxiliary inputs	-12 V	-	12 V
damage threshold digital I/O (including DIO 0 and DIO 1 BNC connectors)	0 V	-	5 V
damage threshold Clock input	0 V	-	5 V

Table 9.3. Host system requirements

Parameter	Description
supported Windows operating systems	32-bit and 64-bit versions of Windows 10, 8.1, 7, XP ¹
supported Linux distributions	Ubuntu 16.04 (AMD64), 14.04 LTS (AMD64, i386), 64-bit

Parameter	Description
	systems require the IA32 package (install with <code>sudo apt-get install ia32-libs</code>)
minimum processor requirements	AMD K8 (Athlon 64), Intel Pentium 4
minimum memory requirements	1 GB RAM, 2 GB recommended
list of supported processors (requiring SSE2)	AMD K8 (Athlon 64, Sempron 64, Turion 64, etc.), AMD Phenom, Intel Pentium 4, Xeon Celeron, Celeron D, Pentium M, Celeron M, Core, Core 2, Core i5, Core i7, Atom

¹Software version is available for download but not officially supported.

Table 9.4. Maximum sample readout rate

Active demodulators	Maximum sample readout rate	Comments
1	460 kSamples/s	To achieve highest rates, it is advised to remove all other data transfer that loads the USB. It is recommended to check the sample loss flag (in the status tab) from time to time when using high readout rate settings.
2 – 3	230 kSamples/s	
4 – 6	115 kSamples/s	
7 – 8	57 kSamples/s	

Note

The sample readout rate is the rate at which demodulated samples are transferred from the Instrument to the host computer. This rate has to be set to at least 2 times the signal bandwidth of the related demodulator in order to satisfy the Nyquist sampling theorem. As the total rate is limited by the USB interface, the maximum rate becomes smaller when the number of active demodulators is increased. This is summarized in the table above for HF2LI / HF2PLL (6 demodulators) and HF2IS (8 demodulators). An up-to-date and performing host computer is required to achieve these rates.

9.2. Analog Interface Specifications

Table 9.5. HF signal inputs

Parameter	min	typ	max
connectors	front-panel single-ended/differential BNC		
input impedance (low value)	–	50 Ω	–
input impedance (high value)	500 kΩ	1 MΩ	–
input frequency range	0.7 μHz	–	50 MHz
input A/D conversion	14 bit, 210 MSamples/s		
input noise amplitude (> 10 kHz, AC coupling, 50 Ω and 1 MΩ), for detailed information see Figure 9.5	–	5 nV/√Hz	–
input amplitude accuracy (5 MHz), for detailed information see Figure 9.10	–	–	5%
input amplitude accuracy (10 MHz), for detailed information see Figure 9.10	–	–	10%
input amplitude stability	–	–	0.2 %/°C
input DC offset (<1 V input range)	–	–	20 mV
input DC offset (>1 V input range)	–	–	2%
input bias current	–	–	6 μA
input range settings	1 mV	–	1.5 V
input full range sensitivity (10 V lock-in amplifier output)	1 nV	–	1.5 V
input range (AC) with AC coupling	–0.6 V	–	0.6 V
input range (AC) with DC coupling	–1.5 V	–	1.5 V
input range (common mode)	–3.0 V	–	3.0 V
input range (AC + common mode)	–3.3 V	–	3.3 V
dynamic reserve	–	100 dB	120 dB
common mode rejection (CMRR), for detailed information see Figure 9.9	–	75 dB	–
AC coupling cutoff frequency	–	1 kHz	–

Table 9.6. Reference

Parameter	min	typ	max
internal reference frequency range	0.7 μHz	–	100 MHz
internal reference frequency resolution	0.7 μHz	–	–
internal reference phase range	–180 °	–	180 °
internal reference phase resolution	0.1 μ°	–	–
internal reference acquisition time (lock time)	instantaneous		
internal reference orthogonality	–	0 °	–
external reference at Input 2/Ref, signal type	arbitrary, active at rising edge		
external reference at Input 2/Ref, frequency range	1 Hz	–	50 MHz

Parameter	min	typ	max
external reference at Input 2/Ref, amplitude – note: for low-swing input signals the gain should be set to full-swing range to achieve best performance	100 mV	–	1 V
external reference at Input 2/Ref, amplitude (using HF2LI-PLL option) – note: for low-swing input signals the gain should be set to full-swing range to achieve best performance	10 mV	–	1 V
external reference at Input 2/Ref, reference acquisition time (lock time)	–	–	100 reference cycles or 1.2 ms whatever is larger
external reference at DIO0/DIO 1, signal type	digital TTL versus ground		
external reference at DIO0/DIO1, frequency range	1 Hz	–	2 MHz
external reference at DIO0/DIO1, high level	2.0 V	–	5 V
external reference at DIO0/DIO1, low level	0 V	–	0.8 V
external reference at DIO0/DIO1, reference acquisition time (lock time)	–	–	100 reference cycles or 1.2 ms whatever is larger
external reference at AUXIN1/AUXIN2, signal type	sine or rectangular		
external reference at AUXIN1/AUXIN2, frequency range	1 Hz	–	20 kHz
external reference at AUXIN1/AUXIN2, amplitude	0.5 V	–	1 V
external reference at AUXIN1/AUXIN2, reference acquisition time (lock time)	–	–	100 reference cycles
auto reference at Input 1/Input 2, signal type	AC signal with zero crossings, AC input setting		
auto reference at Input 1/Input 2, frequency range	1 Hz	–	50 MHz
auto reference at Input 1/Input 2, reference acquisition time (lock time)	–	–	100 reference cycles or 1.2 ms whatever is larger

Table 9.7. Demodulators

Parameter	Description
demodulator number	HF2IS: 4 dual-phase, 8 dual-phase with multi-frequency kit
	HF2LI: 6
	HF2PLL: 6
demodulator harmonic setting range	1 to 1023

Parameter	Description
demodulator filter time constant	0.8 μ s to 580 s
demodulator filter slope / roll-off	6, 12, 18, 24, 30, 36, 42, 48 dB/oct, consisting of up to 8 cascaded critical damping filters
demodulator output resolution	X, Y, R, Θ with 64-bit resolution
demodulator output rate (readout rate), for detailed specifications refer to Table 9.4	on Aux outputs: 921 kSamples/s
	on USB to host PC: maximum cumulative 700 kSamples/s
demodulator measurement bandwidth	80 μ Hz to 200 kHz
demodulator harmonic rejection	max -90 dB
demodulator sinc filter operating range	0.1 Hz to 10 kHz

Table 9.8. HF signal outputs

Parameter	min	typ	max
connectors	front-panel single-ended BNC		
output impedance (Out and Sync)	50 Ω		
input impedance (Add)	1 M Ω		
output frequency range	DC	-	50 MHz
output frequency range with 10 V amplitude. see also Figure 9.11	DC	-	5 MHz
output D/A conversion	16 bit, 210 MSamples/s		
output amplitude ranges (restrictions apply for high amplitudes and high frequencies, see Figure 9.11)	± 10 mV, ± 100 mV, ± 1 V, ± 10 V		
output maximum current	-	-	100 mA
output amplitude accuracy @ 3 MHz, < 5 V (restrictions apply for high amplitudes and high frequencies, see Figure 9.11)	-	-	1%
output total harmonic distortion THD (1 V, < 10 MHz), see Figure 9.12	-50 dB	-	-
output total harmonic distortion THD (0.1 V, < 10 MHz), see Figure 9.12	-60 dB	-	-
output noise amplitude (frequencies > 10 kHz), 50 Ω termination	-	25 nV/ $\sqrt{\text{Hz}}$	-
output phase noise @ 10 MHz, BW = 0.67 Hz, offset 100 Hz	-100 dBc/Hz	-	-
output phase noise @ 10 MHz, BW = 0.67 Hz, offset 1 kHz	-120 dBc/Hz	-	-
output offset amplitude (range setting < 1 V)	-	-	10 mV
output offset amplitude (range setting > 1 V)	-	-	200 mV
input Add signal range	-10 V	-	+10 V
input Add signal bandwidth	DC	-	50 MHz
output Sync signal range (effective range = $\pm 1 \times \text{set_amplitude} / \text{set_range}$)	-1 V	-	1 V

Parameter	min	typ	max
output synchronization signal resolution	–	30 μ V	–

Table 9.9. Auxiliary Inputs and Outputs

Parameter	Description
auxiliary output connectors	front-panel single-ended BNC
auxiliary output impedance	50 Ω
auxiliary output number and type of signals	4, amplitude, phase, frequency, X/Y, manual
auxiliary output specification	± 10 V, 200 kHz, 16-bit, 921 kSamples/s
auxiliary output resolution	0.3 mV
auxiliary input connectors	back-panel single-ended BNC
auxiliary input impedance	1 M Ω
auxiliary input number	2
auxiliary input specification	± 10 V, 100 kHz, 16-bit, 400 kSamples/s
auxiliary input resolution	0.3 mV
group delay (lag time from HF input to auxiliary output)	7 μ s (typical), 10 μ s (maximum)

Table 9.10. Oscillator and clocks

Parameter	min	typ	max
internal oscillator frequency	–	10 MHz	–
internal oscillator output (sine)	–1 V	–	+1 V
internal oscillator initial accuracy (serial number HF2-DEV1141 and lower) ¹	–	–	± 30 ppm
internal oscillator aging (stability; serial number HF2-DEV1141 and lower) ¹	–	–	± 5 ppm/year
internal oscillator temperature stability ($23^\circ \pm 5^\circ$; serial number HF2-DEV1141 and lower) ¹	–	–	± 30 ppm
internal oscillator initial accuracy (serial number HF2-DEV1142 and higher) ²	–	–	± 1.5 ppm
internal oscillator temperature coefficient ($23^\circ \pm 5^\circ$; serial number HF2-DEV1142 and higher) ²	–	–	0.05 ppm/C
internal oscillator phase noise (at 100 Hz)	–	–125 dBc/Hz	–
internal oscillator phase noise (at 1 kHz)	–	–140 dBc/Hz	–
UHS (option) oscillator initial accuracy ³	–	–	± 0.5 ppm
UHS (option) oscillator aging (stability) ³	–	–	± 0.4 ppm/year
UHS (option) oscillator temperature stability ($23^\circ \pm 5^\circ$) ³	–	–	± 0.03 ppm

Parameter	min	typ	max
UHS (option) oscillator phase noise (at 100 Hz) ³	-130 dBc/Hz	-	-
UHS (option) oscillator phase noise (at 1 kHz) ³	-140 dBc/Hz	-	-
UHS (option) oscillator reference stability (over 30 s) ³	0.00005 ppm	-	-
UHS (option) oscillator time to reach specification ³	-	-	60 s
external clock connector	back-panel single-ended BNC		
external clock input impedance	1 MΩ		
external clock input voltage	0 V	-	+3.3 V
external clock frequency	9.98 MHz	10 MHz	10.02 MHz

¹ Specification valid for instruments with serial number HF2-DEV1141 and lower without HF2LI-UHS option installed

² Specification valid for instruments with serial number HF2-DEV1142 and higher

³ Specification valid for instruments with serial number HF2-DEV1141 and lower with HF2LI-UHS option installed

9.3. Digital Interface Specifications

Table 9.11. Digital interfaces

Parameter	Description
host computer connection	USB 2.0 high-speed, 480 Mbit/s
ZCtrl pre-amplifier control bus	proprietary bus to control external pre-amplifiers
ZSync synchronization bus	proprietary bus to locally interconnect ZI instruments
DIO connector	32 bit, general purpose

The DIO connector is a HD 68 pin connector, typically also used by SCSI-2 and SCSI-3 interfaces, 47 mm wide male connector. The DIO port features 16 bits that can be configured byte-wise as inputs or outputs, as well as 16 input only bits. The digital signals follow the CMOS/TTL specification.

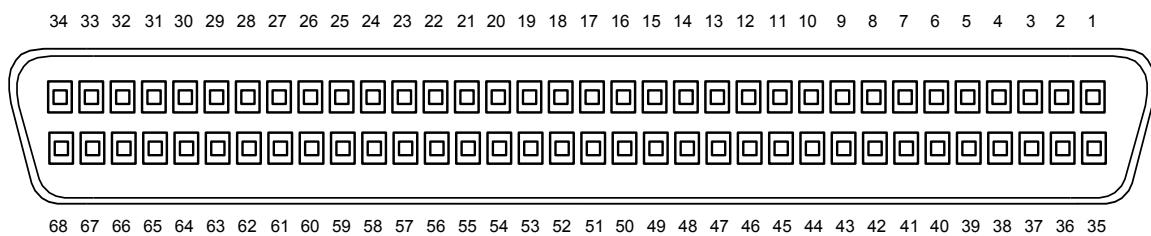


Figure 9.1. DIO HD 68 pin connector

Table 9.12. DIO pin assignment

Pin	Name	Description	Range specification
68	CLKI	clock input, used to latch signals at the digital input ports - can also be used to retrieve digital signals from the output port using an external sampling clock	5 V CMOS/TTL
67	DOL	DIO output latch, 64 MHz clock signal, the digital outputs are synchronized to the falling edge of this signal	5 V CMOS
66–51	DI[31:16]	digital input	digital input CMOS/TTL level
50–35	DIO[15:0]	digital input or output (set by user)	output CMOS 5 V, input is CMOS/TTL
34–3	GND	digital ground	–
2–1	PWR	5 V supply (100 mA max)	–

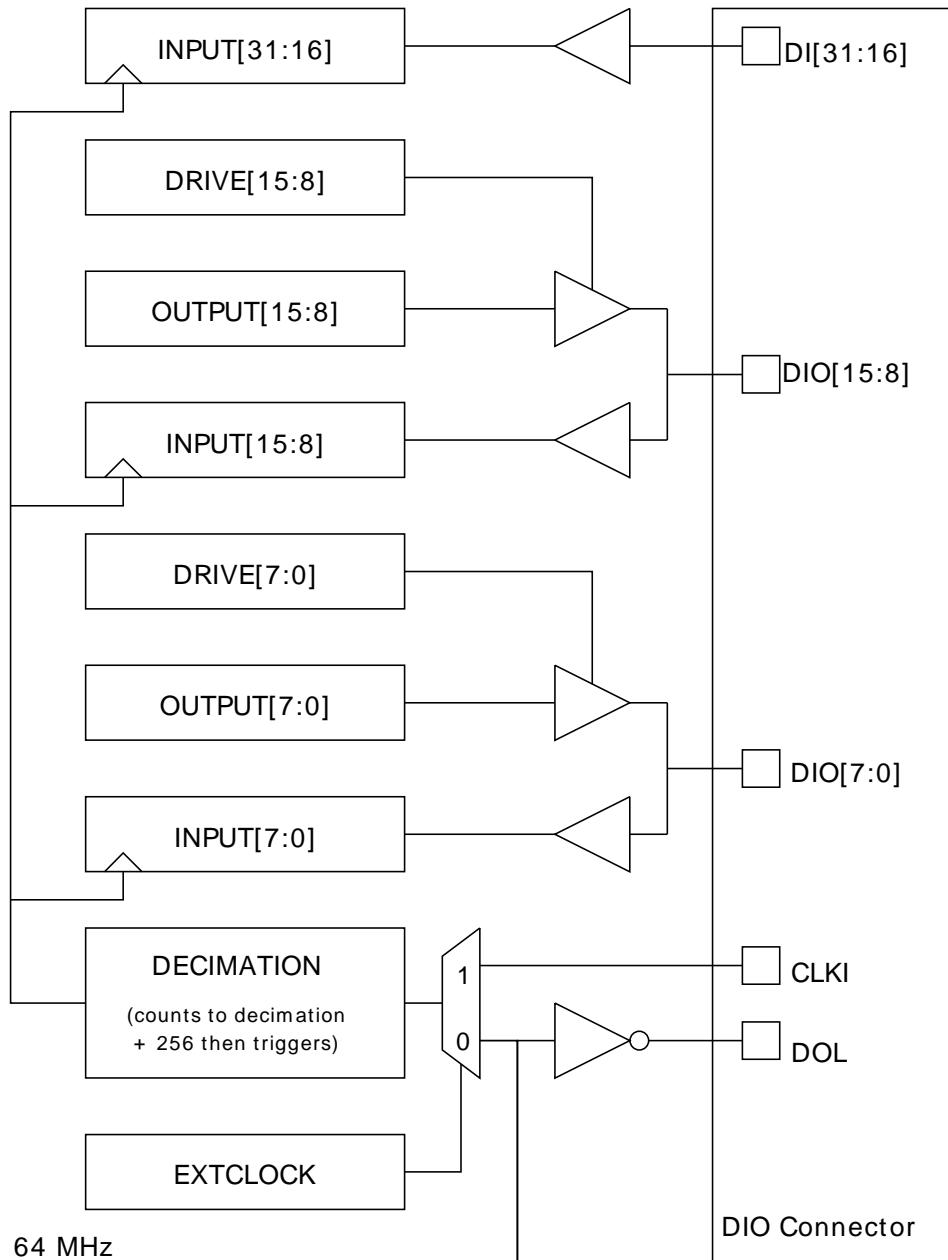


Figure 9.2. DIO input/output architecture

The HF2 Digital I/O Breakout Board provides an easy way to access all pins of the DIO Connector. The board consists of 68 pin headers and a 68-pin female socket to be connected to the HF2 using a ribbon cable. For description of the pins, refer to the [Table 9.12](#). The HF2 DIO Breakout Board is available with Zurich Instruments on demand.

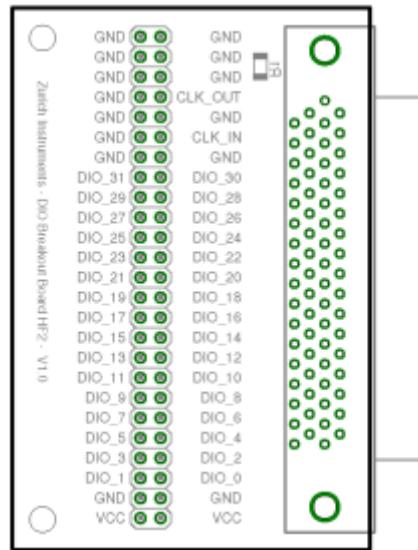


Figure 9.3. HF2 digital I/O breakout board

The internally generated 10 MHz clock is made available for external synchronization at the ZSync Out RJ45 connector. The clock signal is at pin 1, ground at pin 2. To connect: simply prepare a cable assembly that allows you to connect the 10 MHz signal from the HF2 to the BNC input of the other device external clock.

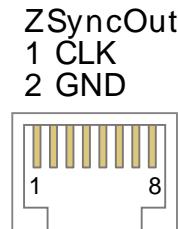
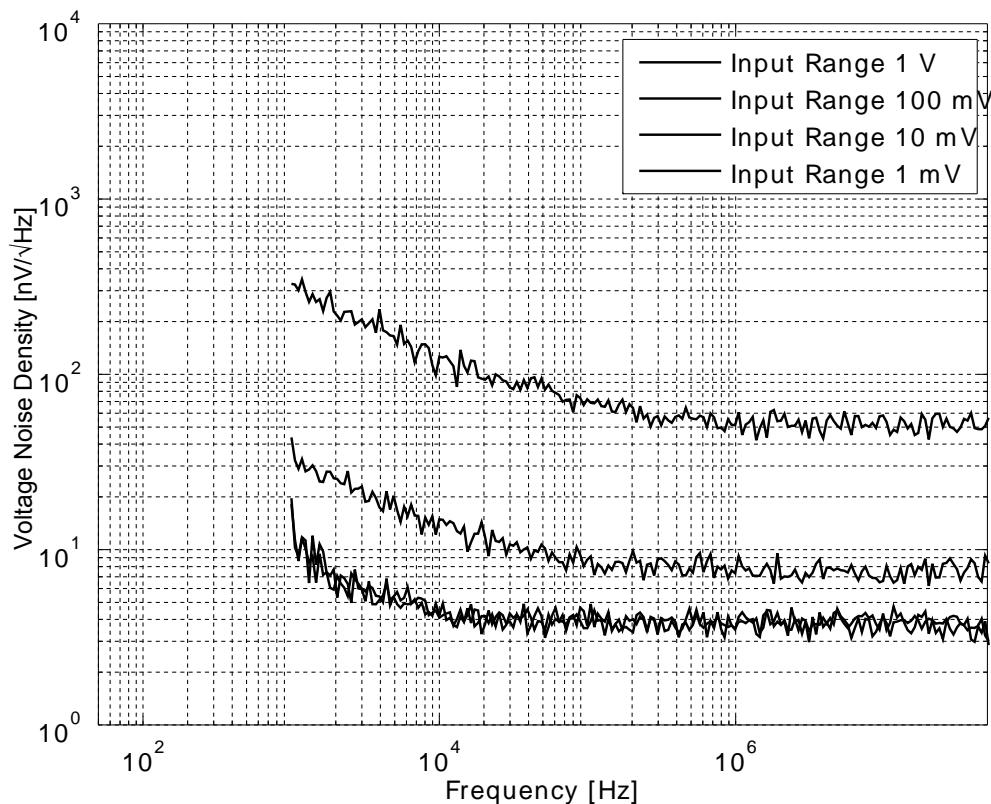


Figure 9.4. The pinout of the RJ45 jack

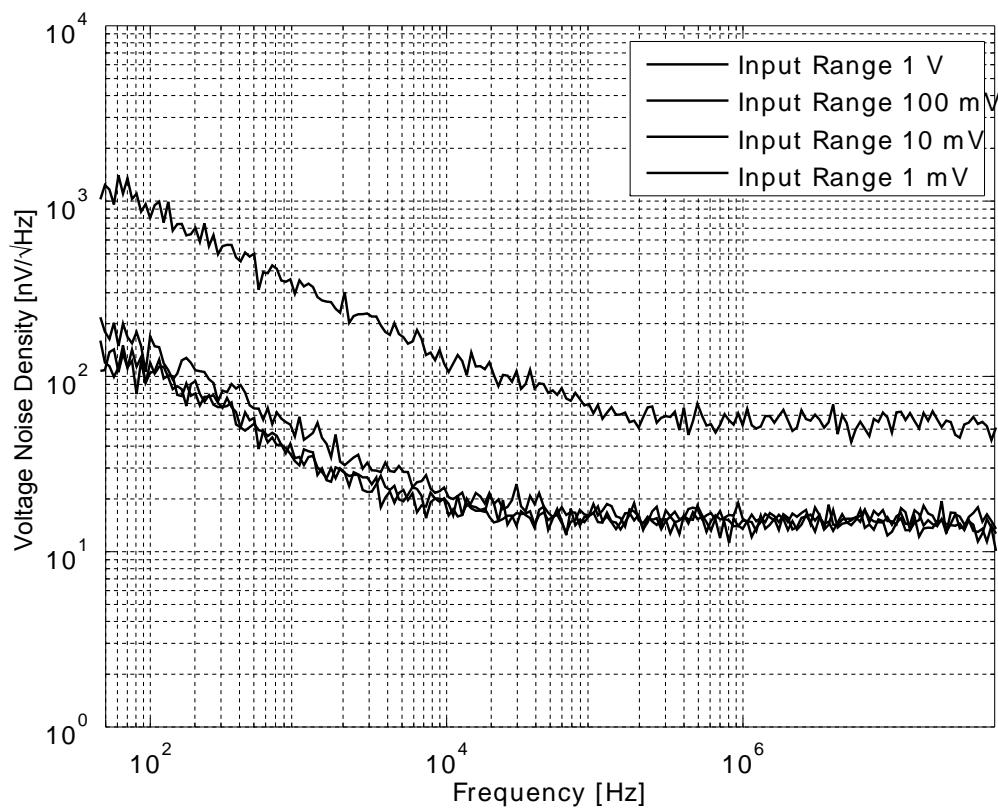
9.4. Performance Diagrams

Many parameters mentioned in [Section 9.2](#) are valid without specific conditions. Other parameters instead are typical specifications because they depend on several parameters, such as range settings, and frequency. This section completes the previous chapters with detailed performance diagrams in order to support the validation of applications.



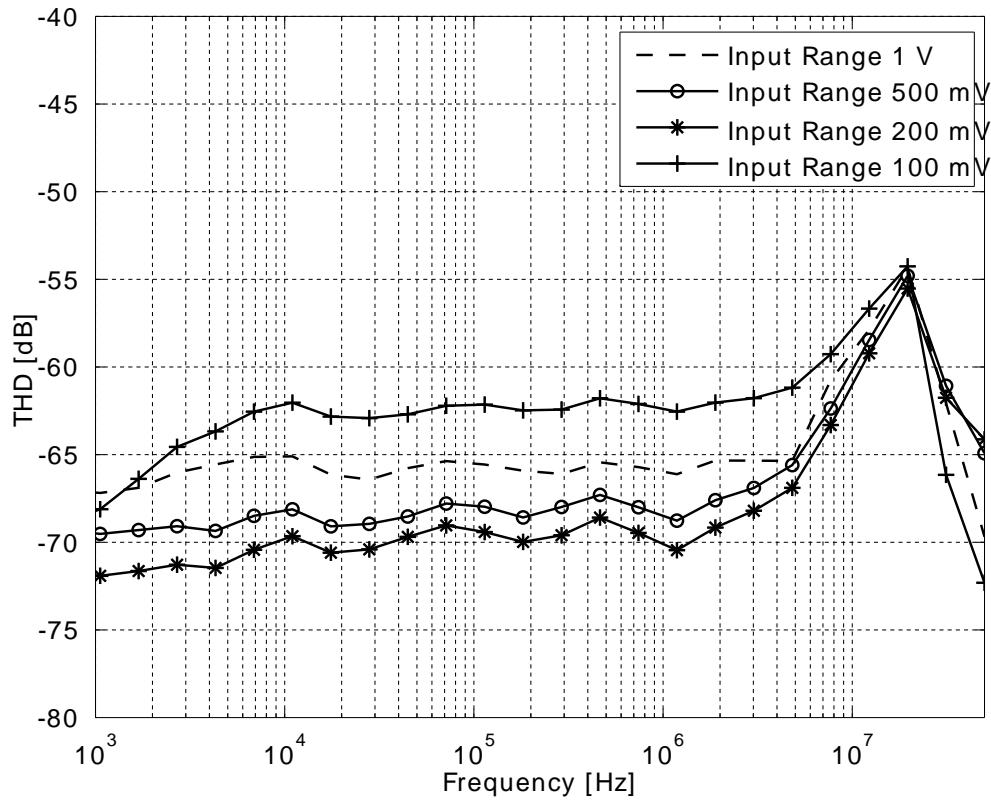
Input noise amplitude depends on several parameters, and in particular on the frequency and the setting for the input range. The noise is lower for smaller input ranges, and it is recommended to perform noise measurements with the AC coupling setting. In AC coupling mode, both 10 mV and 1 mV signal ranges have the same input noise performance. The corner frequency of the 1/f noise is in the range of 10 kHz and the white noise floor is around 5 nV/ $\sqrt{\text{Hz}}$ in AC coupling mode.

Figure 9.5. HF input noise with AC coupling



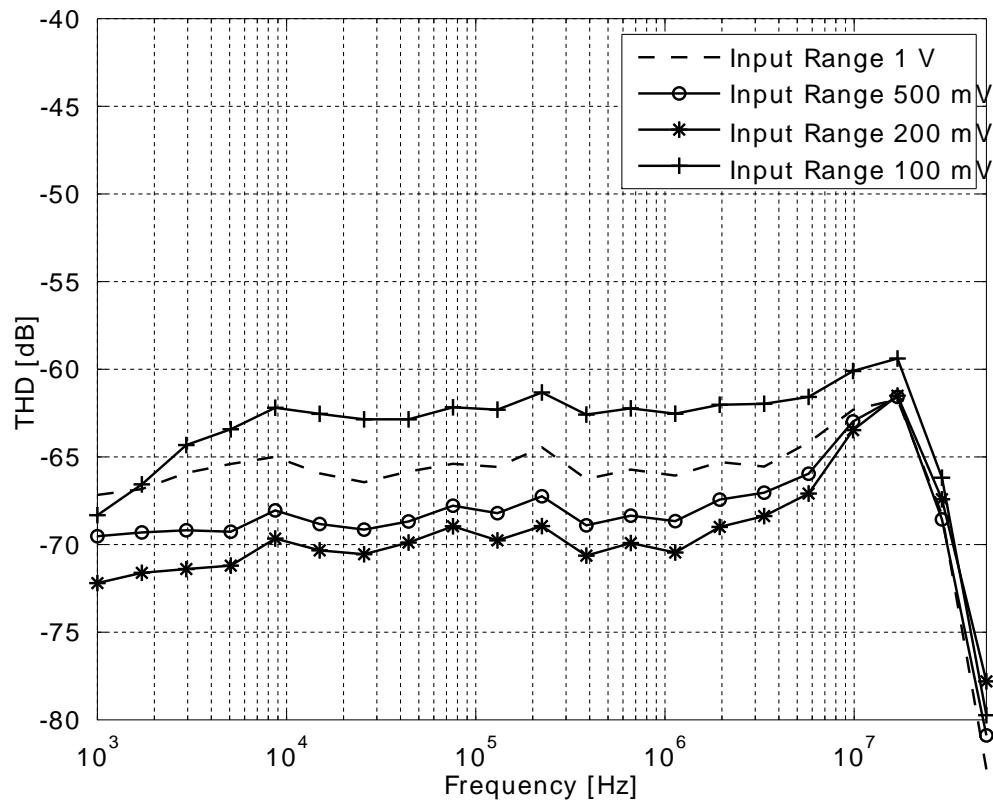
Input noise amplitude depends on several parameters, and in particular on the frequency and the setting for the input range. The noise is lower for smaller input ranges. The corner frequency for AC coupling is at 1 kHz. Therefore, for noise measurements above 1 kHz, AC coupling should be used, for noise measurements below 1 kHz, DC coupling should be used. In DC coupling mode, the input noise for low ranges is limited by the coupling selection. The corner frequency of the 1/f noise is in the range of 10 kHz and the white noise floor is around 15 nV/VHz in DC coupling mode.

Figure 9.6. HF input noise with DC coupling



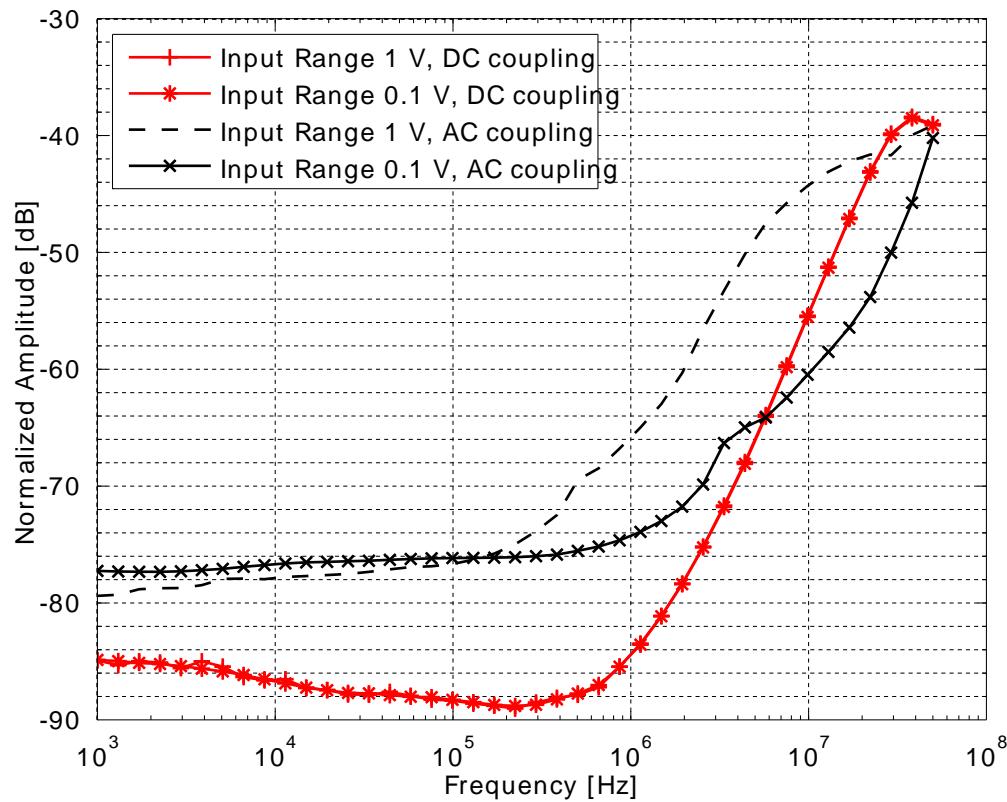
Input total harmonic distortion (THD) depends on several parameters, and in particular on the frequency and the setting for the input range. The test is performed with the input amplitude at 50% of the range setting. For frequencies below 5 MHz input THD is below -60 dB for any range setting. For frequencies above 10 MHz the AC coupling mode inserts about 5 dB more distortion than the DC mode. The total harmonic distortion is calculated from the measurement of the second and the third harmonic.

Figure 9.7. HF input total harmonic distortion with AC coupling



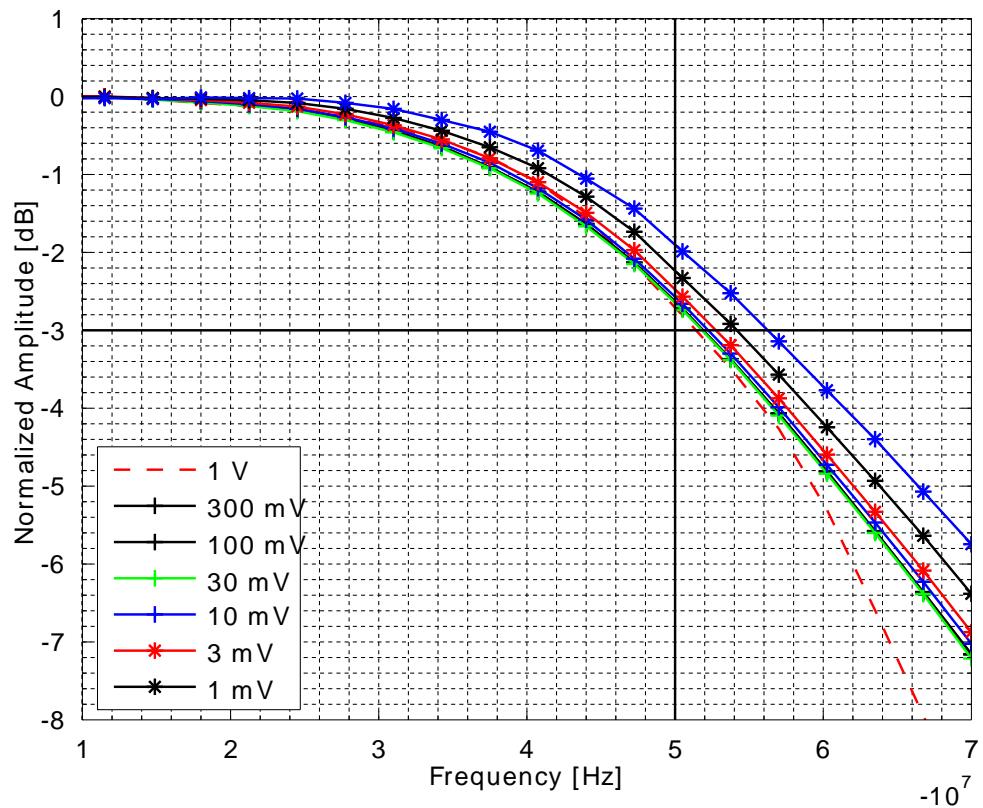
Input total harmonic distortion (THD) depends on several parameters, and in particular on the frequency and the setting for the input range. The test is performed with the input amplitude at 50% of the range setting. For frequencies below 10 MHz input THD is below $-60 for any range setting. Smaller range settings can be as good as -70 dB. The total harmonic distortion is calculated from the measurement of the second and the third harmonic.$

Figure 9.8. HF input total harmonic distortion with DC coupling



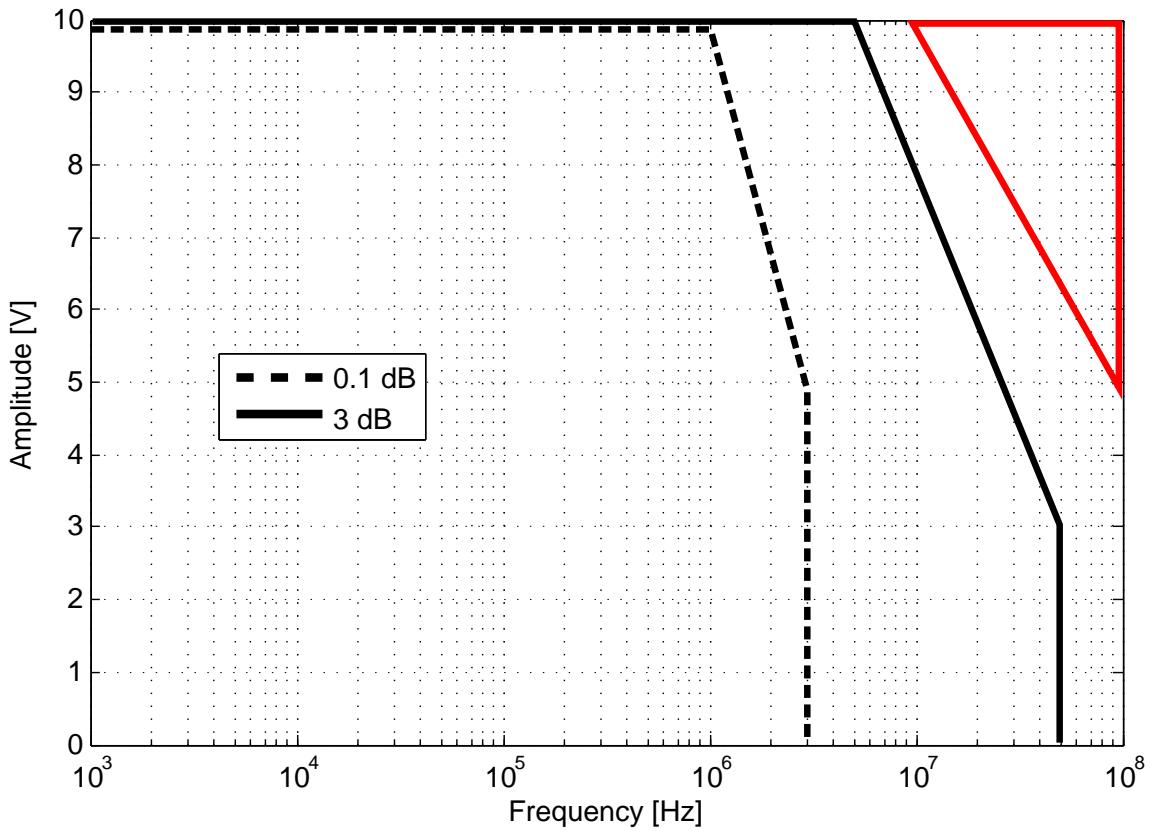
Input common mode rejection ratio (CMRR) of the signal input for different frequencies and different input amplitudes. For this test the same 2 V_{pp} signal is applied to the differential inputs on the instrument and the differential measurement is captured. The CMRR is better in DC mode for frequencies up to 7 MHz. Above 7 MHz, the CMRR in AC mode is better suited, but only for amplitudes up to 100 mV.

Figure 9.9. HF input common mode rejection ratio



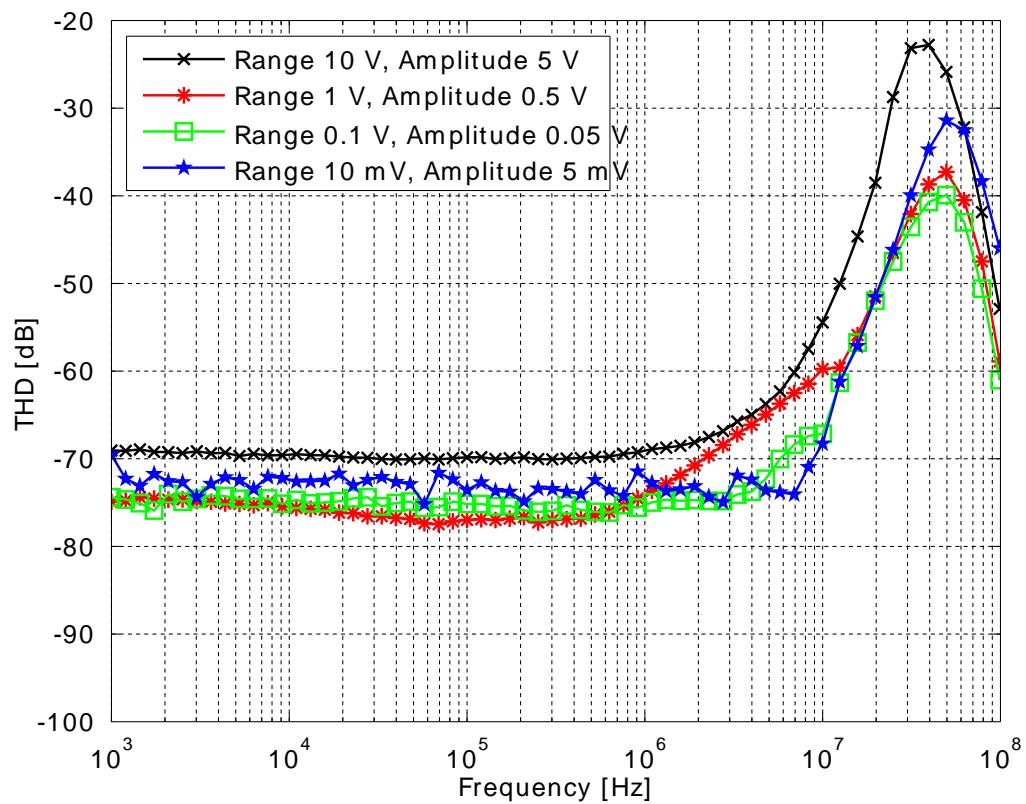
Signal input bandwidth versus frequency for different range settings. The vertical solid black line marks the 50 MHz specification and the horizontal solid black line corresponds to 3 dB attenuation. The bandwidth is between 50 MHz and 60 MHz for all displayed range settings (intermediate range selections can be chose in the graphical user interface).

Figure 9.10. HF input bandwidth



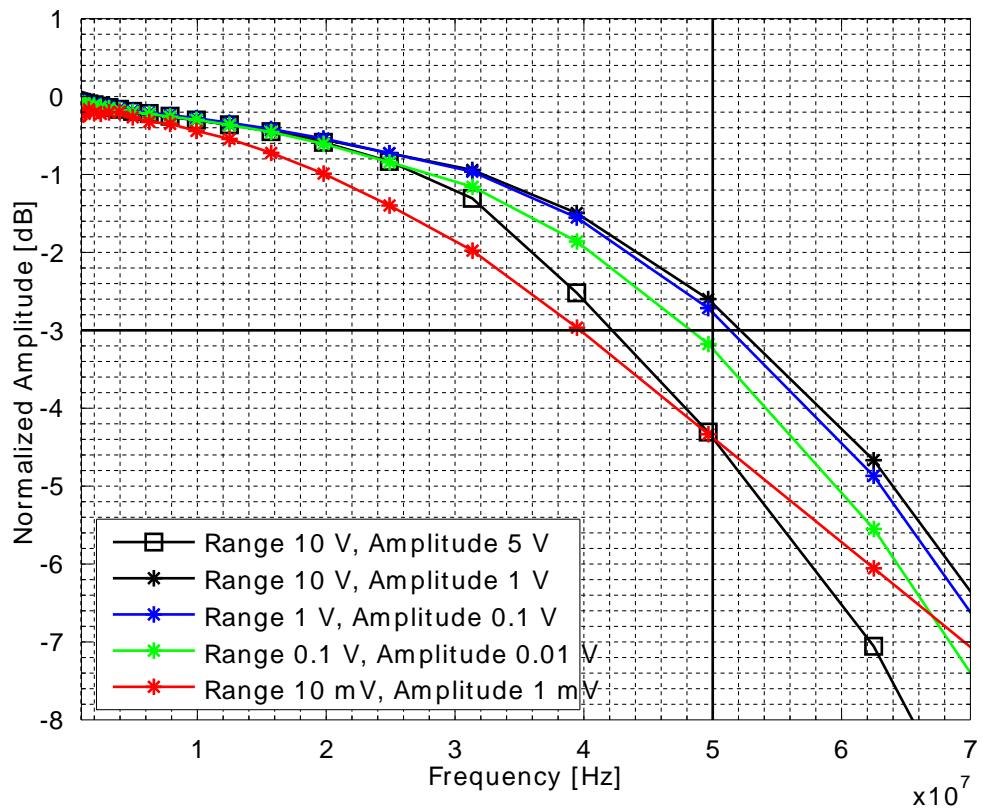
The amplitude accuracy of the output signal generator depends on the frequency and the amplitude. The plot shows the output amplitude accuracy region for 0.1 dB (roughly 1%) and 3 dB (around 40%) of the expected value. The measurement was performed with 50Ω load and is better for larger loads. The plot shows that the signal amplitude up to 1 MHz is accurate at 0.1 dB at any output voltage (area within dashed line). Above this frequency, the anti-aliasing filters at 50 MHz impact the absolute value of the generated signal: the output amplitude is reduced. Above 5 MHz the maximum amplitude is below 10 V, and decreases further reaching 3 V_{RMS} at 50 MHz (area within solid line). It is not recommended to operate the instrument at high amplitudes (above 5 V_{RMS}) and high frequencies (above 10 MHz) because of the large signal distortion and potential long-term damage to the output drivers (area within solid red line).

Figure 9.11. HF output amplitude accuracy



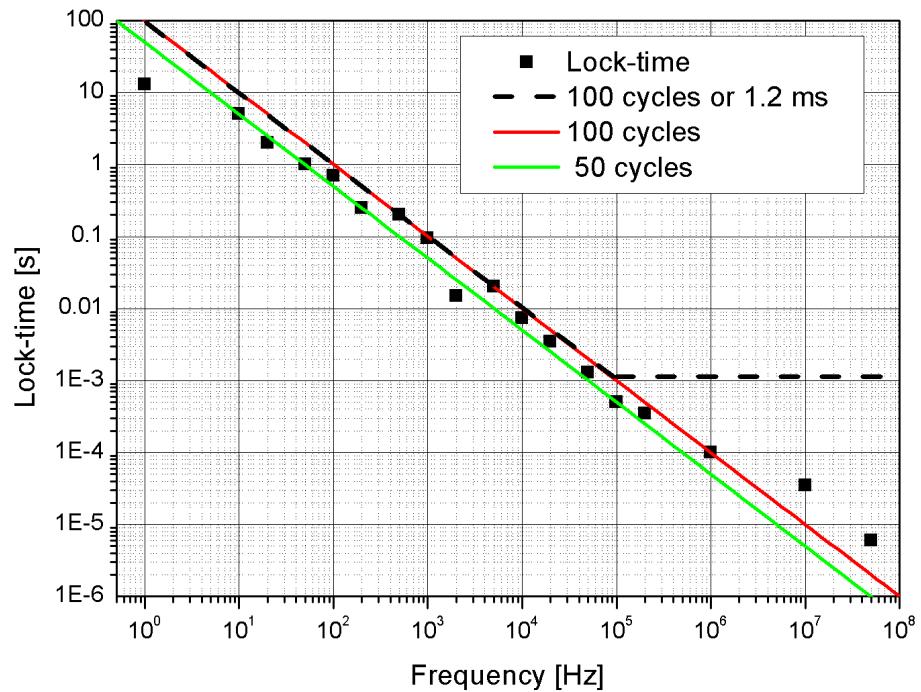
The total harmonic distortion of the signal outputs was measured with $50\ \Omega$ load and by applying amplitude at 50% of the corresponding range. The THD is around 75 dB for signals below 0.5 V, and increases for larger amplitudes. The distortion increases significantly at frequencies above 10 MHz. The total harmonic distortion is calculated from the measurement of the second and the third harmonic.

Figure 9.12. HF output total harmonic distortion



The vertical solid black line marks the 50 MHz specification and the horizontal solid black line corresponds to 3 dB attenuation. The bandwidth depends on the range and amplitude. The measurement is performed with 50Ω load.

Figure 9.13. HF output bandwidth



The dashed black line marks the specified lock time corresponding to 100 reference cycles or 1.2 ms. The 100 and 50 cycle are also plotted for convenience. The graph indicates that the lock time of the HF2 Instruments follows the 100 reference cycles spec, is slightly worse for frequencies above 1 MHz, and is better for frequency below 1 Hz.

Figure 9.14. Lock time

9.5. Ground and Earth Scheme

Ground loops may introduce noise at the line frequency (mostly 50/60 Hz) and its harmonics, and aliasing in the demodulated signal that is measurable for frequencies up to 10 MHz. Some lock-in amplifiers implement a line filter which has the effect to exclude low frequency measurements. This is not the case for the HF2 Instruments where an effective ground strategy is implemented.

In order to suppress large signal components at line frequency and higher harmonics avoiding ground loops within the measurement setup is required. Possible reasons for line frequency components include parasitics resistances between the different signal grounds, inductive coupling from line transformers and other electrical apparatus into the signal paths, and pre-amplifiers that generate additional loops.

Counter measures are to break loops using differential wiring, by implementing star ground connections in the measurement setup, with the main ground closest to the setup as possible, connect all instrument casing to earth, and using optocouplers and transformers that provide a galvanic decoupling in the signal path.

The grounding of the HF2 Instrument is implemented connecting analog ground and digital ground in a star network. This reduces the digital ground noise that flows into the analog domain considerably. All analog grounds are connected together before they are connected to the digital ground (e.g. USB ground). All grounds are decoupled by the Earth by means of a $1\text{ M}\Omega$ resistor, which is however generally shorted by a PC connected by means of a USB cable. The earth connection of the power plug connects at the same time the chassis and the banana plug on the rear Instrument panel.

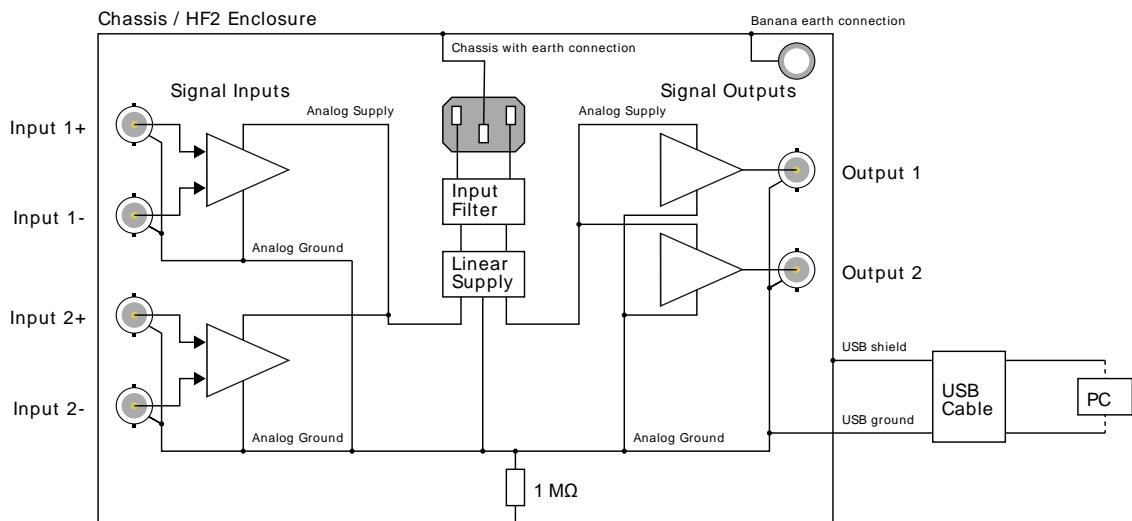


Figure 9.15. Instrument ground & earth connection scheme

For applications that require floating ground, it is suggested to make use of the differential inputs by connecting the BNC shield to the negative BNC connector. The limitation for this strategy is that the floating ground should not exceed the specified maximum input common mode offset.

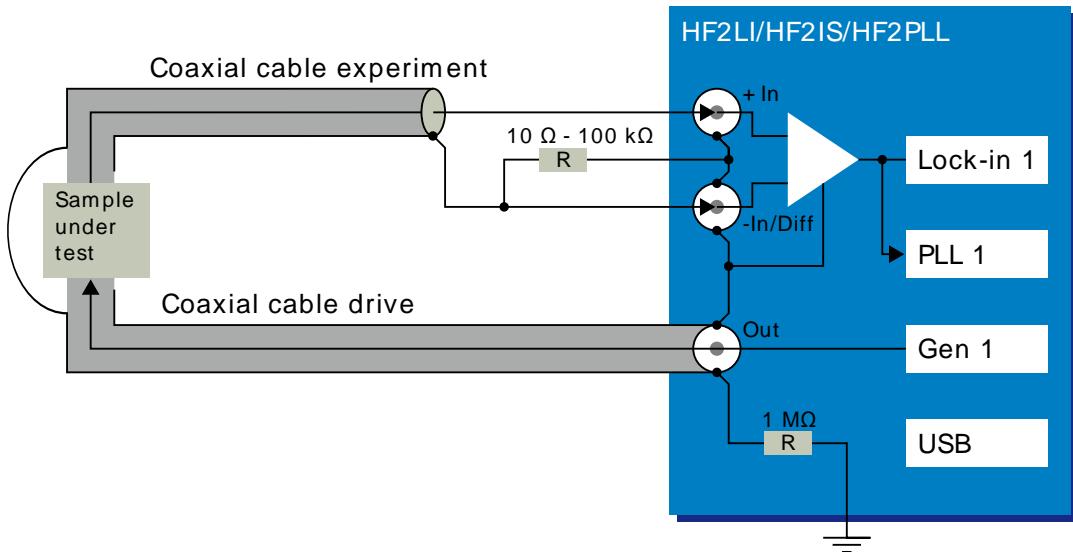


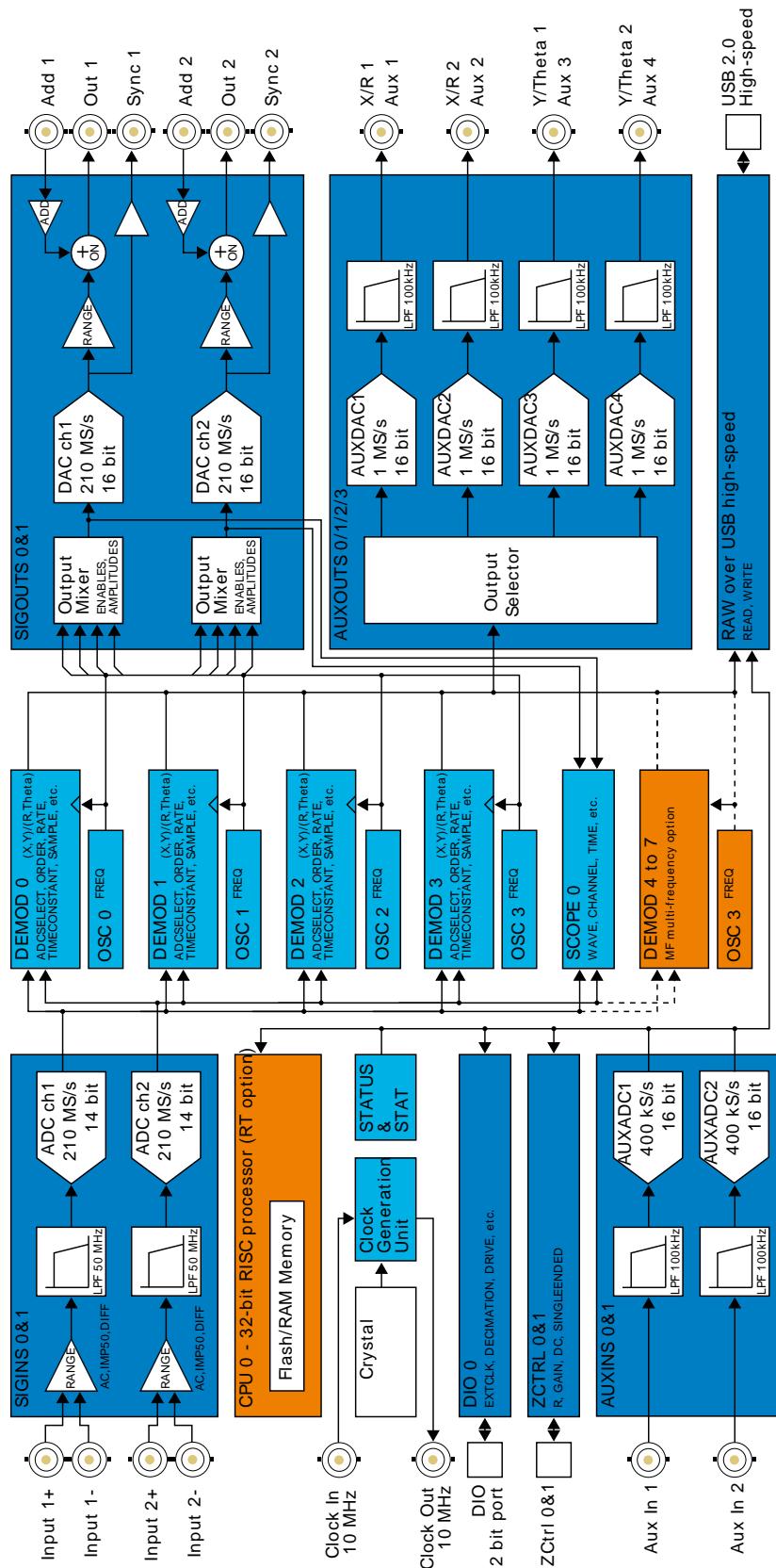
Figure 9.16. Differential connection scheme reducing ground loops

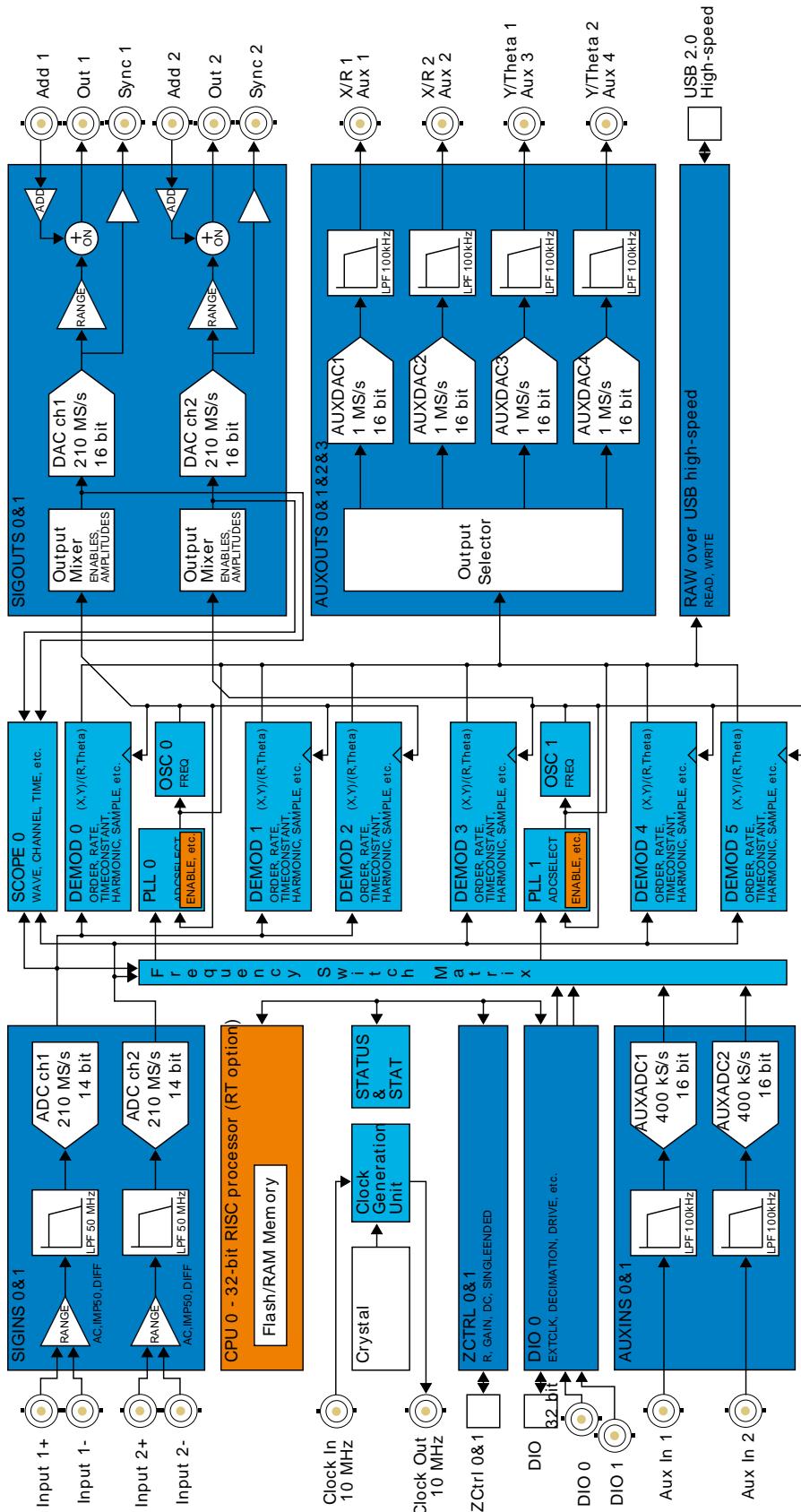
When using ZSync to synchronize two HF2 Instruments, the 10 MHz clock may couple into the signal path and disturb the lock-in measurement at certain frequencies. Below are two measures to counteract this potential problem.

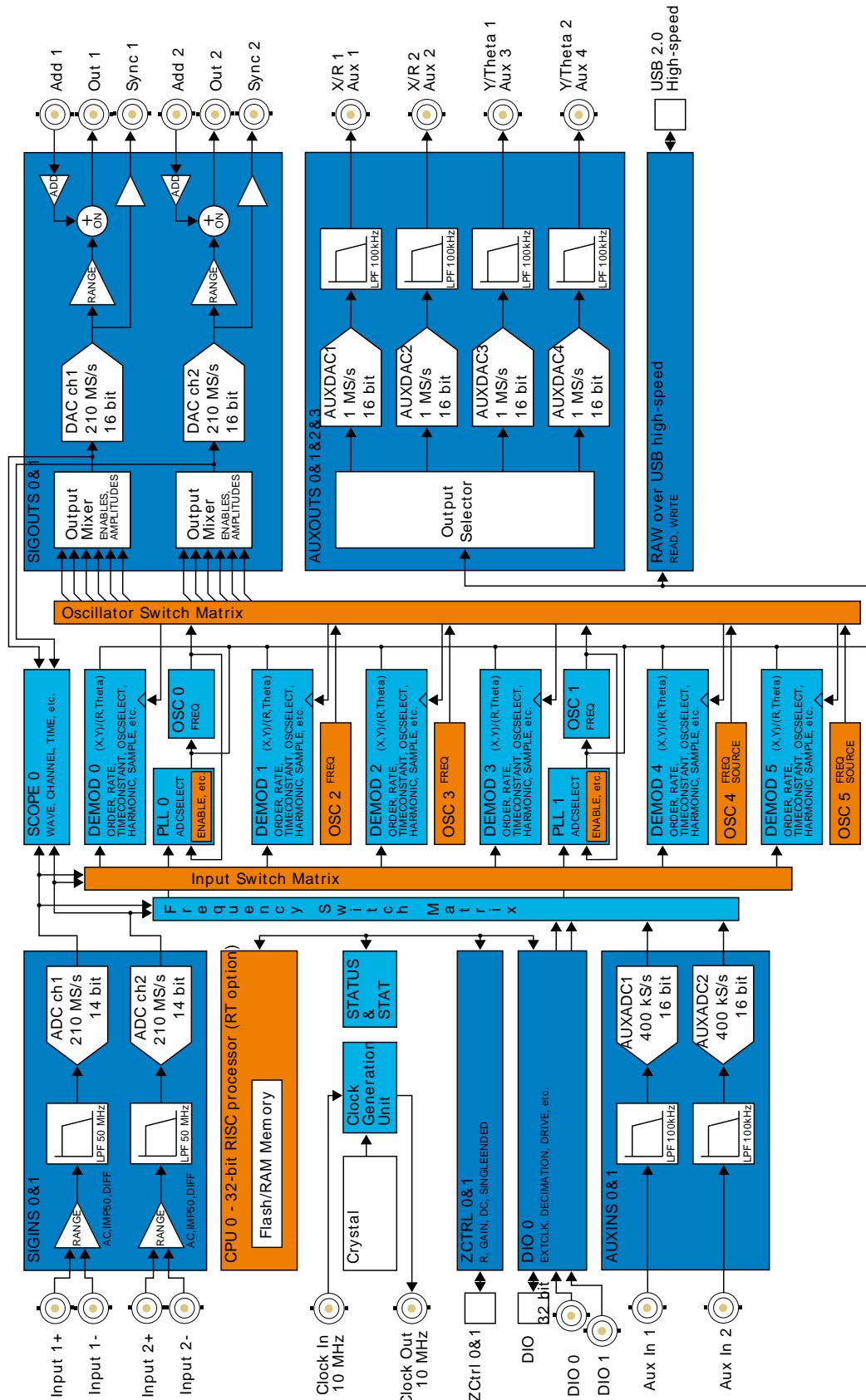
- Connect the Clock In connectors on the back panel of the instruments with a short BNC cable. This cable has the sole purpose of connecting the digital grounds of the instruments and has no effect on the 10 MHz clock.
- Wind the Ethernet cable used to connect the ZSync ports into small coils and/or attach a ferrite bead to the cable.

9.6. Reference Images

The following figures are intended for advanced users with programming projects on the HF2 Instruments.







9.7. Test Specifications

This chapter describes the performance tests for the Zurich Instruments HF2 Instruments. Users are encouraged to verify that the Instrument performs as specified, not only after shipping but also to ensure continuous performance over time. Some of the measurements described are setup as a self-test, where no additional measurement equipment is needed. A few tests, however, require additional measurement equipment, such as the references needed for the accuracy tests.

The HF2 instruments have two analog input/output channels, four auxiliary output and two auxiliary analog input channels. Tests described here are performed on one channel of each type and need to be repeated accordingly on the other channels.

The following table gives a list of equipment that meets the accuracy requirements for the described tests.

Table 9.13. Required equipment

Equipment	Specifications	Recommendation
HF2 base instrument	No options required	HF2LI, HF2IS, HF2PLL
Function generator	>50 MHz range, sine waveform generation	Agilent 33250A
Digital multimeter	0.1 mV resolution, 20 V range	Agilent 34410A
BNC cables	2 x 50 Ω, male-to-male cables (<50 cm)	
BNC T-connector	1 x 50 Ω, male-female-female connector	

In addition, the following need to be fulfilled:

- The test equipment should be connected to a common AC power circuit. Connecting the test equipment to separate AC power circuits can result in offset voltages between them and will introduce an additional error into the testing procedure. If the user is unsure about the AC power circuit layout, a common power strip for all the test equipment is advised.
- Allow the test equipment to warm up for at least 30 minutes.
- Operating temperature and humidity should be within the instrument's specified range.
- Make sure that the latest versions of the LabOne and ziControl software packages have been installed on the host computer. More information about installing software can be found in the [Getting Started Chapter](#).

During testing, the user should adopt the measurement setup and instrument settings as described for each specific test. The user should compare the obtained tests results with the HF2 instrument's specifications. HF2 connection to the host computer via USB 2.0 cable is assumed and indicated in all the described setups, unless stated otherwise.

9.7.1. Test Input Noise

Definition

Input noise voltage (INV) is the sum of all the noise sources internal to the instrument, referenced to the signal input. Noise is expressed in its power spectral density (noise power per unit frequency) in units V^2/Hz , and more typically, its square root in units V/\sqrt{Hz} .

Typical HF2 Instrument settings that influence the noise at the input are input range, input impedance, measurement frequency and the selected input coupling (AC or DC).

Setup

Disconnect all BNC cables from the HF2's inputs and outputs. The input noise test setup is shown in the figure below.

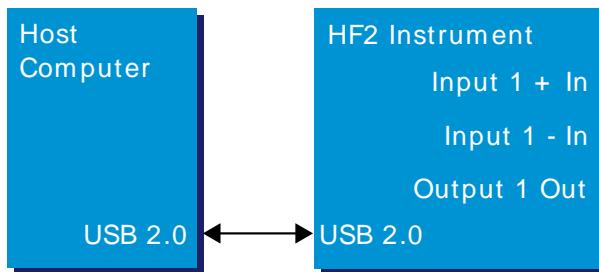


Figure 9.20. Input noise test setup

The HF2 instrument settings on the ziControl panel should be according to the table below:

Table 9.14. HF2 instrument settings

Ch1 Signal Input Range	1 mV
Ch1 Signal Input AC/ Diff/ 50	ON/ OFF/ ON
Ch1 Filter dB/Oct	24
Ch1 Filter BW	10 Hz
Ch1 Demodulator 1 Readout	Cont.
Ch1 Frequency (Hz)	Variable

Measurement

The measurements are performed using the frequency sweeper with the following settings:

Table 9.15. Frequency sweeper settings

Sweep Range Start	1 kHz
Sweep Range Stop	50 MHz
Sweep Range Points	100
Sweep Range Log Sweep	ON
Auto BW	ON
Settling Time	10 TC
Averaging	32
Result Unit	V_{RMS}/\sqrt{Hz}
Display Polar	ON

After the settings have been applied, start the measurement by pressing the Sweep Control Single button. The plots of the input noise (units of V_{RMS}/\sqrt{Hz}), as a function of frequency, in the range

from 1 kHz to 50 MHz are displayed in the frequency sweeper. The figures below show the results obtained with AC and DC coupling.

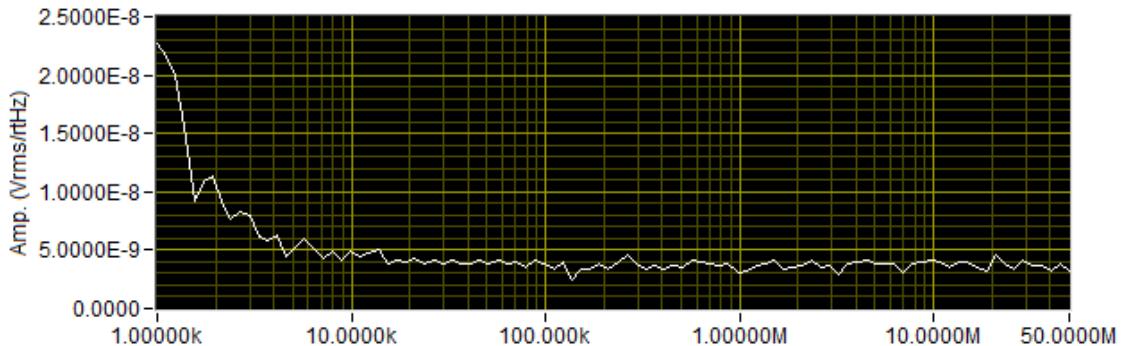


Figure 9.21. Input noise test results with AC coupling

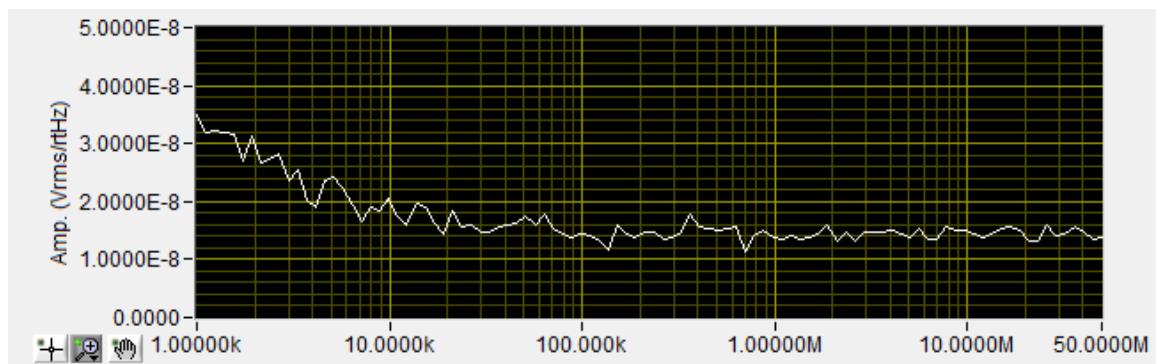


Figure 9.22. Input noise test results with DC coupling

The noise spectral density of the input channel with AC coupling is below $5 \text{ nV}_{\text{RMS}}/\sqrt{\text{Hz}}$ in the frequency range from 10 kHz to 50 MHz. In the same frequency range and with the DC coupling, the input noise is around $15 \text{ nV}_{\text{RMS}}/\sqrt{\text{Hz}}$. The noise spectral density at low frequencies (below 10 kHz) is dominated by $1/f$ noise. Users who are interested in the noise spectral density below 1 kHz should perform their tests using the DC coupling and longer time constants, which will result in longer measurement time.

9.7.2. Test Dynamic Reserve

Definition

The dynamic reserve (DR) of a lock-in amplifier is a measure of its capability to withstand the disturbing signals and noise at non-reference frequencies, while maintaining the specified measurement accuracy within the signal bandwidth. It is usually given in units of dB and expresses the ratio between the amplitude of the disturbing signal (V_{disturb}) and the minimal signal amplitude that is measured with 1% accuracy (V_{meas}) and can be calculated using the formula,

$$\text{DR} = 20 * \log\left(\frac{V_{\text{disturb}}}{V_{\text{meas}}}\right) \quad (9.1)$$

Typical HF2 Instrument settings that influence the dynamic reserve are the measurement and the disturbing signal frequency, the input range, the demodulator filter settings and the selected input coupling (AC or DC).

Setup

Connect Signal Output 1 Out and the Signal Input 1 + In using a short BNC cable. Use second BNC cable to connect the Signal Output 2 Out and the Signal Input 1 - In Diff. The Output 1 provides V_{meas} , and the Output 2 $V_{disturb}$ signal. The setup is shown in the figure below.

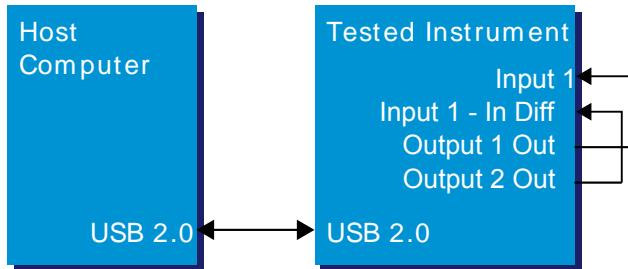


Figure 9.23. Dynamic reserve test setup

The HF2 instrument settings on the ziControl panel should be according to the table below:

Table 9.16. HF2 instrument settings

Ch1 Signal Inputs Range	1.2 V
Ch1 Signal Inputs AC/ Diff/ 50	ON/ ON/ OFF
Ch1 Frequency	Variable
Ch1 Filters dB/Oct	24/48
Ch1 Filters BW	Variable
Ch1 Demodulators 1	ON
Ch1 Outputs range	10 mV
Ch1 Outputs Amplitude	6.943 μ V
Ch2 Outputs range	1.0 V
Ch2 Outputs Amplitude	1.0 V
Ch2 Frequency (Hz)	1.1 MHz

Measurement

Two test methods are explained in this section. The first method uses the frequency sweeper with the following settings:

Table 9.17. Frequency sweeper settings

Sweep Range Start	1.095 MHz
Sweep Range Stop	1.105 MHz
Sweep Range Points	101
Auto BW	OFF
Settling Time	10 TC eff
Filter Averaging	32
Display Coord. Sys.	Polar

Mapping	dB
Result Unit	Vpk
Persistent Display	Manual Memorize
Display Depth #	2

The Sweeper tool directly measures the dynamic reserve as a function of frequency. The measurement is done with $V_{\text{disturb}} = 1 \text{ V}$, at a fixed frequency of 1.1 MHz, while the $V_{\text{meas}} = 6.943 \mu\text{V}$ signal's frequency is swept in the frequency window of 10 kHz around 1.1 MHz. One may perform two measurements by setting the demodulators filter bandwidth BW 3dB, first to 10 Hz, then to 1 Hz. Start each measurement by pressing the Sweep Control Single button in order to plot the amplitude and phase graphs. The white curve is taken with bandwidth of 10 Hz and the red one with bandwidth of 1 Hz.

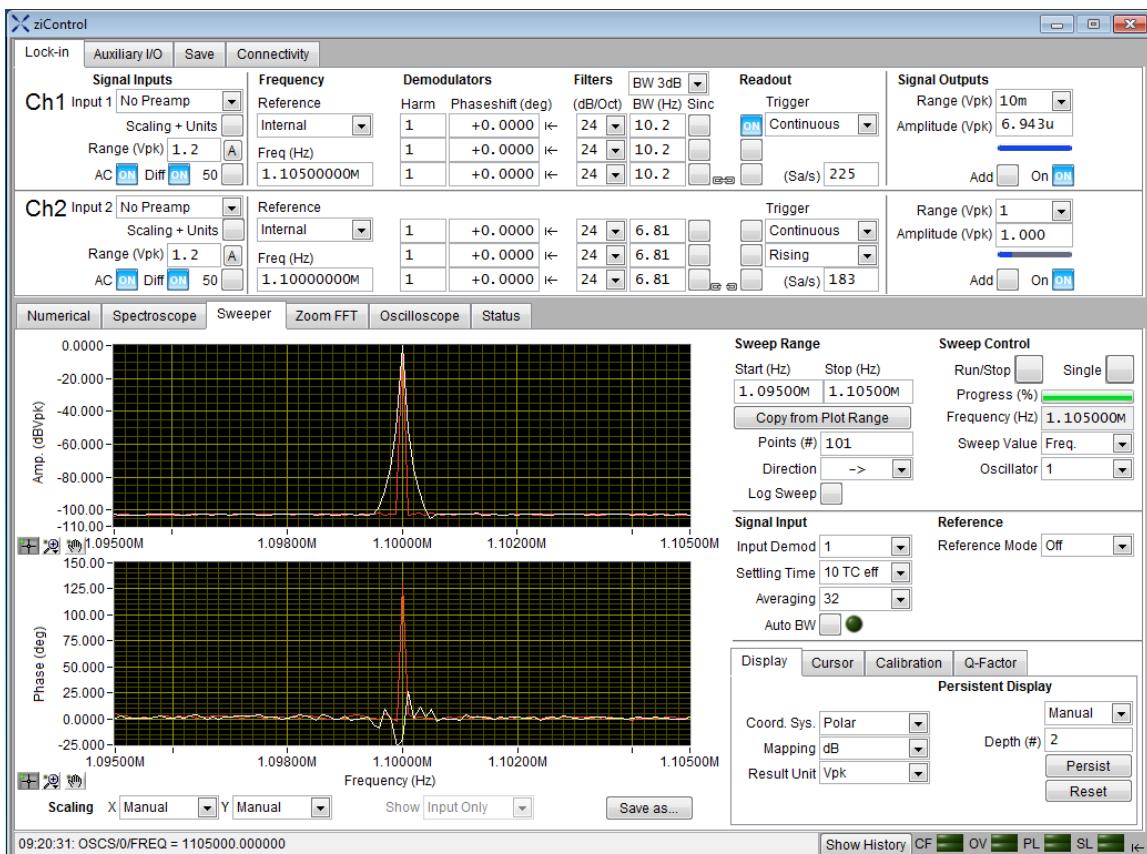


Figure 9.24. Dynamic reserve test results with Sweeper tool

When the disturbing frequency is equal to the measurement frequency, the DR is 0 dB, regardless of the filter settings. For larger filter bandwidths (white curve), the instrument is less capable of rejecting the disturbing signal, than in the case of the narrower bandwidth (red curve). This is directly observed from the presented measurements. Note that the apparent width also depends on the sweep resolution. As the distance in frequency between the two signals increases, the DR increases as well. Sufficiently far away from the disturbing signal (this depends on the filter order and the BW), the DR can be calculated as: $DR = 20 \cdot \log(V_{\text{disturb}} / V_{\text{meas}}) = 20 \cdot \log(1 \text{ V} / 6.943 \mu\text{V}) = 103.17 \text{ dB}$. To obtain the better separation; higher filter order, narrower bandwidth and additional averaging should be used. Note that the settling time increases with the filter order, which will result in longer measurement time.

In the second test, one may use the Spectroscope tool to verify that the accuracy of the measurement is as specified. Set the Ch1 and Ch2 reference frequency to 1.1 MHz and 1.09 MHz.

Set the filter order to 48 dB/Oct and the time constant, TC eff to 2.3 s. Choose the Demod 1 in the Control section and set the Time scale to 50 s/Div. First measure the RMS of the 6.943 μ V signal without the disturbance (Ch2 Signal Outputs amplitude should not be activated). Wait about 120 s and then apply the 1 V signal by pressing the ON button of the Ch2 Signal Outputs. Note the sharp increase of the signal amplitude. After the filter is settled (more than 15 TC) the measured signal converges to the expected RMS value of 4.91 μ V. Stop the acquisition after about 120 s by pressing Acq Stop in the Spectroscope Control section. Activate cursors C1 and C2 to estimate the RMS amplitudes of the signal with and without the disturbance. Cursors C1 and C2 indicate estimated average RMS values before (4.889 μ V) and after (4.936 μ V) the disturbing signal has been applied. Both signals are measured with the 1% accuracy.

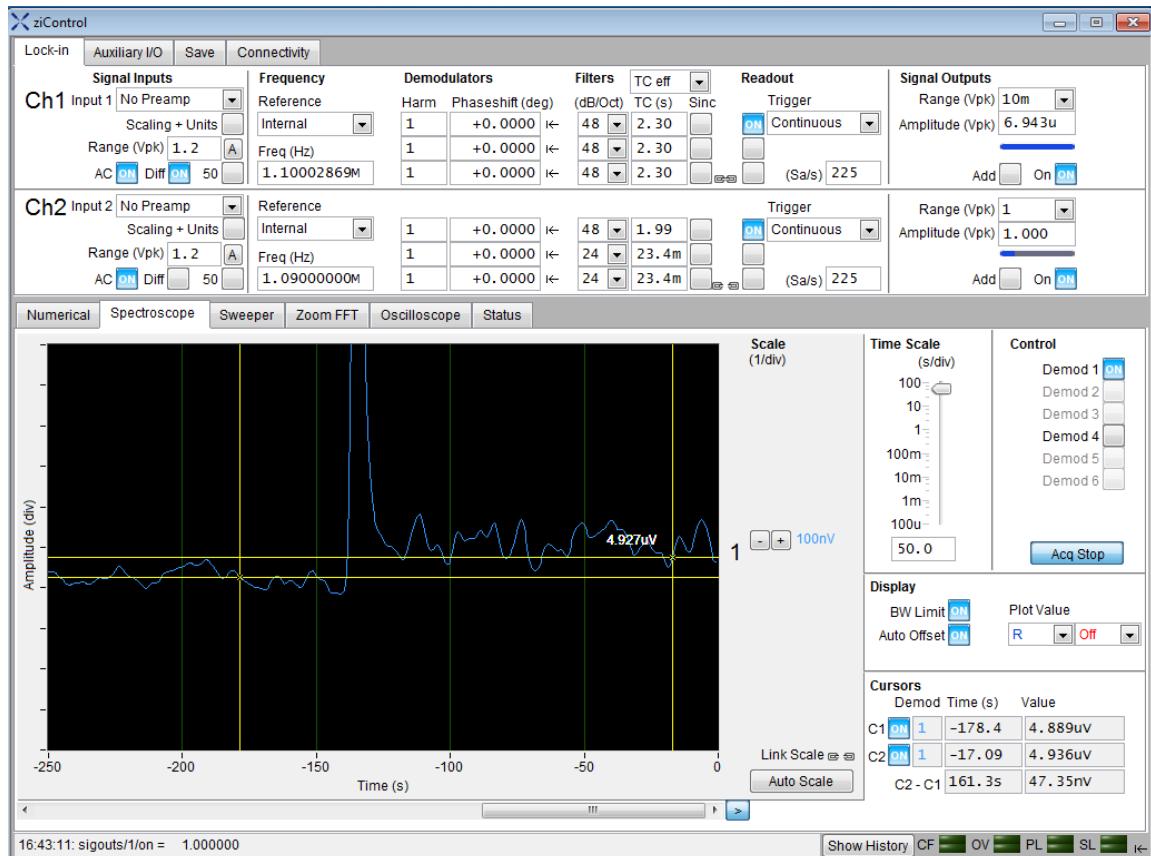


Figure 9.25. Dynamic reserve test results with Spectroscope tool

9.7.3. Test Common Mode Rejection Ratio

Definition

Common mode rejection ratio (CMRR) is the measure of the instrument's capability to reject signals that are common to both inputs (common mode). It is given in units of dB and expresses the ratio between the amplitude of the common mode interference (V_{cmm}) and the measured differential signal (V_{meas}),

$$CMRR = 20 * \log\left(\frac{V_{cmm}}{V_{meas}}\right) \quad (9.2)$$

Typical HF2 Instrument settings that influence the CMRR are the measurement frequency, the input range, and the selected input coupling (AC or DC).

Setup

Split the signal from the Output 1 Out and feed differentially to Input 1 (+ In and - Diff), using a T-connector and two short equal-length BNC cables. Make sure that the two BNC cables are not forming a large area loop, which is susceptible to the magnetic pick-ups. The setup is shown in the figure below.

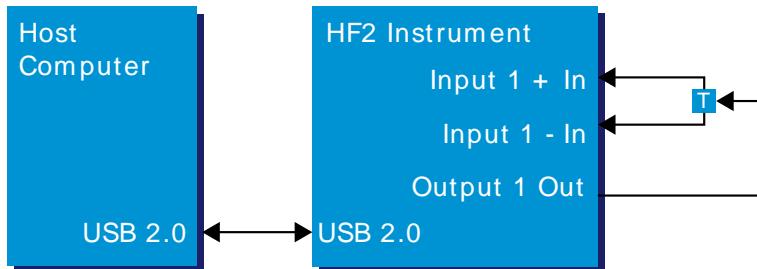


Figure 9.26. CMRR test setup

The HF2 instrument settings on the ziControl panel should be according to the table below:

Table 9.18. HF2 instrument settings

Ch1 Signal Input Range	100 mV
Ch1 Signal Input AC/ Diff/ 50	OFF/ ON/ OFF
Ch1 Demodulator 1 Readout	Cont.
Ch1 Signal Output Range	1.0 V
Ch1 Signal Output Amplitude	1.0 V
Ch1 Signal Output On	ON

Measurement

The measurements are performed using the frequency Sweeper with the following settings:

Table 9.19. Frequency Sweeper settings

Sweep Range Start	1 kHz
Sweep Range Stop	50 MHz
Sweep Range Points	100
Sweep Range Log Sweep	ON
Auto BW	OFF
BW 3dB	10 Hz
Settling Time	10 TC
Averaging	1
Display Mapping	dB
Result Unit	V _{RMS}
Display Polar	ON

After the settings have been applied, start the measurement by pressing the Sweep Control Single button. The plots of CMRR, as a function of frequency, in the range from 1 kHz to 50 MHz are

displayed in the frequency sweeper. CMRR test examples with AC and DC coupling are shown below.

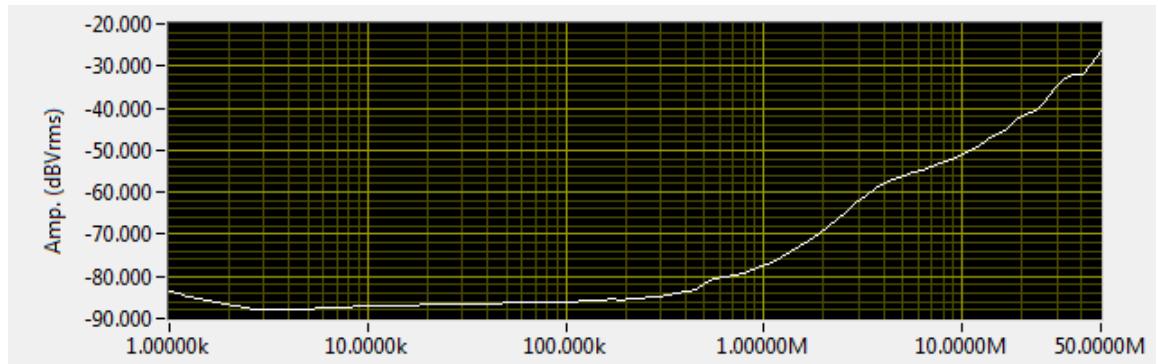


Figure 9.27. CMRR test results with AC coupling

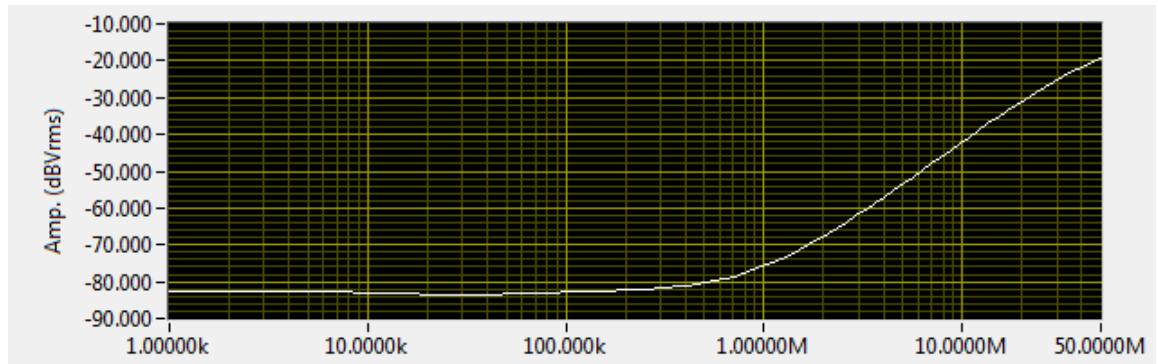


Figure 9.28. CMRR test results with DC coupling

The test shows that the common mode is rejected at, and below 75 dB, in the frequency range from 1 kHz to 1 MHz and gradually decreases as the frequency increases to 50 MHz.

9.7.4. Test Total Harmonic Distortion

Definition

Total harmonic distortion (THD) is a measure of the non-linearity of the input and output channels. It is given in units of dB and expresses the ratio of the root-mean-square, of all higher harmonics ($H_{n>1}$), and the amplitude of the fundamental frequency (H_1),

$$\text{THD} = 20 * \log \left(\frac{\sqrt{\sum_{n=2}^{\infty} H_n^2}}{H_1} \right) \quad (9.3)$$

The HF2 Instruments allows the user to measure harmonics up to $n = 1023$, limited by the bandwidth of the instrument. However, to determine the THD, it is sufficient to measure the first few significant harmonics (up to $n = 5$) and neglect the higher order ones. With the HF2 Instrument, simultaneous measurements of up to three harmonics can be done per each channel. The THD can be estimated using the simplified formula:

$$\text{THD} = 20 * \log \left(\frac{\sqrt{\sum_{n=2}^5 H_n^2}}{H_1} \right) \quad (9.4)$$

Typical HF2 Instrument settings that influence the THD are the input and output ranges, the measurement frequency, and the selected input coupling (AC or DC).

Setup

Connect the Signal Output 1 Out to the Signal Input 1 +In using a short BNC cable. The setup is shown in the figure below.

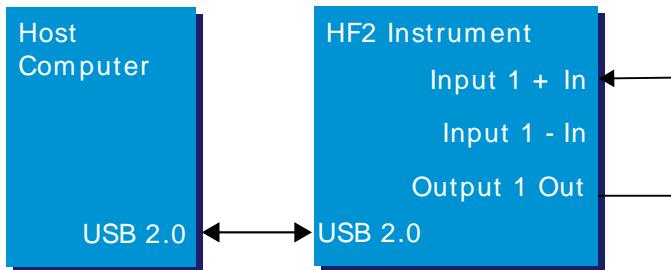


Figure 9.29. THD test setup

The HF2 instrument settings on the ziControl panel should be according to the table below:

Table 9.20. HF2 instrument settings

Ch1 Signal Inputs Range	1.0 V
Ch1 Signal Inputs AC/ Diff / 50	OFF/ OFF /OFF
Ch1 Frequency (Hz)	1 MHz
Ch1 Demodulators 1/ 2 /3 Harm	1/ 2 /3 (1/ 4/ 5)
Ch1 Demodulators 1/ 2 /3/ Osc	1
Ch1 Filters 1/ 2 /3 Input	1
Ch1 Filters 1/ 2 /3 BW	1 Hz
Ch1 Filters 1/ 2 /3 dB/Oct	24
Ch1 Demodulators 1/ 2 /3 Readout	Cont.
Ch1 Signal Output Range	1 V
Ch1 Signal Output Amplitude	100 mV

Measurement

The user should perform the test in two or more steps to make sure that all the significant harmonics are included. Measure the first three harmonics 1, 2, and 3 in the first step. Continue with harmonics 1, 4, and 5 in the second step. Calculate the THD using the simplified formula given in the definition section.

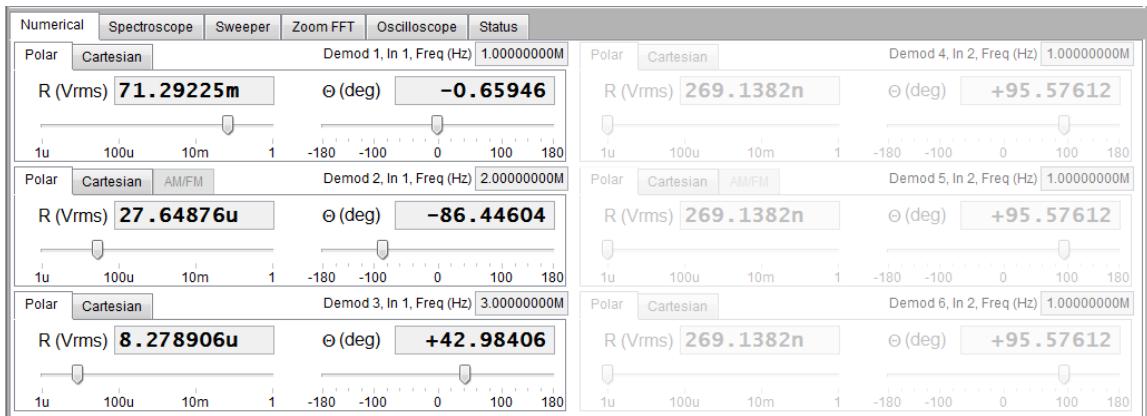


Figure 9.30. THD test results for harmonics n=1, 2, 3

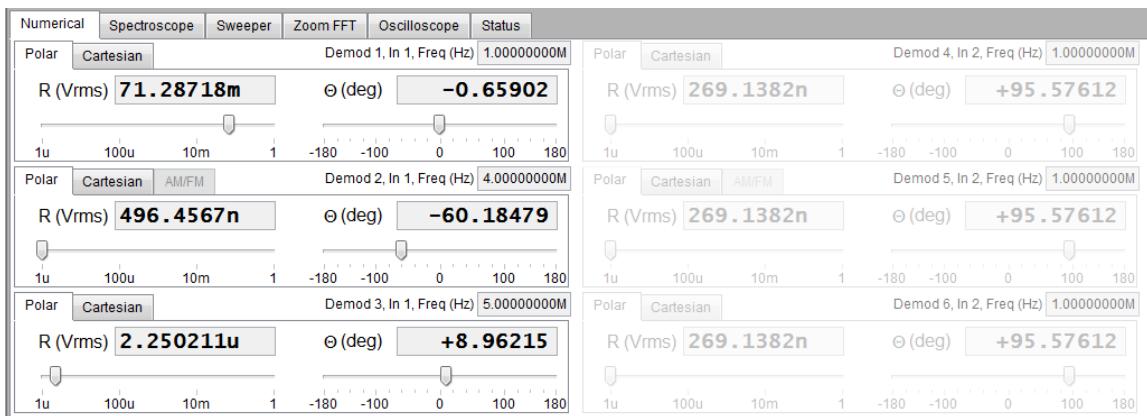


Figure 9.31. THD test results for harmonics n=1, 4, 5

Using the first five harmonic amplitudes, measured at 1 MHz, the THD of the HF2 instruments input and output is approximately - 68 dB.

9.7.5. Test Lock time

Definition

The lock time of a lock-in amplifier is the time the instrument takes to react on a sudden change of the external reference frequency. This is technically equivalent to the time the internal phase-locked loop takes to reach the locked state starting from an unlocked state. It is usually given as the number of reference signal cycles (periods) or in seconds. The lock time can be calculated as, $T_{lock\ time} = T_{lock} - T_{unlock}$, where T_{unlock} is the time at which an external signal with frequency f_s is applied at the signal inputs, and T_{lock} is the time after which the reference frequency is within the 0.5% of f_s .

The HF2 Instrument graphical panel also displays a locked state indicator which turns green when additional phase error conditions are met.

Typical HF2 Instrument settings that influences the lock time depends on the external reference frequency and amplitude, the input range.

Setup

Connect the Signal Output 2 Out connector to the Signal Input 1 +In connector using a short BNC cable. The setup is shown in the figure below.

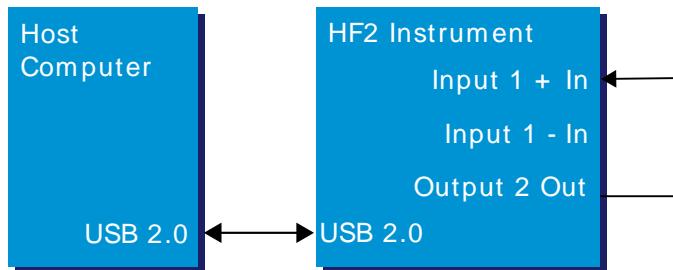


Figure 9.32. Lock time test setup

The HF2 Instrument settings on the ziControl panel should be according to the table below:

Table 9.21. HF2 instrument settings

Ch1 Signal Inputs Range	200 mV
Ch1 Signal Inputs AC/ Diff/ 50	ON/ OFF/ OFF
Ch1 Reference	Signal In 1 (auto)
Ch1 Frequency	45 Hz
Ch1 Filters dB/Oct	24
Ch1 Filters BW	1 Hz
Ch1 Demodulator 1 Readout	28.1
Ch1 Demodulators 1	ON
Ch2 Outputs range	1 V
Ch2 Outputs	ON
Ch2 Outputs Amplitude	0 V, (unlocked state)
Ch2 Outputs Amplitude	100 mV (at T_{unlock} , external signal applied)
Ch2 Frequency (Hz)	50 Hz

Measurement

The measurements are performed using the Spectroscope tool with the following settings:

Table 9.22. HF2 Spectroscope settings

Time Scale	1.00 s/Div
Control	Demod 1
Display Plot Value	R, Freq
Cursors	C1, C2

Observe the Demod 1 values (R and Freq.) vs time for a few seconds and then change the amplitude of Ch2 from 0 V to 0.1 V. Stop the acquisition when the frequency is settled to the 50 Hz value and after the locked indicator turned green. Use the activated cursors to mark the onset of the external signal application as well as the position in time where the frequency has settled. In this particular case the estimated lock time is $T_{lock\ time} = 1.21 s, as shown in the figure below, which is approximately 60 cycles.$

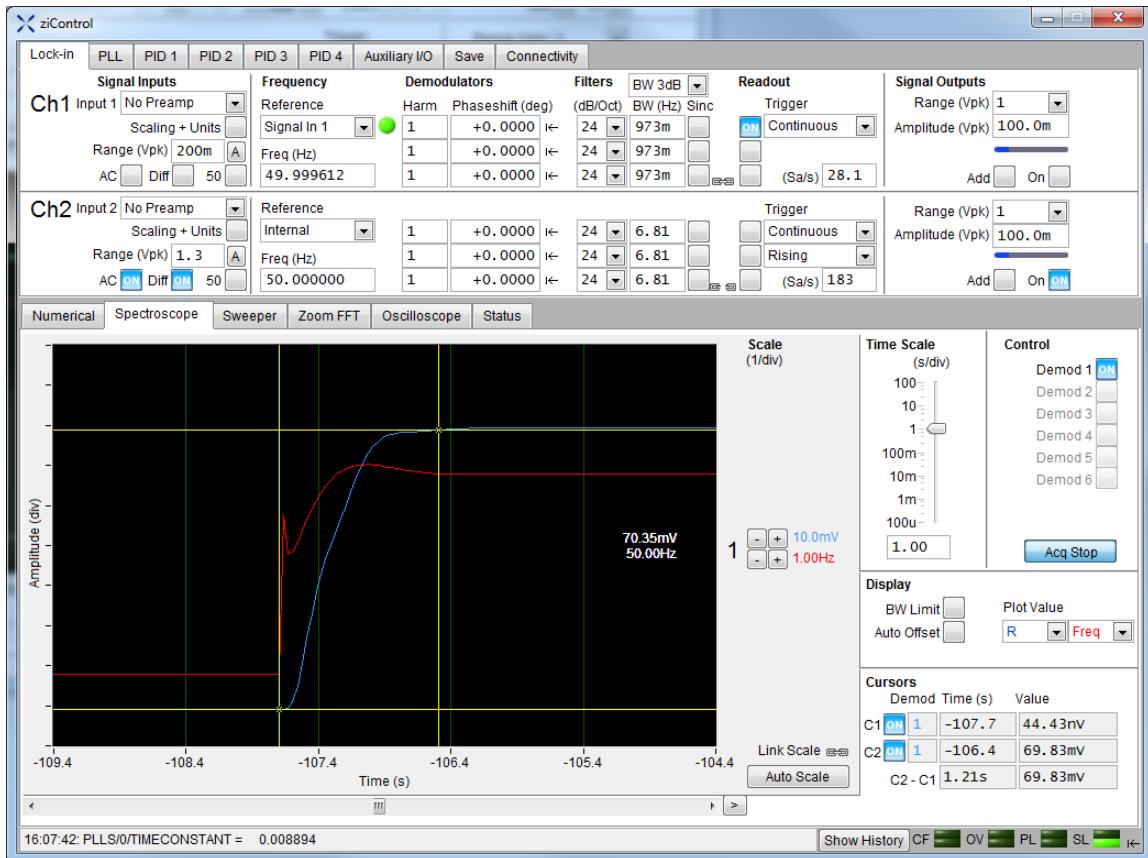


Figure 9.33. Lock time test results for frequency of 50 Hz

Note that the Spectroscopic tool is suitable for monitoring the HF2 instrument's locking to low frequencies, where the lock time is larger. For external frequencies higher than 1 MHz, user is advised to save the desired demodulator data and plot demodulator frequency as a function of time. Make sure that the sampling rate is increased accordingly with the signal frequency to obtain sufficient time stamp resolution. The PLL lock time can be then determined similarly to the procedure described above.

9.7.6. Test Frequency Accuracy

Definition

Frequency accuracy (FA) is a measure of an instrument's ability to faithfully indicate (f_m) the correct frequency versus a traceable standard of an internal or external reference signal (f_r). It is usually given in ppm (parts per million) and can be calculated using the expression,

$$FA(\text{ppm}) = \frac{f_r - f_m}{f_m} * 1000000 = \frac{f_e}{f_r} * 1000000 \quad (9.5)$$

where, $f_e = f_r - f_m$ is the frequency measurement error.

Setup

Connect the output of a function generator to the Input 2 + In, using a short BNC cable. The function generator should have at least 3 ppm frequency accuracy. The setup is shown in the figure below.

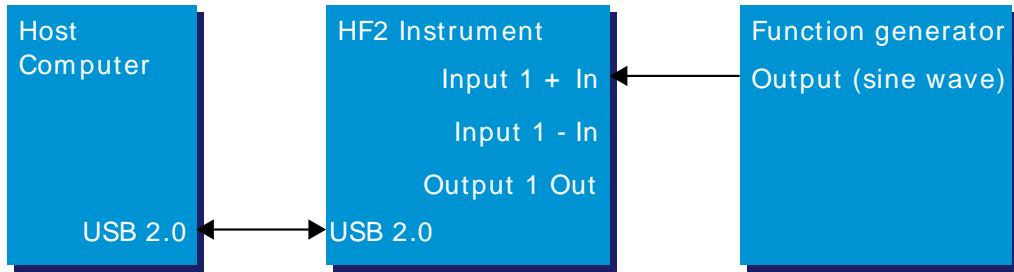


Figure 9.34. Frequency accuracy test setup

The HF2 instrument settings on the ziControl panel should be set according to the table below:

Table 9.23. HF2 instrument settings

Ch2 Signal Inputs Range	1.0 V
Ch2 Signal Inputs AC/ Diff/ 50	OFF/ OFF/ OFF
Ch2 Frequency Reference	Signal Input 2

Measurement

The measurement procedure consists of the following steps:

1. Set the amplitude of the function generator to 500 mV.
2. Set the reference frequency, f_r , of the function generator using the table below, or any other frequency of interest.
3. Wait for the first five digits of the measured frequency, f_m , to stabilize.
4. Read out the displayed reference frequency and calculate the error according to $f_e = f_r - f_m$.

Example: For the reference frequency $f_r = 10$ kHz measured frequency is $f_m = 9.99991$ kHz. The frequency error is thus, $f_e = 0.09$ Hz. Finally, frequency accuracy $FA = f_e / f_m * 1000000 = 0.09$ Hz/10 kHz *1000000 = 9 ppm \sim 10 ppm.

Repeat steps 2-4 using frequencies given in the table below.

Table 9.24. Frequency accuracy test table

Reference Frequency	Measured Frequency	Frequency accuracy
1 kHz		
10 kHz		
100 kHz		
1 MHz		
10 MHz		

9.7.7. Test Output Amplitude Accuracy

Definition

The output amplitude accuracy (OAA) accuracy is a measure of an instrument's ability to faithfully output a set voltage at a given frequency. It is usually given as the percentage, %, of the measurement deviation from a traceable standard, and can be calculated using the expression,

$$OAA (\%) = \frac{OA_{expect} - OA_{meas}}{OA_{expect}} * 100 \quad (9.6)$$

Typical HF2 Instrument settings which influence output amplitude accuracy are the output range and the frequency of the output signal. Above a certain frequency the OAA is heavily impacted by the output filters.

Setup

Connect the Output 1 Out of the HF2 to the digital multimeter using a short BNC cable. The total digital multimeter measurement error should be at least 10 times smaller than HF2's measurement error in order for it to be suitable as the test device. The digital multimeter should be adjusted to measure AC voltage. The setup is shown in the figure below.

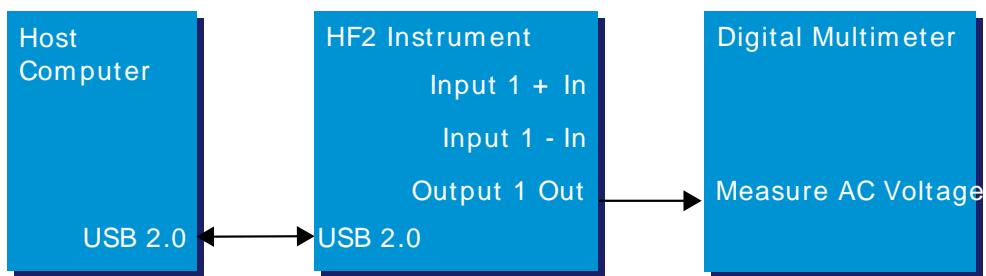


Figure 9.35. Output amplitude accuracy test setup

The HF2 Instrument settings on the ziControl panel should be according to the table below:

Table 9.25. HF2 instrument settings

Ch1 Signal Output Range	Variable
Ch1 Signal Output Amplitude	Variable
Ch1 Signal Output On	ON
Ch1 Frequency (Hz)	10 kHz (or other, subject to multimeter bandwidth)

Measurement

The measurement procedure consists of the following steps:

1. Select the HF2 channel output range.
2. Set the output amplitude, OA_{expect} , to the 10 %, followed by 50 % and 80 % of the output range selected in step 1.
3. Record multimeter reading, OA_{meas} .
4. Calculate OAA using the formula given in the definition section.
5. Calculate total measurement accuracy of the digital multimeter. The total multimeter reading error is specified in the multimeter user manual and an example of how to calculate it is shown below.

Example: the reading error of the set amplitude 80 mV and measured 79.95 mV is $0.0006 * 79.95 \text{ mV}$, plus the range error of $0.0003 * 100 \text{ mV}$. This gives the total measurement error of $\pm 0.07797 \text{ mV}$ and a total multimeter measurement accuracy of $0.07797 \text{ mV} / 80 \text{ mV} * 100 = 0.09746 \% \sim 0.1 \%$.

6. Compare the digital multimeter measurement accuracy to that of OA. It should be at least an order of magnitude better than the OAA of the HF2 Instrument. If this is not the case, this multimeter cannot be used for accuracy measurements in this particular range.
7. Go through steps 1-6 for all the output ranges, as indicated in the test table.

Table 9.26. Output amplitude accuracy test table

Output range	Output amplitude	Multimeter reading	Accuracy (%)
1 V			
100 mV			
10 mV			

9.7.8. Test Input Amplitude Accuracy

Definition

The input amplitude accuracy (IAA) is a measure of instrument's capability to faithfully indicate the signal amplitude at the input channel. It is given usually as percentage, %, of the measurement deviation from a traceable standard, and can be calculated using the expression,

$$\text{IAA} (\%) = \frac{\text{IA}_{\text{expect}} - \text{IA}_{\text{meas}}}{\text{IA}_{\text{expect}}} * 100 \quad (9.7)$$

Typical HF2 Instrument settings that influence input amplitude accuracy are the input range, measurement frequency, input impedance and selected coupling (AC or DC). Above a certain frequency, the IAA is heavily impacted by the input anti-aliasing filters.

This parameter is also called input gain accuracy.

Setup

Use a T-connector to split the output of a function generator and feed it into the HF2 Signal Input 1 +In, and a digital multimeter. Adjust the digital multimeter for AC voltage measurements. The digital multimeter's total measurement error should be at least 10 times smaller than HF2's measurement error in order for it to be suitable as the test device. In addition, multimeter's bandwidth for AC voltage measurements is usually limited to 10 or 20 kHz. To measure the accuracy at higher frequencies, a spectrum analyzer may be used. The setup is shown in the figure below.

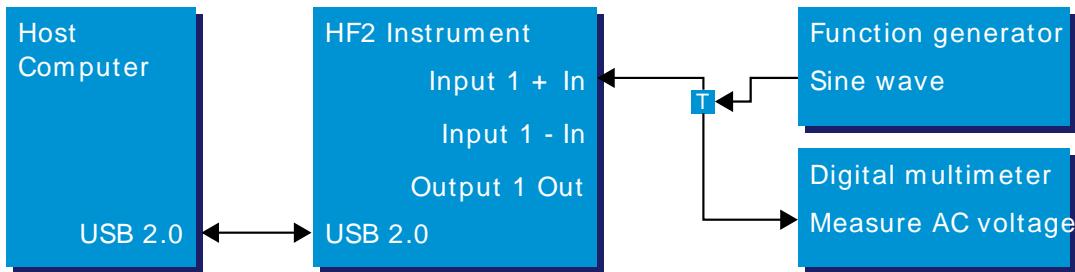


Figure 9.36. Input amplitude accuracy test setup

The HF2 instrument settings on the ziControl panel should be set according to the table below:

Table 9.27. HF2 instrument settings

Ch1 Signal Inputs Range	Variable
Ch1 Signal Inputs AC/ Diff/ 50	OFF/ OFF/ OFF
Ch1 Frequency Reference	Signal Input 1
Ch1 Demodulators 1 Harm	1
Ch1 Demodulators 1 Readout	Cont.
Ch1 Filters TC (s)	100 ms
Ch1 Filters dB/Oct	48

Measurement

The measurement procedure consists of the following steps:

1. Select the HF2 input channel range.
2. Set the frequency of the function generator to 10 kHz and amplitudes to the 10 %, followed by 50% and 80% of the input range set in step 1.
3. Wait for the measured signal to settle and read out the RMS amplitudes of the HF2 Demodulator 1, IA_{meas} , and the digital multimeter, IA_{expect} .
4. Calculate IA accuracy according to the formula given in the definition section.
5. Calculate total measurement error with given accuracy of the digital multimeter. The total multimeter reading error is specified in the multimeter user manual and an example of how to calculate it is shown below.

Example: for 100 mV amplitude whose RMS amplitude is 70.71 mV, digital multimeter reading error is $0.0006 * 71.07$ mV, plus the range error of $0.0003 * 100$ mV. This gives the total measurement error of ± 0.0726 mV and accuracy of 0.0726 mV/ 100 mV* 100 ~ 0.1 %.

6. Compare the digital multimeter measurement accuracy to that of IA. It should be at least an order of magnitude better than that of HF2 IA accuracy. If this is not the case, this multimeter cannot be used for accuracy measurements in this particular range.
7. Repeat steps 1-6 for all input ranges, as indicated in the test table.

Table 9.28. Input amplitude accuracy test table

Input range	Multimeter reading	HF2 reading	Accuracy (%)
1 V			

100 mV			
10 mV			

Chapter 10. Signal Processing Basics

This chapter provides insights about several lock-in amplifier principles not necessarily linked to a specific instrument from Zurich Instruments. Since the appearance of the first valve-based lock-in amplifiers in the 1930s the physics have not changed, but the implementation and the performance have evolved greatly. Many good lock-in amplifier primers have appeared in the past decades, and some of them appear outdated now because they were written with analog instruments in mind. This section does not aim to replace any existing primer, but to complete them with a preferred emphasis on digital lock-in amplifiers.

The first subsection describes the principles of lock-in amplification, followed by the description of the function of discrete-time filters. After, we discuss the definition of the full range sensitivity, a specification parameter particularly important for analog lock-in amplifiers but with somewhat reduced importance for digital instruments. In the following, we describe the function and use of sinc filtering in particular for low-frequency lock-in measurements. The last section is dedicated to the zoom FFT feature. Innovative in the context of lock-in amplifiers, zoom FFT offers a fast and high-resolution spectral analysis around the lock-in operation frequency.

10.1. Principles of Lock-in Detection

Lock-in demodulation is a technique to measure the amplitude A_s and the phase Θ of a periodic signal with the frequency $\omega_s = 2\pi f_s$ by comparing the signal to a reference signal. This technique is also called phase-sensitive detection. By averaging over time the signal-to-noise ratio (SNR) of a signal can be increased by orders of magnitude, allowing very small signals to be detected with a high accuracy making the lock-in amplifier a tool often used for signal recovery. For both signal recovery and phase-sensitive detection, the signal of interest is isolated with narrow band-pass filtering therefore reducing the impact of noise in the measured signal.

Figure 10.1 shows a basic measurement setup: a reference V_r signal is fed to the device under test. This reference signal is modified by the generally non-linear device with attenuation, amplification, phase shifting, and distortion, resulting in a signal $V_s = A_s \cos(\omega_s t + \Theta_s)$ plus harmonic components.

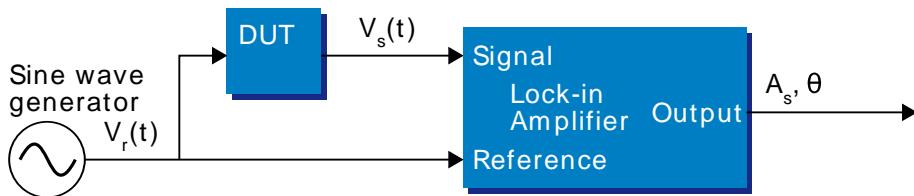


Figure 10.1. Basic measurement setup incorporating a lock-in amplifier

For practical reasons, most lock-in amplifiers implement the band-pass filter with a mixer and a low-pass filter (depicted in Figure 10.2): the mixer shifts the signal of interest into the baseband, ideally to DC, and the low-pass filter cuts all unwanted higher frequencies.

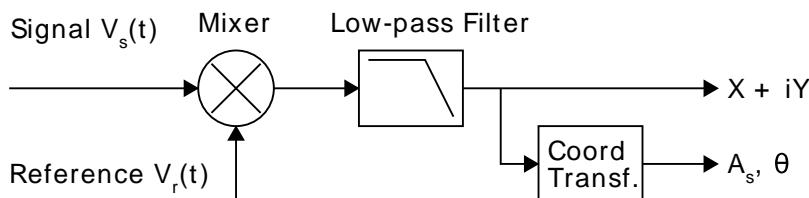


Figure 10.2. Mixing and low-pass filtering performed by the lock-in amplifier

The input signal $V_s(t)$ is multiplied by the reference signal $V_r(t) = \sqrt{2} e^{i\omega_r t}$, where $\omega_r = 2\pi f_r$ is the demodulation frequency and i is the imaginary unit. This is the complex representation of a sine and cosine signal (phase shift 90°) forming the components of a quadrature demodulator, capable of measuring both the amplitude and the phase of the signal of interest. In principle it is possible to multiply the signal of interest with any frequency, resulting in a heterodyne operation. However the objective of the lock-in amplifier is to shift the signal as close as possible to DC, therefore the frequency of the reference and the signal is chosen similar. In literature this is called homodyne detection, synchrodyne detection, or zero-IF direct conversion.

The result of the multiplication is the signal

$$V_s(t) \cdot V_r(t) = V_s(t) \cdot \sqrt{2} e^{i\omega_r t} = \frac{A_s}{\sqrt{2}} e^{i[(\omega_s - \omega_r)t + \Theta]} + \frac{A_s}{\sqrt{2}} e^{i[(\omega_s + \omega_r)t + \Theta]}$$

Equation 10.1. Multiplication of signal of interest with reference signal

It consists of a slow component with frequency $\omega_s - \omega_r$ and a fast component with frequency $\omega_s + \omega_r$.

The demodulated signal is then low-pass filtered with an infinite impulse response (IIR) RC filter, indicated by the symbol $\langle \cdot \rangle$. The frequency response of the filter $F(\omega)$ will let pass the low frequencies $F(\omega_s - \omega_r)$ while considerably attenuating the higher frequencies $F(\omega_s + \omega_r)$. Another way to consider the low-pass filter is an averager.

$$X + iY = \langle V_s(t) \cdot \sqrt{2} e^{i\omega_r t} \rangle \approx F(\omega_s - \omega_r) \frac{A_s}{\sqrt{2}} e^{i[(\omega_s - \omega_r)t + \Theta]}$$

Equation 10.2. Averaging the result of the signal multiplication

The result after the low-pass filter is the demodulated signal $X+iY$, where X is the real and Y is the imaginary part of a signal depicted on the complex plane. These components are also called in-phase and quadrature components. The transformation of X and Y into the amplitude R and phase Θ information of $V_s(t)$ can be performed with trigonometric operations.

It is interesting to note that the value of the measured signal corresponds to the RMS value of the signal, which is equivalent to $R = A_s/\sqrt{2}$.

Most lock-in amplifiers output the values (X, Y) and (R, Θ) encoded in a range of -10 V to $+10 \text{ V}$ of the auxiliary output signals.

10.1.1. Lock-in Amplifier Applications

Lock-in amplifiers are employed in a large variety of applications. In some cases the objective is measuring a signal with good signal-to-noise ratio, and then that signal could be measured even with large filter settings. In this context the word phase sensitive detection is appropriate. In other applications, the signal is very weak and overwhelmed by noise, which forces to measure with very narrow filters. In this context the lock-in amplifier is employed for signal recovery. Also, in another context, a signal modulated on a very high frequency (GHz or THz) that cannot be measured with standard approaches, is mixed to a lower frequency that fits into the measurement band of the lock-in amplifier.

One example for measuring a small, stationary or slowly varying signal which is completely buried in the $1/f$ noise, the power line noise, and slow drifts. For this purpose a weak signal is modulated to a higher frequency, away from these sources of noise. Such signal can be efficiently mixed back and measured in the baseband using a lock-in amplifier. In [Figure 10.3](#) this process is depicted. Many optical applications perform the up-mixing with a chopper, an electro-optical modulator, or an acousto-optical modulator. The advantage of this procedure is that the desired signal is measured in a spectral region with comparatively little noise. This is more efficient than just low-pass filtering the DC signal.

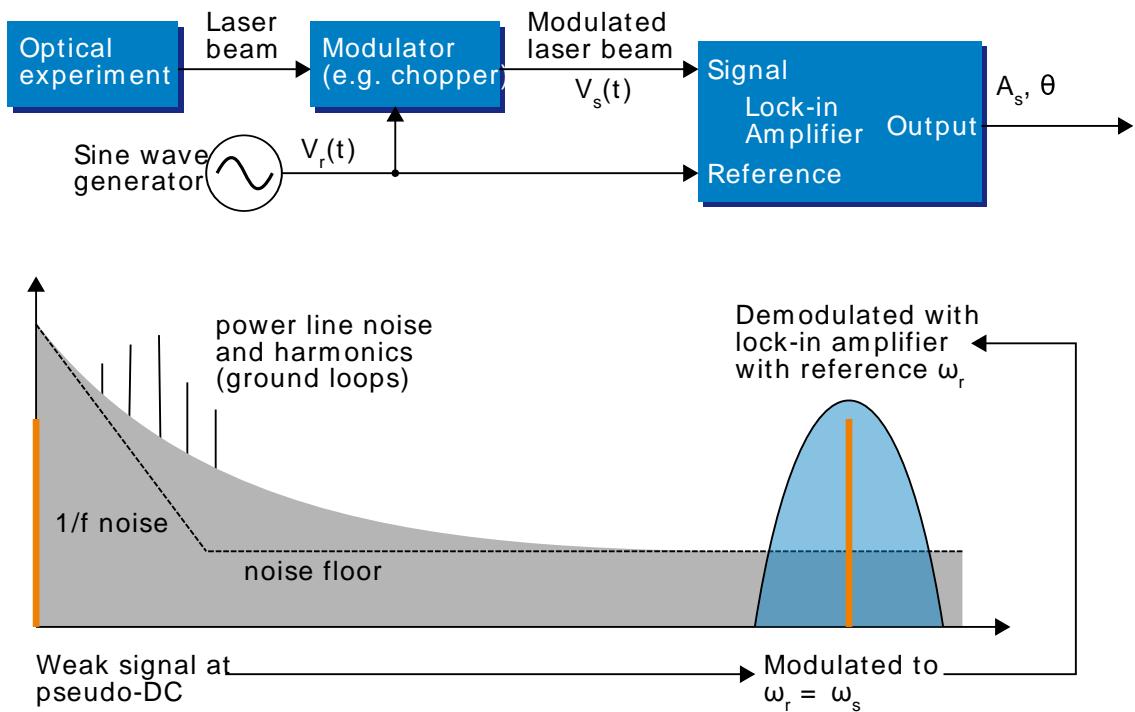


Figure 10.3. Lock-in measurement of a noisy DC signal

10.2. Signal Bandwidth

The signal bandwidth (BW) theoretically corresponds to the highest frequency components of interest in a signal. In practical signals, the bandwidth is usually quantified by the cut-off frequency. It is the frequency at which the transfer function of a system shows 3 dB attenuation relative to DC ($BW = f_{\text{cut-off}} = f_{-3\text{dB}}$); that is, the signal power at $f_{-3\text{dB}}$ is half the power at DC. The bandwidth, equivalent to cut-off frequency, is used in the context of dynamic behavior of a signals or separation of different signals. This is for instance the case for fast-changing amplitudes or phase values like in a PLL or in imaging applications, or when signals closely spaced in frequency need to be separated.

The noise equivalent power bandwidth (NEPBW) is also a useful figure, and it is distinct from the signal bandwidth. This unit is typically used for noise measurements: in this case one is interested in the total amount of power that passes through a low-pass filter, equivalent to the area under the solid curve in [Figure 10.4](#). For practical reasons, one defines an ideal brick-wall filter that lets pass the same amount of power under the assumption that the noise has a flat (white) spectral density. This brick-wall filter has transmission 1 from DC to f_{NEPBW} . The orange and blue areas in [Figure 10.4](#) then are exactly equal in a linear scale.

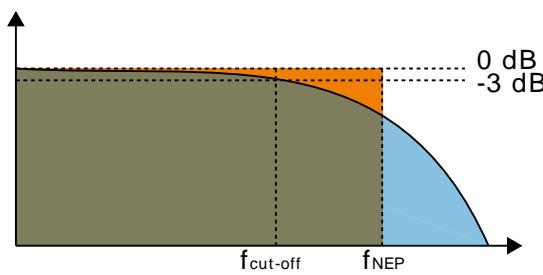


Figure 10.4. Signal bandwidth and noise equivalent power bandwidth

It is possible to establish a simple relation between the $f_{\text{cut-off}}$ and the f_{NEPBW} that only depends on the slope (or roll-off) of the filter. As the filter slope actually depends on the time constant (TC) defined for the filter, it is possible to establish the relation also to the time constant. It is intuitive to understand that for higher filter orders, the $f_{\text{cut-off}}$ is closer to the f_{NEPBW} than for smaller orders.

The time constant is a parameter used to interpret the filter response in the time domain, and relates to the time it takes to reach a defined percentage of the final value. The time constant of a low-pass filter relates to the bandwidth according to the formula

$$TC = \frac{FO}{2\pi f_{\text{cut-off}}} \quad (10.3)$$

where FO is said factor that depends on the filter slope. This factor, along with other useful conversion factors between different filter parameters, can be read from the following table.

Table 10.1. Summary of conversion factors for bandwidth definitions

filter order	filter roll-off	FO	$f_{\text{cut-off}}$	f_{NEPBW}	$f_{\text{NEPBW}} / f_{\text{cut-off}}$
1 st	6 dB/oct	1.0000	0.1592 / TC	0.2500 / TC	1.5708
2 nd	12 dB/oct	0.6436	0.1024 / TC	0.1250 / TC	1.2203
3 rd	18 dB/oct	0.5098	0.0811 / TC	0.0937 / TC	1.1554
4 th	24 dB/oct	0.4350	0.0692 / TC	0.0781 / TC	1.1285
5 th	30 dB/oct	0.3856	0.0614 / TC	0.0684 / TC	1.1138

filter order	filter roll-off	FO	$f_{\text{cut-off}}$	f_{NEPBW}	$f_{\text{NEPBW}} / f_{\text{cut-off}}$
6 th	36 dB/oct	0.3499	0.0557 / TC	0.0615 / TC	1.1046
7 th	42 dB/oct	0.3226	0.0513 / TC	0.0564 / TC	1.0983
8 th	48 dB/oct	0.3008	0.0479 / TC	0.0524 / TC	1.0937

10.3. Discrete-Time Filters

10.3.1. Discrete-Time RC Filter

There are many options how to implement digital low-pass filters. One common filter type is the exponential running average filter. Its characteristics are very close to those of an analog resistor-capacitor RC filter, which is why this filter is sometimes called a discrete-time RC filter. The exponential running average filter has the time constant $TC = \tau_N$ as its only adjustable parameter.

It operates on an input signal $X_{in}[n, T_S]$ defined at discrete times $nT_S, (n+1)T_S, (n+2)T_S$, etc., spaced at the sampling time T_S . Its output $X_{out}[n, T_S]$ can be calculated using the following recursive formula,

$$X_{out}[n, T_S] = e^{-T_S/\tau_N} X_{out}[n-1, T_S] + (1 - e^{-T_S/\tau_N}) X_{in}[n, T_S]$$

Equation 10.4. Time domain response of the discrete-time RC filter

The response of that filter in the frequency domain is well approximated by the formula

$$H_1(\omega) = \frac{1}{1 + i \cdot \omega \cdot \tau_N}$$

Equation 10.5. Frequency domain response of the first-order discrete-time RC filter

The exponential filter is a first-order filter. Higher-order filters can easily be implemented by cascading several filters. For instance the 4th order filter is implemented by chaining 4 filters with the same time constant $TC = \tau_N$ one after the other so that the output of one filter stage is the input of the next one. The transfer function of such a cascaded filter is simply the product of the transfer functions of the individual filter stages. For an n-th order filter, we therefore have

$$H_n(\omega) = \frac{1}{(1 + i \cdot \omega \cdot \tau_N)^n}$$

Equation 10.6. Frequency domain response of the n-th order discrete-time RC filter

The attenuation and phase shift of the filters can be obtained from this formula. Namely, the filter attenuation is given by the absolute value squared $|H_n(\omega)|^2$. The filter transmission phase is given by the complex argument $\arg[H_n(\omega)]$.

10.3.2. Filter Settling Time

The low-pass filters after the demodulator cause a delay to measured signals depending on the filter order and time constant $TC = \tau_N$. After a change in the signal, it will therefore take some time before the lock-in output reaches the correct measurement value. This is depicted in [Figure 10.5](#) where the response of cascaded filters to a step input signal this is shown.

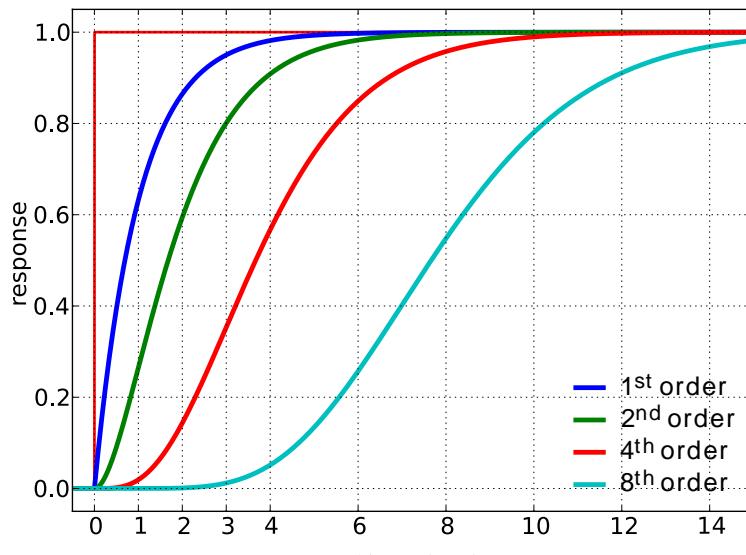


Figure 10.5. Time domain step response of the RC low-pass filters

More quantitative information on the settling time can be obtained from [Table 10.2](#). In this table, you find settling times in units of the filter time constant τ_N for all filter orders available with the HF2 Series Lock-in Amplifier. The values tell the time you need to wait for the filtered demodulator signal to reach 5%, 95% and 99% of the final value. This can help in making a quantitatively correct choice of filter parameters for example in a measurement involving a parameter sweep.

Table 10.2. Summary of Filter Rise Times

filter order	Setting time to		
	5%	95%	99%
1 st	0.025 · TC	3.0 · TC	4.6 · TC
2 nd	0.36 · TC	4.7 · TC	6.6 · TC
3 rd	0.82 · TC	6.3 · TC	8.4 · TC
4 th	1.4 · TC	7.8 · TC	10 · TC
5 th	2.0 · TC	9.2 · TC	12 · TC
6 th	2.6 · TC	11 · TC	12 · TC
7 th	3.3 · TC	12 · TC	15 · TC
8 th	4.0 · TC	13 · TC	16 · TC

10.4. Full Range Sensitivity

The sensitivity of the lock-in amplifier is the RMS value of an input sine that is demodulated and results in a full scale analog output. Traditionally the X, Y, or R components are mapped onto the 10 V full scale analog output. In such a case, the overall gain from input to output of the lock-in amplifier is composed of the input and output amplifier stages. Many lock-in amplifiers specify a sensitivity between 1 nV and 1 V. In other words the instrument permits an input signal between 1 nV and 1 V to be amplified to the 10 V full range output.

Analog Lock-in Amplifiers:



Digital Lock-in Amplifiers:



Figure 10.6. Sensitivity from signal input to signal output

In analog lock-in amplifiers the sensitivity is simple to understand. It is the sum of the analog amplification stages between the input and the output of the instrument: in particular the input amplifier and the output amplifier.

In digital lock-in amplifiers the sensitivity less straightforward to understand. Analog-to-digital converters (ADC) operate with a fixed input range (e.g. 1 V) and thus require a variable-gain amplifier to amplify the input signal to the range given by the ADC. This variable-gain amplifier must be in the analog domain and its capability determines the minimum input range of the instrument. A practical analog input amplifier provides a factor 1000 amplification, thus 1 V divided by 1000 is the minimum input range of the instrument.

The input range is the maximum signal amplitude that is permitted for a given range setting. The signal is internally amplified with the suited factor, e.g. (1 mV)·1000 to result in a full swing signal at the ADC. For signals larger than the range, the ADC saturates and the signal is distorted – the measurement result becomes useless. Thus the signal should never exceed the range setting.

But the input range is not the same as the sensitivity. In digital lock-in amplifiers the sensitivity is only determined by the output amplifier, which is an entirely digital signal processing unit which performs a numerical multiplication of the demodulator output with the scaling factor. The digital output of this unit is then fed to the output digital-to-analog converter (DAC) with a fixed range of 10 V. It is this scaling factor that can be retrofitted to specify a sensitivity as known from the analog lock-in amplifiers. A large scaling factor, and thus a high sensitivity, comes at a relatively small expense for digital amplification.

One interesting aspect of digital lock-in amplifiers is the connection between input resolution and sensitivity. As the ADC operates with a finite resolution, for instance 14 bits, the minimum signal that can be detected and digitized is for instance 1 mV divided by the resolution of the ADC. With 14 bits the minimum level that can be digitized would be 122 nV. How is it possible to reach 1 nV sensitivity without using a 21 bit analog-to-digital converter? In a world without noise it is not possible. Inversely, thanks to noise and current digital technology it is possible to achieve a sensitivity even below 1 nV.

Most sources of broadband noise, including the input amplifier, can be considered as Gaussian noise sources. Gaussian noise is equally distributed in a signal, and thus generates equally

distributed disturbances. The noise itself can be filtered by the lock-in amplifier down to a level where it does not impact the measurement. Still, in the interplay with the signal, the noise does have an effect on the measurement. The input of the ADC is the sum of the noise and the signal amplitude. Every now and then, the signal amplitude on top of the large noise will be able to toggle the least significant bits even for very small signals, as low as 1 nV and below. The resulting digital signal has a component at the signal frequency and can be detected by the lock-in amplifier.

There is a similar example from biology. Rod cells in the human eye permit humans to see in very low light conditions. The sensitivity of rod cells in the human eye is as low as a single photon. This sensitivity is achieved in low light conditions by a sort of pre-charging of the cell to be sensitive to the single photon that triggers the cell to fire an impulse. In a condition with more surround light, rod cells are less sensitive and need more photons to fire.

To summarize, in digital lock-in amplifiers the full range sensitivity is only determined by the scaling factor capability of the digital output amplifier. As the scaling can be arbitrary big, 1 nV minimum full range sensitivity is achievable without a problem. Further, digital lock-in amplifiers exploit the input noise to heavily increase the sensitivity without impacting the accuracy of the measurement.

10.5. Sinc Filtering

As explained in [Section 10.1](#), the demodulated signal in an ideal lock-in amplifier has a signal component at DC and a spurious component at twice the demodulation frequency. The components at twice the demodulation frequency (called the 2ω component) is effectively removed by regular low-pass filtering. By selecting filters with small bandwidth and faster roll-offs, the 2ω component can easily be attenuated by 100 dB or more. The problem arises at low demodulation frequencies, because this forces the user to select long integration times (e.g. >60 ms for a demodulation frequency of 20 Hz) in order to achieve the same level of 2ω attenuation.

In practice, the lock-in amplifier will modulate DC offsets and non-linearities at the signal input with the demodulation frequency, resulting in a signal at the demodulation frequency (called ω component). This component is also effectively removed by the regular low-pass filters at frequencies higher than 1 kHz.

At low demodulation frequencies, and especially for applications with demodulation frequencies close to the filter bandwidth, the ω and 2ω components can affect the measurement result. Sinc filtering allows for strong attenuation of the ω and 2ω components. Technically the sinc filter is a comb filter with notches at integer multiples of the demodulation frequency (ω , 2ω , 3ω , etc.). It removes the ω component with a suppression factor of around 80 dB. The amount of 2ω component that gets removed depends on the input signal. It can vary from entirely (e.g. 80 dB) to slightly (e.g. 5 dB). This variation is not due to the sinc filter performance but depends on the bandwidth of the input signal.

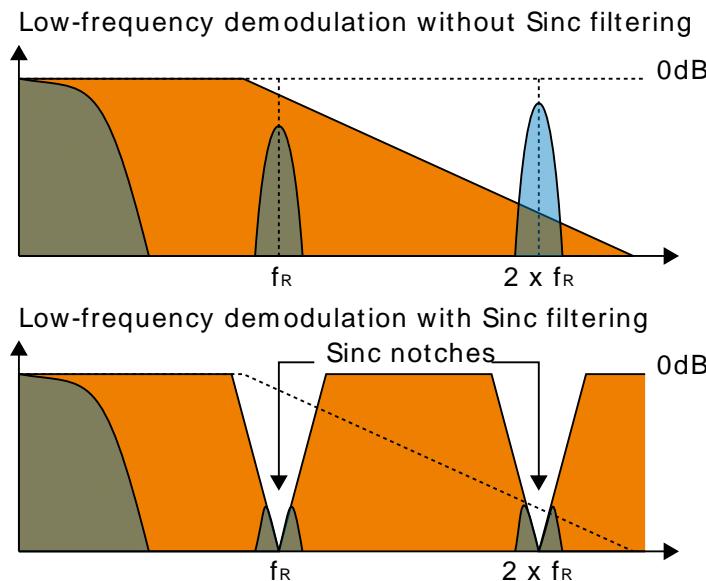


Figure 10.7. Effect of sinc filtering

Table 10.3. Artifacts in the demodulation signal

Input signal	Demodulation result before low-pass filter	Result
Signal at ω	DC component	Amplitude and phase information (wanted signal)
	2ω component	Unwanted component (can additionally be attenuated by sinc filter)

Input signal	Demodulation result before low-pass filter	Result
DC offset	ω component	Unwanted component (can additionally be attenuated by sinc filter)

We can observe the effect of the sinc filter by using the Spectrum Analyzer Tool of the HF2 Series Lock-in Amplifier. As an example, consider a 30 Hz signal with an amplitude of 0.1 V that is demodulated using a filter bandwidth of 100 Hz and a filter order 8. In addition 0.1 V offset is added to the signal so that we get a significant ω component.

Figure 10.8 shows a spectrum with the sinc filter disabled, whereas for Figure 10.9 the sinc filter is enabled. The comparison of the two clearly shows how the sinc options dampens both the ω and 2ω components by about 100 dB.

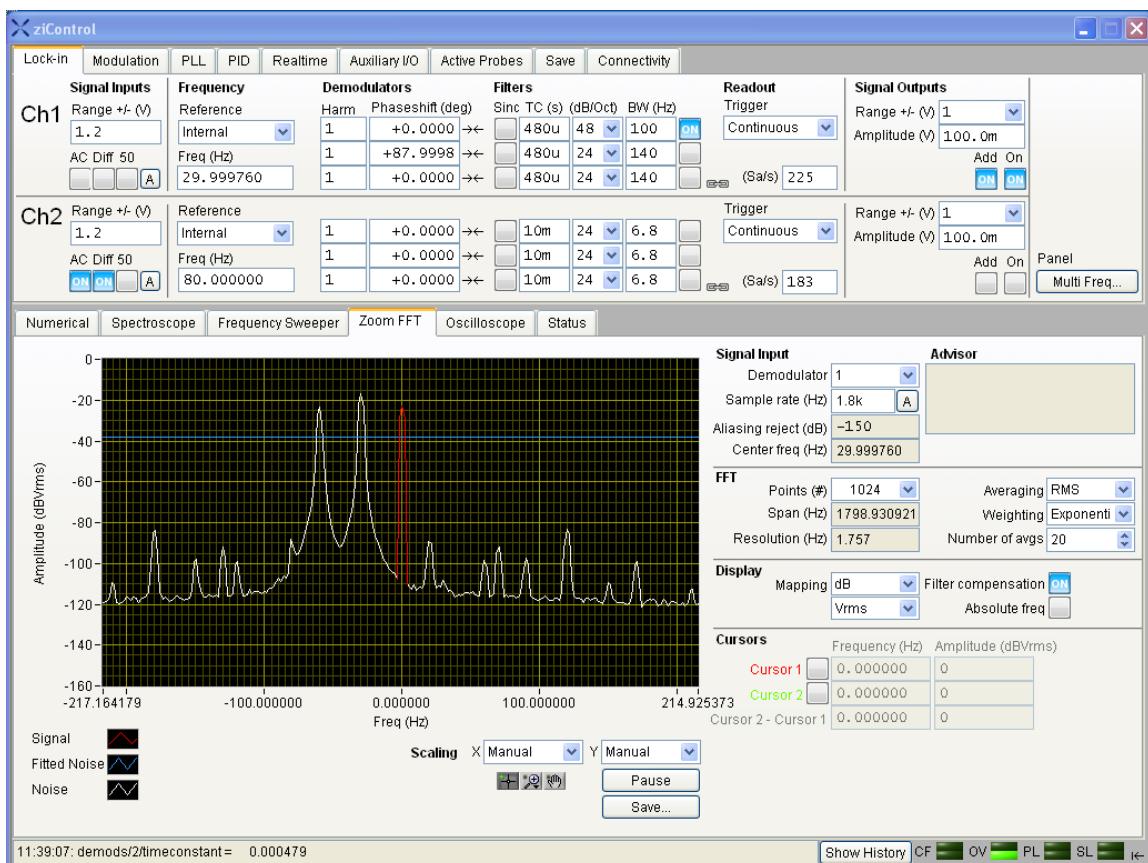


Figure 10.8. Spectrum of a demodulated 30 Hz signal without sinc filter

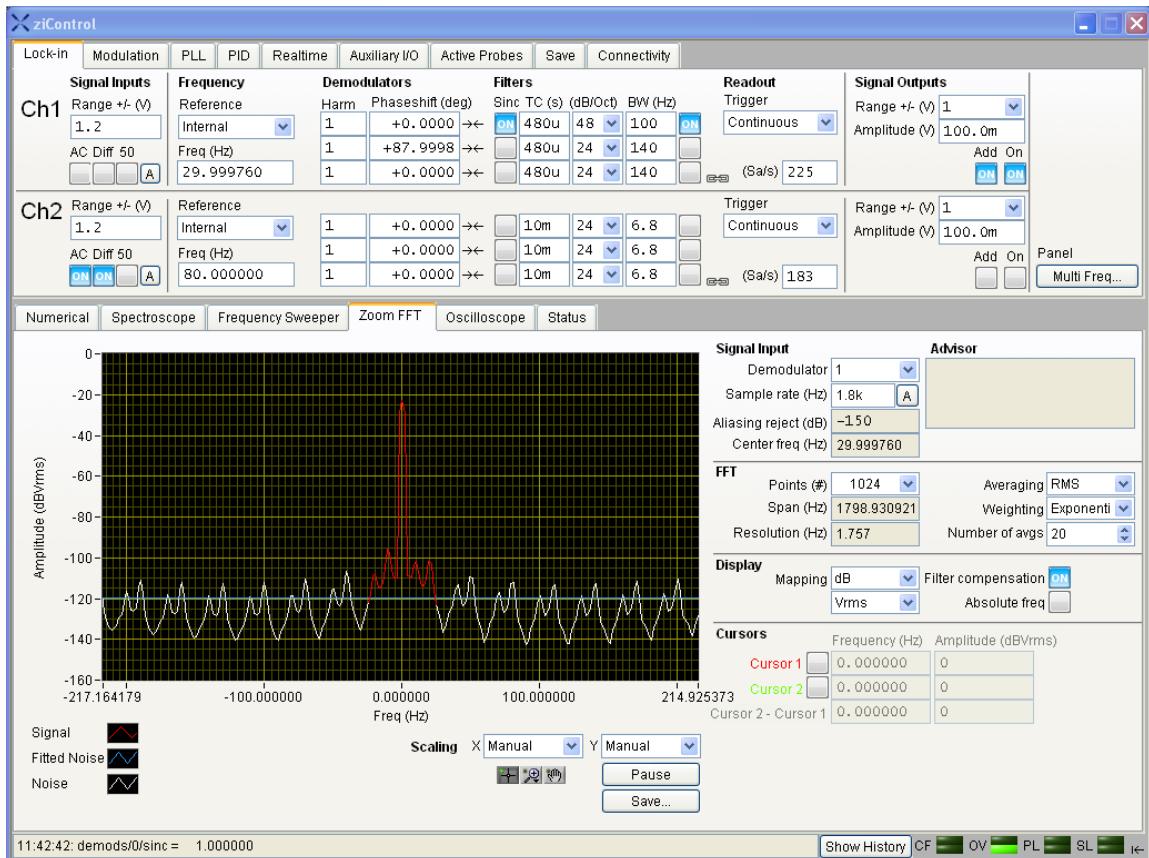


Figure 10.9. Spectrum of a demodulated 30 Hz signal with sinc filter

Note

In order to put the notches of the digital filter to ω and 2ω , the sampling rate of the filter would have to be precisely adjusted to the signal frequency. As this is technically not feasible, the generated signal frequency is adjusted instead by a very small amount.

10.6. Zoom FFT

The concept of zoom FFT allows the user to analyze the spectrum of the input signal around a particular frequency by zooming in on a narrow frequency portion of the spectrum. This is done by performing a Fourier transform of the demodulated in-phase and quadrature (X and Y) components or more precisely, on the complex quantity $X+iY$, where i is the imaginary unit. In the LabOne user interface, this functionality is available in the Spectrum tab.

In normal FFT, the sampling rate determines the frequency span and the total acquisition time determines the frequency resolution. Having a large span and a fine resolution at the same time then requires long acquisition times at high sample rates. This means that a lot of data needs to be acquired, stored, and processed, only to retain a small portion of the spectrum and discard most of it in the end. In zoom FFT, the lock-in demodulation is used to down-shift the signal frequency, thereby allowing one to use both a much lower sampling rate and sample number to achieve the same frequency resolution. Typically, to achieve a 1 Hz frequency resolution at 1 MHz, FFT would require to collect and process approximately 10^6 points, while zoom FFT only processes 10^3 points. (Of course the high rate sampling is done by the lock-in during the demodulation stage, so the zoom FFT still needs to implicitly rely on a fast ADC.)

In order to illustrate why this is so and what benefits this measurement tool brings to the user, it is useful to remind that at the end of the demodulation of the input signal $V_s(t) = A_s \cos(\omega_s t + \Theta)$, the output signal is $X + iY = F(\omega_s - \omega_r)(A_s/\sqrt{2})e^{i[(\omega_s - \omega_r)t + \Theta]}$ where $F(\omega)$ is the frequency response of the filters.

Since the demodulated signal has only one component at frequency $\omega_s - \omega_r$, its power spectrum (Fourier transform modulus squared) has a peak of height $(|A_s|^2/2) \cdot |F(\omega_r - \omega_s)|^2$ at $\omega_s - \omega_r$: this tells us the spectral power distribution of the input signal at frequencies close to ω_r within the demodulation bandwidth set by the filters $F(\omega)$.

Note that:

- the ability of distinguishing between positive and negative frequencies works only if the Fourier transform is done on $X+iY$. Had we taken X for instance, the positive and negative frequencies of its power spectrum would be equal. The symmetry relation $G(-\omega) = G^*(\omega)$ holds for the Fourier transform $G(\omega)$ of a real function $g(t)$ and two identical peaks would appear at $\pm|\omega_s - \omega_r|$.
- one can extract the amplitude of the input signal by dividing the power spectrum by $|F(\omega)|^2$, the operation being limited by the numerical precision. This is implemented in LabOne and is activated by the Filter Compensation button: with the Filter Compensation enabled, the background noise appears white; without it, the effect of the filter roll-off becomes apparent.

The case of an input signal containing a single frequency component can be generalized to the case of multiple frequencies. In that case the power spectrum would display all the frequency components weighted by the filter transfer function, or normalized if the Filter Compensation is enabled.

When dealing with discrete-time signal processing, one has to be careful about aliasing which occurs when the signal frequencies higher than the sampling rate Ω are not sufficiently suppressed. Remember that Ω is the user settable readout rate, not the 210 MSa/s sampling rate of the HF2 Series input. Since the discrete-time Fourier transform extends between $-\Omega/2$ and $+\Omega/2$, the user has to make sure that at $\pm\Omega/2$ the filters provide the desired attenuation: this can be done either by increasing the sampling rate or resolving to measure a smaller frequency spectrum (i.e. with a smaller filter bandwidth).

Similarly to the continuous case, in which the acquisition time determines the maximum frequency resolution ($2\pi/T$ if T is the acquisition time), the resolution of the zoom FFT can be

increased by increasing the number of recorded data points. If N data points are collected at a sampling rate Ω , the discrete Fourier transform has a frequency resolution of Ω/N .

Chapter 11. HF2CA Current Amplifier Data Sheet

This chapter contains the data sheet of the HF2CA Current Amplifier which is a preamplifier dedicated to the HF2 Series instruments. This data sheet is distributed only as part of the HF2 User Manual, and therefore not available separately.

The content of the chapter starts with the list of key features of the preamplifier, and continues with sections including the specifications, the detailed functional description, several possible applications, and finally an extended recommendation for 3rd party cables and connectors.

11.1. Key Features

- Current amplifier for high capacitive loads - shunt resistor based
- Voltage amplifier with selectable gain 1 or 10
- Input impedance switchable between 10 V/A and 1 MV/A
- Bandwidth from DC up to 100 MHz
- 2 differential amplification channels with switchable AC/DC coupling
- Adjustable output gain of 1 or 10
- Very low noise and small input leakage
- Single connector for power supply and control

The HF2CA current amplifier converts a differential input current to a differential output voltage in a wide frequency range. This device functions as an active probe and is conveniently placed close to the measurement setup. It supports applications with high capacitive loads such as dielectric impedance spectroscopy. When no shunt resistor is selected, the current amplifier works as a voltage amplifier. The careful design of the HF2CA insures stable operation over the entire frequency range.

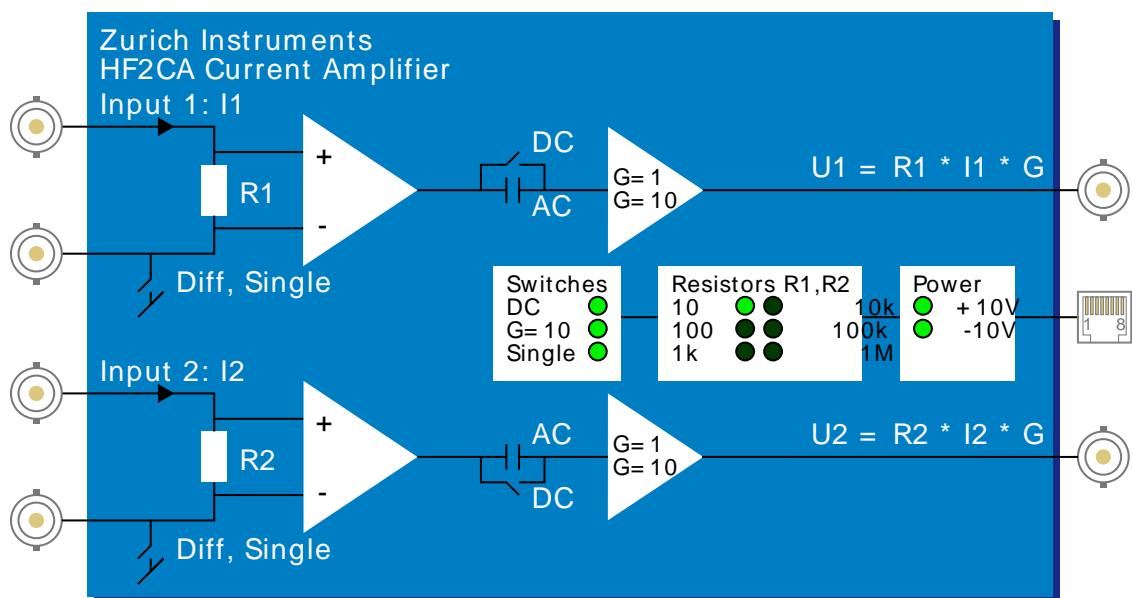


Figure 11.1. HF2CA functional overview

11.2. Specifications

Unless otherwise stated, all specifications apply after 30 minutes of device warming up.

Table 11.1. General

Parameter	Description		
dimensions	100 x 60 x 25 mm		
weight	0.4 kg		
storage temperature	-20 °C to 65 °C		
operating temperature	5 °C to 40 °C		
specification temperature	25 °C		
specification supply voltage	12 V		
connectors	4 SMB inputs, 2 SMB outputs, 1 RJ45 (no Ethernet)		

Table 11.2. Specifications

Parameter	min	typ	max
positive supply voltage VDD+	12 V	15 V	20 V
negative supply voltage VDD-	-20 V	-15 V	-12 V
supply current	60 mA	80 mA	120 mA
frequency response			
frequency range	DC	-	100 MHz
frequency range (AC coupled)	100 Hz	-	100 MHz
small signal bandwidth / 3dB cut-off (0.1 V _{PP} input, 50 pF output load, gain 1)	100 MHz	-	-
small signal bandwidth / 3dB cut-off (0.1 V _{PP} input, 50 pF output load, gain 10)	25 MHz	-	-
large signal bandwidth / 3dB cut-off (1 V _{PP} , 50 pF)	40 MHz	-	-
input			
input voltage noise (10 kHz)	-	7 nV/√Hz	-
input voltage noise (10 MHz)	-	6 nV/√Hz	-
input bias current	-	2 pA	10 pA
transimpedance gain (equivalent to input impedance)	10 V/A	-	1 MV/A
transimpedance gain accuracy (G=1)	-	±0.1 %	-
transimpedance gain accuracy (G=10)	-	±1 %	-
input offset voltage	-	-	1 mV
common-mode offset range	-10 V	-	7.5 V
output			
output voltage gain	1	-	10
control interface			
input high level	2.0 V	-	5 V
input low level	0 V	-	0.8 V

Parameter	min	typ	max
all transitions on SDI, SDO, SCK, SLC	-	-	1 μ s
SCK clock period	10 μ s	-	-
SDI data to clock setup t_{DS}	2 μ s	-	-
SDI data hold from clock t_{DH}	1 μ s	-	-
SLC clock to latch setup t_{LS}	1 μ s	-	-
SLC latch hold t_{LH}	10 μ s	-	20 μ s
SCK clock free time t_{CF}	20 μ s	-	-

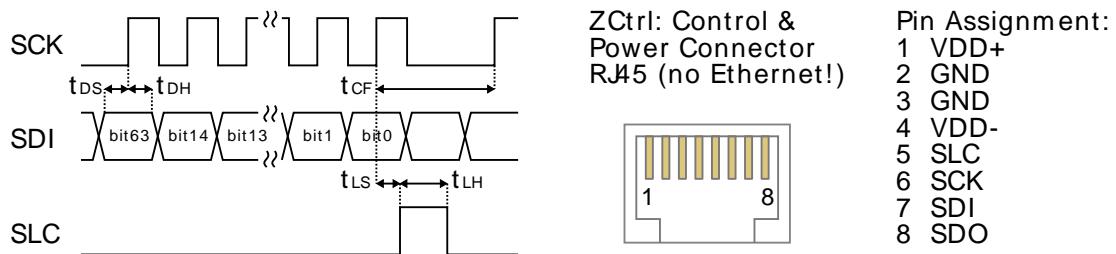


Figure 11.2. Digital control timing

Some parameters depend on the transimpedance gain settings. The following table provides an overview. The values in this table are typical values, they depend on the source capacitance, on the input signal swing, and as well as on the capacitive load on the output of the amplifier.

Table 11.3. Gain dependent parameters

Input impedance setting	Bandwidth / 3dB cut-off frequency	Maximum input current range	Maximum input current noise
10 V/A	100 MHz	± 160 mA	400 pA/ $\sqrt{\text{Hz}}$
100 V/A	50 MHz	± 16 mA	42 pA/ $\sqrt{\text{Hz}}$
1 kV/A	5 MHz	± 1.6 mA	5.6 pA/ $\sqrt{\text{Hz}}$
10 kV/A	500 kHz	± 160 μ A	1.3 pA/ $\sqrt{\text{Hz}}$
100 kV/A	50 kHz	± 16 μ A	400 fA/ $\sqrt{\text{Hz}}$
1 MV/A	5 kHz	± 1.6 μ A	128 fA/ $\sqrt{\text{Hz}}$

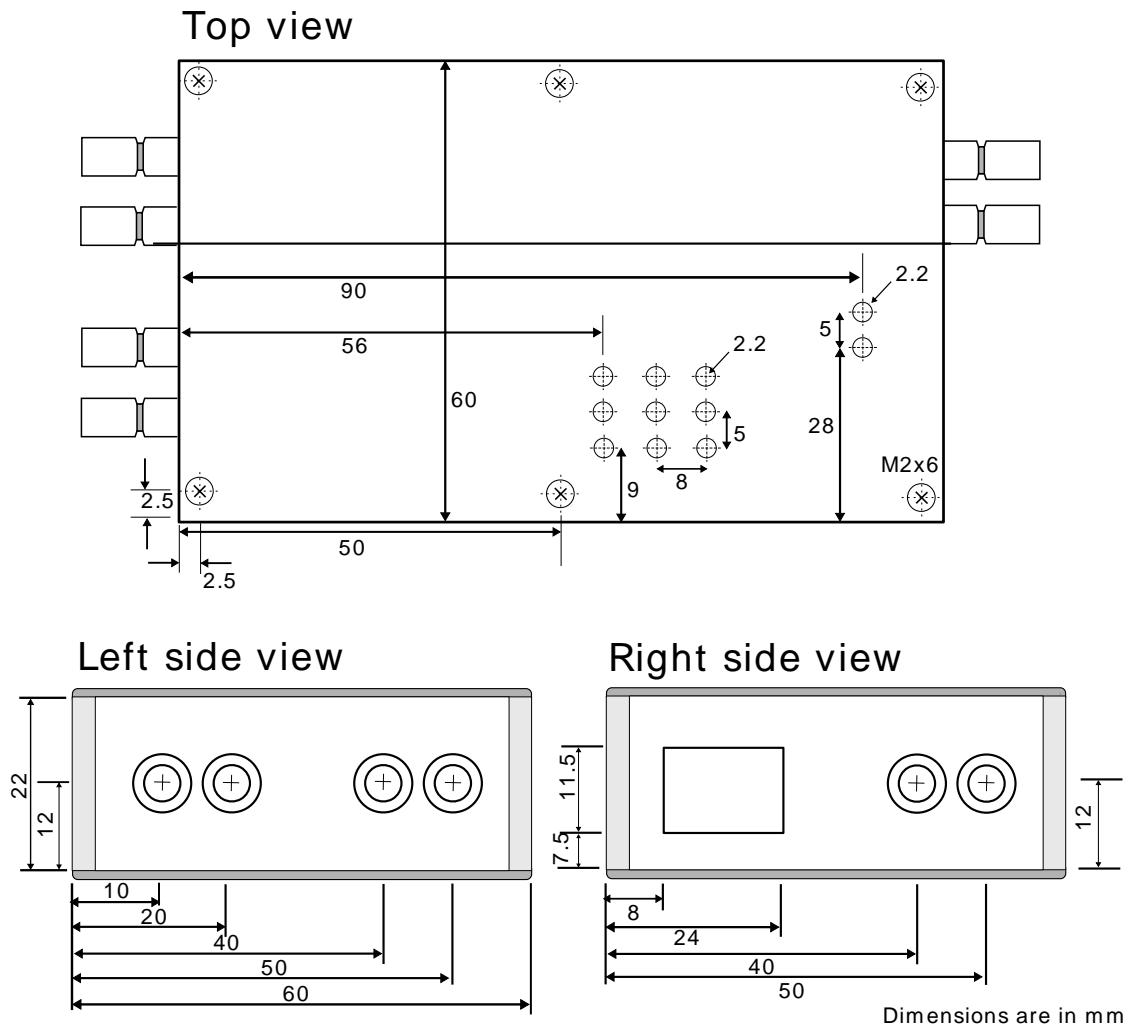


Figure 11.3. Casing dimensions of the HF2CA

11.3. Functional Description

The HF2CA external amplifier can be placed close to the signal source whereas the HF2 Instrument can be several meters away. Such a setup significantly improves the measurement quality due to less parasitics effects and to smaller interferences.

The two signal channels of the HF2CA can be used as separate amplification channels, or alternatively, in differential mode connected to the differential input of the HF2 Instrument.

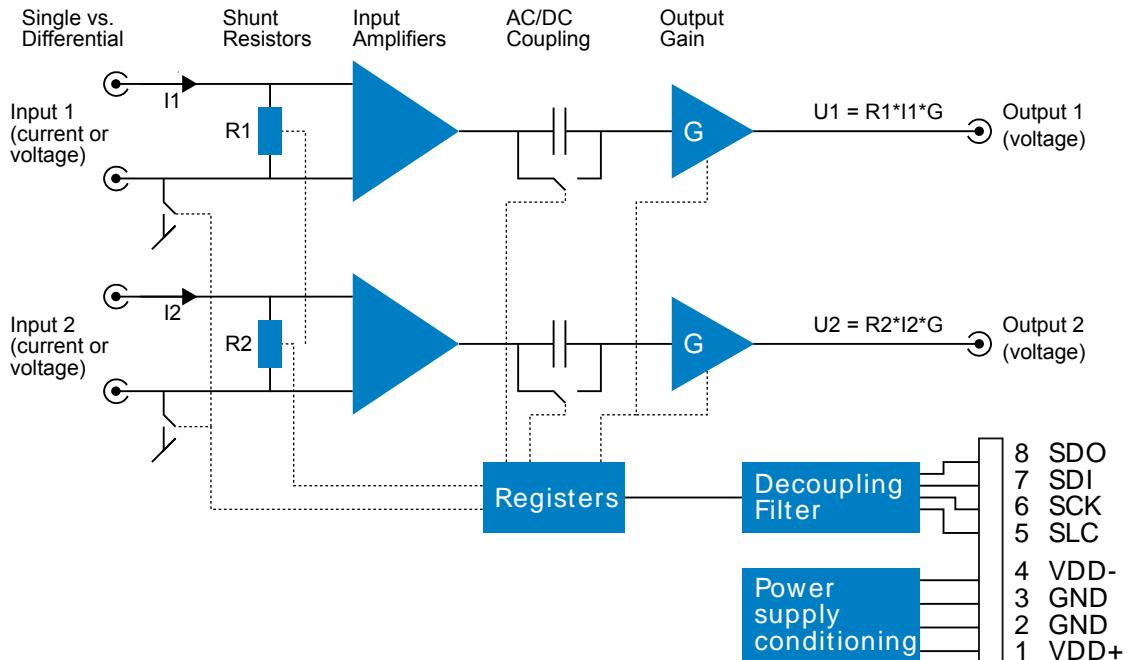


Figure 11.4. Detailed block diagram

11.3.1. Input and Output

Shunt resistors: HF2CA measures the current between the positive and the negative input terminal by measuring the voltage drop across a resistor, that is shunted between the inputs (see Figure 11.4). The supported resistor values are given in Table 11.3. It is also possible to remove all internal resistors and to support any custom resistor that is externally connected (see Section 11.4.4). When all resistors are removed (infinite impedance), then the HF2CA becomes a voltage amplifier with selectable gains 1 and 10.

JFET input amplifiers: the HF2CA is based on JFET input amplifiers that provide very low-noise over a wide frequency range. Additionally, the ultra-low input bias current of typically 2 pA allows for precise current measurements at small signal amplitudes. The input voltage range of the JFET input amplifiers is -10 V to 7.5 V for each input which is also the common mode offset range.

Single vs. differential mode: a selectable switch to amplifier ground allows the user to earth the negative terminal of each input and to operate in single-ended mode without needing external circuits. Alternatively, when leaving the ground switches open, it is possible to use a differential input signal or to connect the negative terminals to local ground externally.

AC vs. DC mode: a selectable switch after the input amplifiers allows the user to measure DC or close to DC signals, or when this is not required, to select AC coupling with a cut-off frequency at 100 Hz and eliminate potential 50/60 Hz noise from the measured signal.

11.3.2. Power Supply and Remote Control

The HF2CA is designed for use with the HF2 Series with its differential signal for improved signal-to-noise, and a single cable that provides power and control signals. A straight-through (as opposed to cross-over) Ethernet cable must be used. The cable carries the following signals:

- **Power:** positive and negative supply, ground
- **Digital control:** SDI digital input signal to control the preamplifier settings, SDO output signal for device detection (details of function not disclosed to users), SCK clock signal, and SLC latch signal. SDI, SCK and SLC are used to program the shift registers on the amplifier and thereby adjust the correct settings. The setting bits are given in [Table 11.4](#). The timing diagram of the digital interface is given in [Figure 11.2](#). The MSB of the register settings is shifted in first.

Table 11.4. HF2CA register settings

Register bit	Name	Description
15 to 10	-	unused
9	gain	0: set output gain to 1
		1: set output gain to 10
8	dcswitch2	0: set AC coupling for input 2
		1: set DC coupling for input 2
7	dcswitch1	0: set AC coupling for input 1
		1: set DC coupling for input 1
6	singleswitch	0: set differential operation
		1: set single-ended operation
5	res1m	1: set resistor 1 MV/A
4	res100k	1: set resistor 100 kV/A
3	res10k	1: set resistor 10 kV/A
2	res1k	1: set resistor 1 kV/A
1	res100	1: set resistor 100 V/A
0	res10	1: set resistor 10 V/A

11.4. Applications

- Impedance spectroscopy
- Large capacitive loads
- Wheatstone-bridge configuration
- Preamplifier for HF2IS impedance spectrometer and HF2LI lock-in amplifier

11.4.1. Differential Current Measurement with Common-mode Offset

The resistors at the input of the amplifier can be inserted in a current path as shown in the figures. With this, fast current transients can be measured at large common-mode voltages, which are in the range from -10 V to 7.5 V. This is used in, e.g., high-energy physics to record the radiation-induced current in a photo diode.

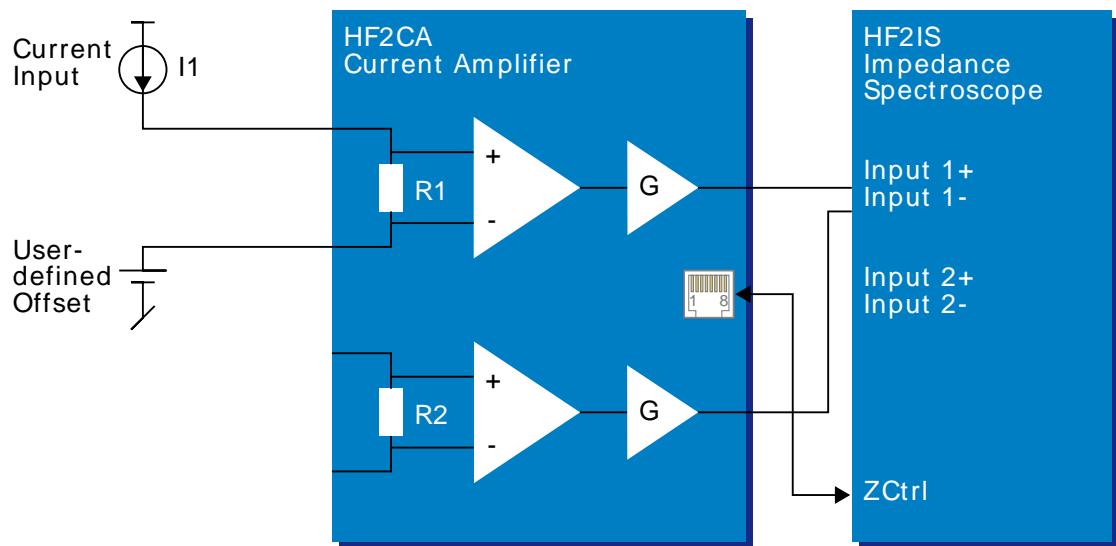


Figure 11.5. HF2CA differential current measurement

11.4.2. Multi-frequency Impedance Spectroscopy

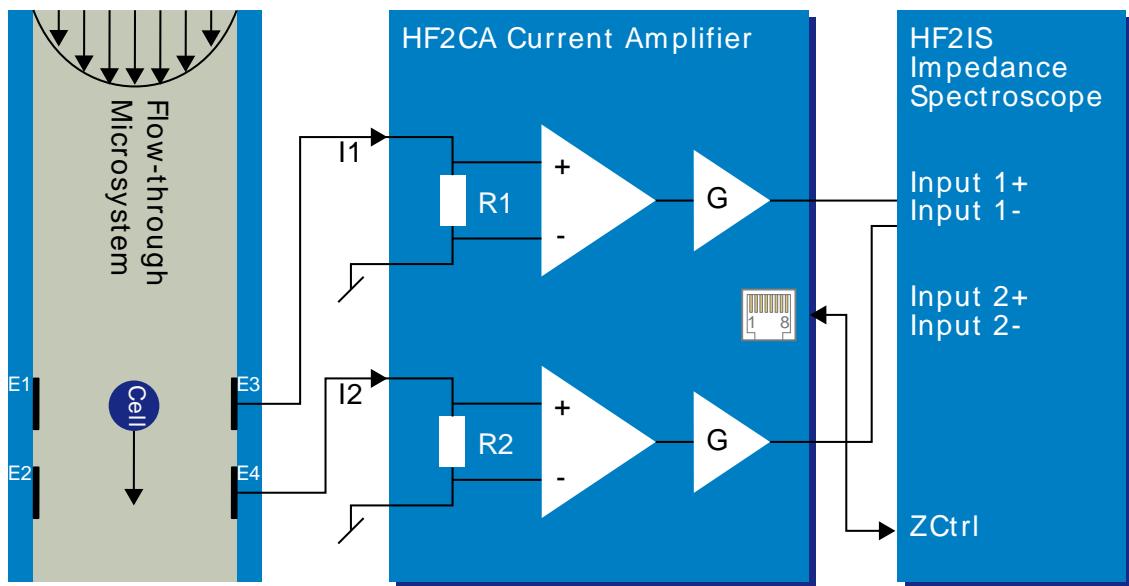


Figure 11.6. HF2CA impedance spectroscopy

The HF2CA in combination with the HF2IS impedance spectrometer is the solution to measure impedance in, for example, flow-through microsystems. The challenge here is to measure the channel impedance at high frequencies (>10 MHz). The large capacitance occurring at electrode electrolyte interfaces can lead to stability issues in a transimpedance amplifier. A solution is to use the electrodes in a Wheatstone bridge configuration with shunt resistors. The HF2CA offers this solution.

As shown in the figure, electrodes are placed on the channel walls of a microfluidic channel (width in the order of 20 to 50 μm). Electrodes E1 and E2 are stimulated with a sinusoidal voltage, the electrodes E3 and E4 are connected to the positive amplifier inputs and thus shunted to GND via resistors R1 and R2. The resulting voltage drops across R1 and R2 are given by the channel impedance. This impedance varies when a particle or a living cell passes the electrode area. An analysis at multiple frequencies at the same time (which is supported by the HF2IS and the HF2CA) allows for concurrently analyzing cell size and dielectric properties. With this information biologists, e.g. sort their cells and detect cell viability or health.

11.4.3. Impedance Measurement

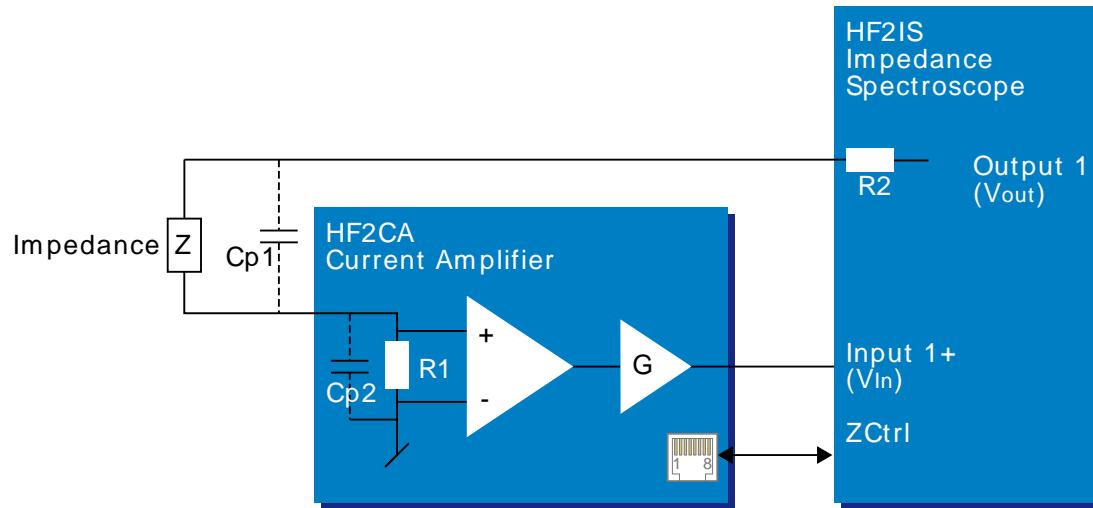


Figure 11.7. Measure an impedance using the HF2CA

The HF2 in conjunction with the HF2CA can be used to measure impedances at various frequencies. The connection diagram is shown in the figure above. The impedance of interest, Z , is connected to the input resistor in the HF2CA preamplifier. For optimal signal-to-noise, the input resistor, $R1$, is set to a value close to the impedance Z . The HF2 generates an output signal of amplitude V_{out} and the output signal from the preamplifier is connected to the positive input (Input +) of the HF2, which is here called V_{in} . With this setup, the impedance Z can be calculated using the following equation:

$$Z = R (V_{\text{out}} - V_{\text{in}}) / V_{\text{in}}$$

Here we neglected the output resistance, $R2$, of the HF2 device. This is valid as long as $Z \gg R1 = 50 \Omega$. Furthermore, at high frequencies the parasitic capacitances $Cp1$ and $Cp2$ will have to be included in the calculation. At even higher frequencies, $Cp1$ and $Cp2$ will be dominant.

11.4.4. Custom Input Impedance

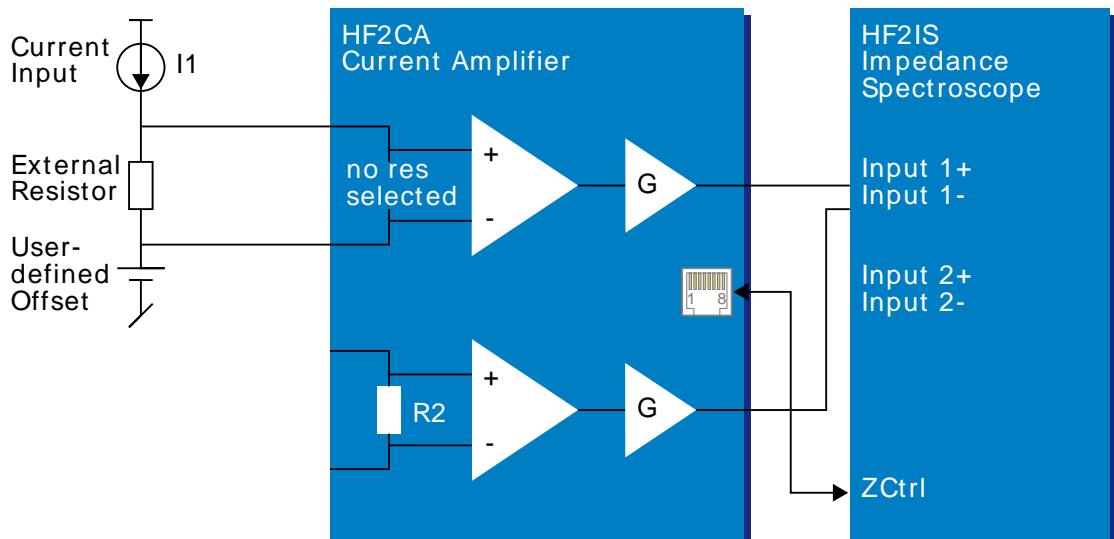


Figure 11.8. HF2CA custom input impedance

Sometimes it is useful to choose a special resistance value in order to optimize the signal to noise by, e.g. impedance matching. In this case, an external resistor can be used instead of using the standard values inside the preamplifier. All internal resistors need to be disconnected in this case, which can be done using the standard preamplifier user interface.

11.5. Cable Recommendation

Table 11.5. HF2CA cable recommendation

Function	Connector / cable type	Vendor / part number
SMB to BNC connection		
SMB to BNC cable	BNC jack to SMB plug	Farnell / Newark 1351896
SMB to BNC adapter	BNC jack to SMB plug	Digikey ACX1386-ND
	BNC jack to SMB jack	Farnell / Newark 4195930
Custom access or cable assembly		
Cable	Cable type RG-174	Digikey A307-100-ND
		Farnell / Newark 1387745
SMB to cable	SMB plug to RG-174 cable	Tyco Electronics 413985-1
		Digikey A4026-ND
		Farnell / Newark 2141206
BNC to cable	BNC plug to RG-174 cable	Tyco Electronics 1-5227079-6
		Digikey A32212-ND
		Farnell / Newark 1831701

Chapter 12. HF2TA Current Amplifier Data Sheet

This chapter contains the data sheet of the HF2TA Current Amplifier which is a preamplifier dedicated to the HF2 Series instruments. This data sheet is distributed only as part of the HF2 User Manual, and therefore not available separately.

The content of the chapter starts with the list of key features of the preamplifier, and continues with sections including the specifications, the detailed functional description, several possible applications, information how to test the specified performance, and finally an extended recommendation for 3rd party cables and connectors.

12.1. Key Features

- 50 MHz operation range
- 2 independent amplification channels with selectable AC/DC coupling
- Wide range of current gain settings (100 V/A to 100 MV/A)
- Impedance measurements from $1 \mu\Omega$ to $100 \text{ M}\Omega$
- Input offset voltage adjustment
- Voltage output amplifier with selectable gain 1 or 10
- Very low noise and low input leakage
- Single connector for power supply and control

The HF2TA current amplifier converts 2 input currents to output voltages in a frequency range up to 50 MHz. This device is an active probe which can be conveniently placed close to the measurement setup. It supports most applications where a current must be converted to a voltage. The advanced design of the HF2TA ensures stability and a smooth operation over the entire frequency range. The HF2TA transimpedance current amplifier with the HF2 Series signal analyzers allows for very high performance measurements and insensitivity to interferences thanks to reduced parasitics.

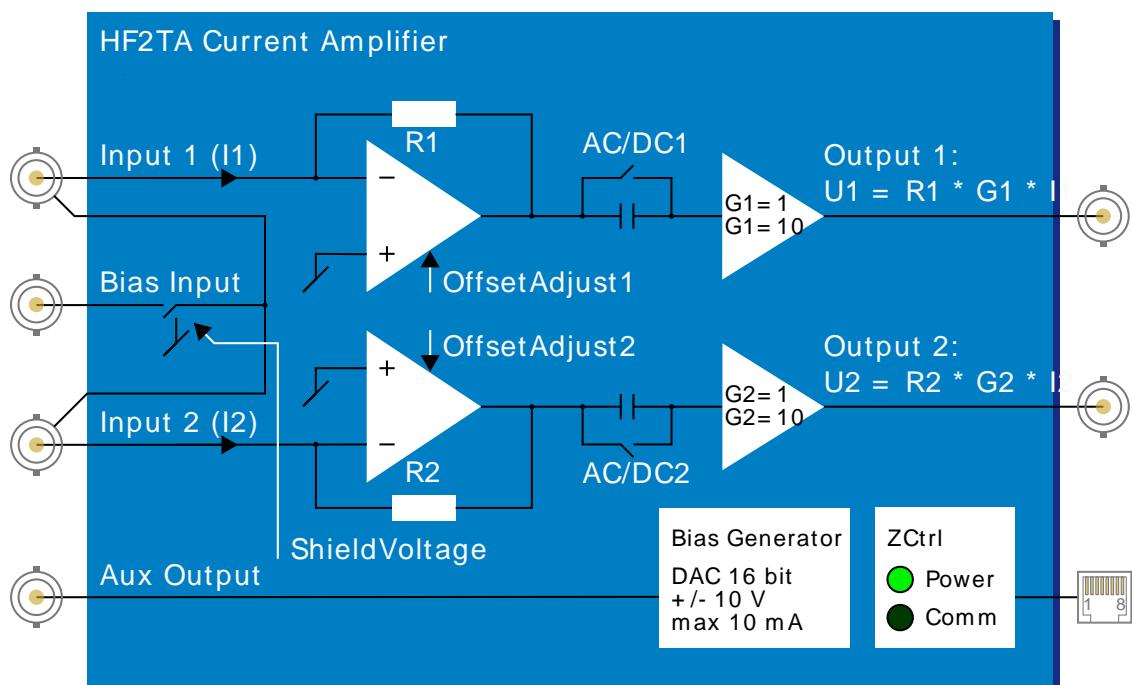


Figure 12.1. HF2TA functional overview

12.2. Specifications

Unless otherwise stated, all specifications apply after 30 minutes of device warming up.

Table 12.1. General

parameter	description
dimensions	101 x 78 x 23 mm
weight	0.4 kg
storage temperature	-20 °C to 65 °C
operating temperature	5 °C to 40 °C
specification temperature	25 °C
connectors	3 SMA inputs female, 3 SMA outputs female, 1 RJ45 (no Ethernet)

Table 12.2. Specifications

parameter	min	typ	max
positive supply voltage VDD+	12 V	13 V	15 V
negative supply voltage VDD-	-15 V	-13 V	-12 V
supply current	50 mA	60 mA	100 mA
frequency response			
frequency range	DC	-	50 MHz
frequency range (AC coupled)	10 Hz	-	50 MHz
small signal bandwidth / 3dB cut-off (0.1 V _{PP} input, 50 pF output load, gain 1)	-	-	50 MHz
small signal bandwidth / 3dB cut-off (0.1 V _{PP} input, 50 pF output load, gain 10)	-	-	50 MHz
large signal bandwidth / 3dB cut-off (1 V _{PP} , 50 pF)	-	-	40 MHz
input			
input current range	depends on R1, R2, G1, G2 settings		
input current noise	depends on R1, R2, G1, G2 settings		
input voltage noise (10 kHz)	-	7 nV/√Hz	-
input voltage noise (10 MHz)	-	5 nV/√Hz	-
input leakage current	-	2 pA	20 pA
input voltage offset compensation range	-10 mV	-	10 mV
input impedance range (Z // 15 pF)	50 Ω	-	70 kΩ
input bias voltage range	-10 V	-	10 V
input signal level (damage threshold)	-5 V	-	5 V
output			
output voltage gain (G1,G2)	1	-	10
transimpedance gain (R1,R2)	100 V/A	-	100 MV/A

parameter	min	typ	max
transimpedance gain accuracy (R1,R2)	-	$\pm 1\%$	-
digital control interface timing			
input high level	2.2 V	-	5 V
input low level	0 V	-	0.8 V
all transitions on SDI, SDO, SCK, SLC	-	-	1 μ s
SCK clock period	10 μ s	-	-
SDI data to clock setup t_{DS}	2 μ s	-	-
SDI data hold from clock t_{DH}	1 μ s	-	-
SLC clock to latch setup t_{LS}	1 μ s	-	-
SLC latch hold t_{LH}	10 μ s	-	20 μ s
SCK clock free time t_{CF}	20 μ s	-	-

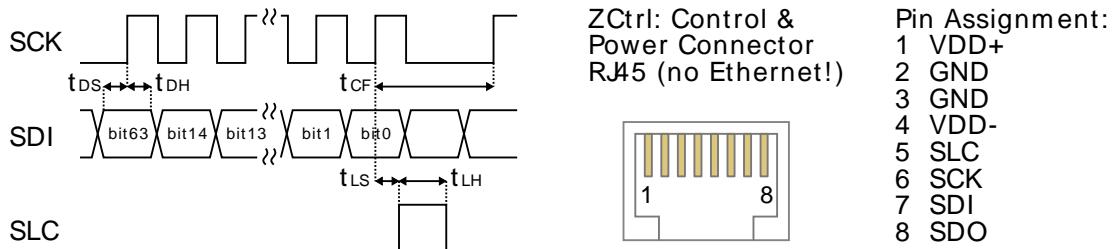


Figure 12.2. Digital control interface timing

Some parameters depend on the transimpedance gain settings. The following table provides an overview. The values in this table are typical values, they depend on the source capacitance, on the input signal swing, and also on the capacitive load on the output of the amplifier.

Table 12.3. Gain dependent parameters 1

input impedance setting	bandwidth / 3dB cut-off	maximum input current range (G=1)	maximum input current range (G=10)
100 V/A	50 MHz	± 10 mA	± 1 mA
1 kV/A	50 MHz	± 1 mA	± 100 μ A
10 kV/A	8 MHz	± 100 μ A	± 10 μ A
100 kV/A	1.5 MHz	± 10 μ A	± 1 μ A
1 MV/A	250 kHz	± 1 μ A	± 100 nA
10 MV/A	25 kHz	± 100 nA	± 10 nA
100 MV/A	12 kHz	± 10 nA	± 1 nA

Table 12.4. Gain dependent parameters 2

input impedance setting	input impedance	maximum input current noise	measured at
100 V/A	50 Ω	150 pA/ $\sqrt{\text{Hz}}$	1 MHz
1 kV/A	50 Ω	15 pA/ $\sqrt{\text{Hz}}$	1 MHz
10 kV/A	50 Ω	2 pA/ $\sqrt{\text{Hz}}$	1 MHz

input impedance setting	input impedance	maximum input current noise	measured at
100 kV/A	100 Ω	500 fA/ $\sqrt{\text{Hz}}$	100 kHz
1 MV/A	300 Ω	250 fA/ $\sqrt{\text{Hz}}$	100 kHz
10 MV/A	1.6 k Ω	100 fA/ $\sqrt{\text{Hz}}$	10 kHz
100 MV/A	70 k Ω	50 fA/ $\sqrt{\text{Hz}}$	10 kHz

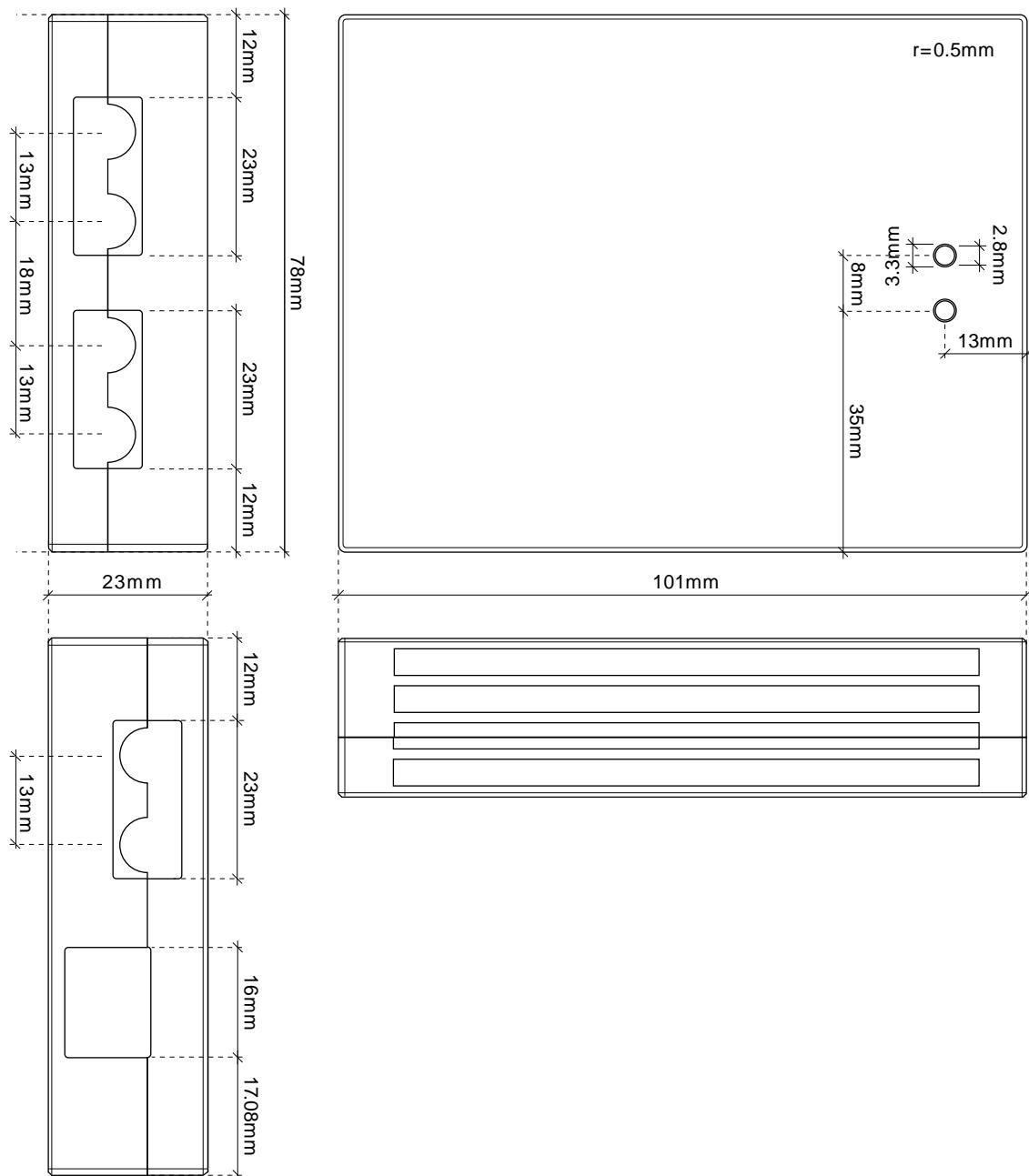


Figure 12.3. Casing dimensions of the HF2TA

12.3. Functional Description

The HF2TA is an external current preamplifier for the HF2 Series instruments from Zurich Instruments. The preamplifier can be placed close to the signal source, which significantly improves the measurement quality due to less parasitics effects and to smaller interferences.

The two signal channels of the HF2TA can be used as separate current amplification channels, or alternatively, in differential mode connected to the differential input of the HF2 Instrument. The channels settings can be set independently.

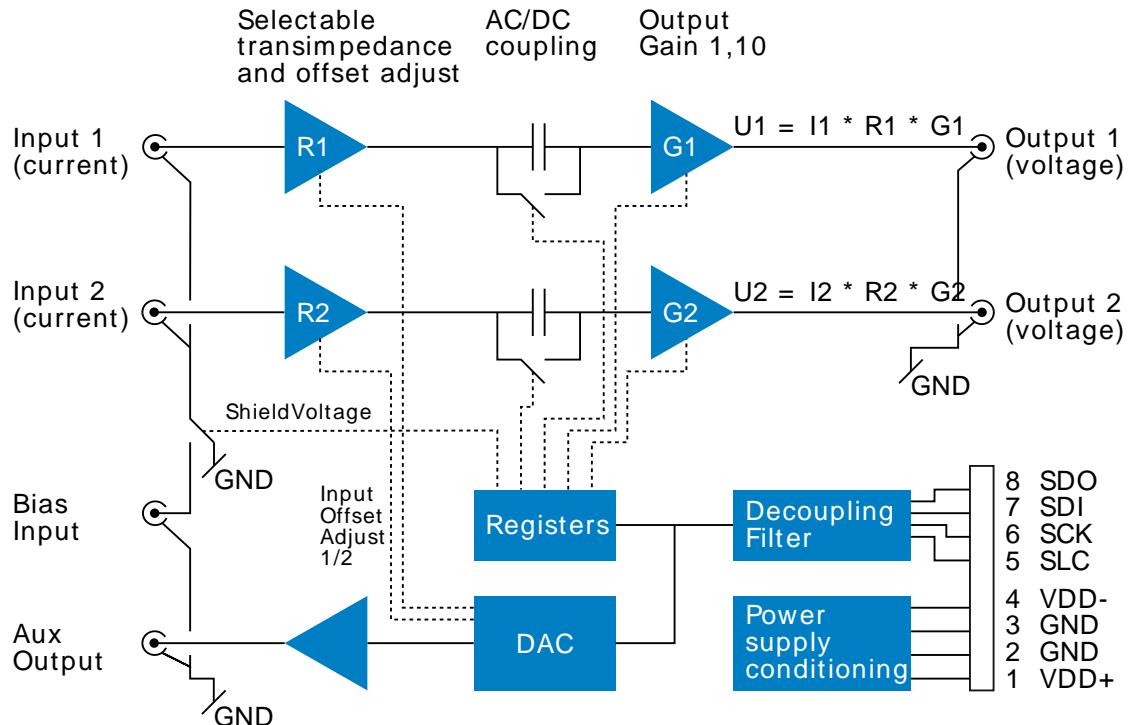


Figure 12.4. Detailed block diagram

12.3.1. Input and Output

Transimpedance stage: the HF2TA measures the current flowing at the two input terminals. The current amplifier uses a standard transimpedance stage to convert the current to a voltage output. The input terminal is matched to 50 Ohms to allow for proper impedance matching at high frequencies. At high current gains, or low input currents, respectively, the input terminal acts like a low-impedance virtual ground. The input impedance depends on the gain settings as described in the table above.

Voltage gain 1 or 10: the HF2TA offers a voltage gain of either 1 or 10 after the transimpedance amplifier. This allows to optimize the signal-to-noise at small amplitudes and high bandwidths. The transimpedance gain often has to be kept small in order to meet the required bandwidth. A voltage amplifier helps in this case to improve the measurement quality.

JFET input amplifiers: the HF2TA is based on JFET input amplifiers that provide very low-noise over a wide frequency range. Additionally, the ultra-low input bias current of typically 2 pA allows for precise current measurements at small signal amplitudes. The input voltage range of the JFET input amplifiers is -5 V to 2 V for each input which is also the common mode offset range.

Offset adjustment: the offset of the input amplifier can be manually compensated. For this purpose, disconnect any signal from the input of the current amplifier and measure the output

voltage. Change the offset voltage until the output is close to zero. All remaining offset should now come from other sources (like offset current or leakage from the device under test).

AC vs. DC mode: a selectable switch after the input amplifiers allows the user to measure DC signals, or when this is not required, to select AC coupling with a cut-off frequency at around 10 Hz to remove the DC offset. When working in AC, make sure that the first amplifier is not saturating. This can be checked by switching to DC and gain 1.

Aux output: the HF2TA comprises a general purpose low-noise analog output. This output can be used as a power supply for, e.g., photo diodes. The photo diode is connected to the auxiliary output and the virtual ground of the input, no additional power supply is needed.

Signal shield voltage: the bias input connector can be used to apply a bias voltage to the signal shield. This can be used, for instance, to power a remote sensor over the signal shield without introducing an additional ground loop. If this option is not used, the signal shield should be conveniently grounded with the control setting "Shield Voltage".

12.3.2. Power Supply and Remote Control

The HF2TA is designed for use with the HF2 Series devices. It has to be connected to the ZCtrl 1/2 connectors of the host device using a single Ethernet cable which provides both power and control signals. A standard straight-through (as opposed to cross-over) cable must be used. The cable carries the following signals:

- **Power:** positive and negative supply, ground
- **Digital control:** SDI digital input signal to control the preamplifier settings, SDO output signal for device detection (details of function not disclosed to users), SCK clock signal, and SLC latch signal. SDI, SCK and SLC are used to program the shift registers on the amplifier and the DAC and thereby adjust the correct settings.

12.4. Applications

- Low-noise and high-speed current amplification
- Photo diode preamplifier
- Impedance measurement
- Semiconductor testing
- Impedance spectroscopy

12.4.1. Recommended Settings

In order to get the maximum performance out of your HF2TA, the following guidelines should be followed.

- low and high input current measurement

The HF2TA gain setting should be selected properly in the measurement path. The gain setting can be set according to [Table 15.3. Gain dependent parameter 1](#). As one can see, each input impedance and G setting has a maximum input current range specified. With each recommended input impedance and G setting, the maximum current will produce the maximum voltage swing of ± 1 V at the output of the HF2TA. At this level the input digitizer of the HF2 input channel will run close to its full dynamic range which results in the optimal SNR.

- low and high bandwidth measurement

HF2TA is specified to work up to the 3dB bandwidth of 50 MHz. Nevertheless, care must be taken when selecting input impedance gain settings. [Table 15.3. Gain dependent parameter 1](#) details as well the maximum 3dB signal bandwidth for each gain setting. For example, with an input current containing frequency components of less than 12 kHz in frequency, the maximum transimpedance gain of 100 MV/A can be selected. At 50 MHz, only 100 V/A of transimpedance gain is available. G=10 can also be selected as well if more gain is required at high input signal frequencies.

- minimize cross-talk and parasitics effects

With the measured impedance placed closely to the input of the HF2TA and the HF2 device, four point measurement setup can help to minimize parasitic effect as well as the noise pickups from the cable. Furthermore, using shielded cable can greatly reduce the high frequency noise pickups from the surrounding environment.

- avoid HF2TA instability

Since HF2TA is a negative feedback amplifier, its feedback loop stability can be sensitive to input capacitance, especially at low R settings. In order to avoid possible under-damped behavior (i.e. oscillation) in the measurement, it is recommended to use as high as possible the selected transimpedance gain R when measuring a capacitive circuit. A short cable to the HF2TA input can also help to reduce the parasitic capacitance seen at the HF2TA input.

12.4.2. Photo Diode Amplifier with HF2LI

The HF2TA current amplifier is suited to read out the current from a photo diode. The following figures shows three possible ways to use the device. In the first option, the photo diode is grounded on one side and connected to the current amplifier on the other side. The recorded signal is amplified and sent to the HF2 Instrument.

The second option provides a solution when it is necessary to apply a bias voltage across the photo diode. For this purpose the auxiliary output of the HF2TA can be used. Voltages in the range of

$\pm 10\text{V}$ and currents up to 10 mA can be delivered by this connector. Alternatively the bias can be provided by another voltage source.

The third option supports the drive of the photo diode by means of the shield of the signal cable. This shield can be conveniently driven by the HF2TA by shorting the auxiliary output to the bias input. This option permits the user to connect the remote sensor with one single coaxial cable and while avoiding to introduce a ground loop in the system.

All HF2TA settings can be conveniently programmed inside the graphical user interface of the HF2 Instrument.

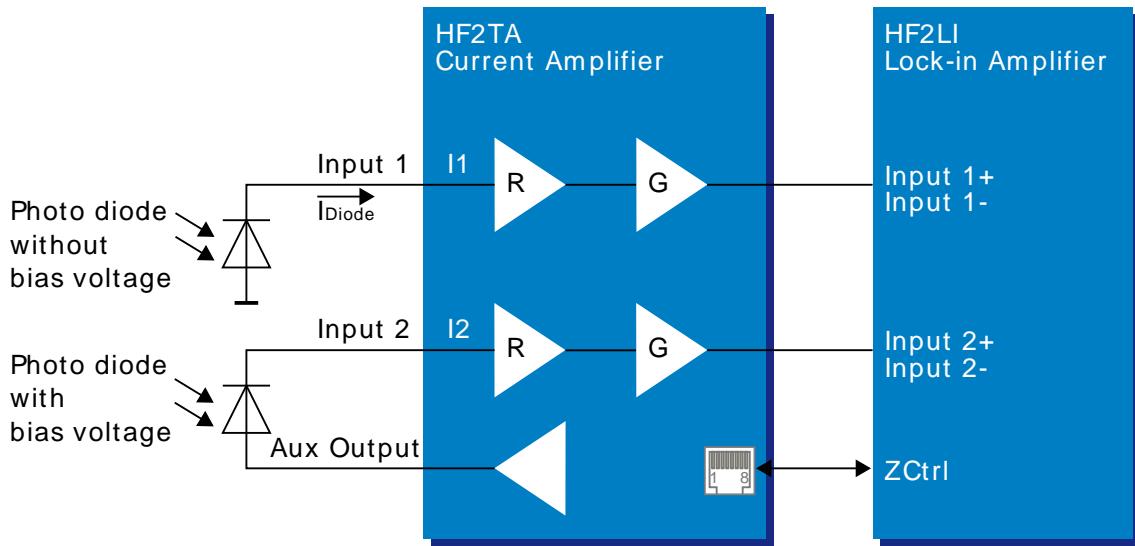


Figure 12.5. HF2TA photo diode amplifier

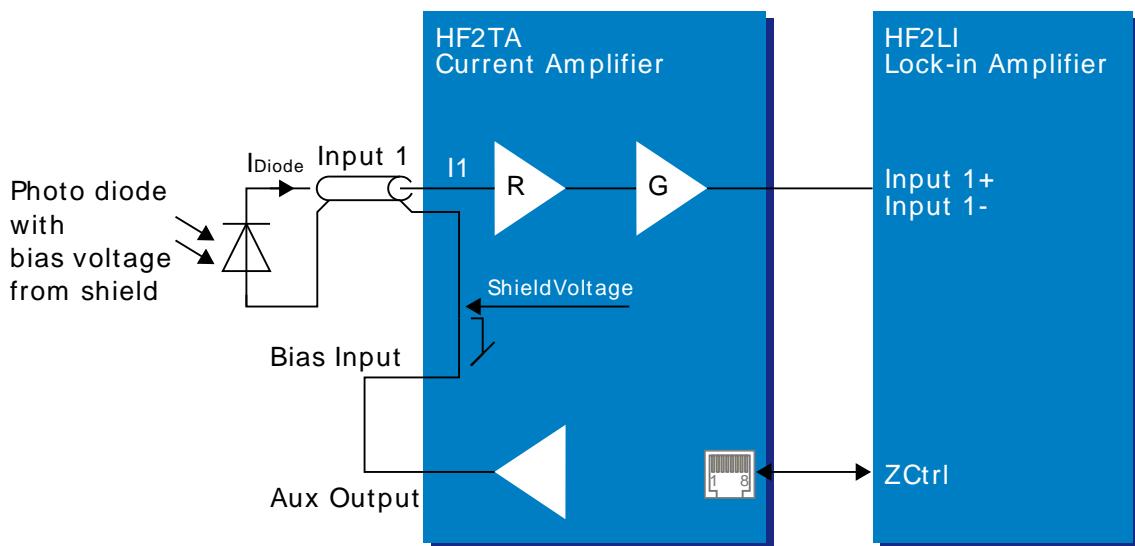


Figure 12.6. HF2TA photo diode amplifier with single coaxial cable

12.4.3. Impedance Measurement with HF2IS

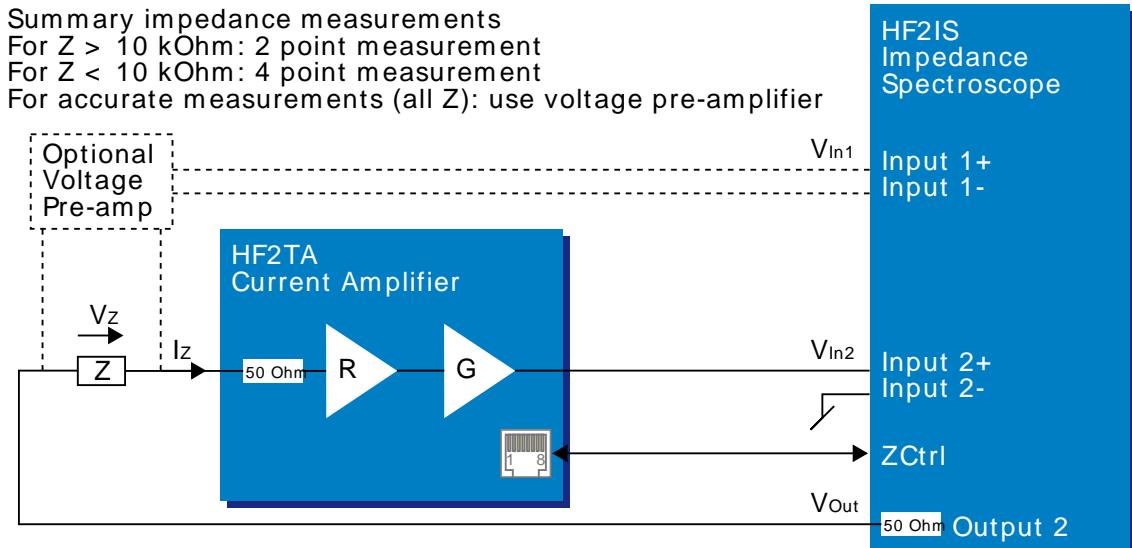


Figure 12.7. Measure an impedance using the HF2TA

The HF2TA current amplifier can be used in conjunction with the HF2IS instrument to measure impedances in a very wide range at frequencies up to 50 MHz. The connection diagram in the figure above shows how the impedance of interest Z is connected to the input of the HF2TA. For optimal amplification versus bandwidth setting, the table in the specification section may be consulted. Three cases and applications need to be distinguished.

- Measuring an impedance $Z > 10 \text{ k}\Omega$

For large impedances it is possible to neglect the output resistance of the HF2IS Instrument and the input resistance of the preamplifier, thus the simple setup provides good accuracy. The HF2IS generates an output signal of amplitude V_{out} and the output signal from the preamplifier is connected to the positive Input 2+ of the HF2, called V_{In} . With this setup, the impedance Z can be calculated using the following equation:

$$Z = R * G * V_{\text{out}} / V_{\text{In}2}$$

- Measuring an impedance $Z < 10 \text{ k}\Omega$

For small impedances and higher precision a four point measurement setup is required. For accuracy in the range of 1%, the voltage V_z can be measured directly by the second differential Input 1+ and Input 1- of the HF2. In this case it is important to select the high ohmic input impedance option ($1 \text{ M}\Omega$) as otherwise too much current is dissipated in the measurement instrument. Also the HF2 should be configured for differential measurement. The resulting impedance Z is calculated using the following equation:

$$Z = R * G * V_z / V_{\text{In}2} = R * G * V_{\text{In}1} / V_{\text{In}2}$$

- Measuring impedances with high accuracy (all values of Z)

Four point measurement setup allows the most accurate measurement by taking into account simultaneously the current flowing through the measured impedance and the voltage drop caused by the current flow. For an accuracy better than 1%, it is recommended to use a voltage preamplifier with high-ohmic input stage to measure the voltage across the impedance $V_z = V_{\text{In}1}$. Assuming $V_{\text{In}2}$, R and G are the output, the resistor setting and the gain

of the HF2TA, respectively, the resulting equation to calculate the impedance will be similar to the previous case:

$$Z = R * G * V_Z / V_{In2} = R * G * V_{In1} / V_{In2} \text{ (assuming voltage pre-amp gain = 1)}$$

A pictorial representation of how to set up the four-point measurement is shown below.

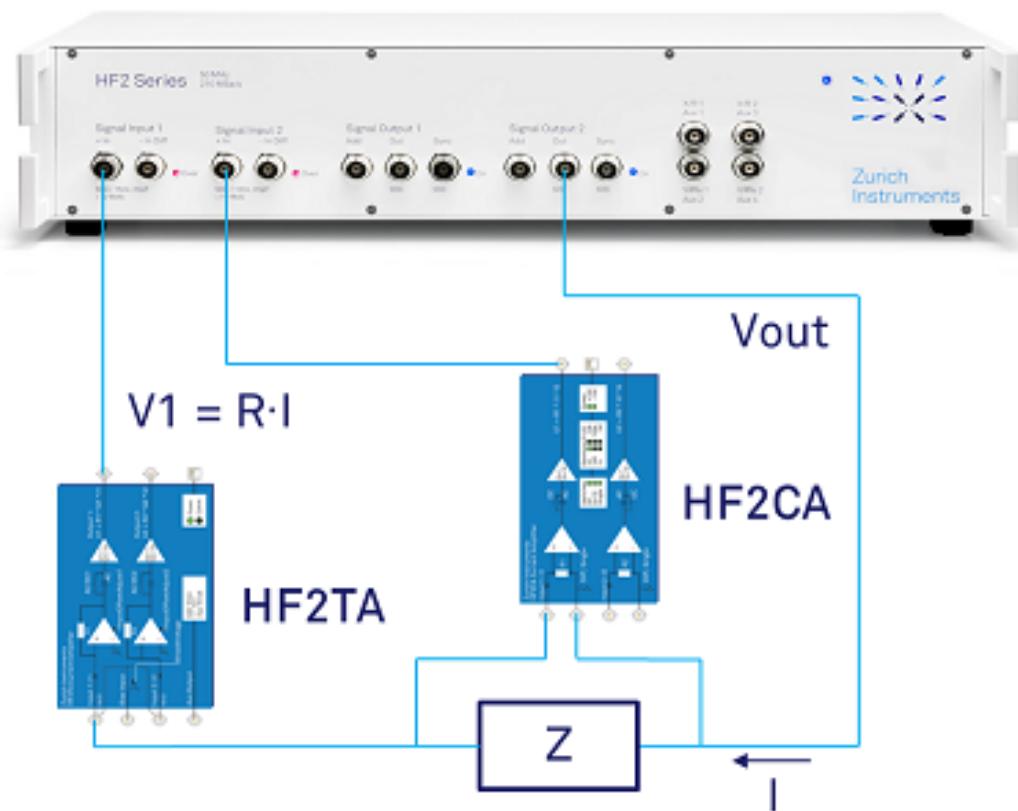


Figure 12.8. HF2IS four-point measurement setup

Note that the voltage measurement is made differentially through HF2CA then converted to single-ended input to the HF2IS while the current measurement remains single-ended throughout the current measurement path. Both HF2TA and HF2CA can be controlled using ziControl. When they are connected through Ethernet cables to the back of the HF2IS instrument, ziControl will automatically add an "Active Probes" tab as shown in the screen shot below.

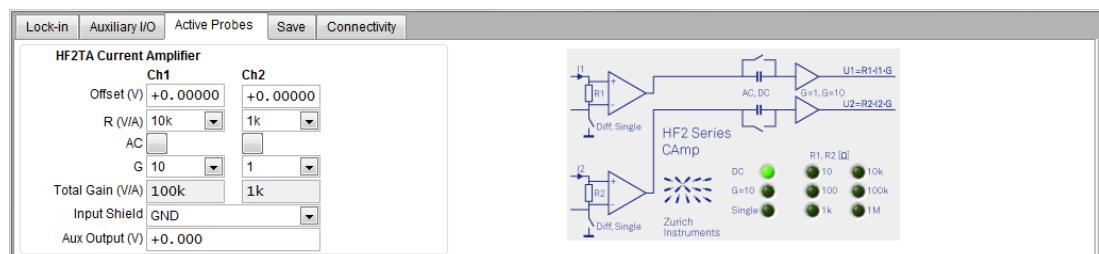


Figure 12.9. Active Probes GUI control screen

Both HF2TA and HF2CA can operate in AC-coupled or DC input. The HF2TA has a high-pass cutoff of 10 Hz while the HF2CA has a high-pass cutoff of 100 Hz. When adjusting the HF2TA transimpedance gain, the field "Total Gain (V/A)" will display the product of $R * G$. It is recommended that no $R1$ and $R2$ values are selected for the HF2CA to obtain maximum input impedance (i.e. no signal current loss through the HF2CA input ports) and therefore the most accurate current measurement.

12.4.4. Impedance Display for Solar Cell Measurement with HF2IS

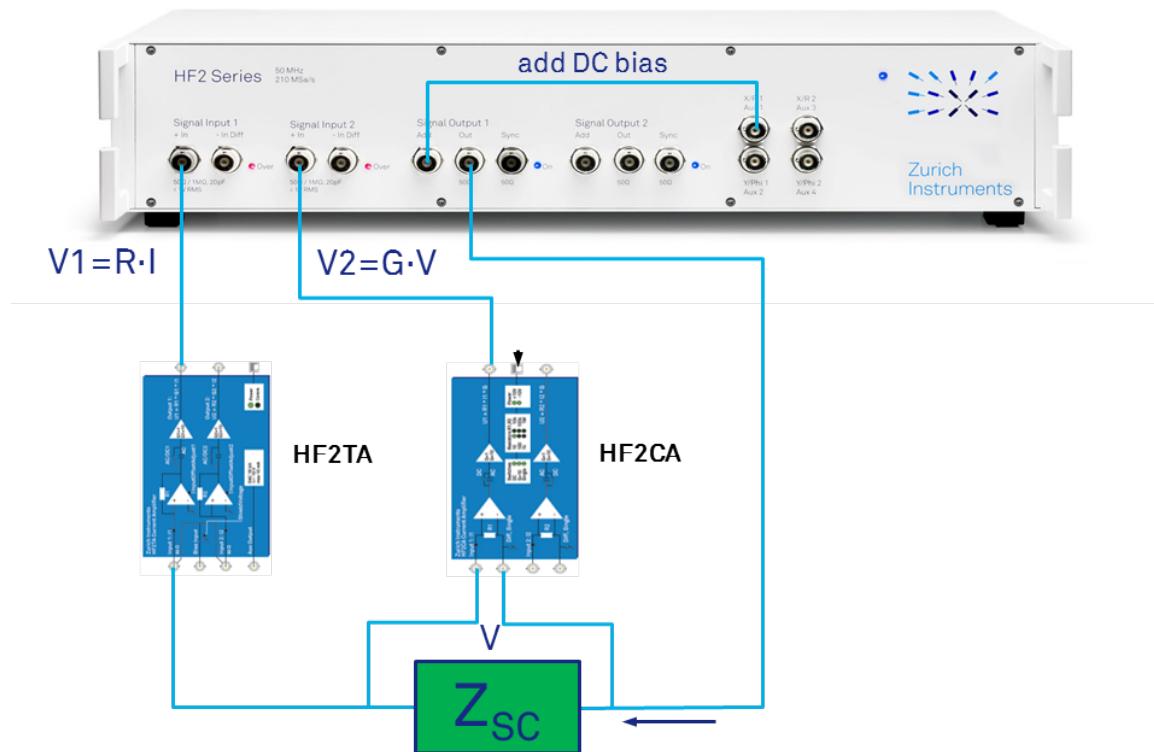


Figure 12.10. Display impedance as Cole-Cole plot

Using HF2IS in combination with HF2TA as a transimpedance amplifier and HF2CA as a voltage buffer, it is possible to characterize a small solar cell by obtaining the Cole-Cole impedance plot directly on the ziControl GUI. The maximum current that can be measured is ± 100 mA which is limited by the output strength of the HF2IS output ports. The typical 4-point setup for such measurement is shown above. In addition, a DC bias can be applied to the solar cell under test through the use of one of the four HF2IS auxiliary outputs and the built-in ADD function to the output port.

On the ziControl setting shown below, HF2TA is selected for Input 1, 4-Term Z is selected for Demodulators and if a DC bias is applied, Add should be clicked for Output 1. The Sweeper can then be used to obtain the frequency response of the solar cell under test. Please note that HF2CA is not selected for Input 2. This is because HF2CA is used as a voltage buffer with ± 10 V range in order to extend the HF2IS input range (± 3.5 V). Since HF2CA has a gain of 1 by default, no special care is required on the ziControl setting. However, if a gain is applied on HF2CA or another 3rd-party buffer, then the Scaling function should be used to normalize the voltage measurement.

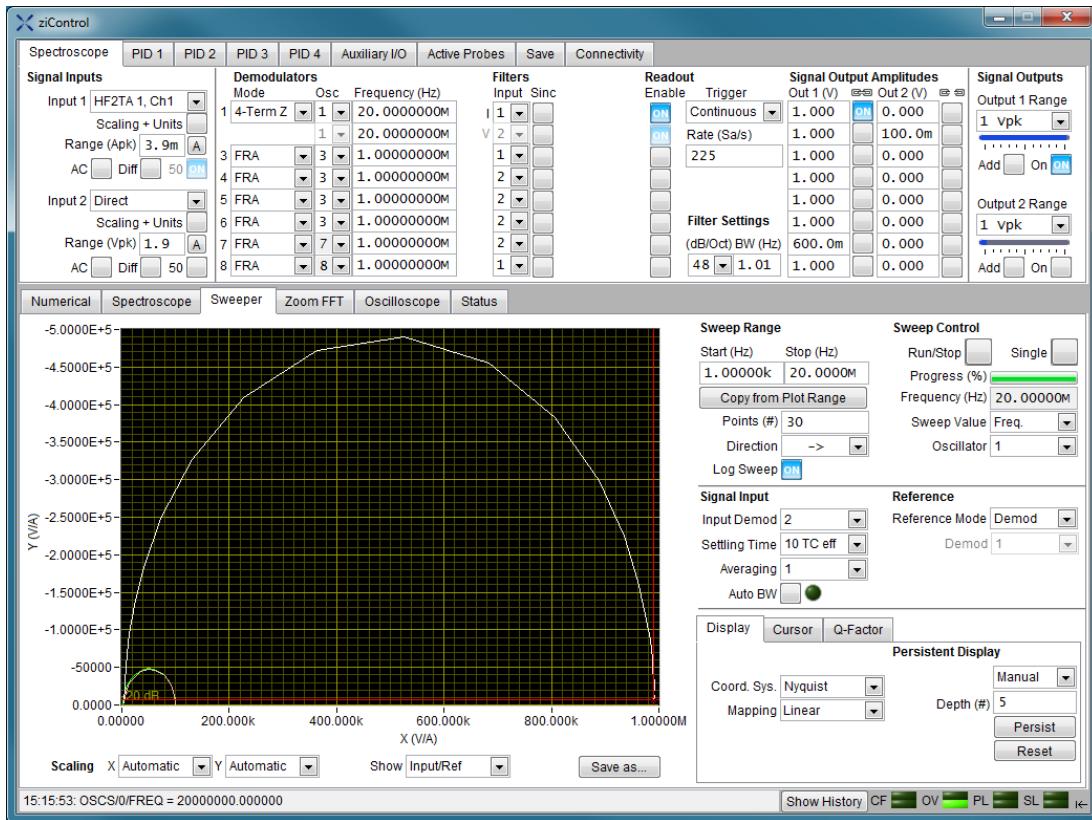


Figure 12.11. HF2IS four-point measurement setup

To observe the sweep result in Cole-Cole plot, one has to simply select Impedance and Nyquist as the display format in the Sweeper. Please ensure that Mapping is set to Linear. Since the Y axis represents the imaginary impedance, the fact that a solar cell is capacitive means that the plotted result is flipped (upside down) initially compared to the typical Cole-Cole plot which plots the imaginary impedance in absolute values. To rectify this display issue, simply choose Scaling Y to manual and set the minimum value of the Y to 0 manually. The plot should now display in the traditional Cole-Cole plot convention.

Lastly, the sweeper plot in ziControl allows user to display the previous measurement simultaneously with the current measurement. This is called Persistent Display in the Sweeper. When one wants to save a measurement plot, he or she has to only press on the Persist button. The ensuing measurement will be plotted alongside with the "persisted" plots. The number of persistent display can be set inside the Depth field.

12.5. Performance Tests

In this section two tests are described that can be used to measure the DC leakage and the AC noise of the HF2TA. They can be performed by the user to do a sanity check on the validity of the measurement with HF2TA.

Table 12.5. Necessary equipment

Required equipment	Specifications	Recommended equipment
HF2 Instrument	No additional installation options required	HF2LI or HF2IS
HF2TA Current Amplifier	HF2TA Specifications	HF2TA
Digital multimeter	0.1 mV Resolution, 20 V range	Agilent 34410A
SMA to BNC cables	2 x 50 Ω, male-to-male connectors	supplied by Zurich Instruments
Ethernet cable	Category 5 or 6	supplied by Zurich Instruments

The following conditions have to be fulfilled:

1. The test equipment must be connect to the same AC power circuit. If you are unsure of the AC power circuit distribution, use a common power strip and connect all test equipment into it. Connecting the test equipment into separate AC power circuits can result in offset voltages between equipment, which can invalidate the verification test.
2. For accurate results, allow the test equipment to warm up for at least 30 minutes.
3. The HF2 Instrument as well as the HF2TA transimpedance amplifier are controlled by ziControl. Please make sure that the latest version of the LabOne and ziControl packages have been installed on the host computer. More information about installing software can be found in the [Chapter 1](#). The ziServer installed by the LabOne software package must be running on the computer. How to check if ziServer is running is described in the [Chapter 6](#).

The HF2TA transimpedance amplifier has 2 analog input channels, 2 analog output channels and 1 external bias input and 1 auxiliary output. For the purpose of the following tests, the external bias input will not be used. The test setup for one channel is equally valid for the other channel.

12.5.1. Input Leakage Test

Definition

This test measures the DC input leakage current of the HF2TA.

Setup

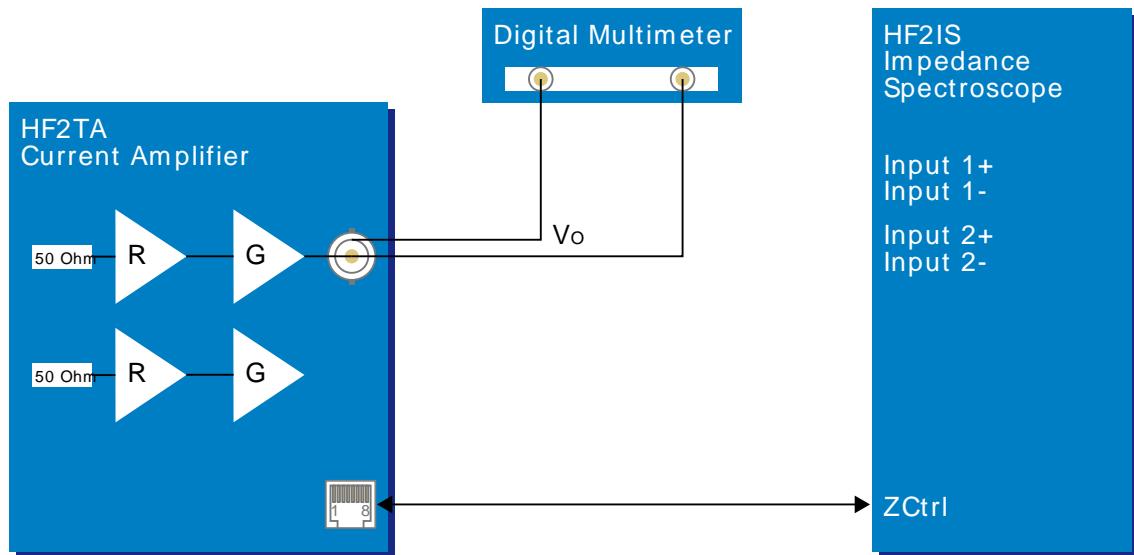


Figure 12.12. HF2TA DC input leakage measurement setup

The HF2TA is connected to the HF2 Instrument via the Ethernet cable for the purpose of configuring the HF2TA. The HF2 Instrument is not used for the measurement.

Table 12.6. HF2TA settings

Ch1 Offset (V)	0.0 V
Ch1 R (V/A)	1 k and 100 M
Ch1 AC	OFF
G	1
Input Shield	GND
Aux Output (V)	0.0 V

Measurement

The DC leakage current can be estimated by subtracting the inherent DC offset V_{01} of the amplifier from the total offset V_{02} due to both the internal offset and the input leakage. For this test the input of the HF2TA is left open. Then the output is measured with a digital multimeter as shown. The input offset V_{01} can be estimated by setting the transimpedance resistor R to 1 k. The sum of the input offset plus leakage V_{02} can be estimated by setting the transimpedance resistor R to 100 M. Then, the approximate leakage can be found by:

$$I_{\text{leakage}} = \frac{|V_{02} - V_{01}|}{100 \text{ M}\Omega} \quad (12.1)$$

12.5.2. Input Noise Test

Definition

The noise generated by the HF2TA transimpedance amplifier itself can be expressed as input referred current noise. The following setup description enables users to verify through measurement if their HF2TA units have indeed the same noise level as specified in [Table 12.4](#).

Setup

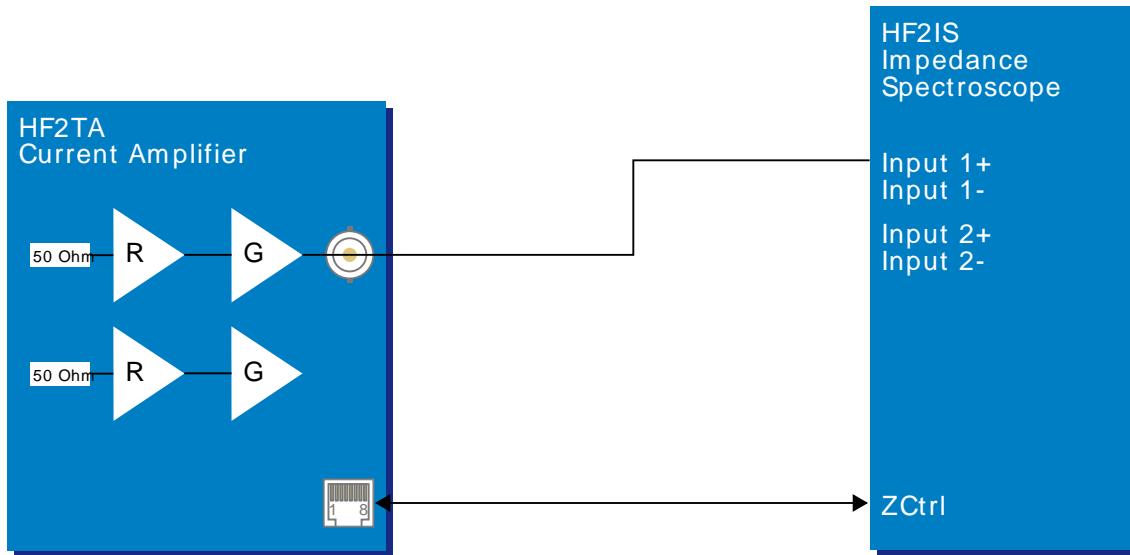


Figure 12.13. HF2TA equivalent input current noise

For this test the HF2TA transimpedance amplifier input is left open. The goal is to refer the total contribution of out noise from the amplifier itself to the input and not from any other external circuits. Since the input of the HF2TA is left open, it is only necessary to define the sweep range in the HF2 instrument since no drive voltage is required.

The HF2 instrument settings for the test are given in the table below.

Table 12.7. HF2 instrument settings

Ch1 Signal Inputs Range	auto range
Ch1 Signal Inputs AC/ Diff/ 50	ON
Ch1 Scale	1/R
Filter BW setting type	BW NEP (noise equivalent power BW)
Filter BW	1 Hz

Notice that Scale has been set to 1/R where R is the HF2TA transimpedance value. This is to obtain the noise current referred back to input.

Measurement

The Sweeper tool will be used to get the measurement result. Be sure to turn off Normalize since we are interested in the absolute value. The settings which have to be set in the Sweeper tool section are following:

Table 12.8. Frequency sweeper settings

Sweep Range Start	1 kHz
Sweep Range Stop	50 MHz
Sweep Range Points	50
Sweep Range Log Sweep	ON

Filter Auto BW	OFF
Filter Precision	3 TC
Filter Averaging	32
Display	Input Only
Display Mapping	linear
Coordinate System	Polar
Result Unit	V_{RMS}/\sqrt{Hz}

After settings have been applied, to start the measurement one should run continuous sweep. The input referred current noise is directly displayed in the Sweeper Tool over the specified frequency. The result can then be compared to the input referred noise table.

12.6. Cable Recommendation

Table 12.9. HF2TA cable recommendation

Function	Connector / cable type	Vendor / part number
SMA to BNC connection		
SMA to BNC cable	BNC jack to SMA plug	Digikey J3606-ND
SMA to BNC adapter	BNC jack to SMA plug	Digikey J10098-ND
		Farnell / Newark 4195930
	BNC plug to SMB plug	Farnell / Newark 1654647
		Digikey ACX1324-ND
Custom access or cable assembly		
Cable	Cable type RG-174	Digikey A307-100-ND
		Farnell / Newark 1387745
SMA to cable	SMA plug to RG-174 cable	Digikey A32326-ND
		Farnell / Newark 2112459
BNC to cable	BNC plug to RG-174 cable	Tyco Electronics 1-5227079-6
		Digikey A32212-ND
		Farnell / Newark 1831701

Glossary

This glossary provides easy to understand descriptions for many terms related to measurement instrumentation including the abbreviations used inside this user manual.

A

A/D	Analog to Digital See Also ADC .
AC	Alternate Current
ADC	Analog to Digital Converter
AM	Amplitude Modulation
Amplitude Modulated AFM (AM-AFM)	AFM mode where the amplitude change between drive and measured signal encodes the topography or the measured AFM variable. See Also Atomic Force Microscope .
API	Application Programming Interface
ASCII	American Standard Code for Information Interchange
Atomic Force Microscope (AFM)	Microscope that scans surfaces by means an oscillating mechanical structure (e.g. cantilever, tuning fork) whose oscillating tip gets so close to the surface to enter in interaction because of electrostatic, chemical, magnetic or other forces. With an AFM it is possible to produce images with atomic resolution. See Also Amplitude Modulated AFM , Frequency Modulated AFM , Phase modulation AFM .
AVAR	Allen Variance

B

Bandwidth (BW)	<p>The signal bandwidth represents the highest frequency components of interest in a signal. For filters the signal bandwidth is the cut-off point, where the transfer function of a system shows 3 dB attenuation versus DC. In this context the bandwidth is a synonym of cut-off frequency $f_{\text{cut-off}}$ or 3dB frequency $f_{-3\text{dB}}$. The concept of bandwidth is used when the dynamic behavior of a signal is important or separation of different signals is required.</p> <p>In the context of a open-loop or closed-loop system, the bandwidth can be used to indicate the fastest speed of the system, or the highest signal update change rate that is possible with the system.</p> <p>Sometimes the term bandwidth is erroneously used as synonym of frequency range. See Also Noise Equivalent Power Bandwidth.</p>
BNC	Bayonet Neill-Concelman Connector

C

CF	Clock Fail (internal processor clock missing)
----	---

Common Mode Rejection Ratio (CMRR) Specification of a differential amplifier (or other device) indicating the ability of an amplifier to obtain the difference between two inputs while rejecting the components that do not differ from the signal (common mode). A high CMRR is important in applications where the signal of interest is represented by a small voltage fluctuation superimposed on a (possibly large) voltage offset, or when relevant information is contained in the voltage difference between two signals. The simplest mathematical definition of common-mode rejection ratio is: $CMRR = 20 * \log(differential\ gain / common\ mode\ gain)$.

CSV Comma Separated Values

D

D/A	Digital to Analog
DAC	Digital to Analog Converter
DC	Direct Current
DDS	Direct Digital Synthesis
DHCP	Dynamic Host Configuration Protocol
DIO	Digital Input/Output
DNS	Domain Name Server
DSP	Digital Signal Processor
DUT	Device Under Test
Dynamic Reserve (DR)	The measure of a lock-in amplifier's capability to withstand the disturbing signals and noise at non-reference frequencies, while maintaining the specified measurement accuracy within the signal bandwidth.

E

XML Extensible Markup Language.
See Also [XML](#).

F

FFT	Fast Fourier Transform
FIFO	First In First Out
FM	Frequency Modulation
Frequency Accuracy (FA)	Measure of an instrument's ability to faithfully indicate the correct frequency versus a traceable standard.
Frequency Modulated AFM (FM-AFM)	AFM mode where the frequency change between drive and measured signal encodes the topography or the measured AFM variable. See Also Atomic Force Microscope .
Frequency Response Analyzer (FRA)	Instrument capable to stimulate a device under test and plot the frequency response over a selectable frequency range with a fine granularity.

Frequency Sweeper See Also [Frequency Response Analyzer](#).

G

Gain Phase Meter See Also [Vector Network Analyzer](#).

GPIB General Purpose Interface Bus

GUI Graphical User Interface

I

I/O Input / Output

Impedance Spectroscope (IS) Instrument suited to stimulate a device under test and to measure the impedance (by means of a current measurement) at a selectable frequency and its amplitude and phase change over time. The output is both amplitude and phase information referred to the stimulus signal.

Input Amplitude Accuracy (IAA) Measure of instrument's capability to faithfully indicate the signal amplitude at the input channel versus a traceable standard.

Input voltage noise (IVN) Total noise generated by the instrument and referred to the signal input, thus expressed as additional source of noise for the measured signal.

IP Internet Protocol

L

LAN Local Area Network

LED Light Emitting Diode

Lock-in Amplifier (LI, LIA) Instrument suited for the acquisition of small signals in noisy environments, or quickly changing signal with good signal to noise ratio - lock-in amplifiers recover the signal of interest knowing the frequency of the signal by demodulation with the suited reference frequency - the result of the demodulation are amplitude and phase of the signal compared to the reference: these are value pairs in the complex plane (X, Y), (R, Θ).

M

Media Access Control address (MAC address) Refers to the unique identifier assigned to network adapters for physical network communication.

Multi-frequency (MF) Refers to the simultaneous measurement of signals modulated at arbitrary frequencies. The objective of multi-frequency is to increase the information that can be derived from a measurement which is particularly important for one-time, non-repeating events, and to increase the speed of a measurement since different frequencies do not have to be applied one after the other.
See Also [Multi-harmonic](#).

Multi-harmonic (MH) Refers to the simultaneous measurement of modulated signals at various harmonic frequencies. The objective of multi-frequency is to increase the

information that can be derived from a measurement which is particularly important for one-time, non-repeating events, and to increase the speed of a measurement since different frequencies do not have to be applied one after the other.

See Also [Multi-frequency](#).

N

Noise Equivalent Power Bandwidth (NEPBW)

Effective bandwidth considering the area below the transfer function of a low-pass filter in the frequency spectrum. NEPBW is used when the amount of power within a certain bandwidth is important, such as noise measurements. This unit corresponds to a perfect filter with infinite steepness at the equivalent frequency.

See Also [Bandwidth](#).

Nyquist Frequency (NF)

For sampled analog signals, the Nyquist frequency corresponds to two times the highest frequency component that is being correctly represented after the signal conversion.

O

Output Amplitude Accuracy (OAA)

Measure of an instrument's ability to faithfully output a set voltage at a given frequency versus a traceable standard.

OV

Over Volt (signal input saturation and clipping of signal)

P

PC

Personal Computer

PD

Phase Detector

Phase-locked Loop (PLL)

Electronic circuit that serves to track and control a defined frequency. For this purpose a copy of the external signal is generated such that it is in phase with the original signal, but with usually better spectral characteristics. It can act as frequency stabilization, frequency multiplication, or as frequency recovery. In both analog and digital implementations it consists of a phase detector, a loop filter, a controller, and an oscillator.

Phase modulation AFM (PM-AFM)

AFM mode where the phase between drive and measured signal encodes the topography or the measured AFM variable.

See Also [Atomic Force Microscope](#).

PID

Proportional-Integral-Derivative

PL

Packet Loss (loss of packets of data between the instruments and the host computer)

R

RISC

Reduced Instruction Set Computer

Root Mean Square (RMS)

Statistical measure of the magnitude of a varying quantity. It is especially useful when variates are positive and negative, e.g., sinusoids, sawtooth, square waves. For a sine wave the following relation holds between the

amplitude and the RMS value: $U_{\text{RMS}} = U_{\text{PK}} / \sqrt{2} = U_{\text{PK}} / 1.41$. The RMS is also called quadratic mean.

RT Real-time

S

Scalar Network Analyzer (SNA)	Instrument that measures the voltage of an analog input signal providing just the amplitude (gain) information. See Also Spectrum Analyzer , Vector Network Analyzer .
SL	Sample Loss (loss of samples between the instrument and the host computer)
Spectrum Analyzer (SA)	Instrument that measures the voltage of an analog input signal providing just the amplitude (gain) information over a defined spectrum. See Also Scalar Network Analyzer .
SSH	Secure Shell

T

TC	Time Constant
TCP/IP	Transmission Control Protocol / Internet Protocol
Thread	An independent sequence of instructions to be executed by a processor.
Total Harmonic Distortion (THD)	Measure of the non-linearity of signal channels (input and output)
TTL	Transistor to Transistor Logic level

U

UHF	Ultra-High Frequency
UHS	Ultra-High Stability
USB	Universal Serial Bus

V

VCO	Voltage Controlled Oscillator
Vector Network Analyzer (VNA)	Instrument that measures the network parameters of electrical networks, commonly expressed as s-parameters. For this purpose it measures the voltage of an input signal providing both amplitude (gain) and phase information. For this characteristic an older name was gain phase meter. See Also Gain Phase Meter , Scalar Network Analyzer .

X

XML	Extensible Markup Language: Markup language that defines a set of rules for encoding documents in a format that is both human-readable and machine-readable.
-----	--

Z

ZCtrl	Zurich Instruments Control bus
ZoomFFT	This technique performs FFT processing on demodulated samples, for instance after a lock-in amplifier. Since the resolution of an FFT depends on the number of point acquired and the spanned time (not the sample rate), it is possible to obtain very highly resolution spectral analysis.
ZSync	Zurich Instruments Synchronization bus

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