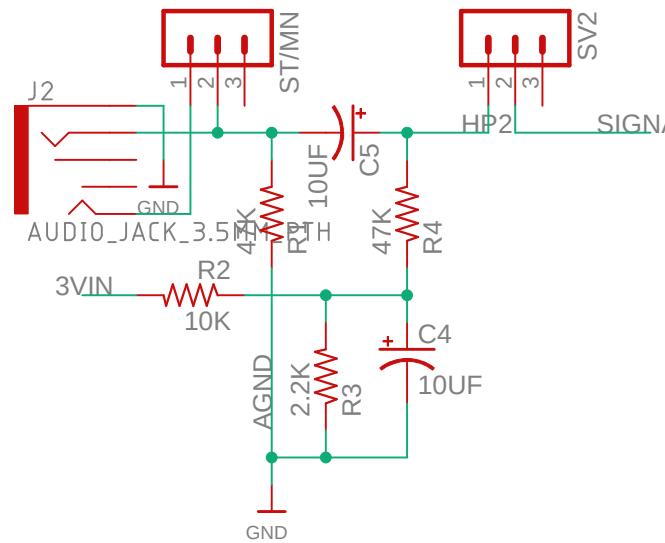


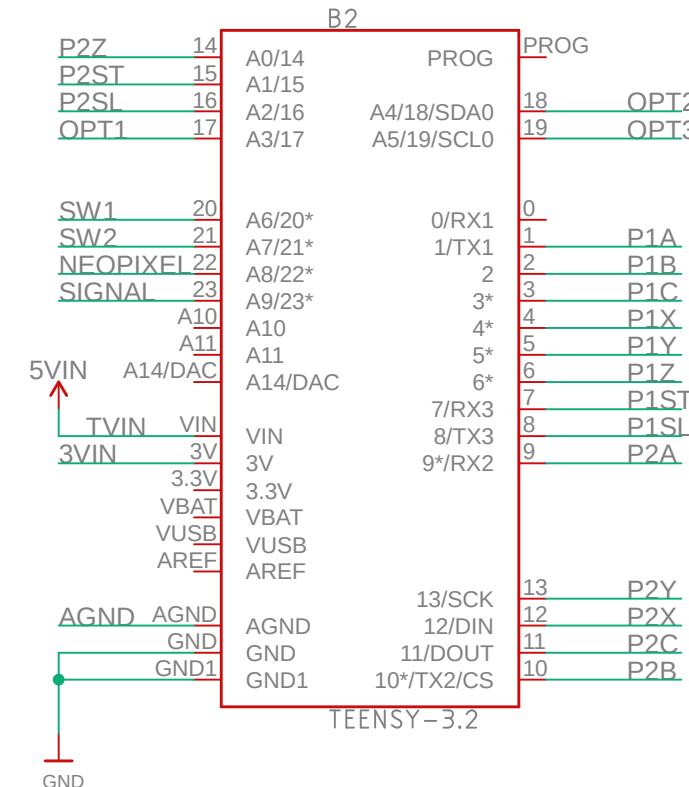
Neopixel Development PCB R3

PG 4/7/2021

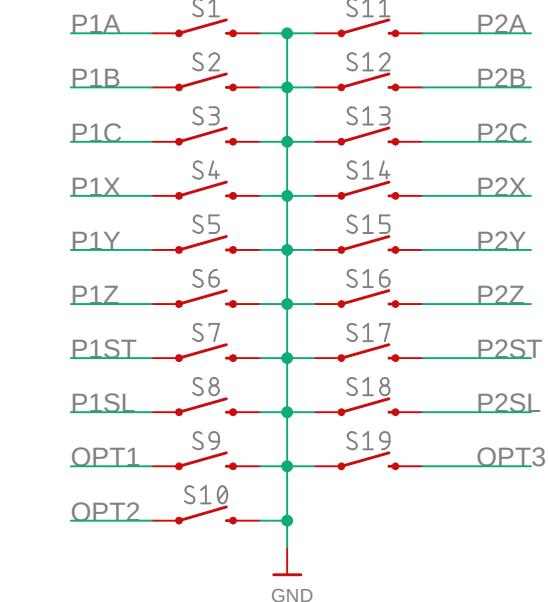
## AUX In Header:



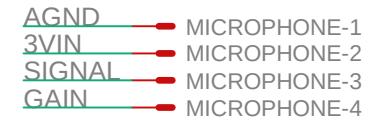
Teensy 3.2



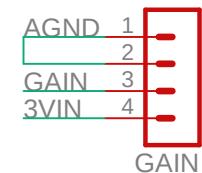
## Button Intercept Headers:



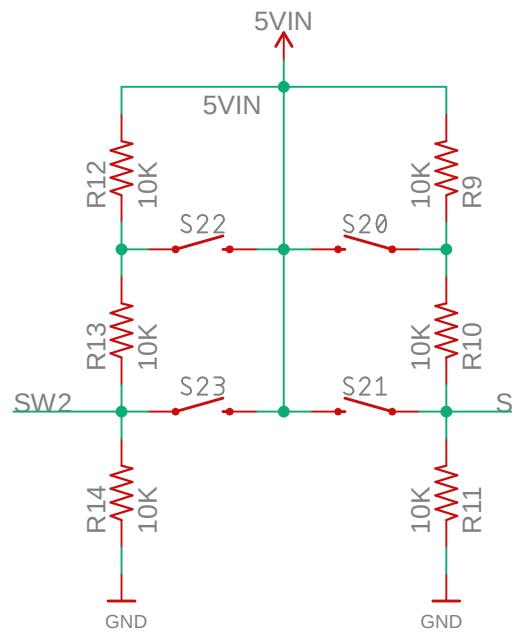
## Microphone Header:



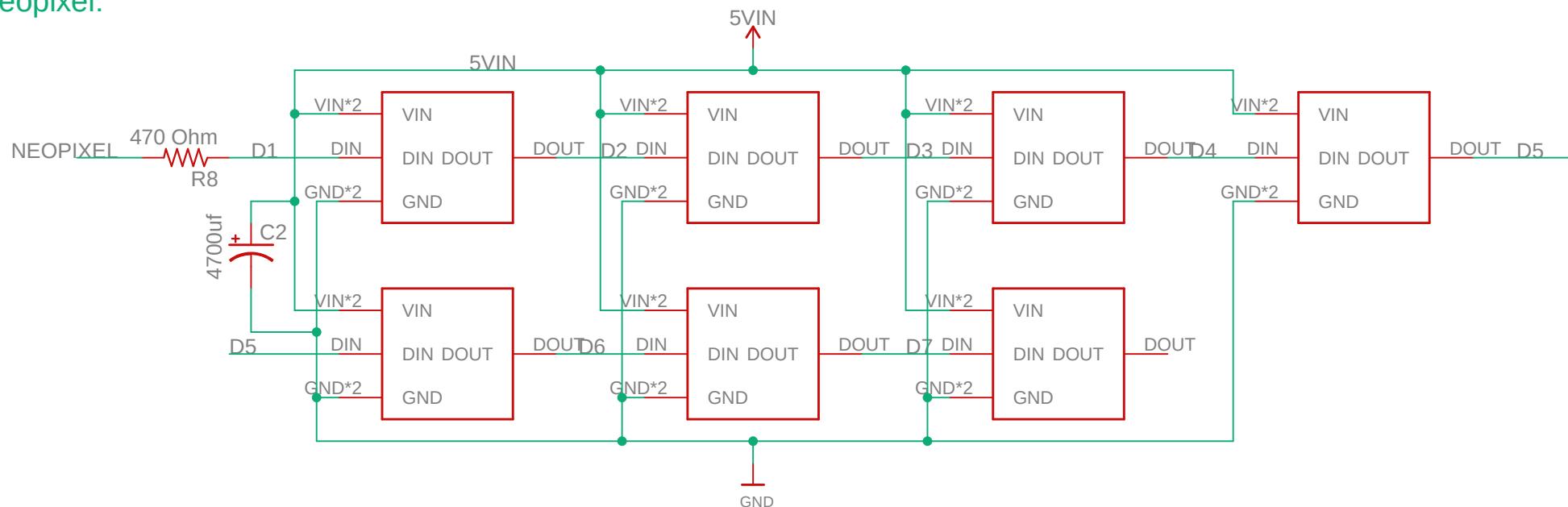
## Microphone Gain Jumper:



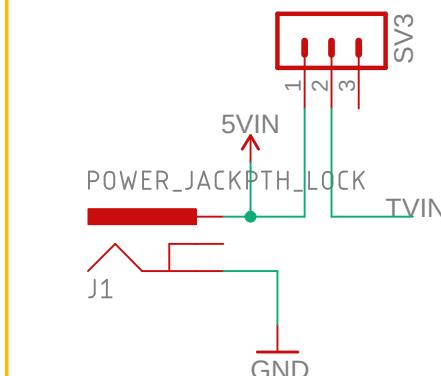
## Switch Headers:



## Neopixel:



## Power Headers:

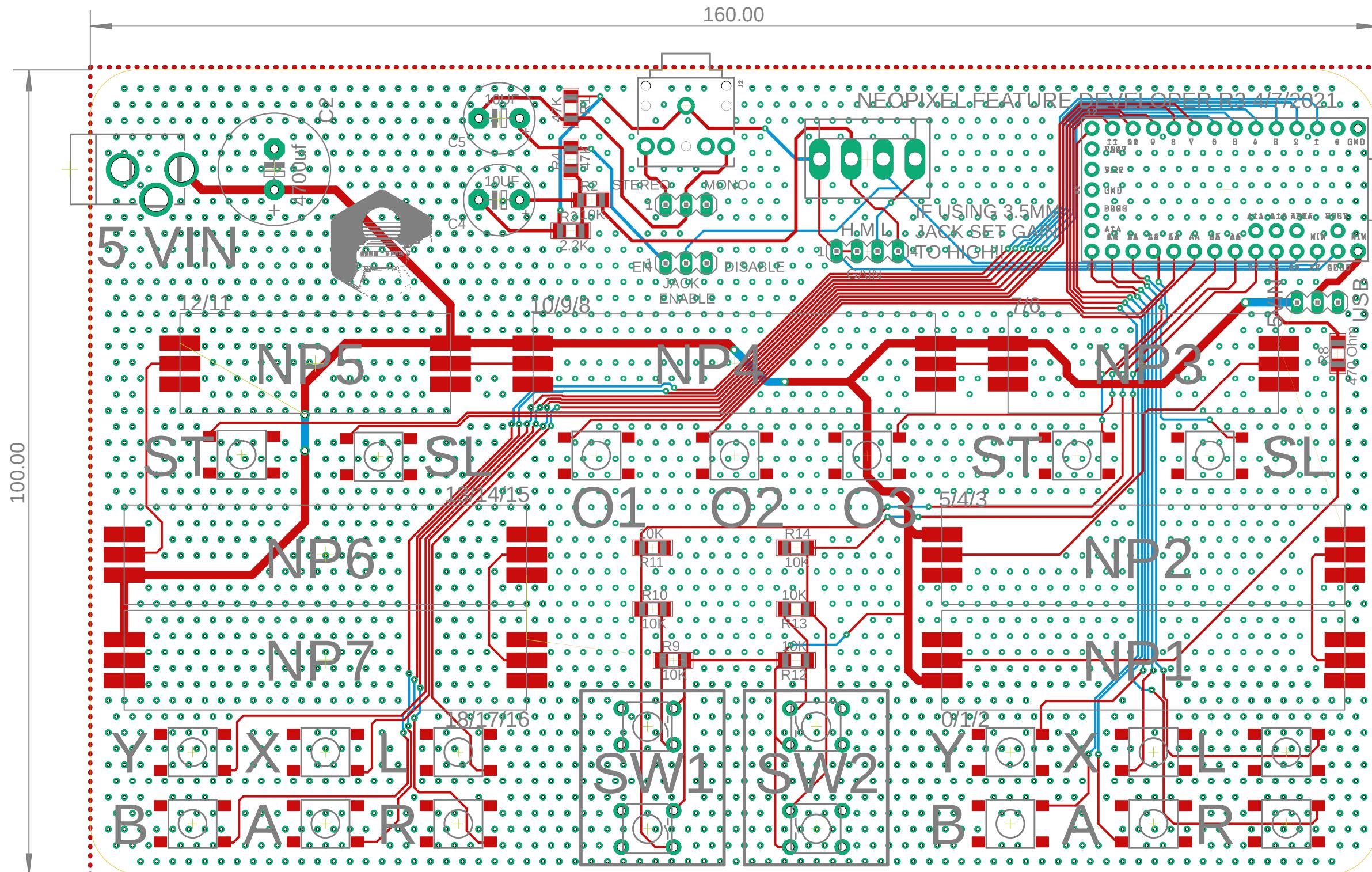


11-4-2020 - (R2) - R2 UPDATES:

- MOVED ALL CONTROLS UP ONE PIN ON TEENSY  
(D0 WOULD NOT REGISTER A ON P1).
  - REMOVED OPT4 AS IT IS NOT USED IN THE DESIGN.

4-7-2021 - (R3) - R3 UPDATES:

- MOVED GND PIN ON ALL INPUT BUTTONS 1/2P (ABCXYZ OPT1/2/3) - BUTTONS THAT ARE USED ON THE PCB BRIDGE HORIZONTALLY NOT VERTICALLY
  - UPDATED R9,R10,R12,R13 TO 10K (FORMERLY 1K) AS THE VOLTAGE NEEDED TO DROP BELOW 3.3V FOR TEENSY TO RECOGNIZE BUTTON PRESSES

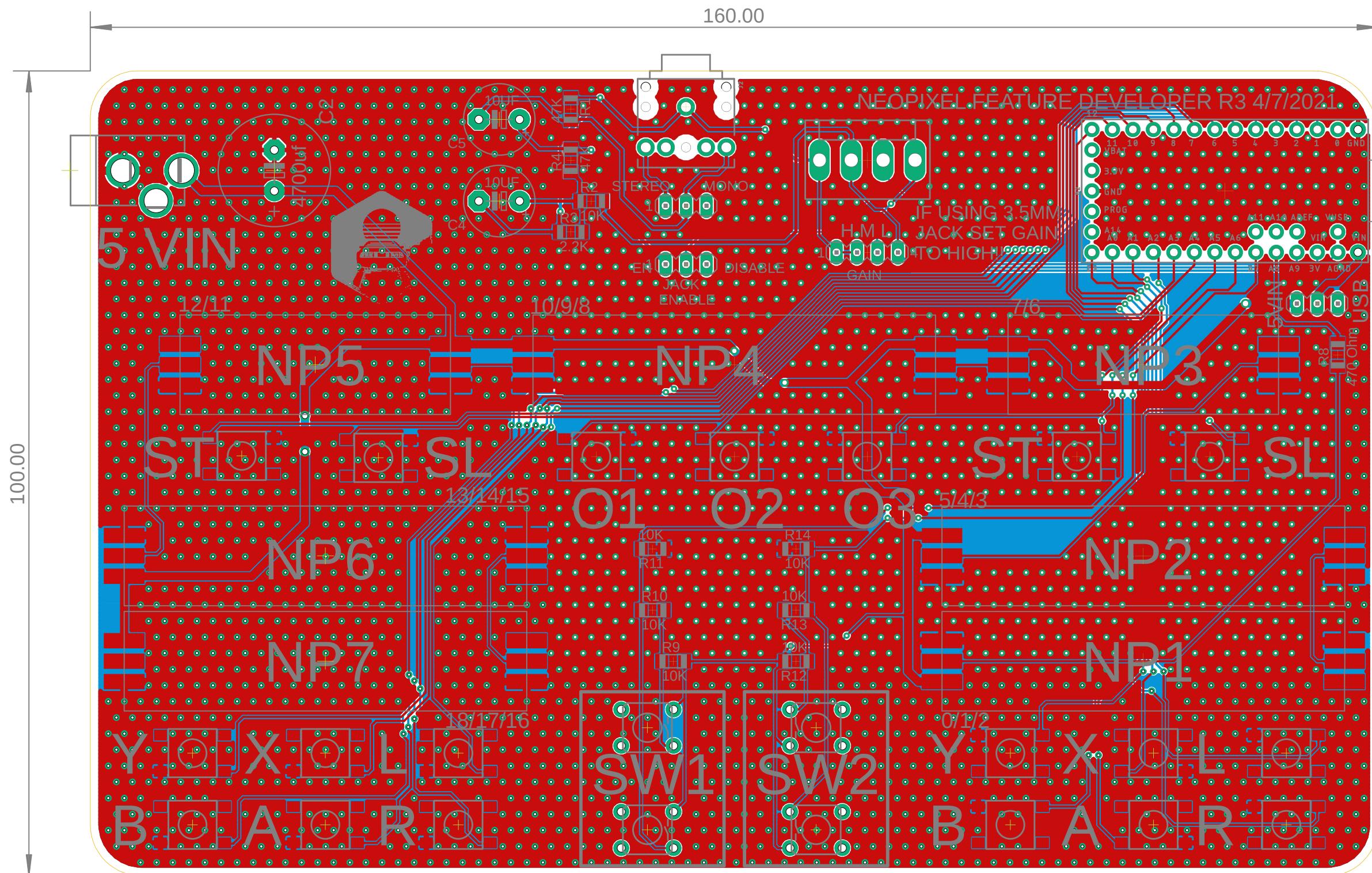


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1. MOVED ALL CONTROLS UP ONE PIN ON TEENSY  
(D0 WOULD NOT REGISTER A ON P1).
2. REMOVED OPT4 AS IT IS NOT USED IN THE DESIGN
3. ALL INPUT TRACES RERUN, STITCHING RERUN TO MATCH NEW TRACES
4. ADDED VIA STITCHING TO GND PLANE

4-7-2021 - (R3) - R3 UPDATES:

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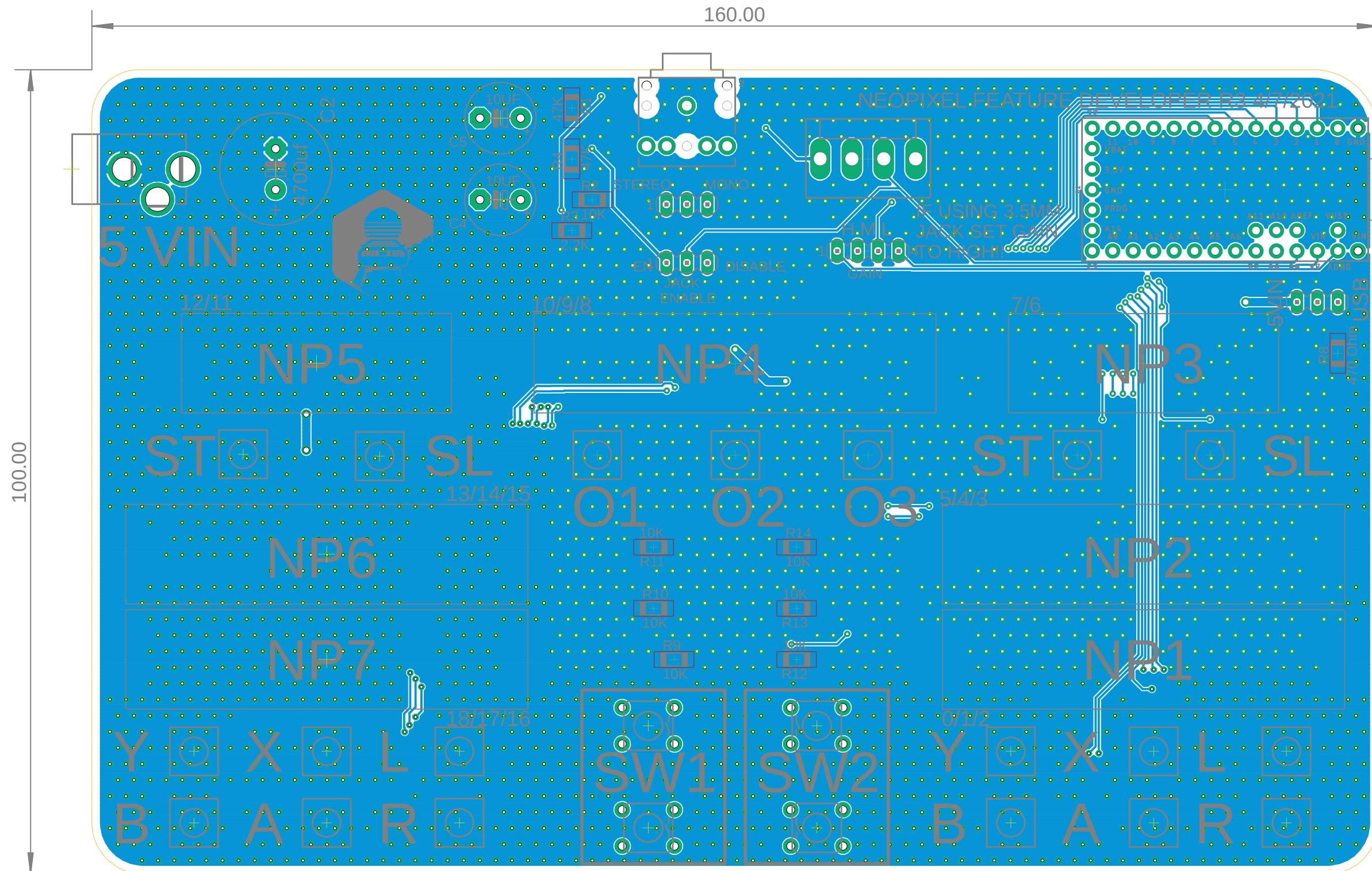


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