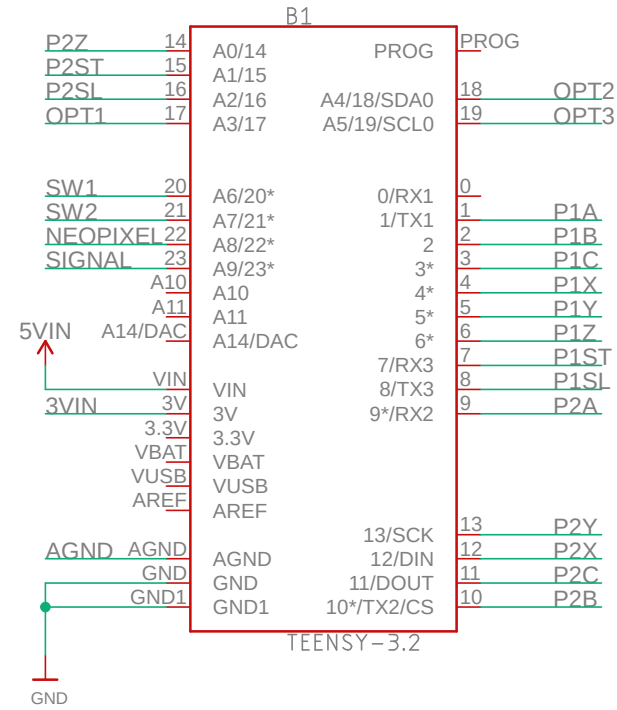


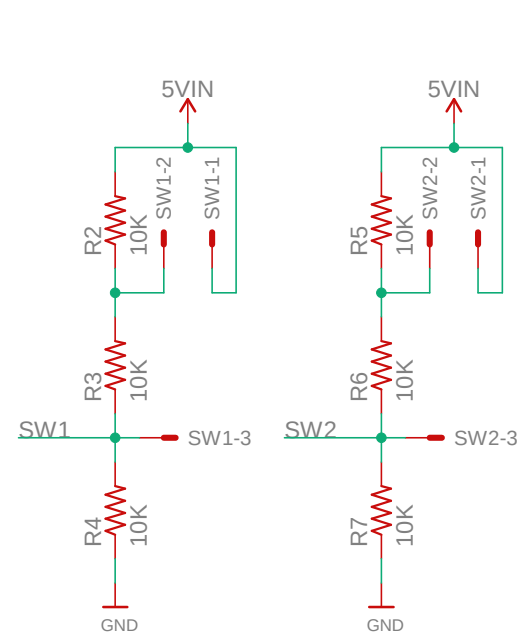
Neopixel Control PCB R3

PG 4/7/2021

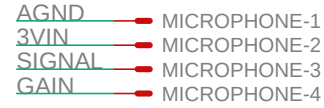
Teensy 3.2:



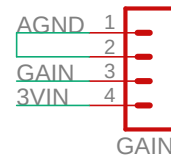
Switch Headers:



Microphone Header:

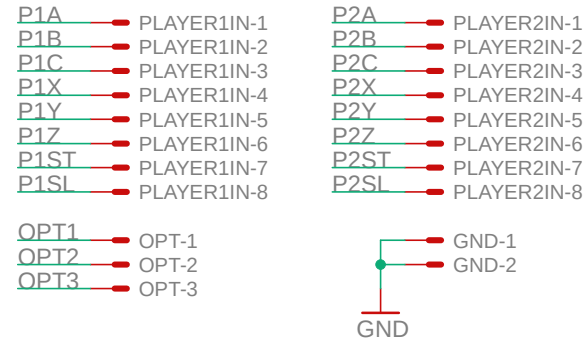


Microphone Gain Jumper:

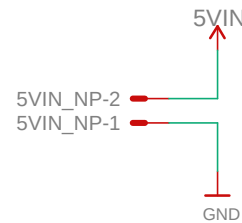
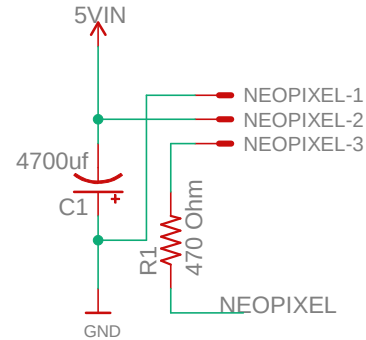


Power Headers:

Button Intercept Headers:



Neopixel:

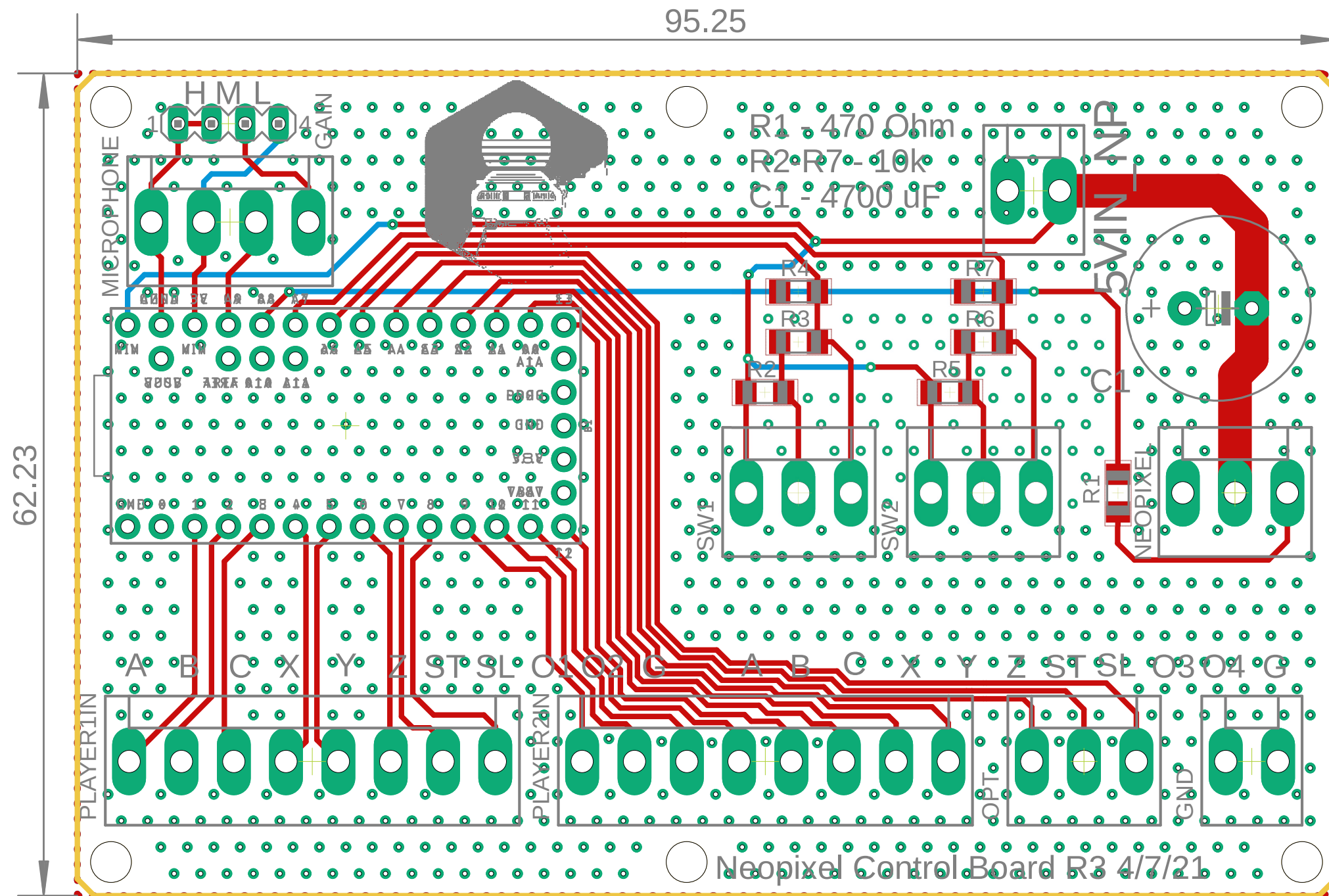


11-4-2020 - (R2) - R2 UPDATES:

1. MOVED ALL CONTROLS UP ONE PIN ON TEENSY (D0 WOULD NOT REGISTER A ON P1).
2. REMOVED OPT4 AS IT IS NOT USED IN THE DESIGN
3. SWAPPED 2X 11 PIN MOLEX HEADERS FOR 2X 8 PIN (P1 AND P2), 1X 3 PIN (OPT) AND 1X 2 PIN (GND) TO BETTER SPLIT THE GROUPS OF INPUTS
4. REMOVED THE 3 PIN 5VIN MOLEX CONNECTOR AND CONSOLIDATED 5VIN TO AN 2 PIN CONNECTOR (FORMERLY 5VINNP)

4-7-2021 - (R3) - R3 UPDATES:

- ## 1. CHANGED R2-R7 TO 10K (FORMERLY 1K)

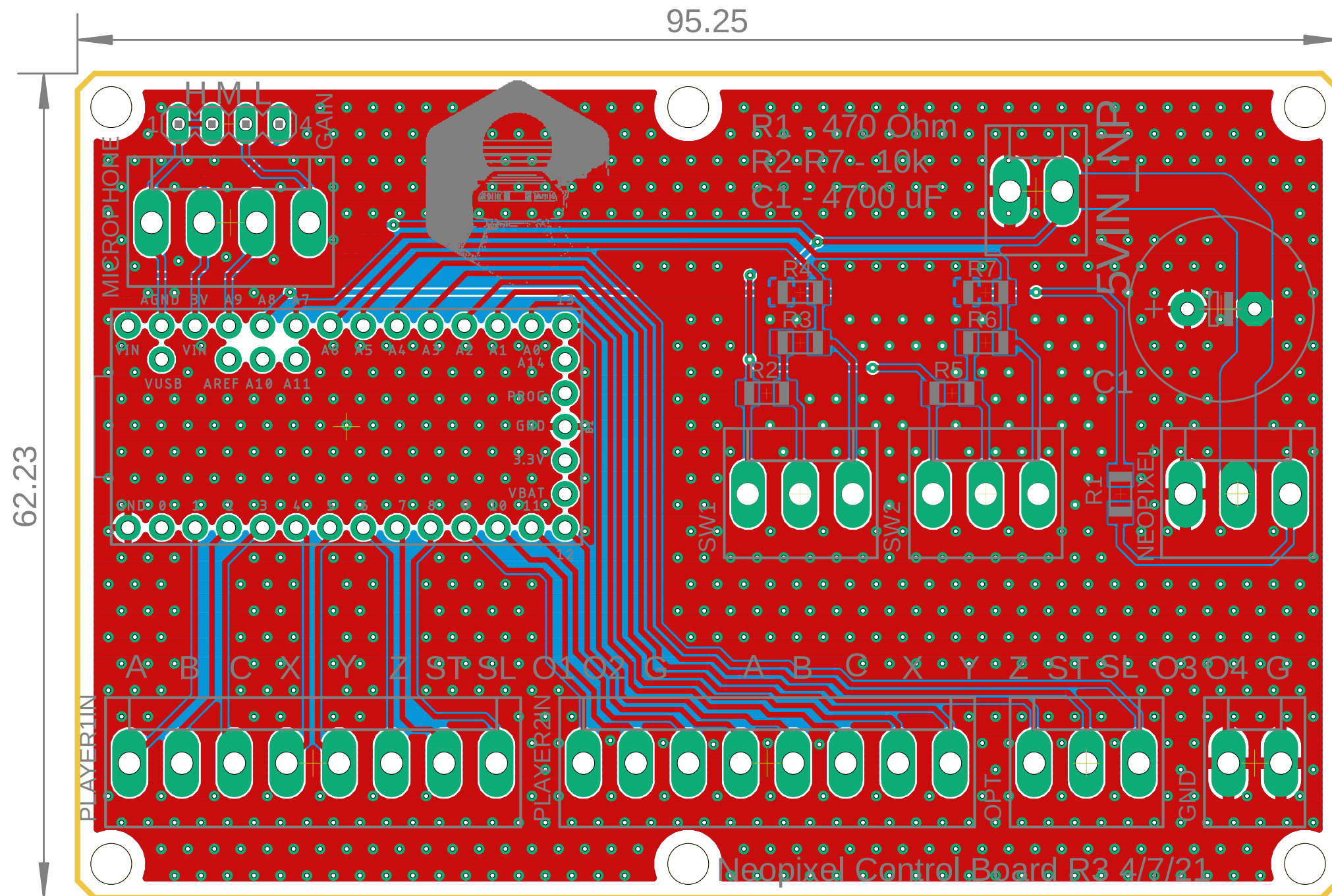


11-4-2020 - (R2) - R2 UPDATES:

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3. SWAPPED 2X 11 PIN MOLEX HEADERS FOR 2X 8 PIN (P1 AND P2), 1X 3 PIN (OPT) AND 1X 2 PIN (GND) TO BETTER SPLIT THE GROUPS OF INPUTS
4. REMOVED THE 3 PIN 5VIN MOLEX CONNECTOR AND CONSOLIDATED 5VIN TO AN 2 PIN CONNECTOR (FORMERLY 5VINNP)
5. ALL INPUT TRACES RERUN, STICHING RERUN TO MATCH NEW TRACES

4-7-2021 - (R3) - R3 UPDATES:

1. CHANGED R2-R7 TO 10K (FORMERLY 1K)
2. ADDED COMPONENT VALUES TO PCB

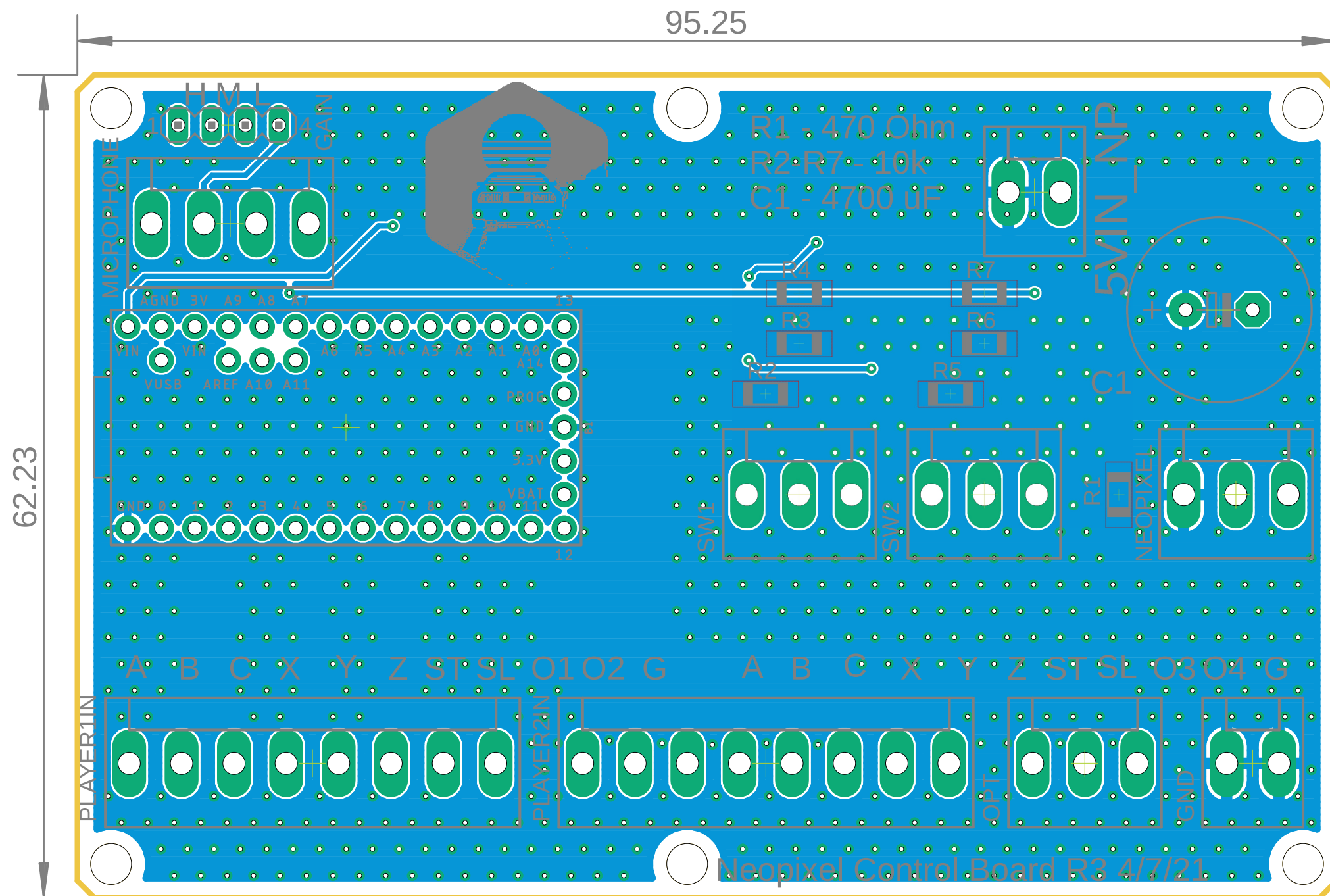


11-4-2020 - (R2) - R2 UPDATES:

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