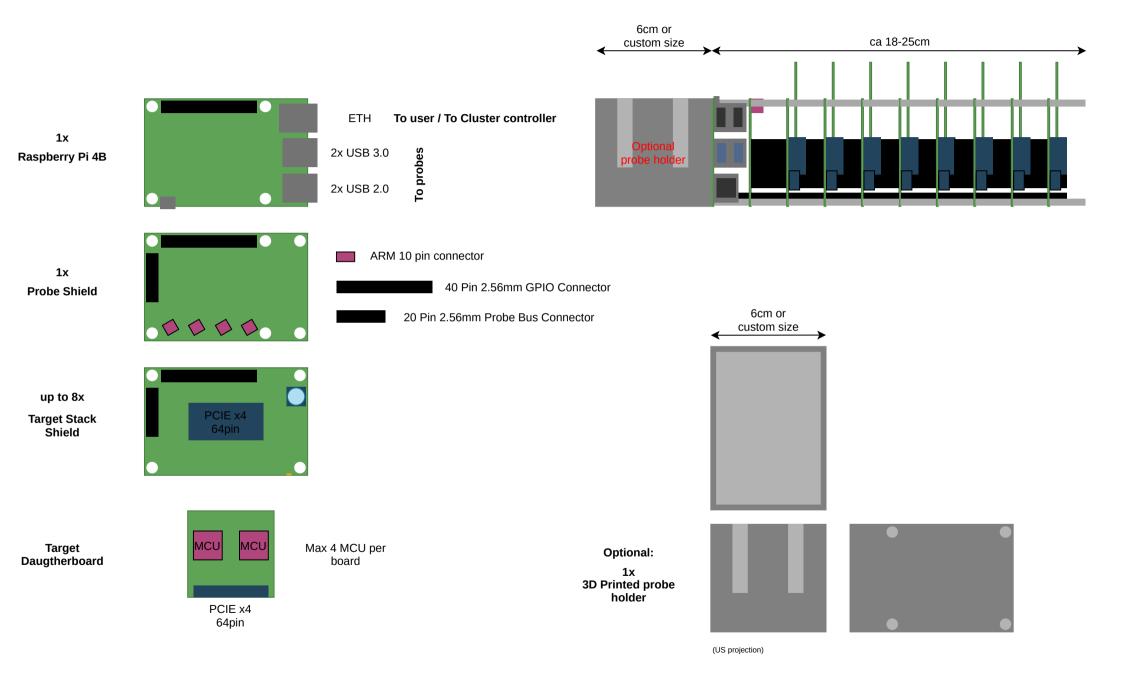


#### Layout



#### **Connectors**

#### Possible GPIO shield / Probe shield pass through connectors:

ESQ-120-34-L/T-D (40Pin) ESQ-110-34-L/T-D (20Pin) Price: ~8-10CHF
Availability: Non plated, ok

SSQ-120-04-L/T-D (40Pin) SSQ-110-04-L/T-D (20Pin) Price: ~3-6CHF

8.5.1mm Availability: non plated ok

14.83mm

TSM-120-03-F/L-DV (40Pin) TSM-110-03-F/L-DV (20Pin) with SSM-120-F/L-DV (40Pin)

SSM-110-F/L-DV (20Pin)

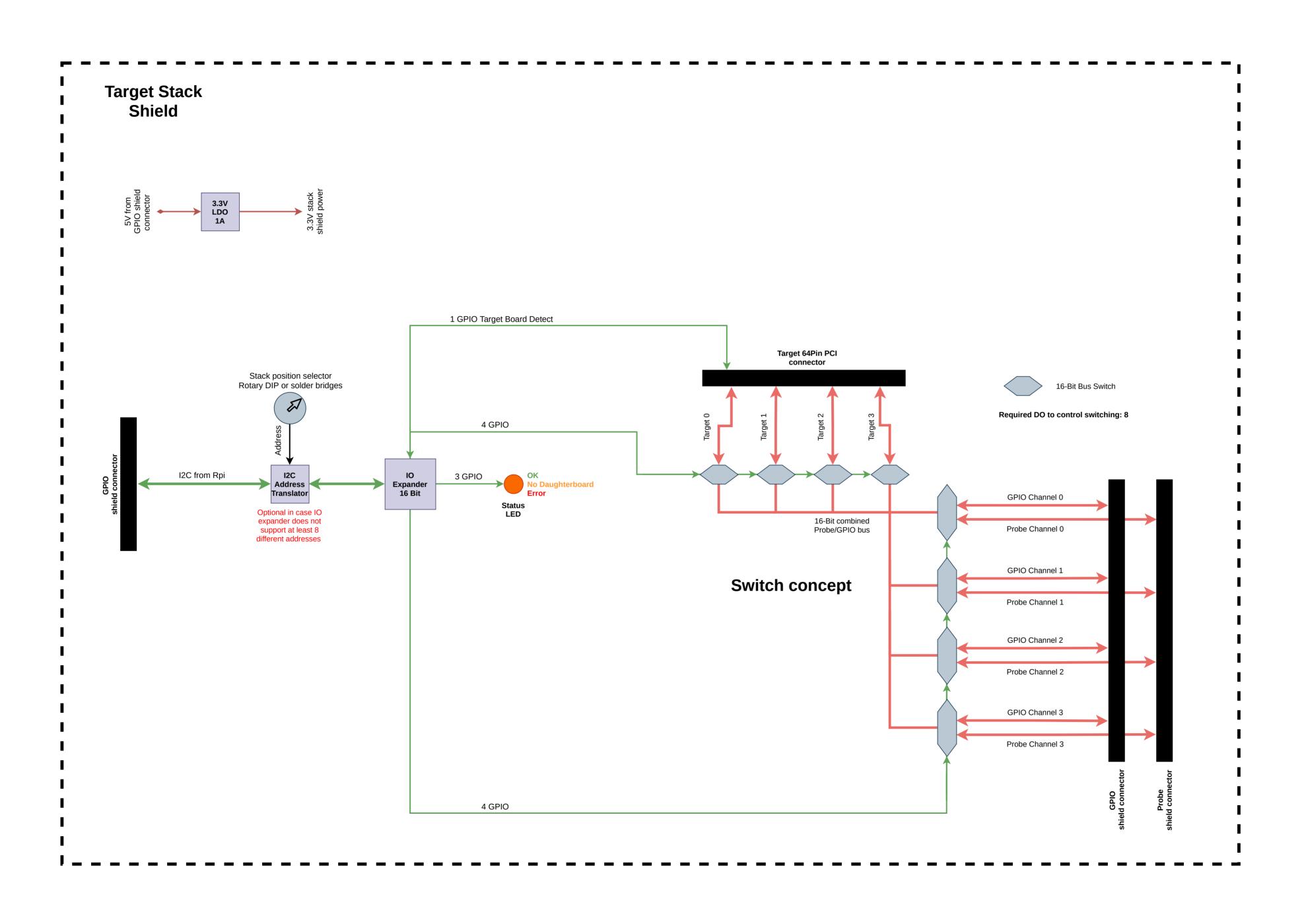


#### Possible ARM 10pin debug connectors:

FTSH-105-01-F-DV-007-K

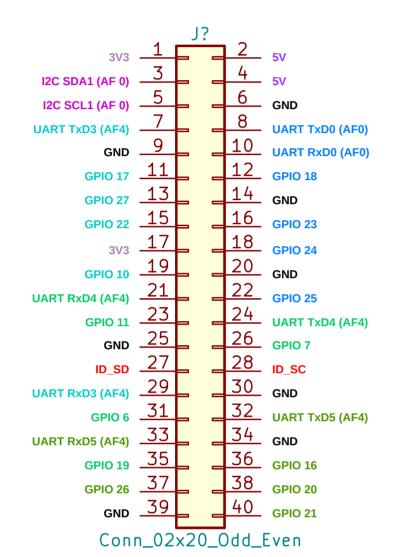


Price: ~3-4CHF Availability: ok



## **Raspberry 40pin** connector config bcm2711 (rpi designations)

Per GPIO channel: 4x GPIO, 1x UART



I2C Bus: Controls all IO expanders on Target Stack Shields

Unused

ID\_XX: Unused

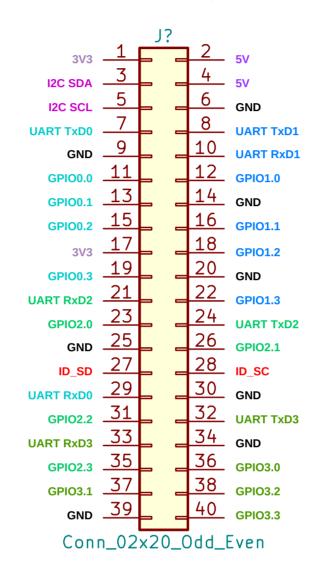
**GPIO Channels:** 

Channel 0 Channel 1 Channel 2

Channel 3

### **GPIO** shield connector (hive designations)

Per GPIO channel: 4x GPIO, 1x UART



I2C Bus: Controls all IO expanders on Target Stack Shields

Unused

Power source for shields

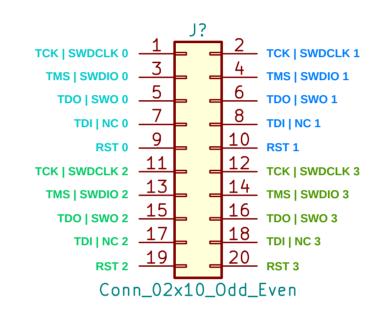
ID\_XX: Unused

**GPIO Channels:** 

Channel 0 Channel 1 Channel 2

Channel 3

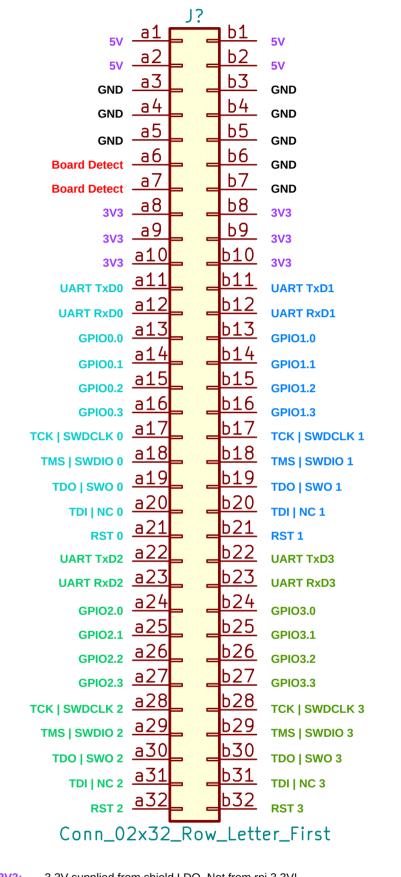
## **Probe shield** connector (hive designations)



**Probe Channels:** 

Channel 0 Channel 1 Channel 2 Channel 3

# **Target 64Pin PCI** connector (hive designations)



3V3: 3.3V supplied from shield LDO. Not from rpi 3.3V!

5V: 5V supplied by power supply usb c rpi input

**Board Detect:** Needs to be connected together on daughterboard, allows shield to detect if daughterboard is present

Channels:

Channel 0 Channel 1

Channel 2

Channel 3