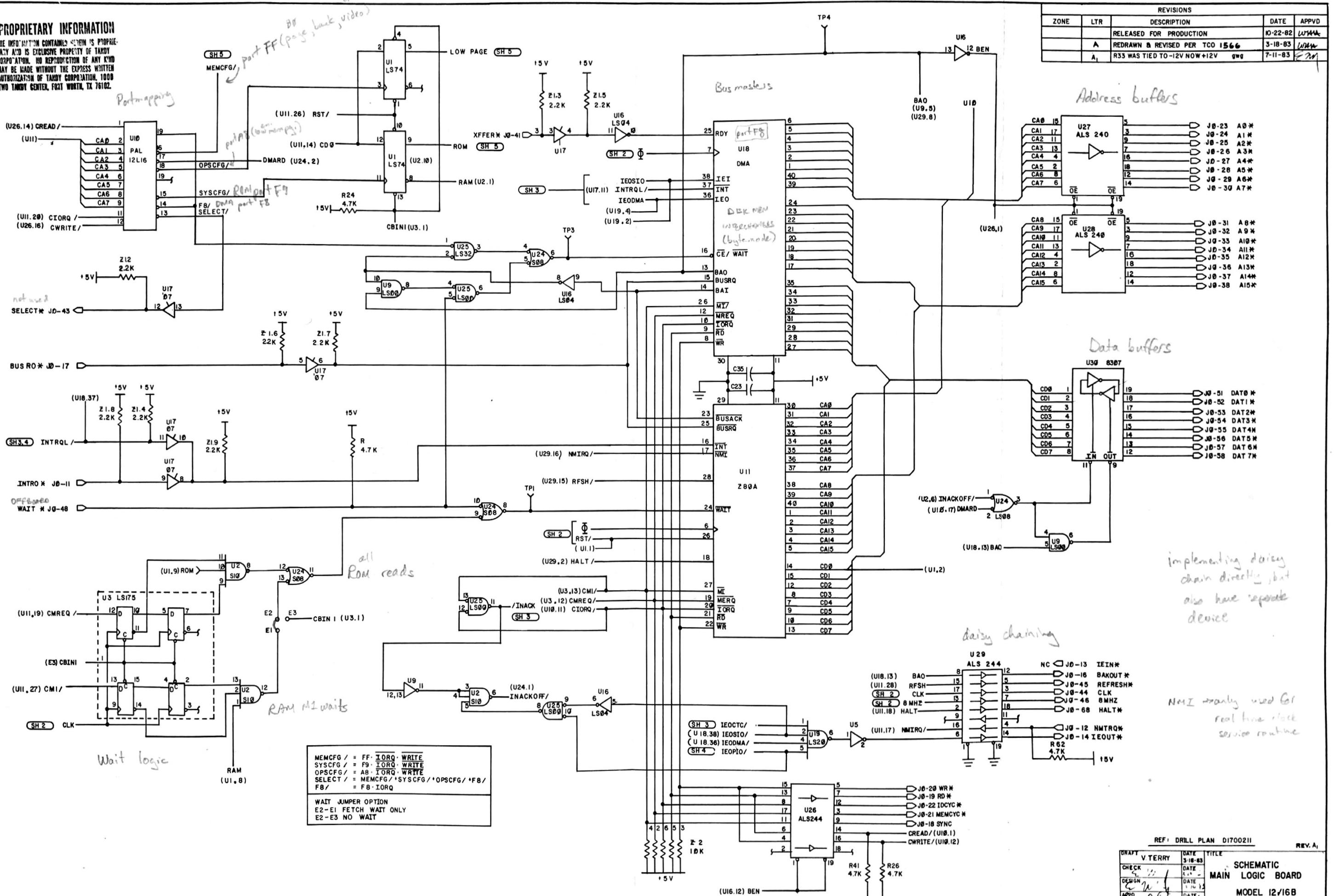


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implementing device  
chain directly, but  
also have separate  
device

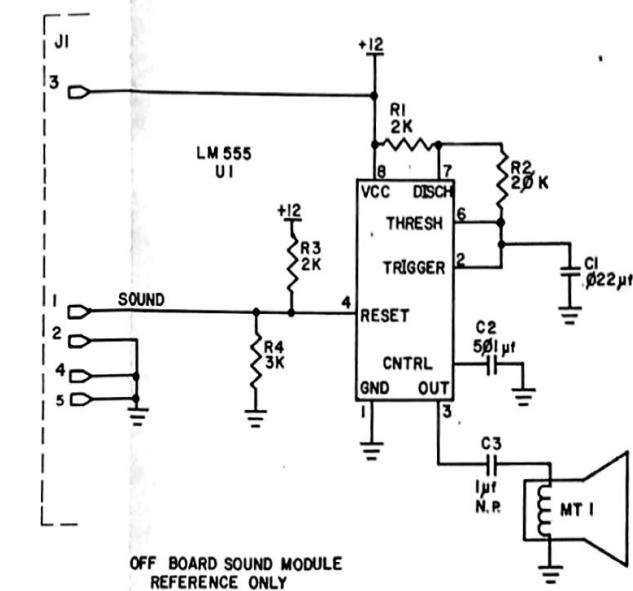
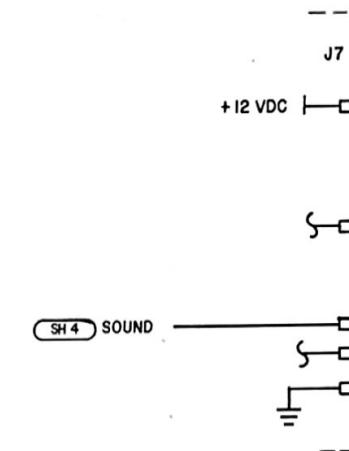
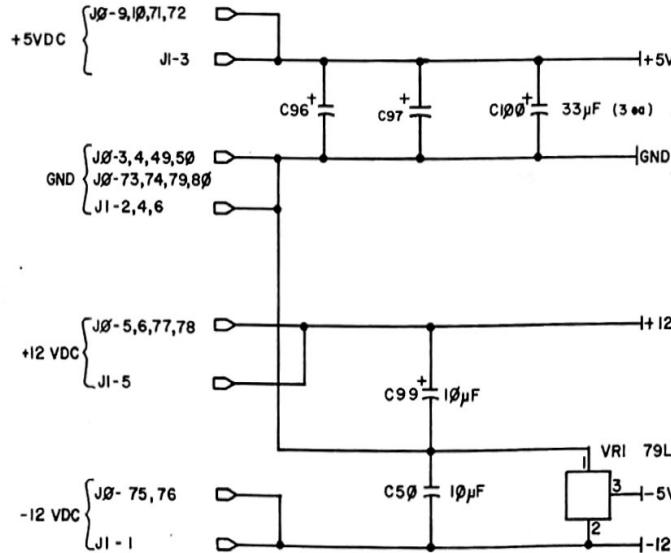
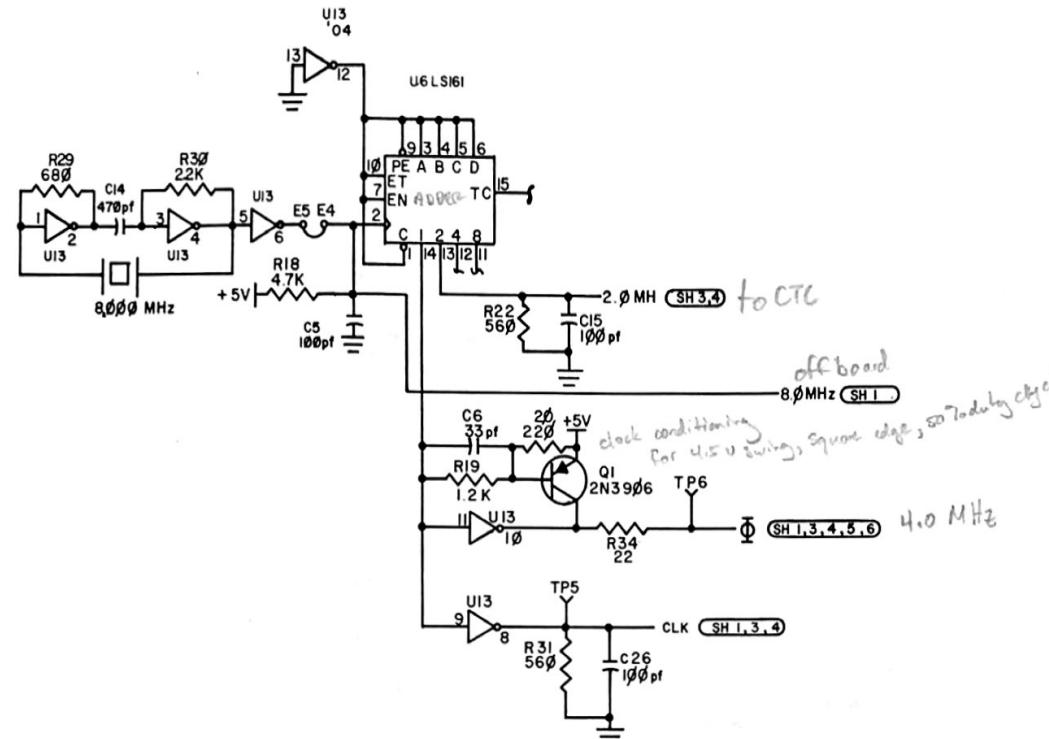
NMI mainly used for  
real time clock  
service routine

REF: DRILL PLAN D17002II		REV. A <sub>1</sub>
CIRCUIT NUMBER	DATE 3-18-83	TITLE SCHEMATIC MAIN LOGIC BOARD MODEL 12/16B
	DATE 1-1-83	DATE 1-16-83
CIRCUIT NUMBER	DATE 3-24-83	DWG NO 8000166
	DATE 3-11-83	SCALE NONE
		SHEET OF 6

**PROPRIETARY INFORMATION**

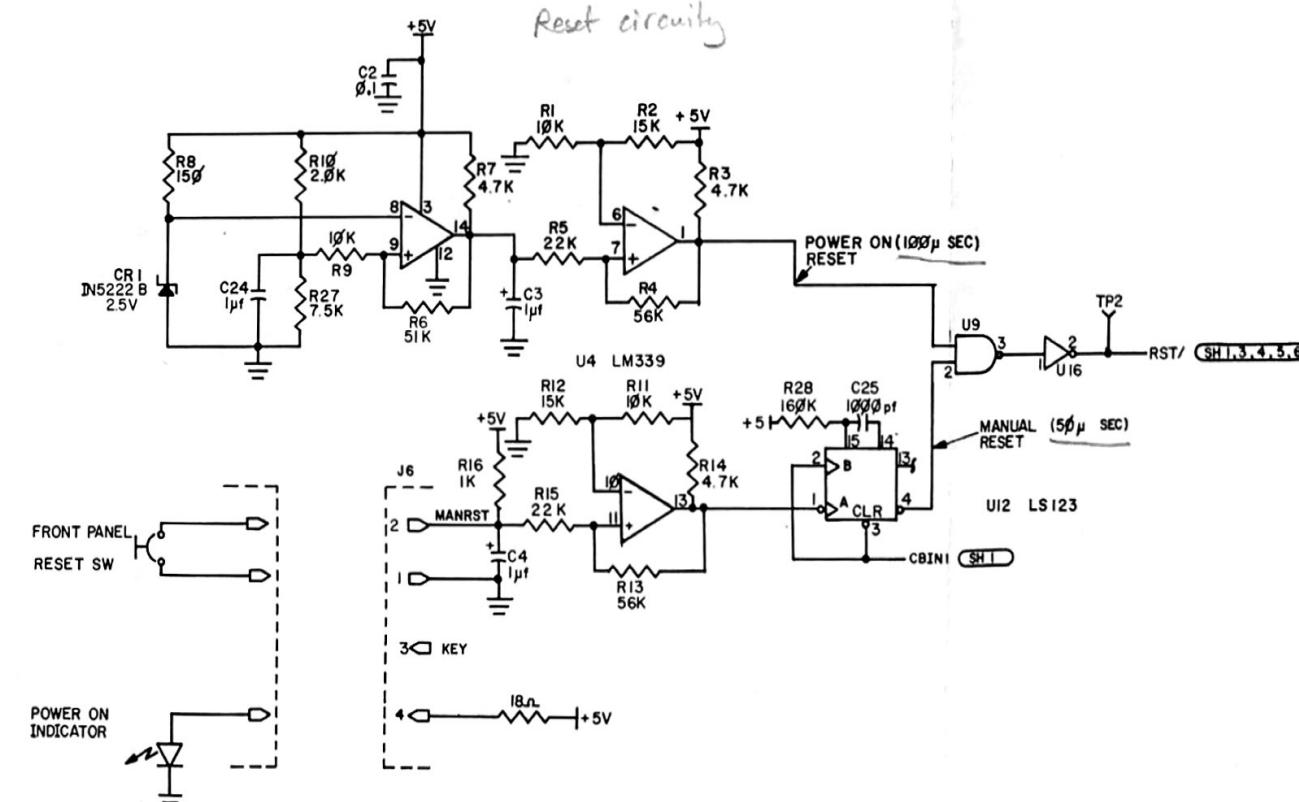
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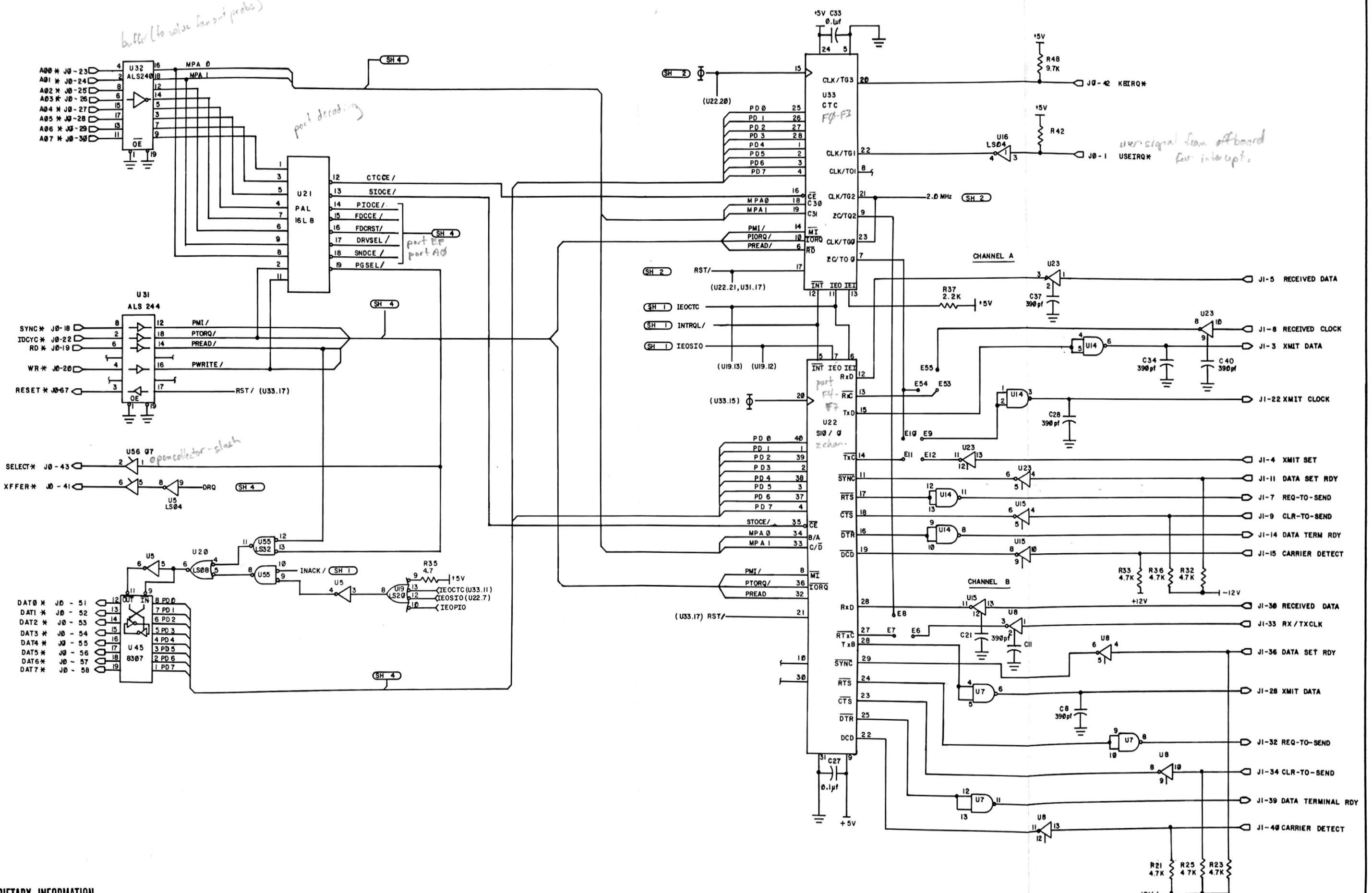
Clock



OFF BOARD SOUND MODULE  
REFERENCE ONLY

Reset circuitry





#### PROPRIETARY INFORMATION

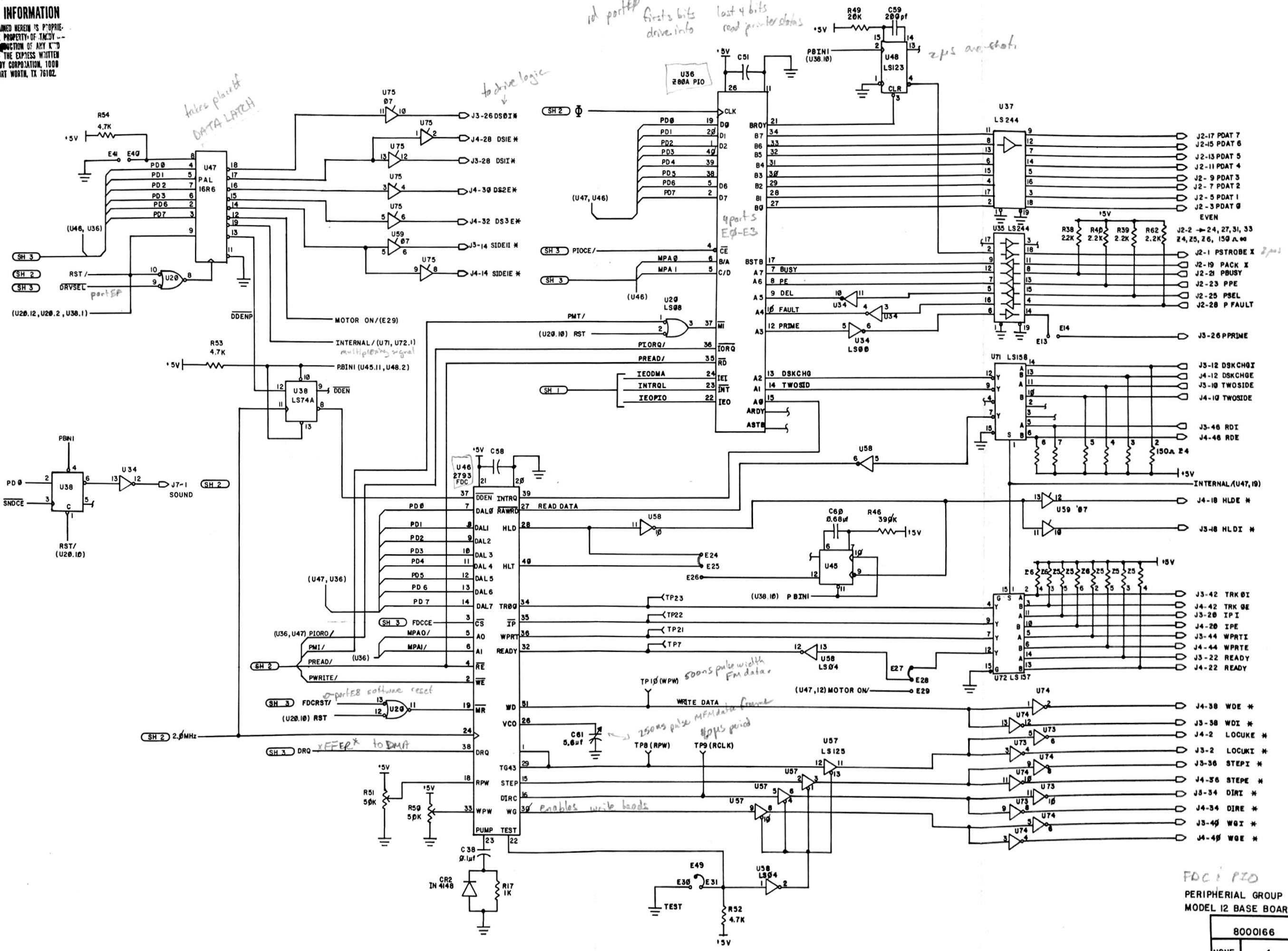
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PERIPHERAL GROUP	
DWG NO	8000166
REV	A1

SCALE SHEET 3 OF 6

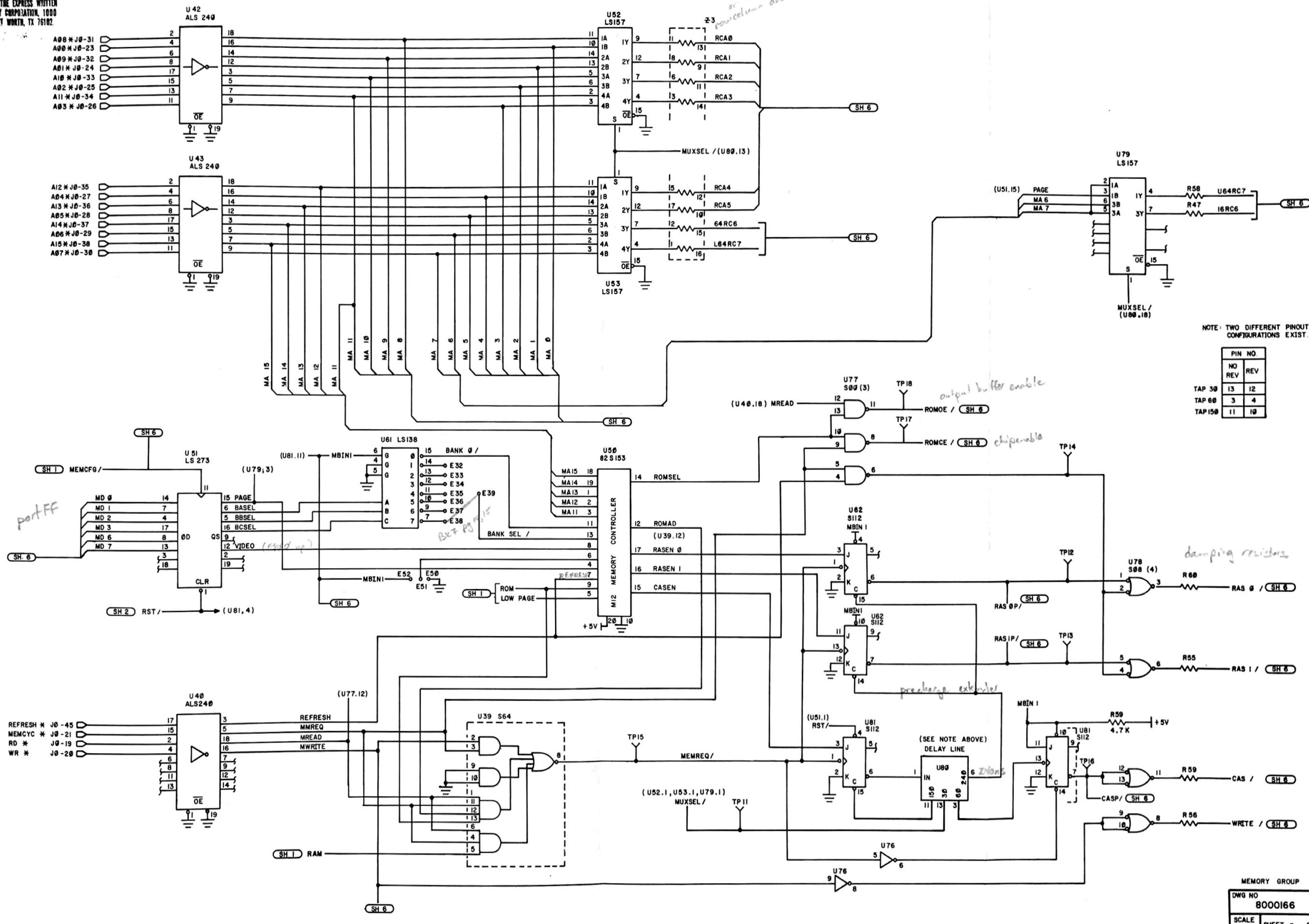
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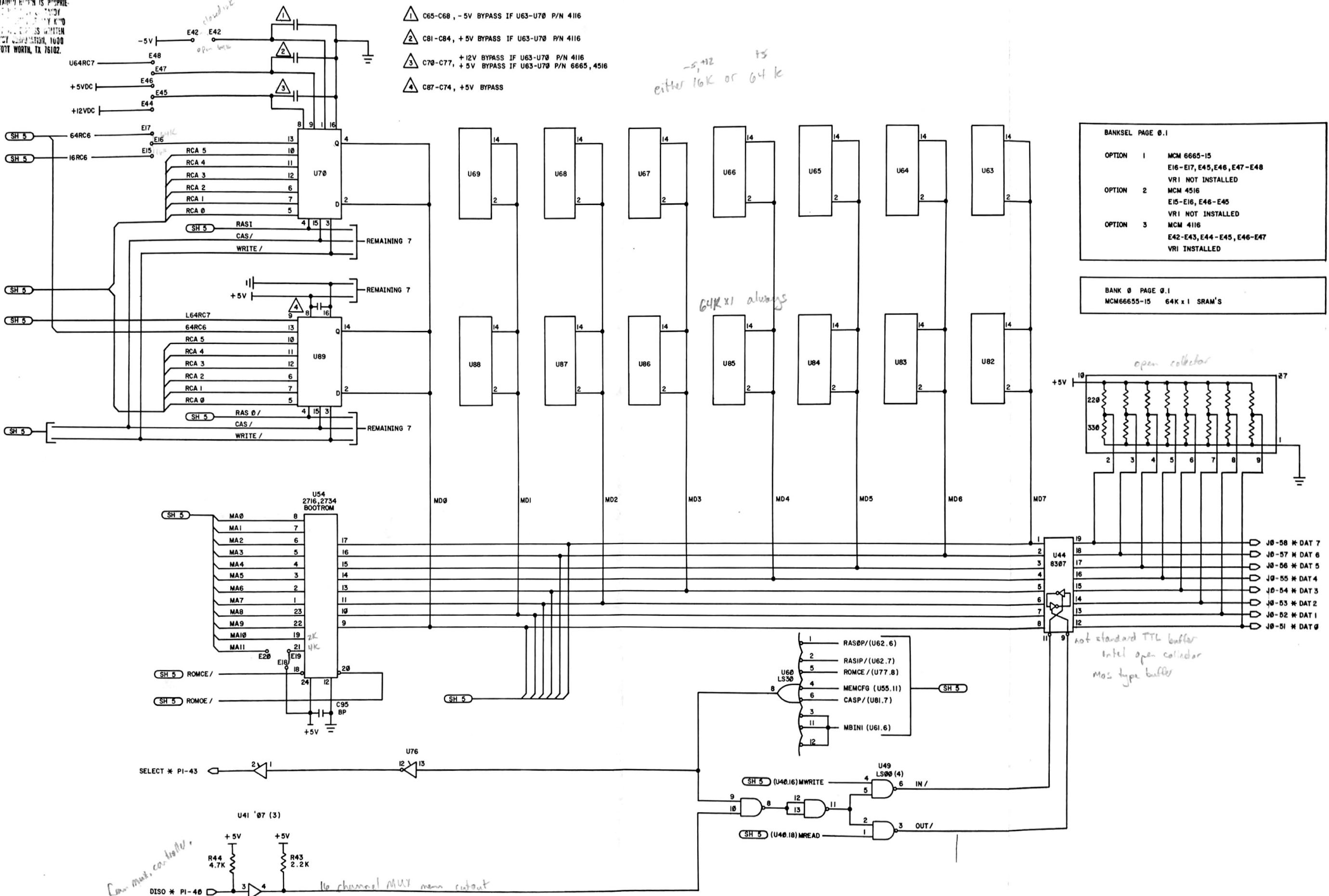
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REVISION  
ZONE LTR DATE APPROVED



REV A1  
DWG NO 8000166  
SCALE NONE  
SHEET 6 OF 6