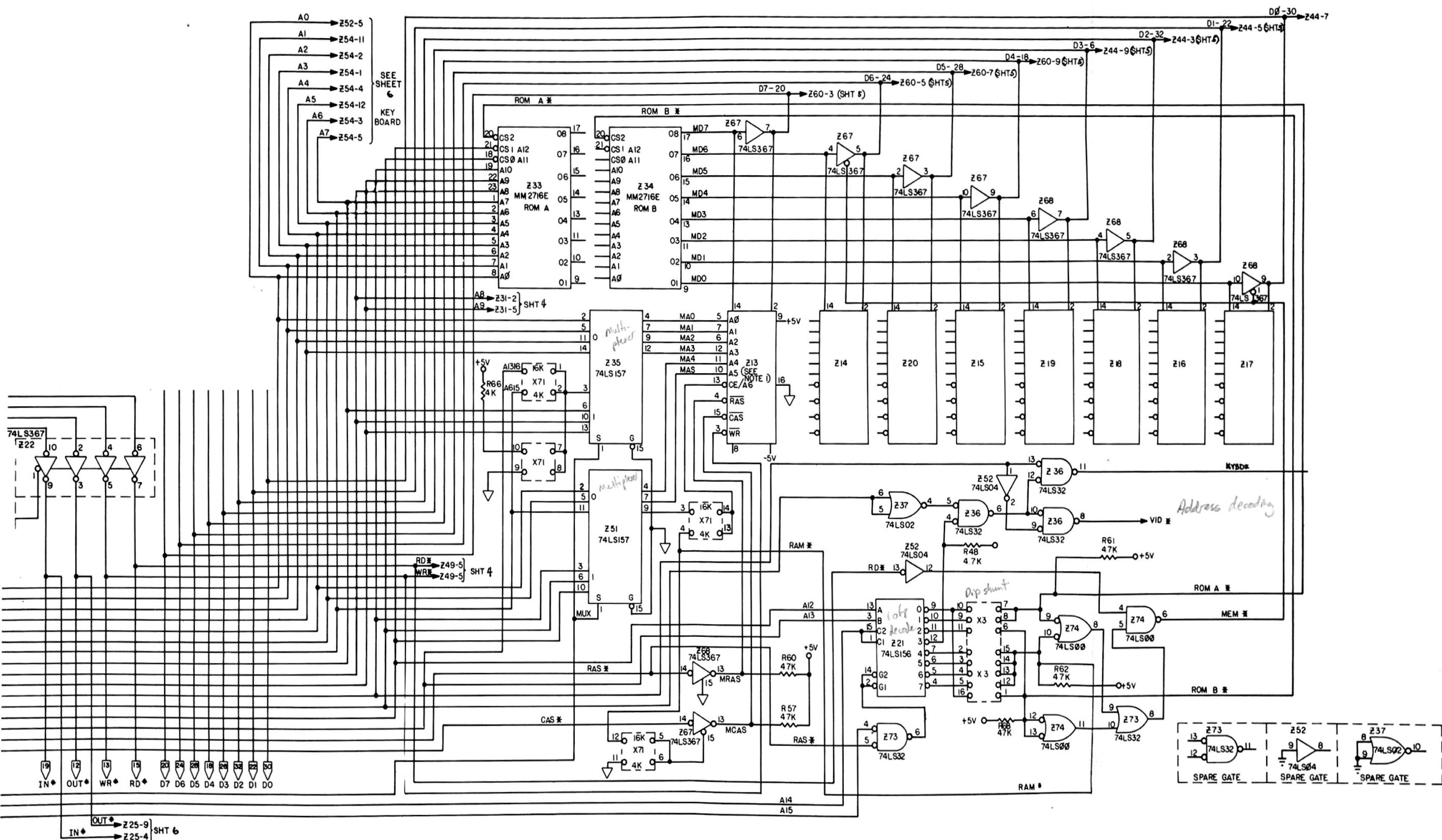


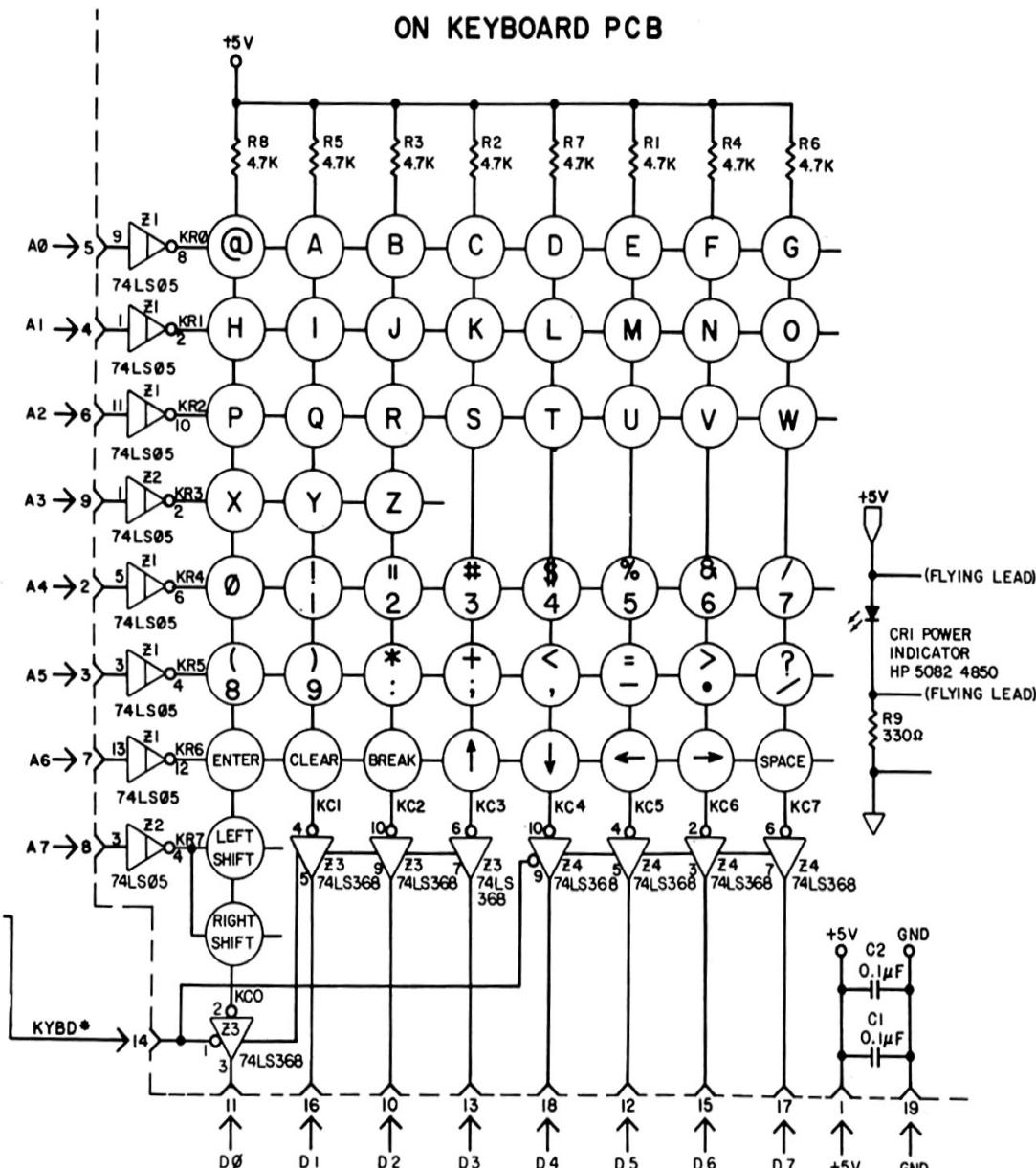
MODEL 1  
PAGE 1 OF 6

8000023

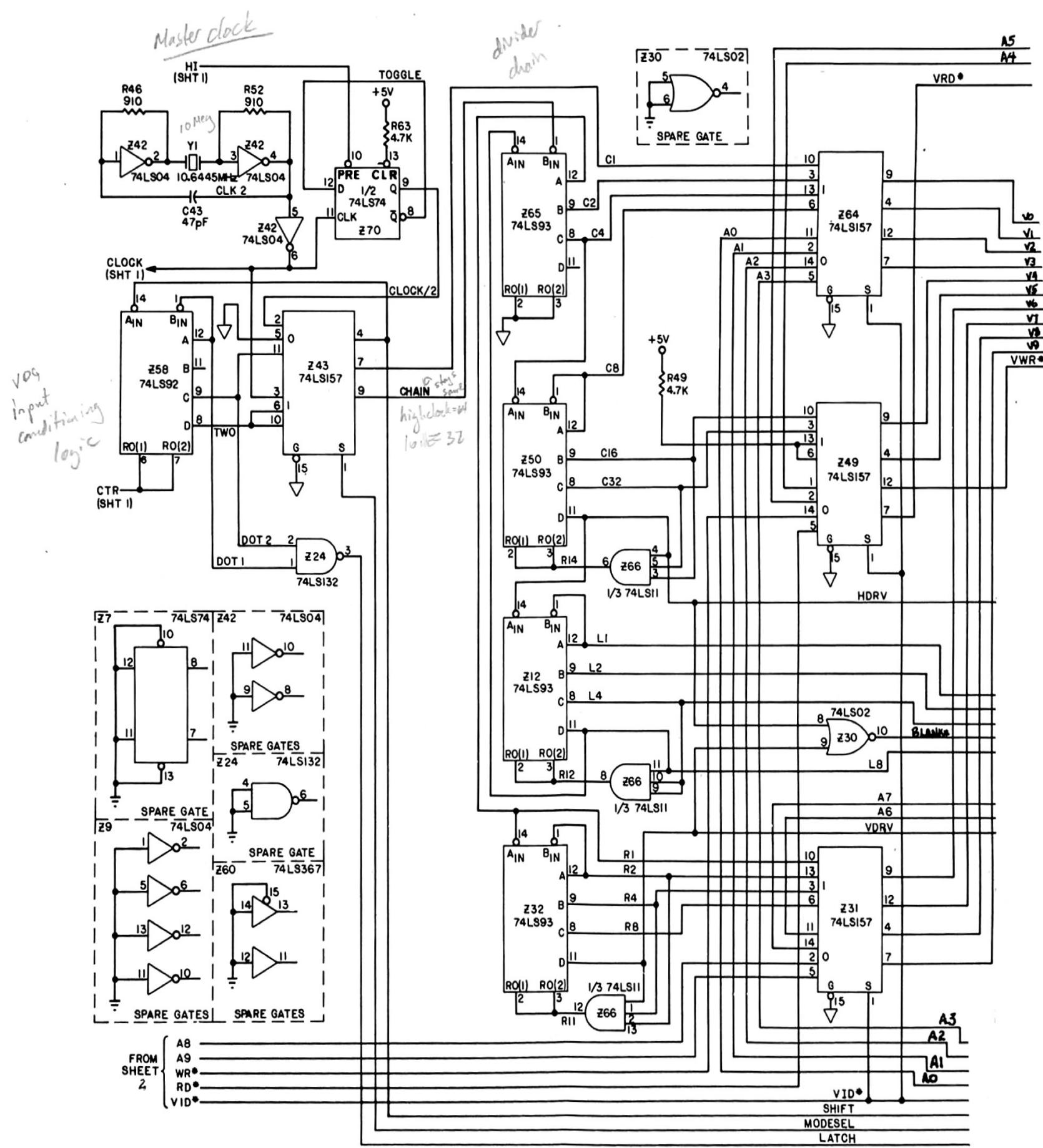
Mod I mem mapped  
except for cassette and RS232

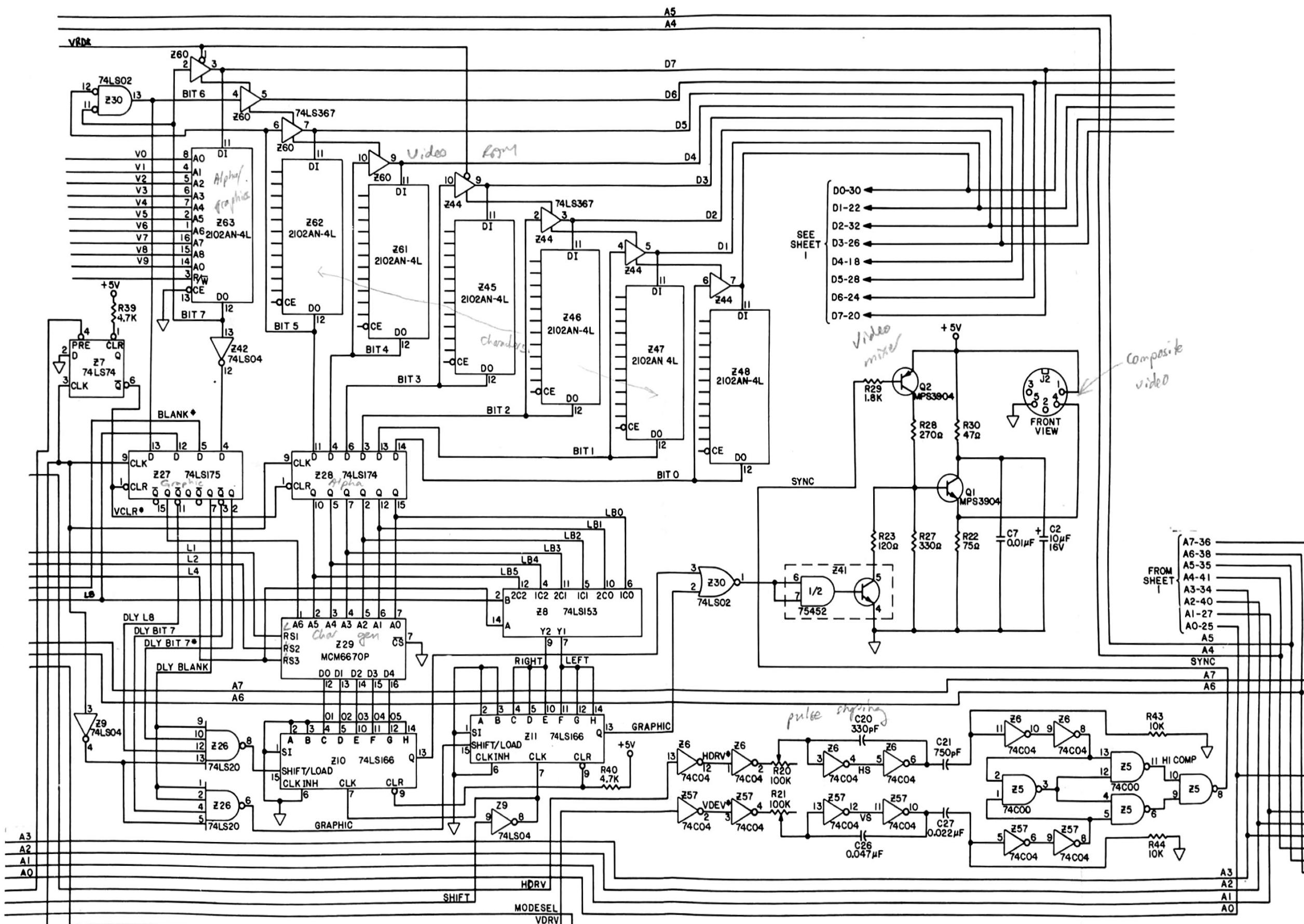


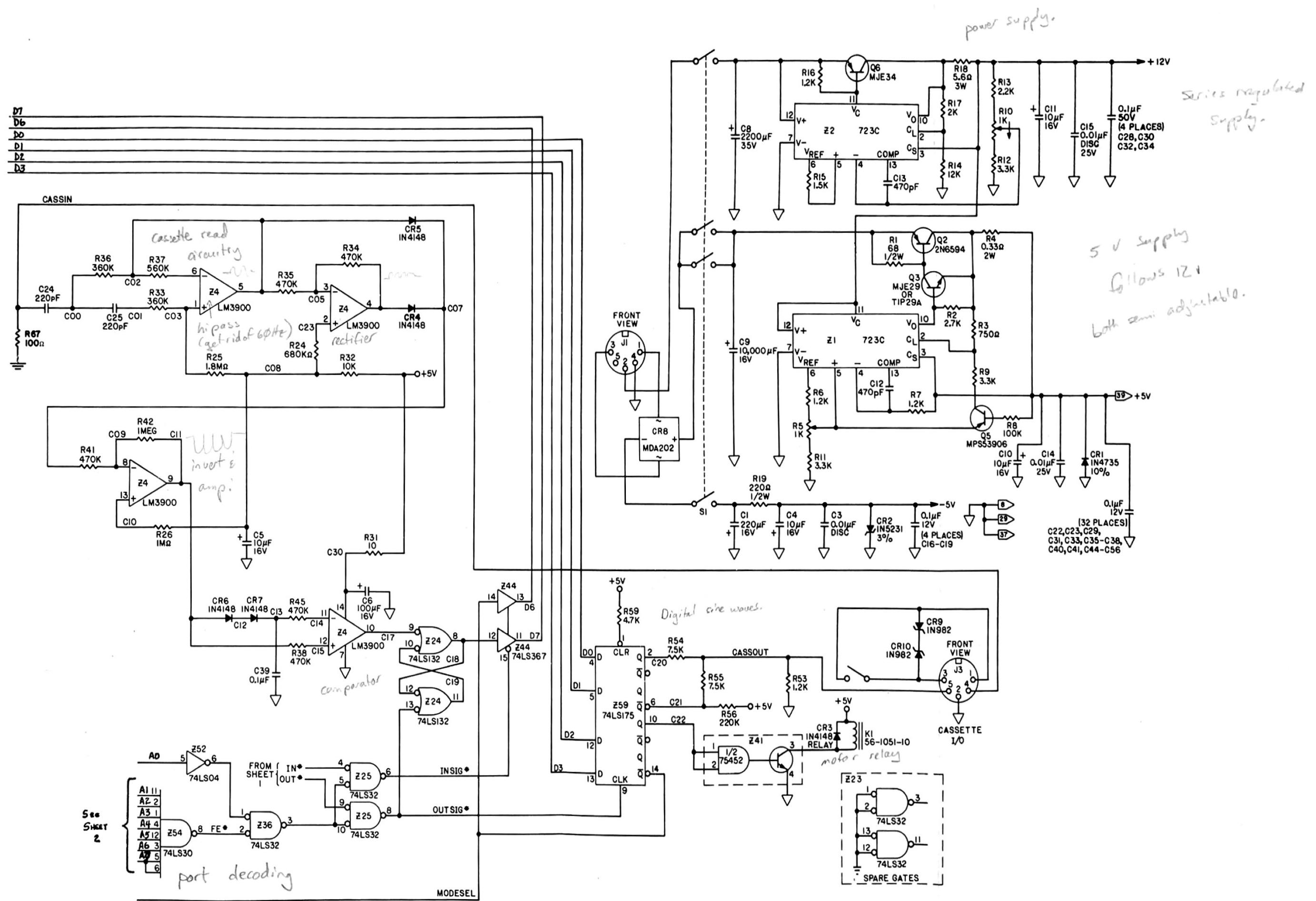
Scanned matrix



ON LOGIC PCB

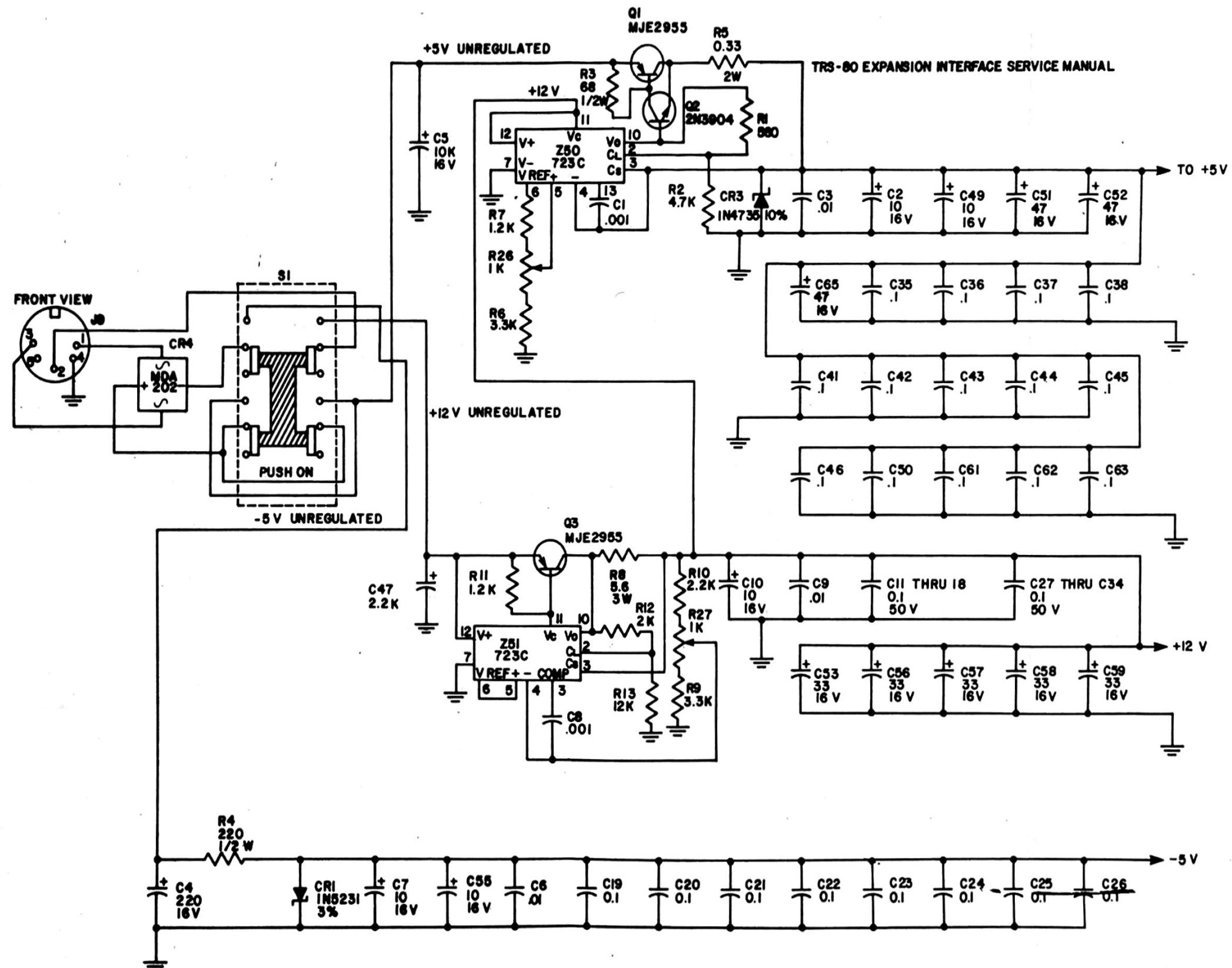






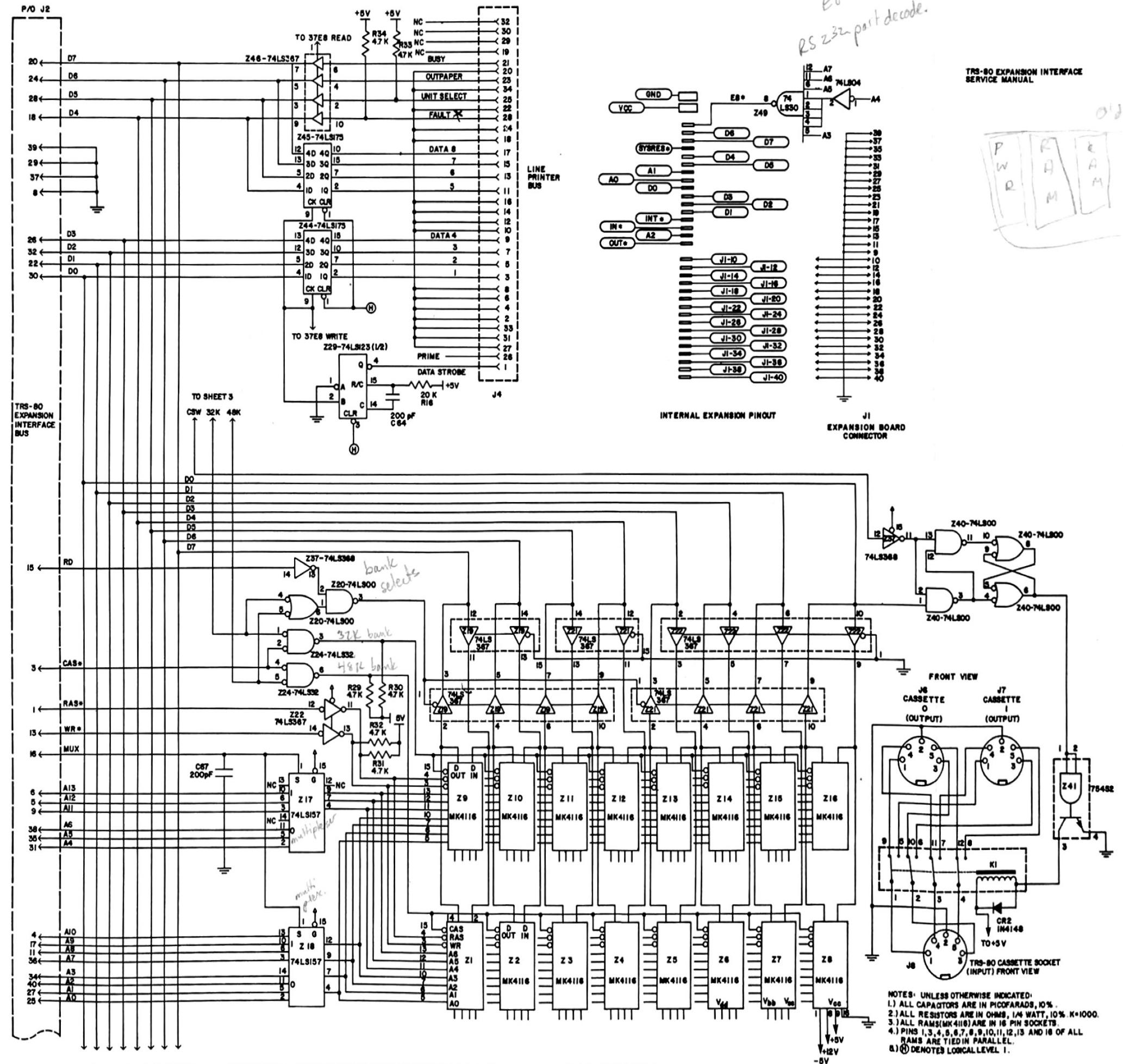
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NOTES: UNLESS OTHERWISE INDICATED.  
 1) ALL CAPACITORS ARE IN MICROFARADS, K=1000.  
 2) ALL RESISTORS ARE IN OHMS, 1/4 WATT, 5%, K=1000.

FIGURE 7. EXPANSION INTERFACE SCHEMATIC DIAGRAM (SHEET 1)



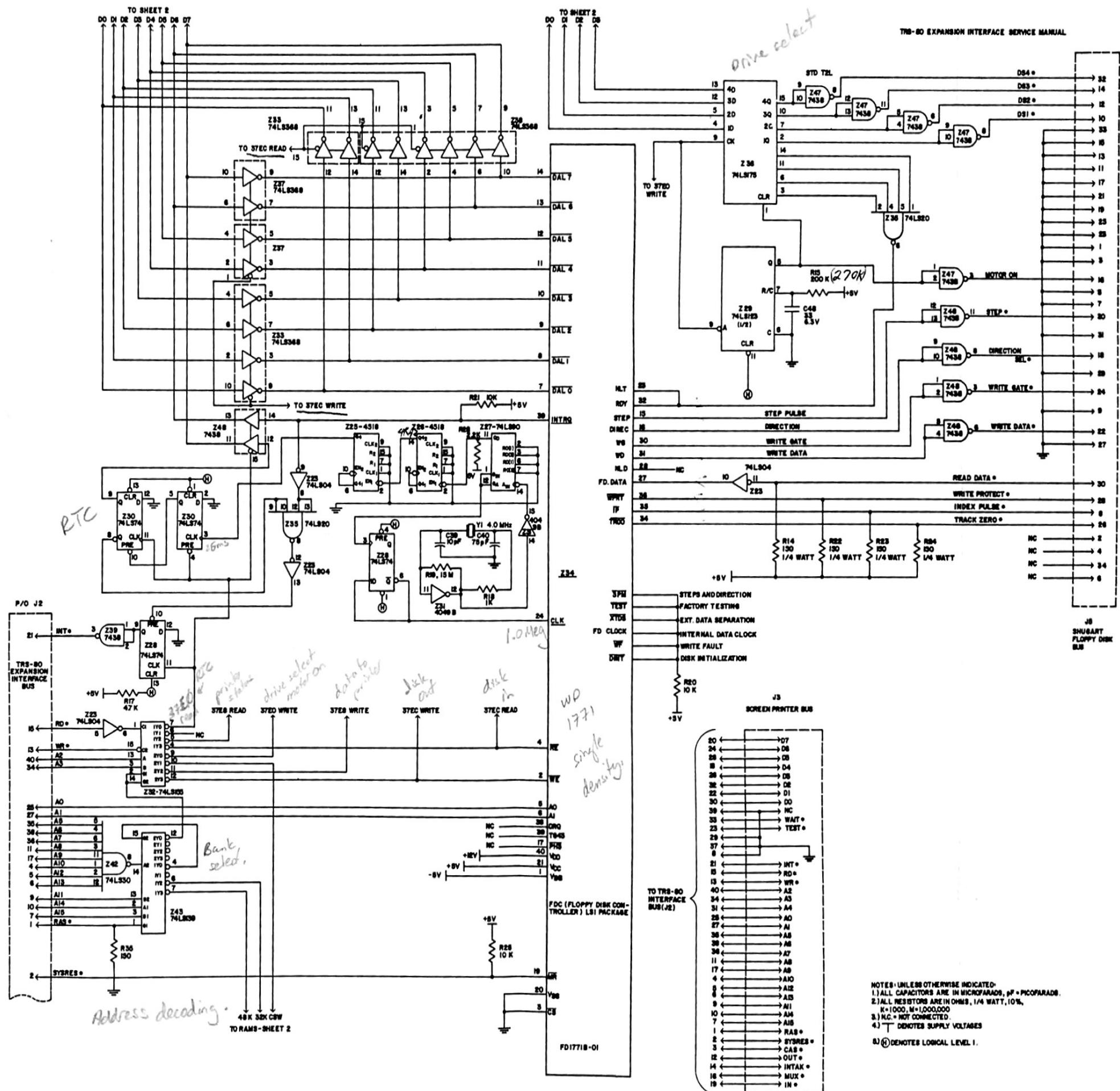


FIGURE 7. EXPANSION INTERFACE SCHEMATIC DIAGRAM (SHEET 3)

MODEL I EI (old)

80000032

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NOTES: UNLESS OTHERWISE INDICATED-

1.) ALL CAPACITORS ARE IN MICROFARADS, pF = PICOFARADS.

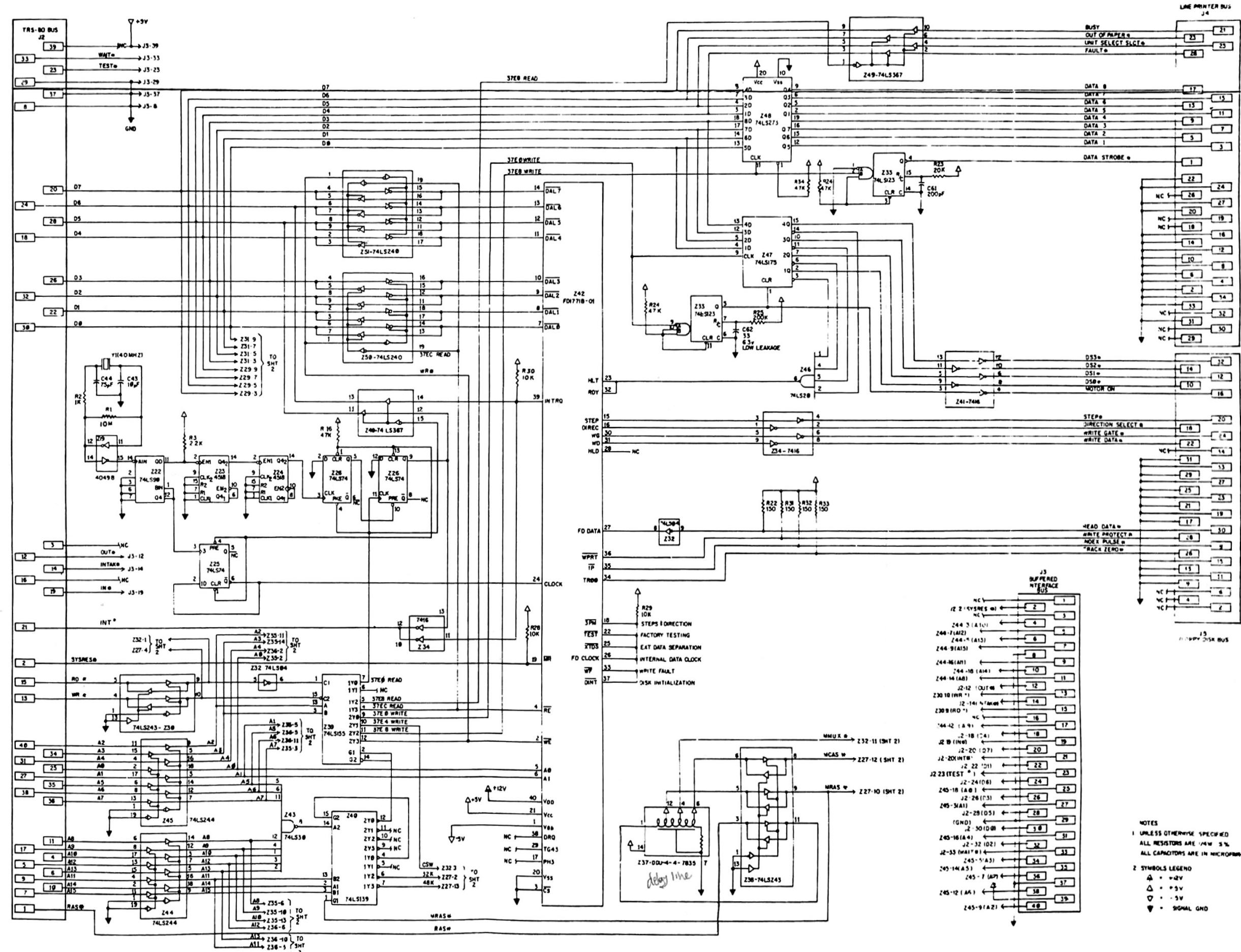
2.) ALL RESISTORS ARE IN OHMS, 1/4 WATT, 10%,

K=1000, M=1,000,000.

3.) NC = NOT CONNECTED.

4.) - DENOTES SUPPLY VOLTAGES.

5.) ( ) DENOTES LOGICAL LEVEL 1.



**FIGURE 7. EXPANSION INTERFACE SCHEMATIC DIAGRAM (SHEET 1)**

MODEL I EI (new)

3000045

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