



NOTES:
(UNLESS OTHERWISE SPECIFIED)
1. PARTIAL REFERENCE DESIGNATION SHOWN; FOR
COMPLETE DESIGNATION, PREFIX WITH UNIT AND /
OR ASSEMBLY DESIGNATION, AX.
2. NUMBERS SHOWN BELOW REFERENCE DESIGNATION
IN LOGIC SYMBOLS ARE SEGMENTS OF VENDOR
PART NUMBER DESCRIBED IN PART LIST.
3. LOGIC SYMBOLS PER USA-STD Y32.14; ELECTRONIC
SYMBOLS PER USA-STD Y32.2-67
4. ALL RESISTOR VALUES ARE IN OHMS, 5% 1/4 W

VOLTAGE CHART			
IC	TYPE	-VCC	GND
75108	INTEGRATED RIM	13	7
MK4118-4		20	39
MK3882-4		12	24
75453B		5	24
LS244, LS373, LS240, S240		4	8
LS166, S163, LS257, S112, LS145, RIM TRANSCEIVER, LS191		10	20
LS30, 25, LS27, S04, S74, LS04, LS00, LS02 LS74, LS33, LS32, 20MHZ OSC		8	16
LS30, 25, LS27, S04, S74, LS04, LS00, LS02 LS74, LS33, LS32, 20MHZ OSC		7	14

REF DES	
LAST USED	NOT USED
T1	
J2	
R46	R32, R33
CRI	
C32	
Z49	
L5	
Q3	
E9	

JUMPER CHART		
FROM	TO	NOTE
E2	E3	STD CONFIGURATION
E4	E5	STD CONFIGURATION
E8	E9	-001 ASSY ONLY
E7	E8	-002 ASSY ONLY

SPARES			
REF DES	TYPE	INPUT	OUTPUT
Z6	LS33	11, 12	13
Z20	S04	11	10
Z31	LS00	13, 12	11
Z31	LS00	9, 10	8
Z31	LS00	4, 5	6
Z13	7425	9, 10, 12,	8
		13	

REF: DRILL PLAN 1700216			
TITLE: SCHEMATIC DIAGRAM ARC INTERFACE BOARD			
DWG NO. 8000178		SIZE	
SCALE	SHEET	OF	1
1:1	1	4	
tandy			
MATERIAL		UNLESS OTHERWISE SPECIFIED	
TOLERANCES		DRAFTSMAN: BOYER, TERRY	
XX = ± 010 XXX = ± 005		DATE: 12-6-82	
ANGLES = ± 1°		CHECK DATE	
HOLE DIA TOLERANCES		DESIGN DATE	
.014 - .250 = + .006 -.001		APPD DATE	
.251 - .750 = + .008 -.001		APPD DATE	
.751 - UP = + .015 -.001			
DIMENSIONS ARE IN INCHES AND APPLY AFTER PLATING			
DO NOT SCALE THIS DRAWING		NEXT ASSY.	USED ON





