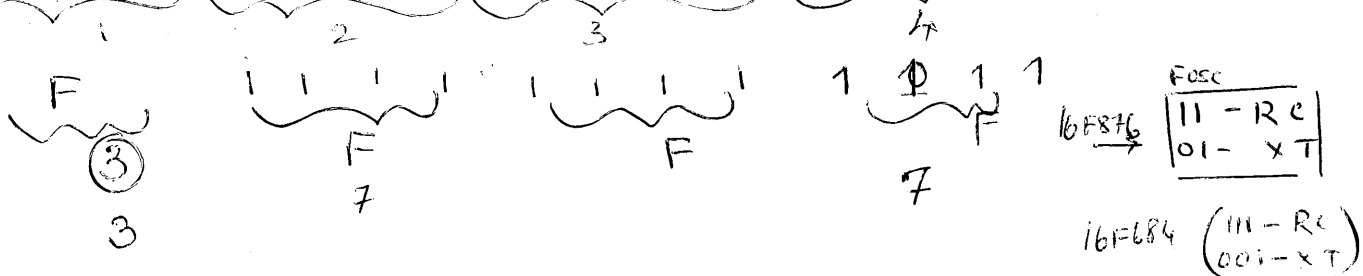


CONFIG

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
0	0	CP1	CP2	DBG	DBG	NRT	CPP	LVP	BOR	CPI	CP0	PWRT	WDTE	FOSC1	FOSC2	PIC 16F876
-	-	-	-	PCME	IESO	BOREN	BOREN	CPD	CP	MCLR	PWRT	WDTE	FOSC2	FOSC1	FOSC0	PIC 16F684
←	F	CP	CP	CP	CP	CP	CP	CP	CP	CP	CP	PWRT	WDTE	FOSC1	FOSC0	PIC 16F84



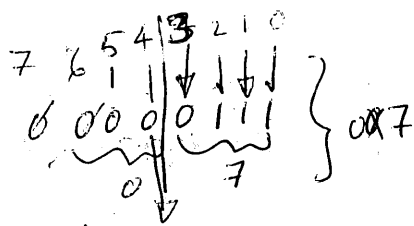
BIT NO.	PIN
TRISC	PIN
RC0	10
1	9
2	8
3	7
RC4	6
5	5

PORTC 0x7
 STATUS 0x3
 CCP1CON 0x15
 PCON 0x8F
 ANSEL 0x01
 RPO 0x05
 RP1 0x06

41'0005'

3
 1+4
 +4
 +8
 10
 001100
 143210

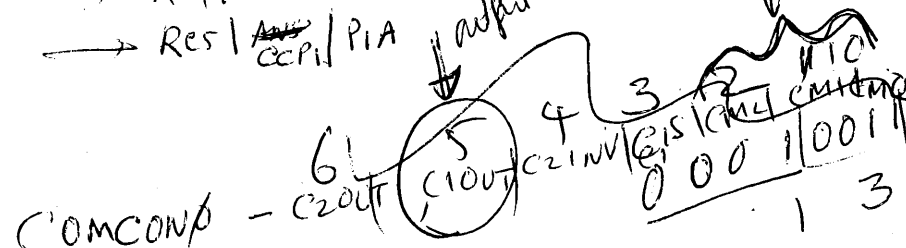
0A - 1010
 0B - 4,5 - 1011
 0C - 5,4,0 - 1100
 0D - 5,4,1 - 1101
 0E - 5,4,0 - 1110
 0F - 5,4,1 - 1111



CMCON

B5 - C2INV
 B2 - CM2
 B1 - CM1
 B0 - CM0

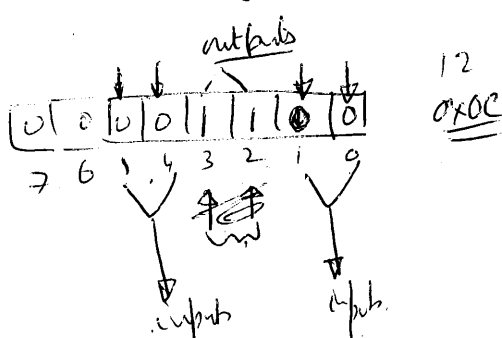
→ RC0/AN4/C2INV
 RC1/AN5/C2INV
 RC2/AN6/PIB
 RC3/AN7/PIB
 → RC4/AN8/PIB
 → RES/AN9/PIA



C_{grd} - NOR
 F_{grd} - E - OK
 D_{No} - OK
 C_{grd} - OK
 B -
 A -

5,4 3 - 0
 11001111
 C

3x7
 2 2 1 0
 1 1 1 1
 3 2+2+1
 2+2+2+1
 = 8+6+2+1



7	6	5	4	3	2	1	0
C2OUT	C1OUT	C2INV	C1INV	CIS	CM2	CM1	CM0

1110
 2322
 14
 A10
 B11
 C12
 D13
 E14