Omeron LCD display signal Processor

TITLE		Omeron LCD display signal Processor	
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PAGE		OF	DRAWN BY:	Ravi Saripalli

U1 – C0 and Segment Signal Summer

U2 – Convert to digital signal with XOR
(why XOR ... just because we had it)

U3 – Using CMOS buffer (3.3v) to shift
digital signal level to 3.3v

LCD Display Drive Header

ABC pins order in MUX
C is siginficant Bit

Differential OPAMP

XOR

BUF (3.3V VDD)
Level Shifting

Read Button

Pump Status