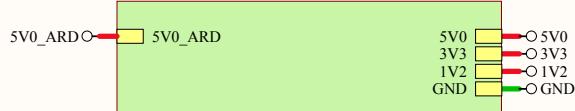
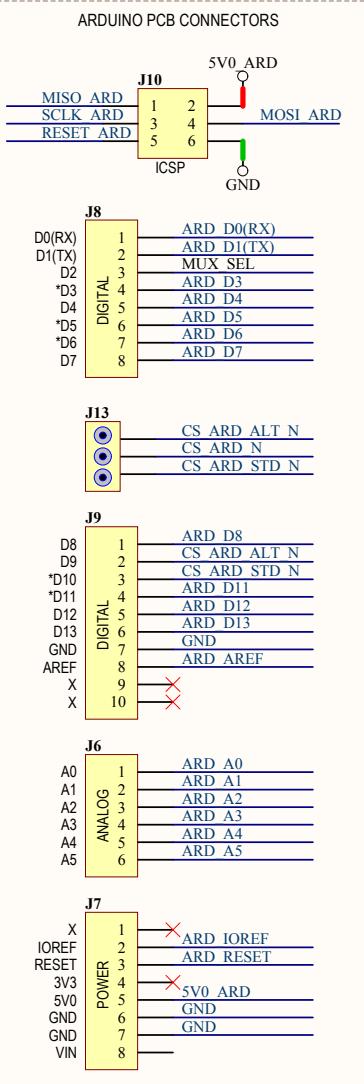


POWER



ARDUINO FPGA DESIGN

3V3 → 3V3
1V2 → 1V2
GND → GND



WP_N → WP_N
HOLD_N → HOLD_N
MUX_SEL → MUX_SEL

MISO_ARD → MISO_ARD
MOSI_ARD → MOSI_ARD
SCLK_ARD → SCLK_ARD
CS_ARD_N → CS_ARD_N

ARD_D0(RX) → ARD_D0(RX)
ARD_D1(TX) → ARD_D1(TX)
ARD_D2 → ARD_D2
ARD_D3 → ARD_D3
ARD_D4 → ARD_D4
ARD_D5 → ARD_D5
ARD_D6 → ARD_D6
ARD_D7 → ARD_D7
ARD_D8 → ARD_D8
CS_ARD_ALT_N → CS_ARD_ALT_N
CS_ARD_STD_N → CS_ARD_STD_N
ARD_D11 → ARD_D11
ARD_D12 → ARD_D12
ARD_D13 → ARD_D13

IO0 → IO0
IO1 → IO1
IO2 → IO2
IO3 → IO3
IO4 → IO4
IO5 → IO5
IO6 → IO6
IO7 → IO7
IO8 → IO8
IO9 → IO9
IO10 → IO10
IO11 → IO11

IO12 → IO12
IO13 → IO13
IO14 → IO14
IO15 → IO15
IO16 → IO16
IO17 → IO17
IO18 → IO18
IO19 → IO19
IO20 → IO20
IO21 → IO21
IO22 → IO22
IO23 → IO23

IO17 → IO17
IO18 → IO18
IO19 → IO19
IO20 → IO20
IO21 → IO21
IO22 → IO22
IO23 → IO23

HARD_JTAG → HARD_TCK
HARD_TDI → HARD_TDI
HARD_TDO → HARD_TDO
HARD_TMS → HARD_TMS

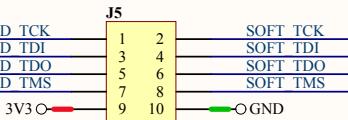
SOFT_JTAG → SOFT_TCK
SOFT_TDI → SOFT_TDI
SOFT_TDO → SOFT_TDO
SOFT_TMS → SOFT_TMS

FPGA_PROG → INIT_B
PROGRAM_B → PROGRAM_B
DONE → DONE

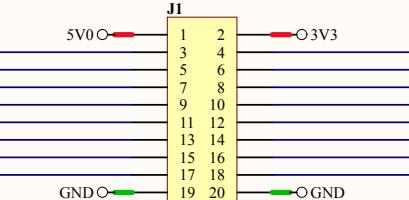
IO_HDR3 → IO24
IO25 → IO25
IO26 → IO26
IO27 → IO27
IO28 → IO28
IO29 → IO29
IO30 → IO30
IO31 → IO31
IO32 → IO32
IO33 → IO33
IO34 → IO34
IO35 → IO35

IO36 → IO36
IO37 → IO37
IO38 → IO38
IO39 → IO39
IO40 → IO40
IO41 → IO41
IO42 → IO42
IO43 → IO43
IO44 → IO44
IO45 → IO45
IO46 → IO46
IO47 → IO47

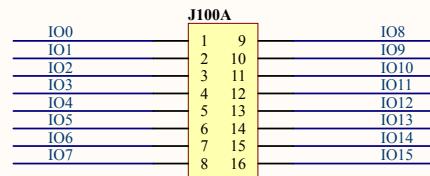
JTAG INTERFACE CONNECTOR



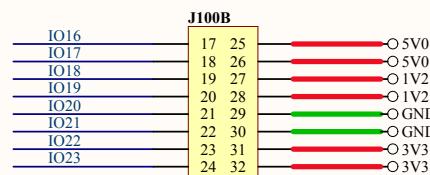
FPGA IO CONNECTORS



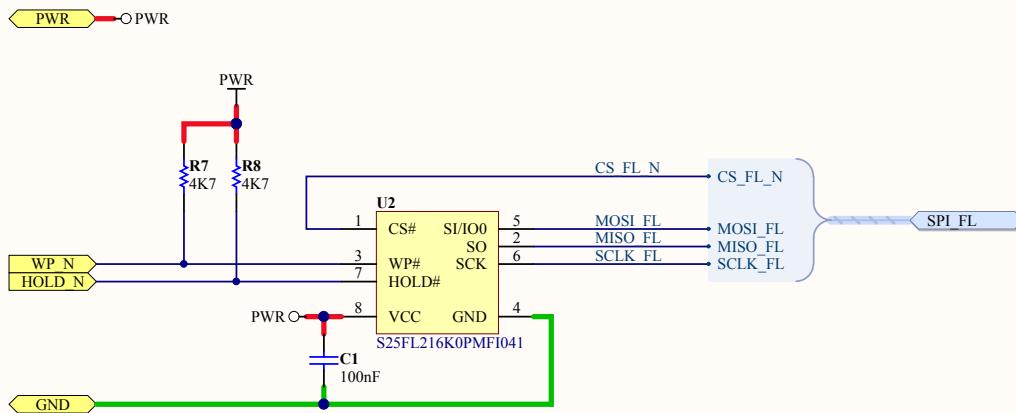
J100A

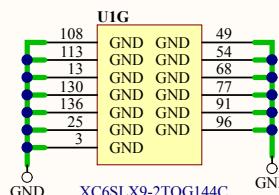
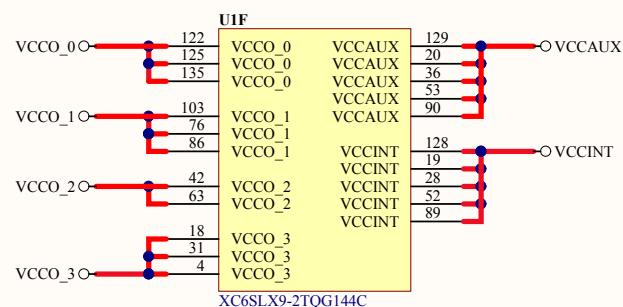
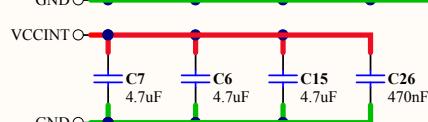
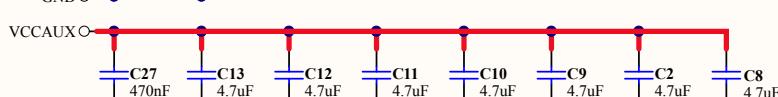
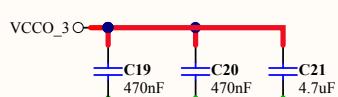
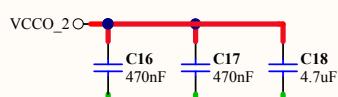
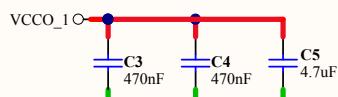
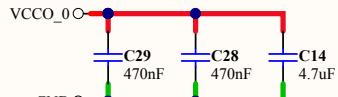
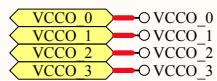


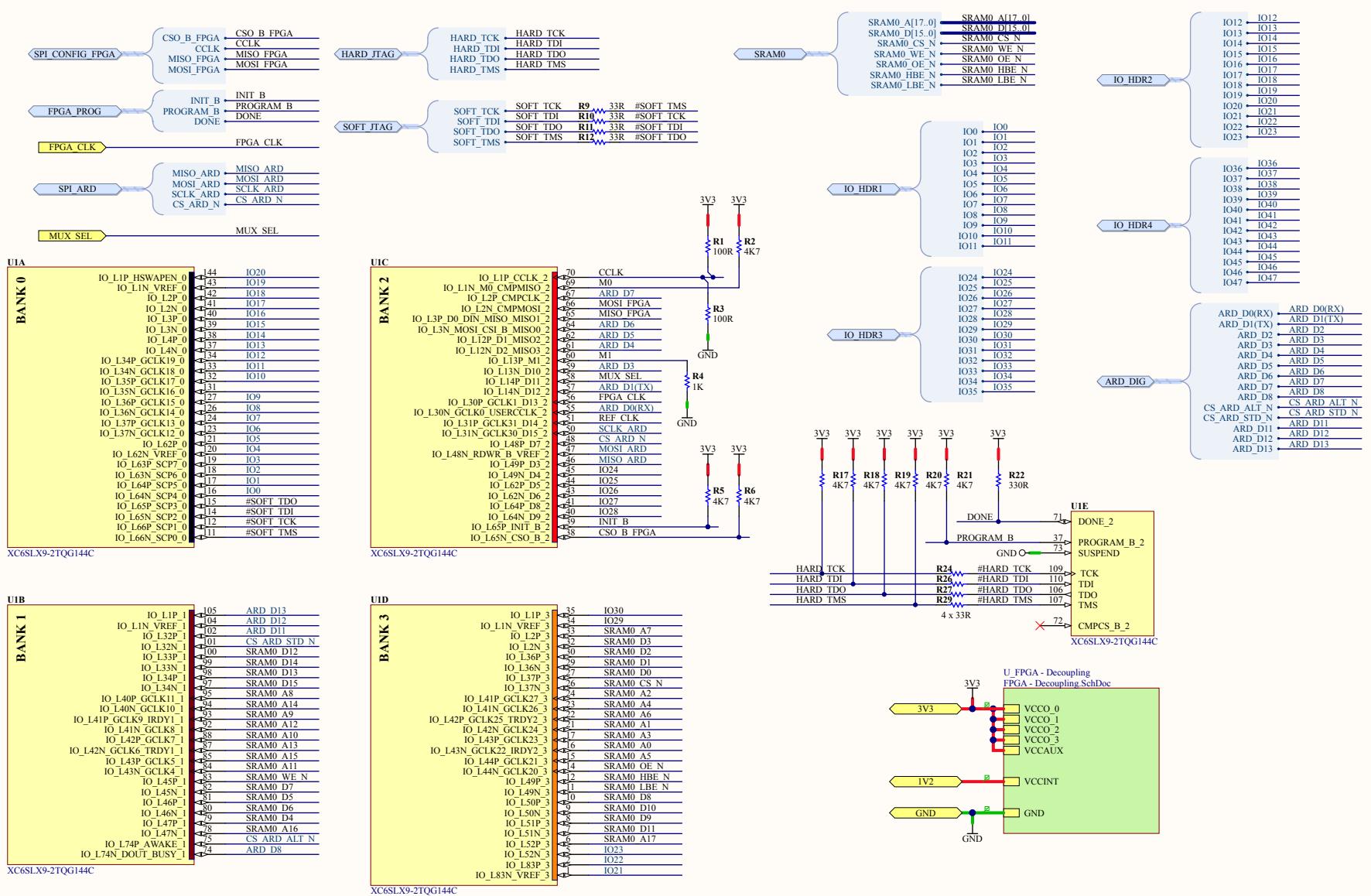
J100B

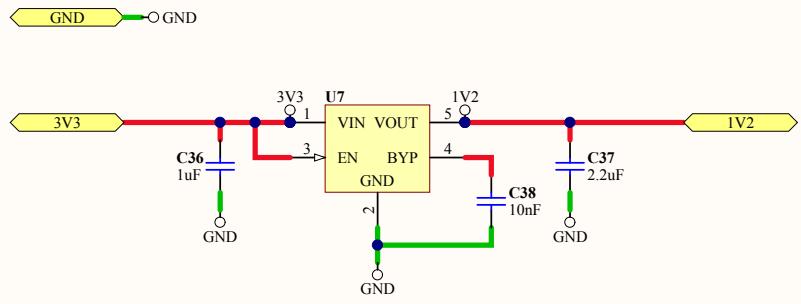


Power Supply 2V4 to 3V7

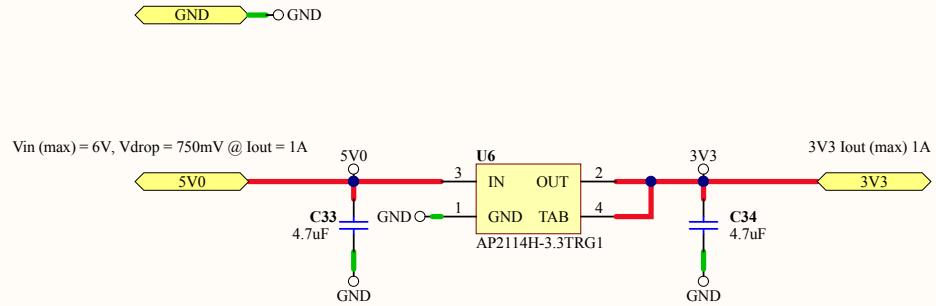


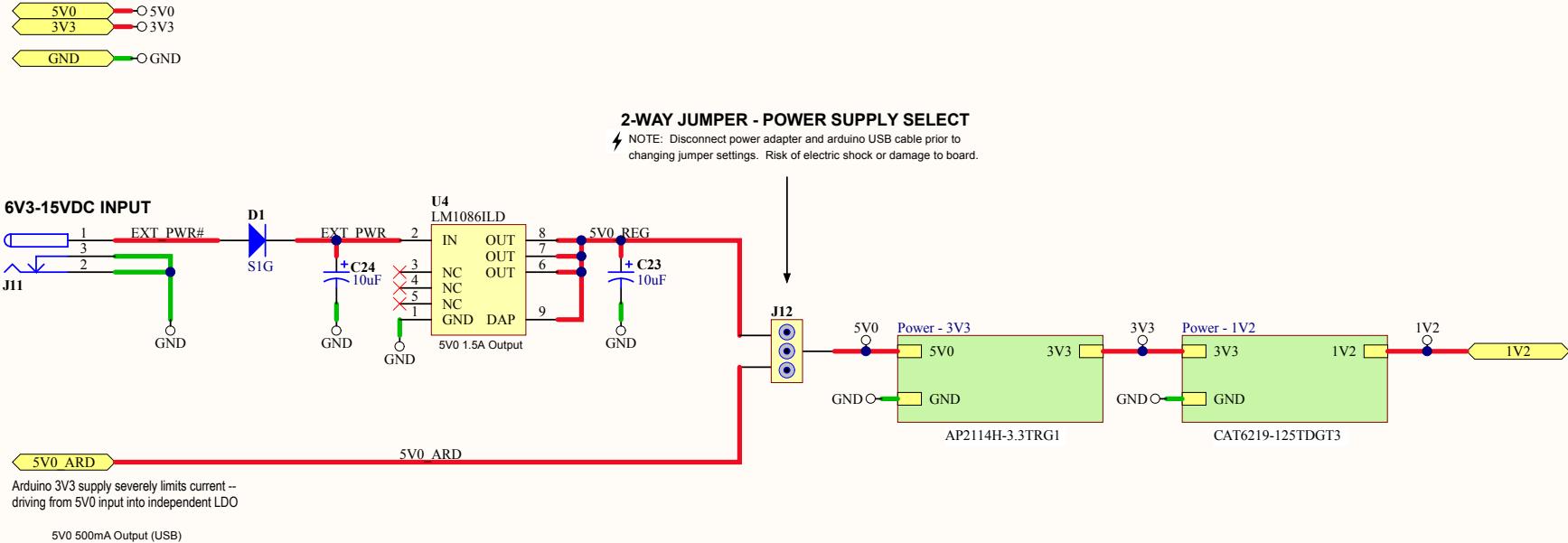






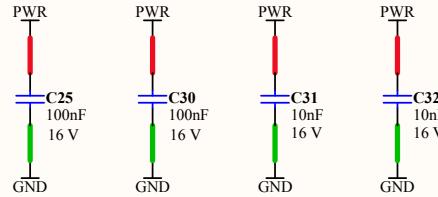
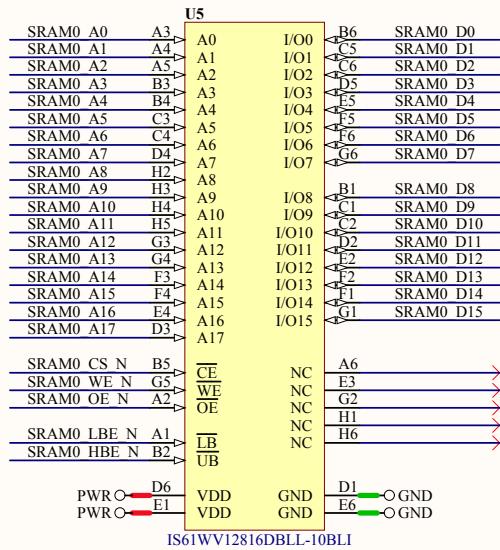
AP2114H - 3V3 Fixed Output Voltage Regulator (Imax = 1A)

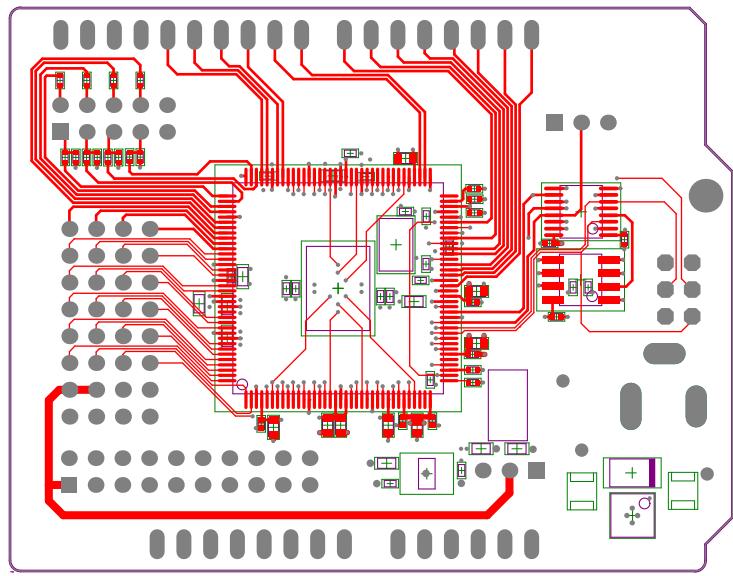


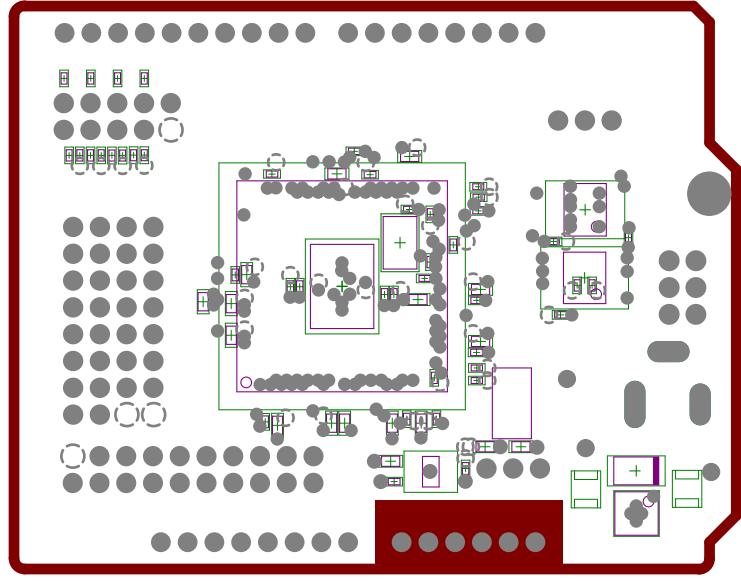


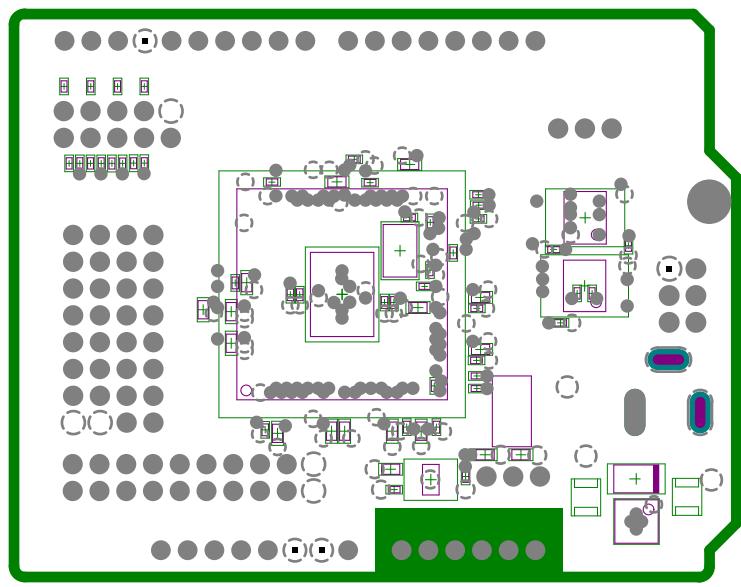


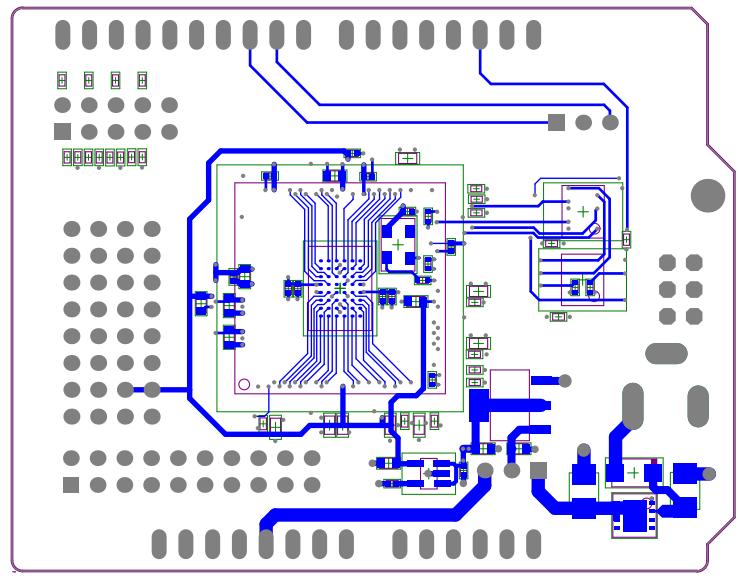
SRAM0 A[17..0] —— SRAM0 A[17..0]
SRAM0 D[15..0] —— SRAM0 D[15..0]
SRAM0 CS_N —— SRAM0 CS_N
SRAM0 WE_N —— SRAM0 WE_N
SRAM0 OE_N —— SRAM0 OE_N
SRAM0_HBE_N —— SRAM0_HBE_N
SRAM0_LBE_N —— SRAM0_LBE_N

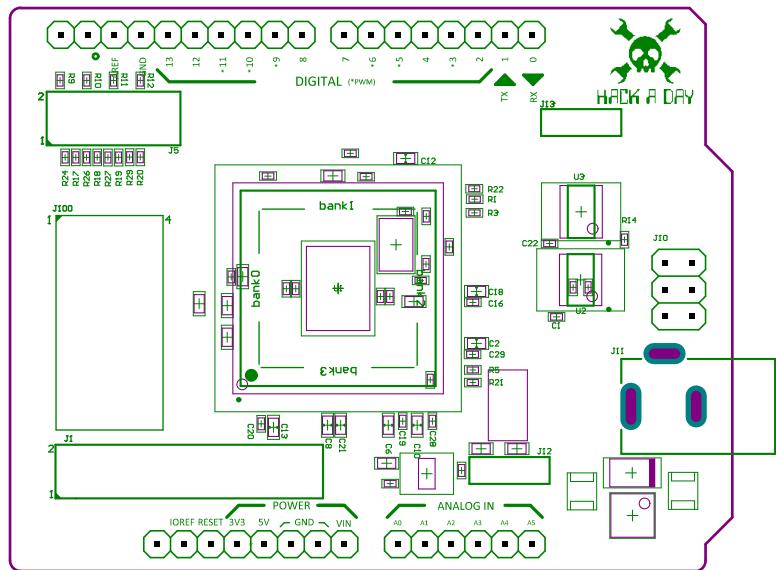


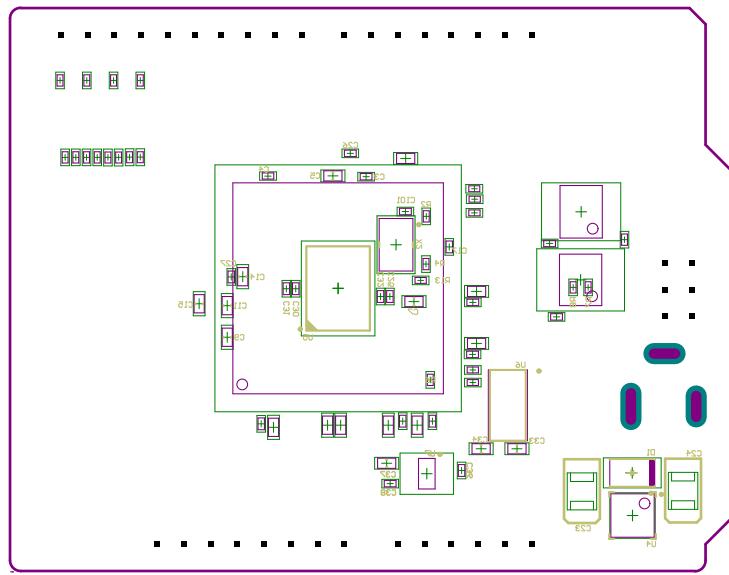


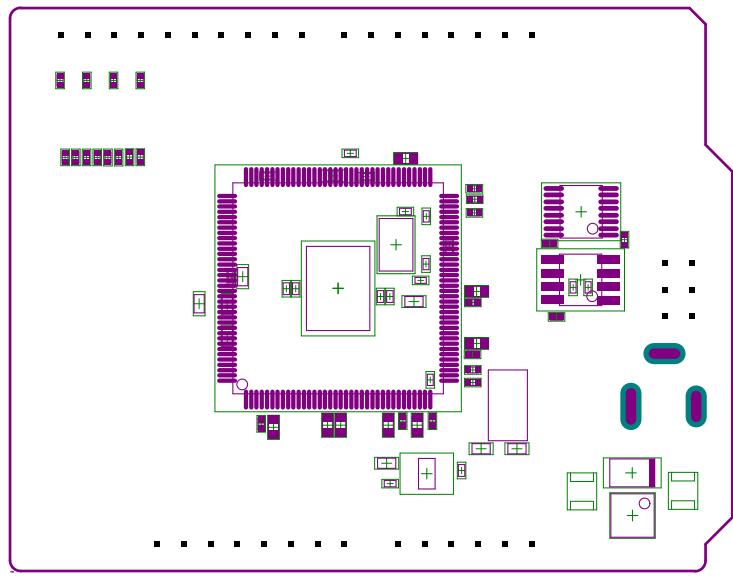


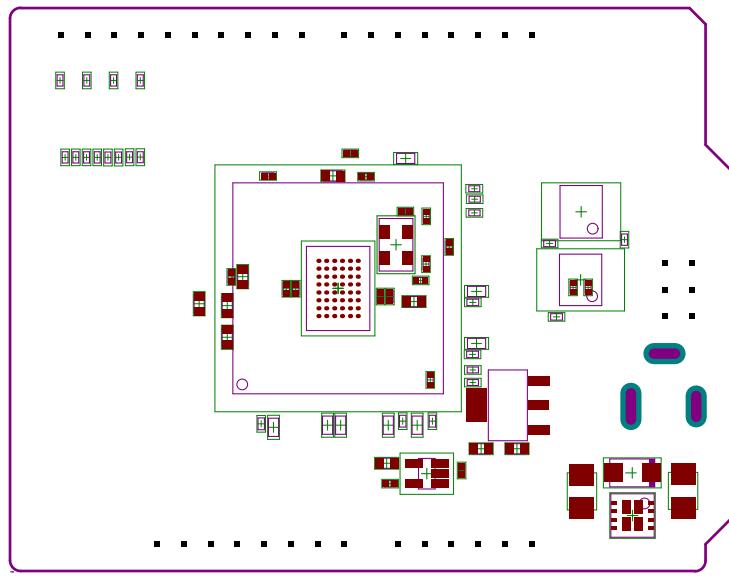


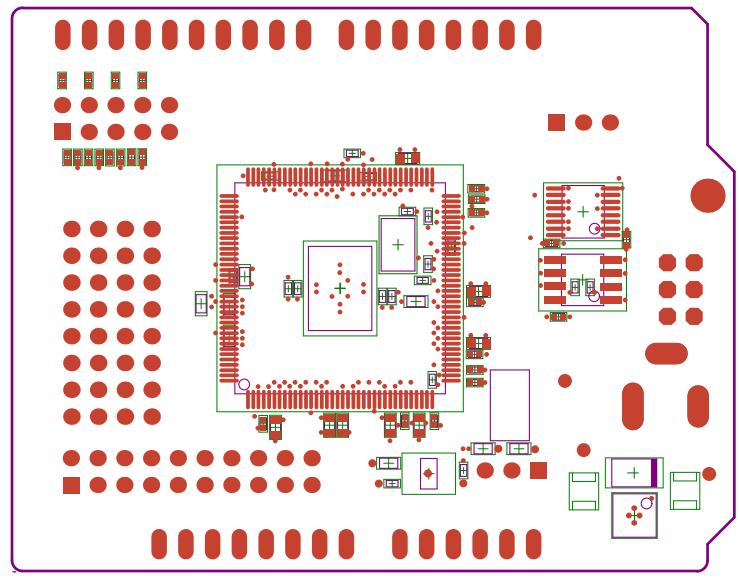


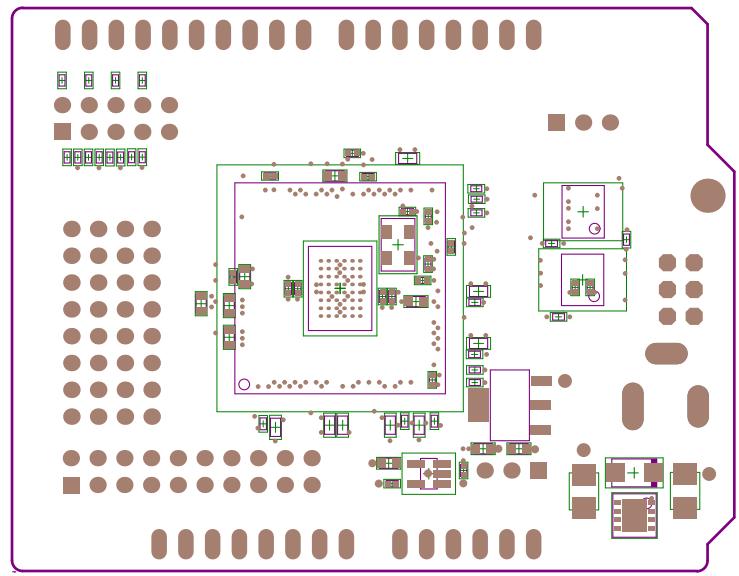


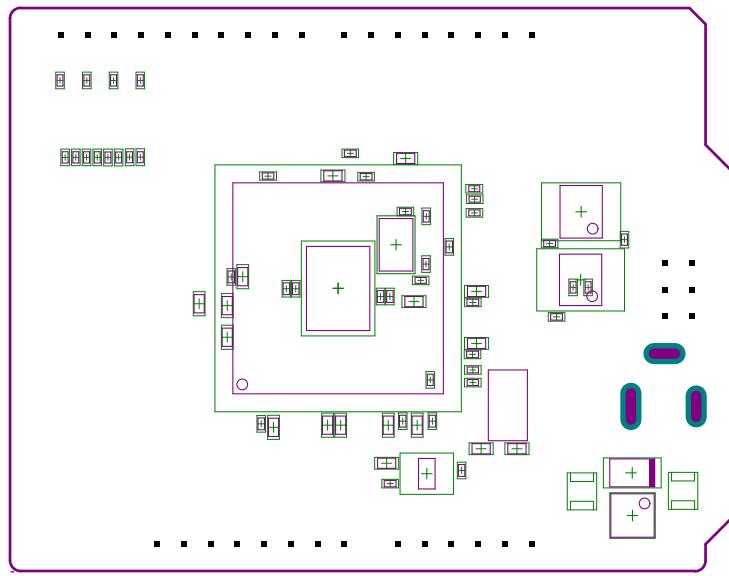


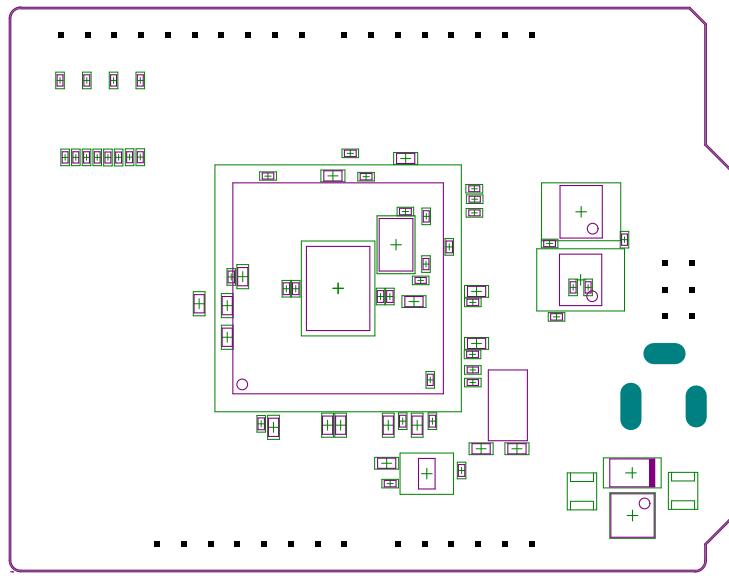


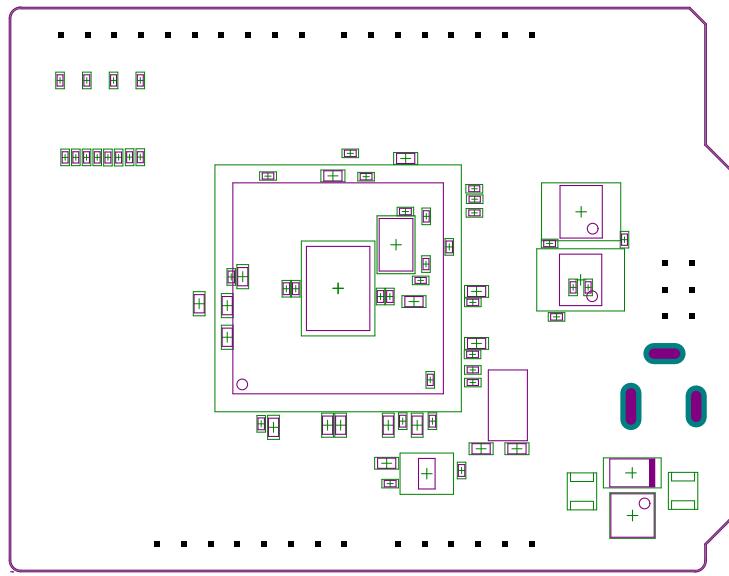


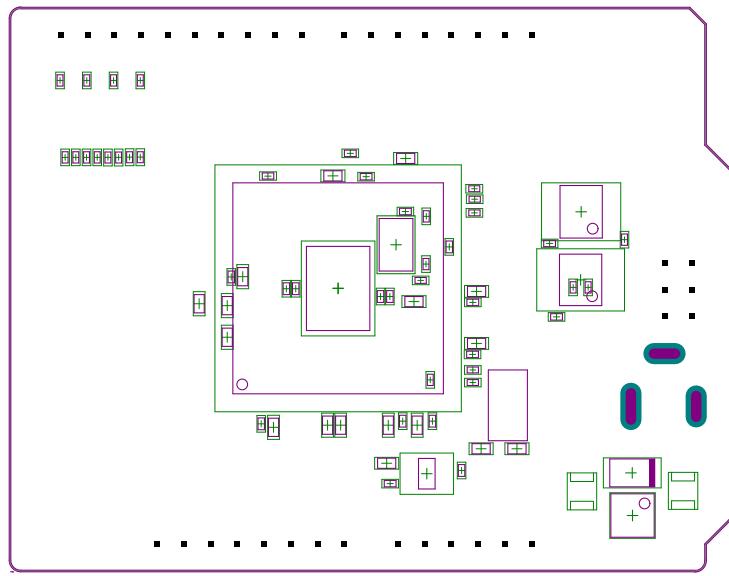


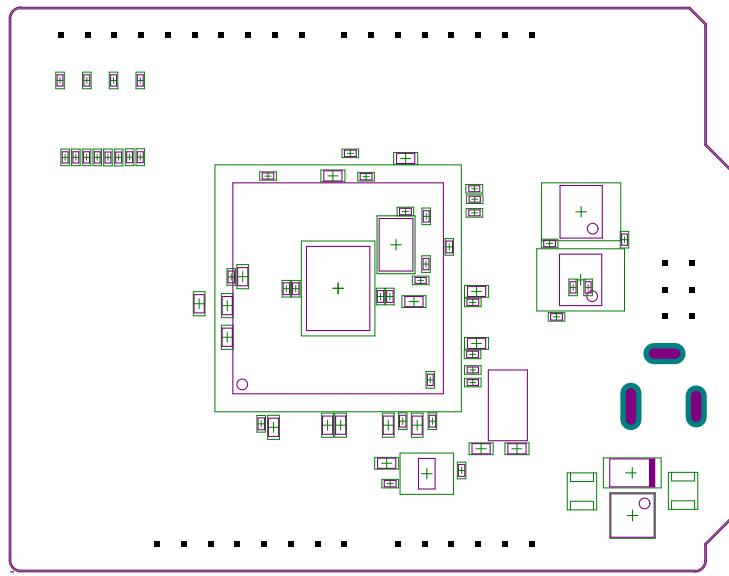


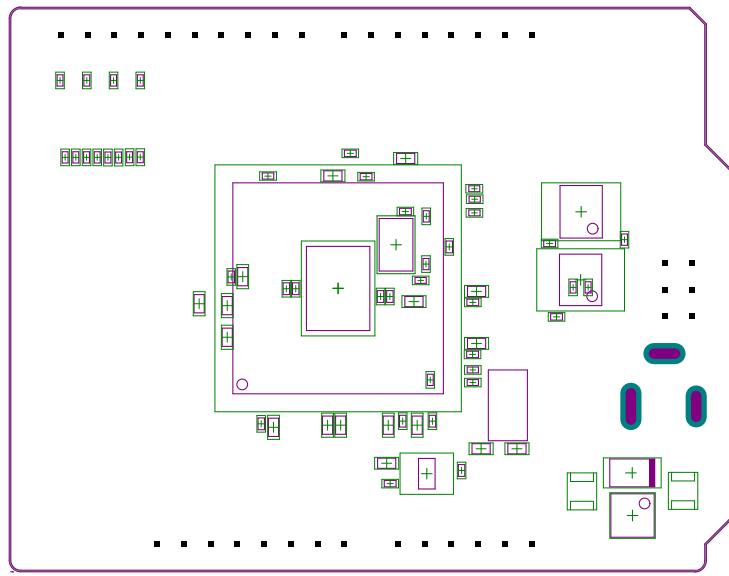


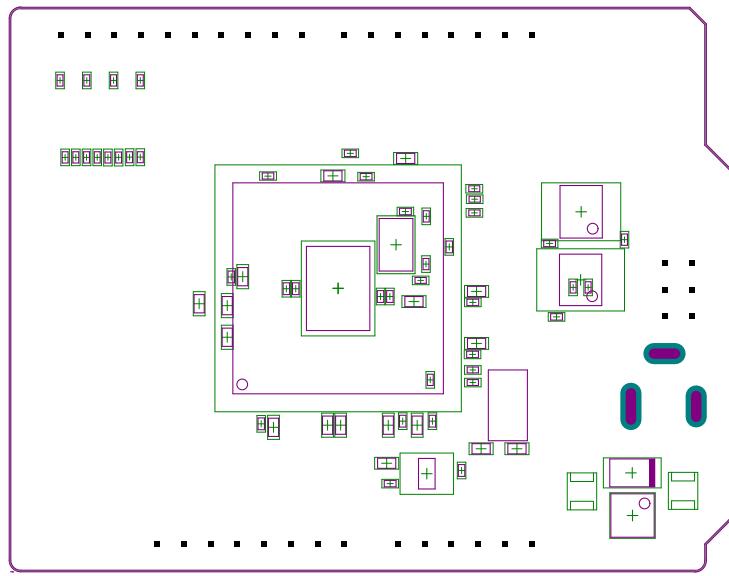


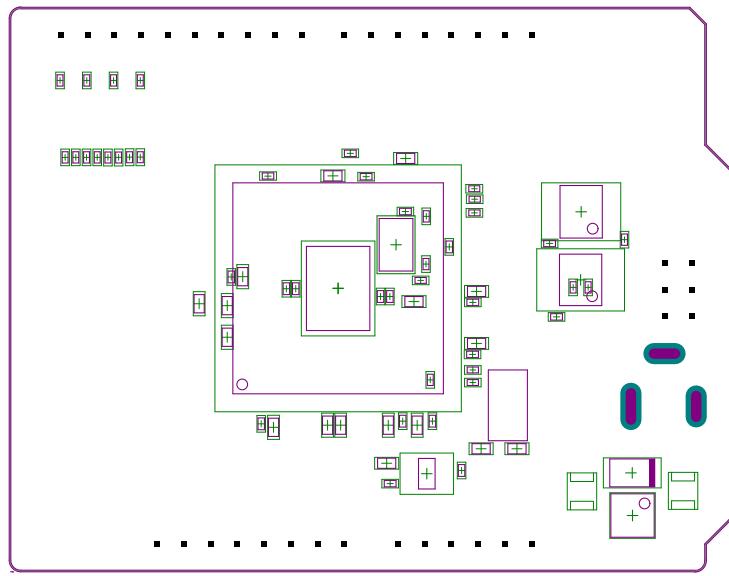


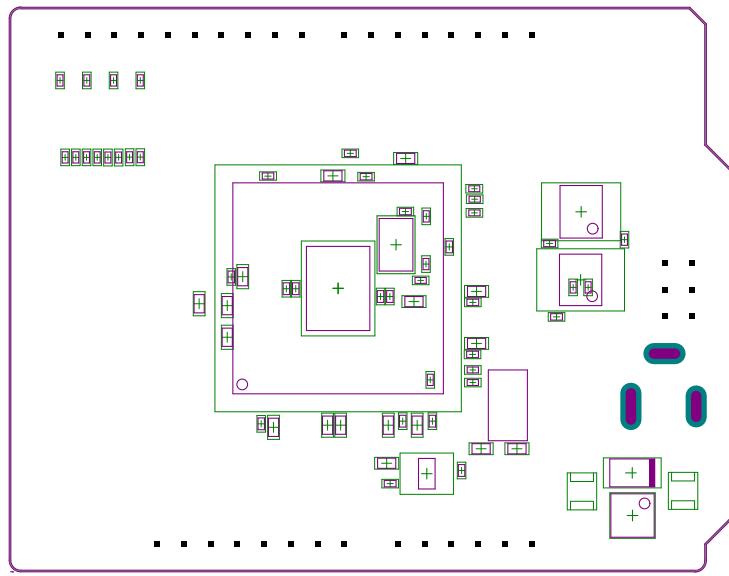


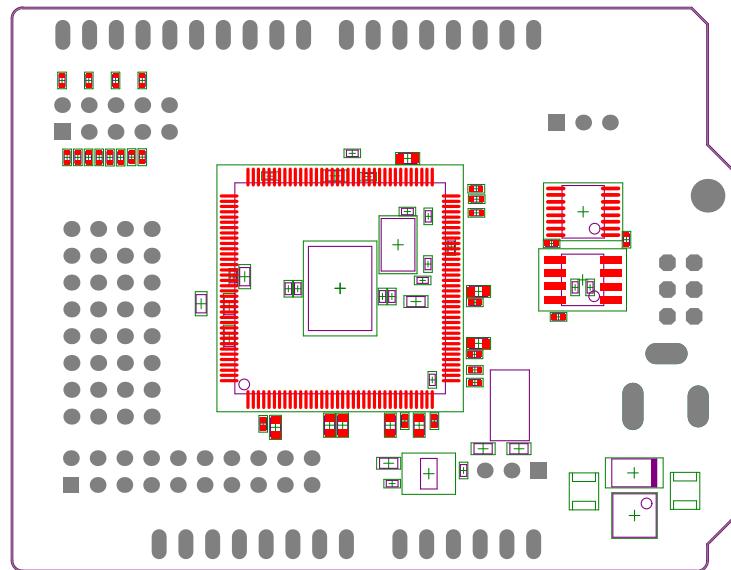


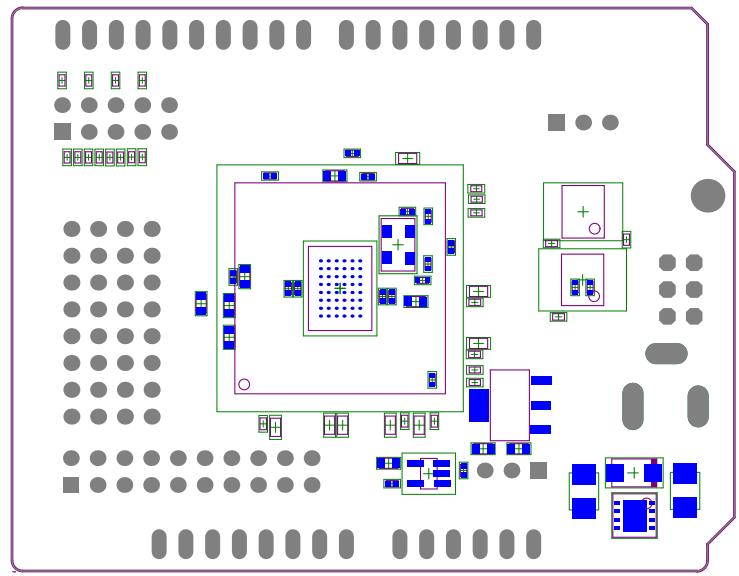


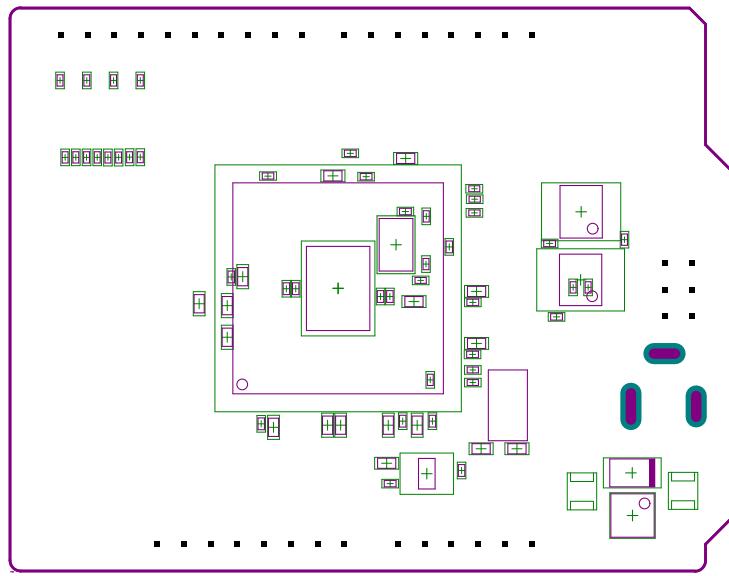




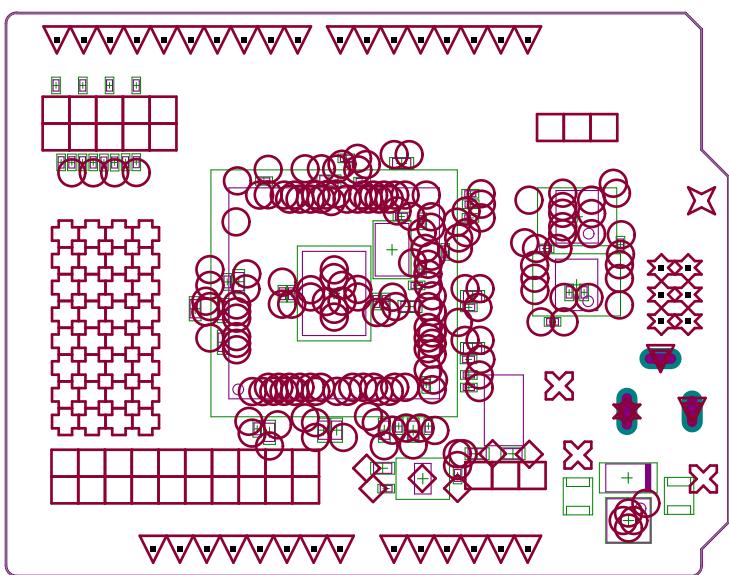
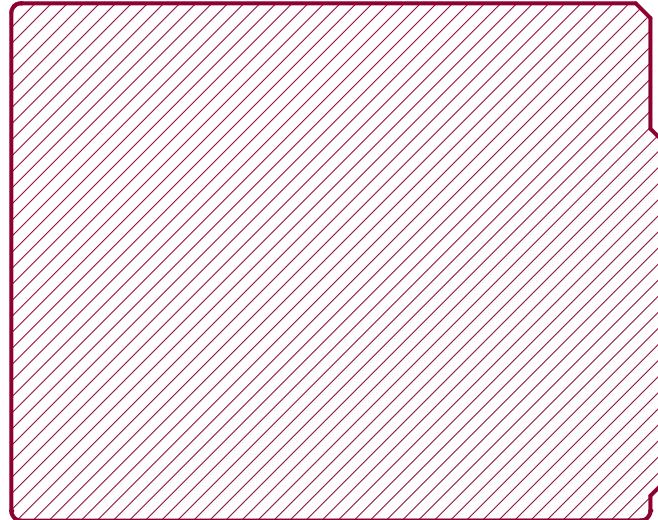








Layer	Name	Material	Thickness	Constant	Board Layer Stack
1	Top Paste				
2	Top Overlay				
3	Top Solder	Solder Resist	0.40mil	3.5	
4	Top	Copper	1.38mil		
5	Dielectric 1	FR-4	59.05mil	4.8	
6	PWR	Copper	1.42mil		
7	Dielectric 4		5.00mil	4.2	
8	GND	Copper	1.42mil		
9	Dielectric 2		10.00mil	4.2	
10	Bottom	Copper	1.38mil		
11	Bottom Solder	Solder Resist	0.40mil	3.5	
12	Bottom Overlay				
13	Bottom Paste				



Symbol	Hit Count	Finished Hole Size	Plated	Hole Type	Physical Length	Rout Path Length
★	1	39.37mil (1.000mm)	PTH	Slot	137.80mil (3.500mm)	98.43mil (2.500mm)
▼	2	39.37mil (1.000mm)	PTH	Slot		118.11mil (3.000mm)
○	182	10.00mil (0.254mm)	PTH	Round		
◊	6	16.00mil (0.406mm)	PTH	Round		
✖	3	28.00mil (0.711mm)	PTH	Round		
▽	32	34.00mil (0.864mm)	PTH	Round		
□	36	35.43mil (0.900mm)	PTH	Round		
❖	32	35.50mil (0.902mm)	PTH	Round		
✿	6	38.00mil (0.965mm)	PTH	Round		
✖	1	126.00mil (3.200mm)	PTH	Round		
301 Total						

