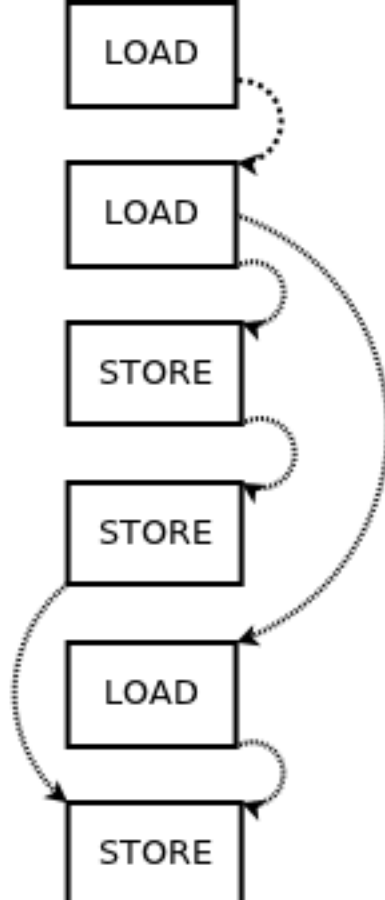
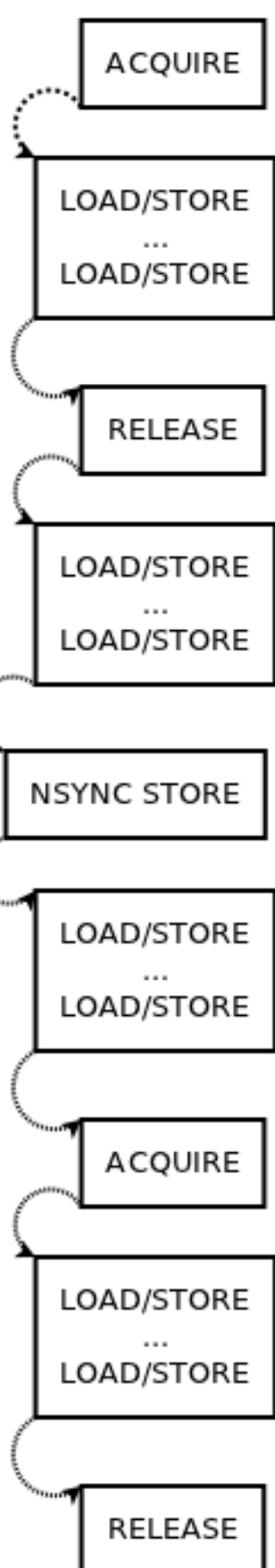
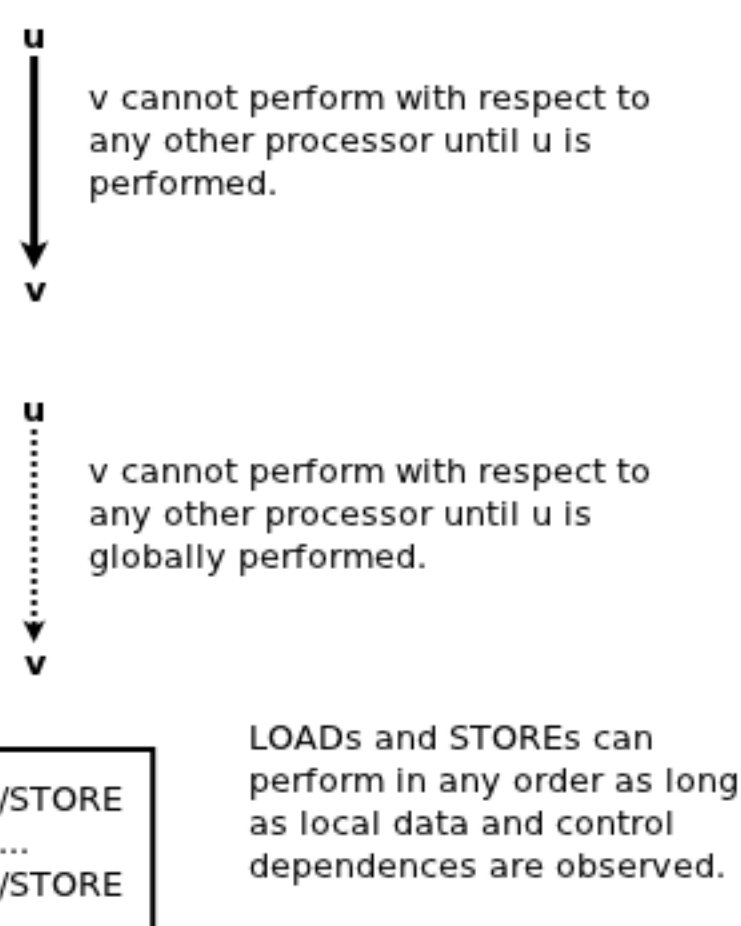


**Sequential Consistency**

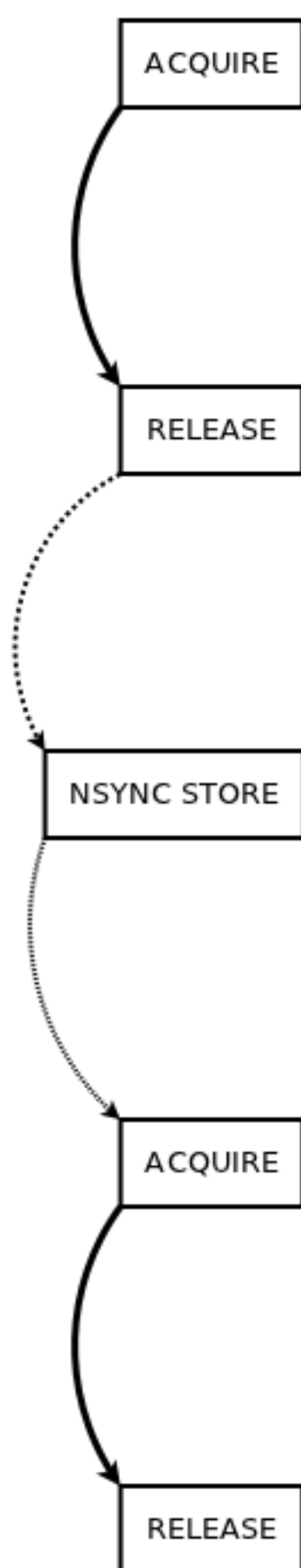


**Processor Consistency**



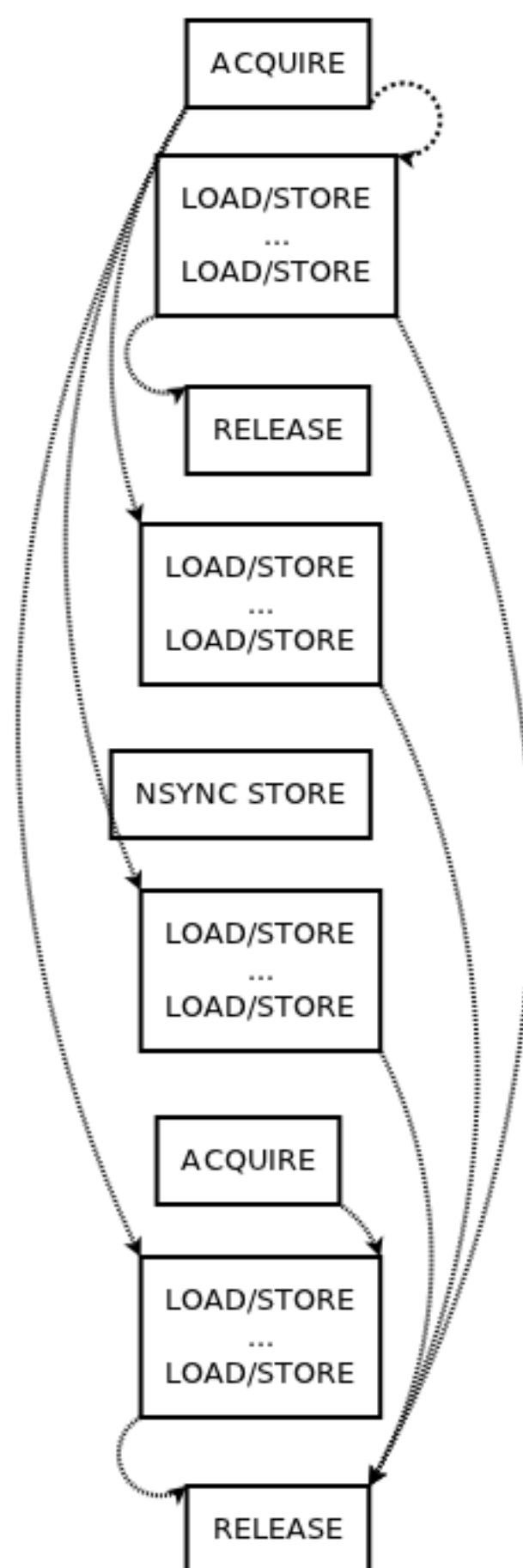
Ordering among non-competing (ordinary) and competing (sync) memory accesses.

Weak Consistency

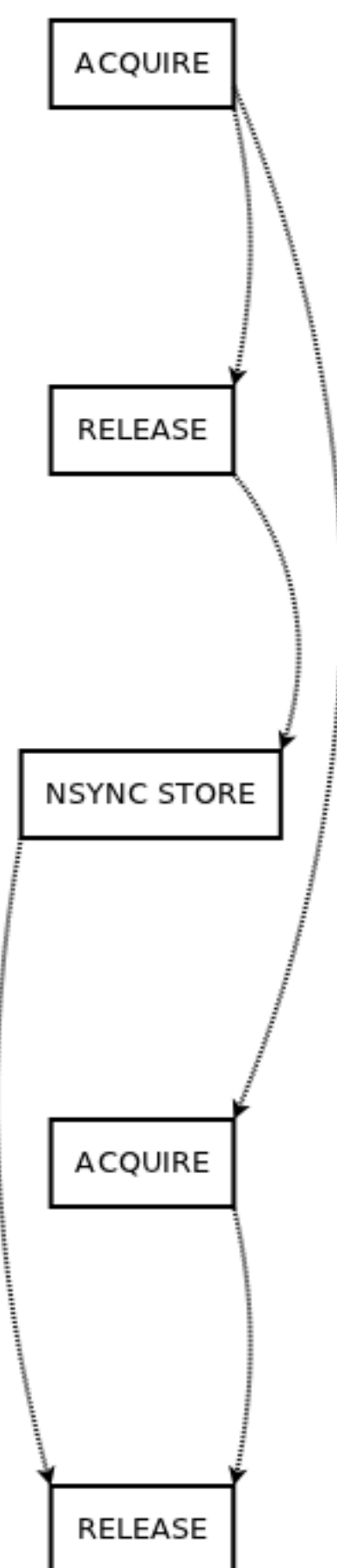


Ordering between competing (sync) memory accesses.

Release Consistency



Ordering among non-competing (ordinary) and competing (sync) memory accesses.



Ordering between competing (sync) memory accesses.