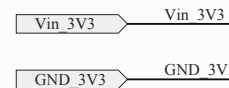
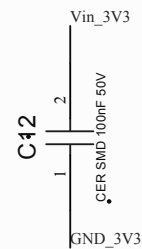

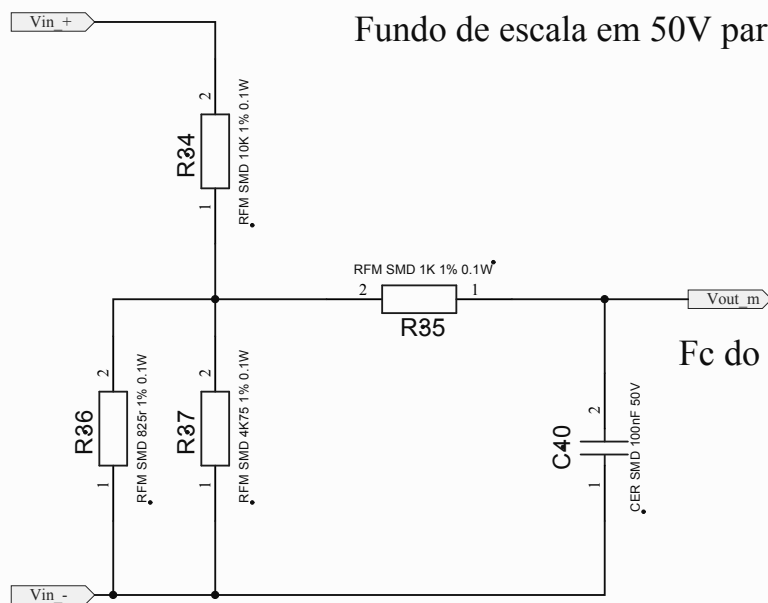


F_c do filtro em $F_s * 2 = 120\text{KHz}$




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File: C:\Users\Naelton\Documents\Naelton\UDESC 2015-1\Mestrado\Projeto_PSE_Updated\trunk\Schematic\Current measure\SchDoc				



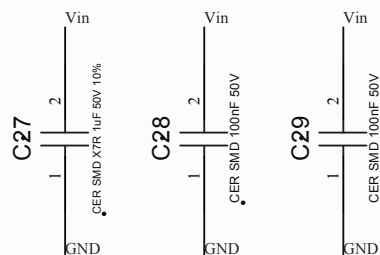
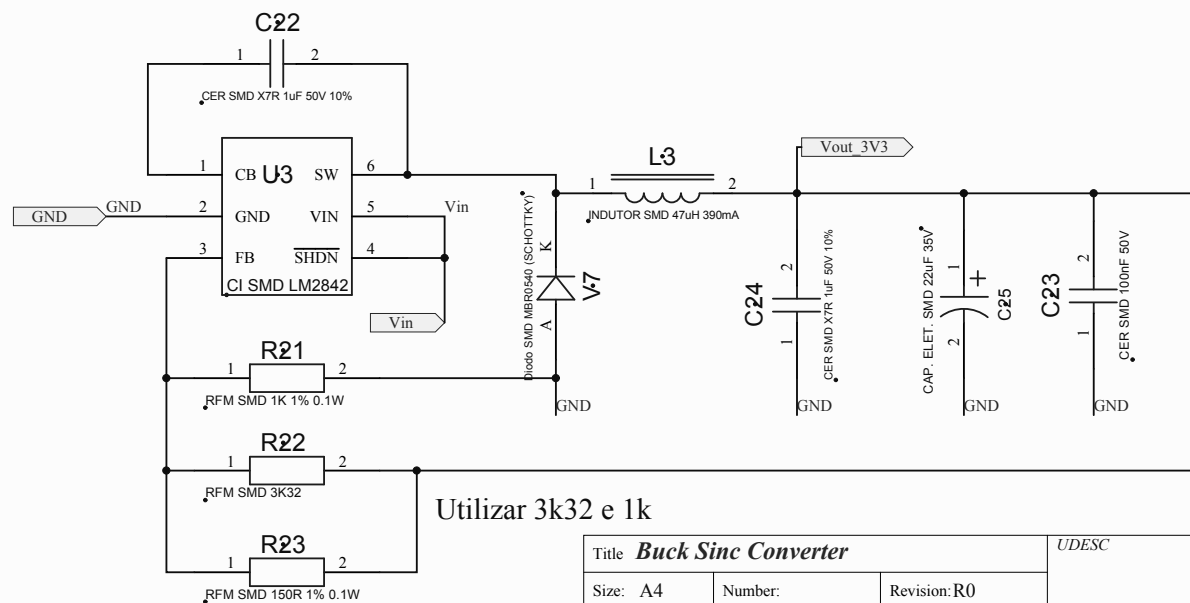
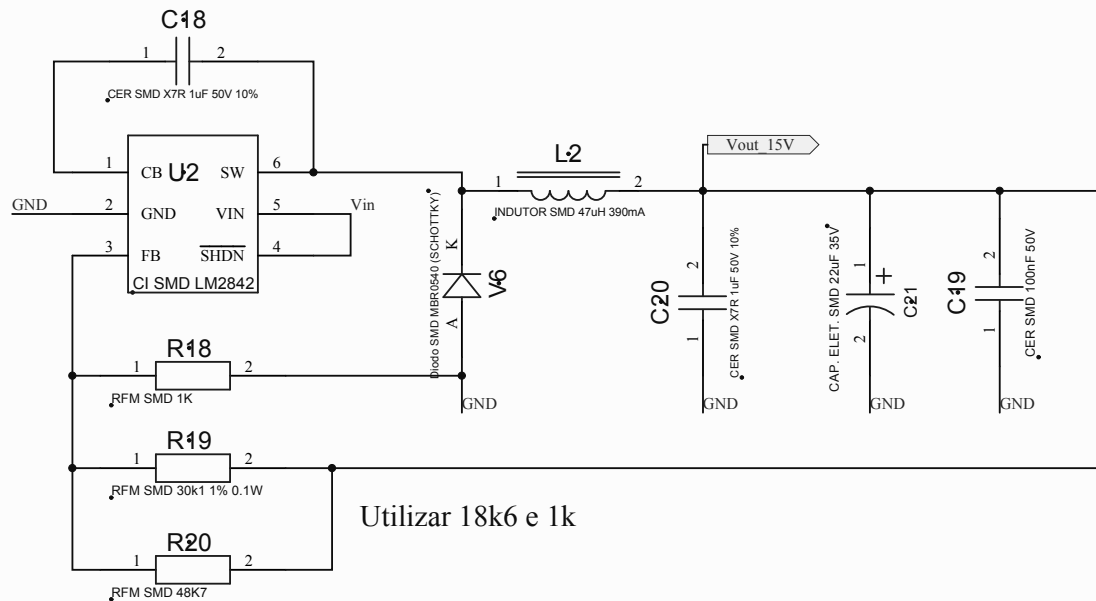
Fundo de escala em 50V para 3V3


F_c do filtro em $F_s/100 = 0.6\text{KHz}$

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File: C:\Users\Naelton\Documents\Naelton\UDESC 2015-1\Mestrado\Projeto_PSE_Updated\trunk\Schematic\Voltage measure.SchDoc				

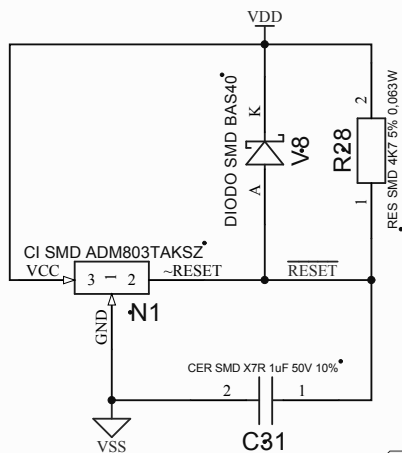
Avaliar de acordo com o posicionamento no layout se estes capacitores de filtro são necessários.

$$V_{OUT}=0,765V(1+(R1/R2))$$



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Colocar conexão para debugger conforme esquema da C2000.
Verificar o pino a ser utilizado para capturar a velocidade.

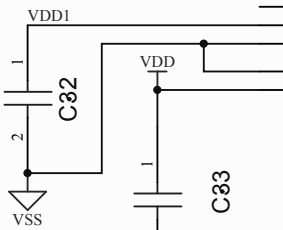


GPIO25 PODE SER UTILIZADO PARA FUNÇÃO?

FAN SPEED FAN_SPEED

LED_OUT ALTERADO PARA GPIO33

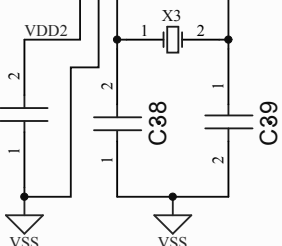
LED_OUT 36
LED BAT 26
LED PV 27
PWM1B 28
FAN PWR 29
PWM1A 30
TEST 31
GPIO32 32
VDD 33
VSS 34
VREGEN 35



LED_FAULT ALTERADO PARA GPIO2

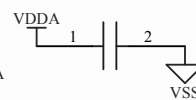
Sw PWM2B
Rect PWM3A

C37



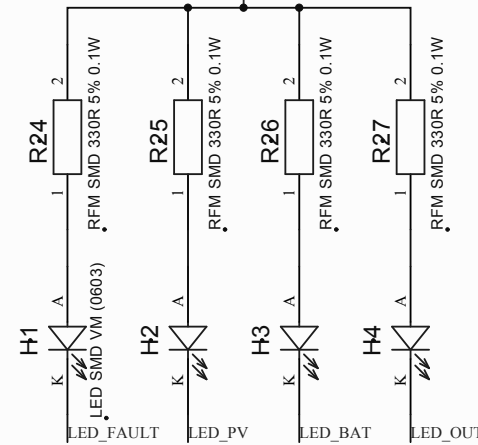
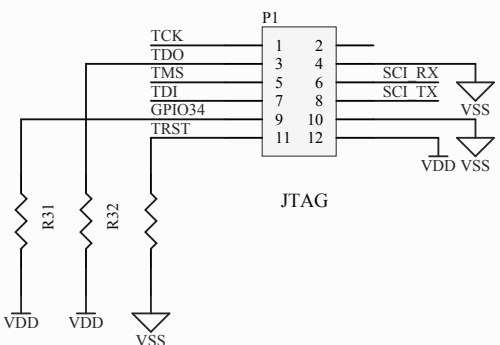
IC1
TMS320F28027_PT


C30



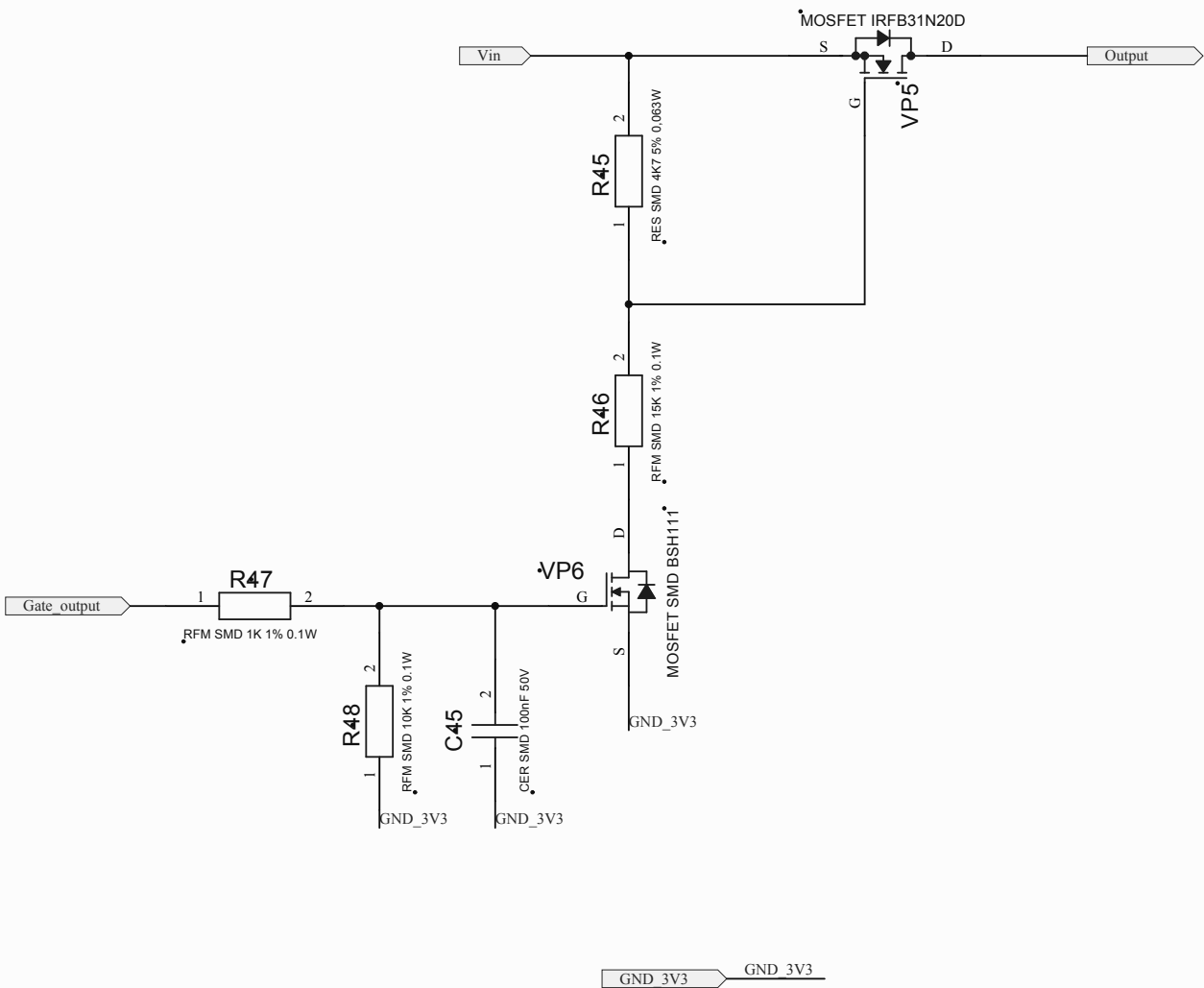
ALTERADO PINOS ADC Vbat >> Vpv
LEMBRAR DE ALTERAR NO CÓDIGO


TRST

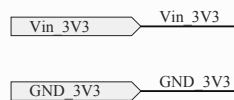
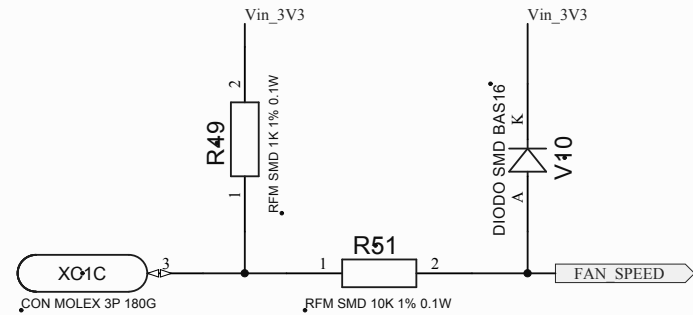
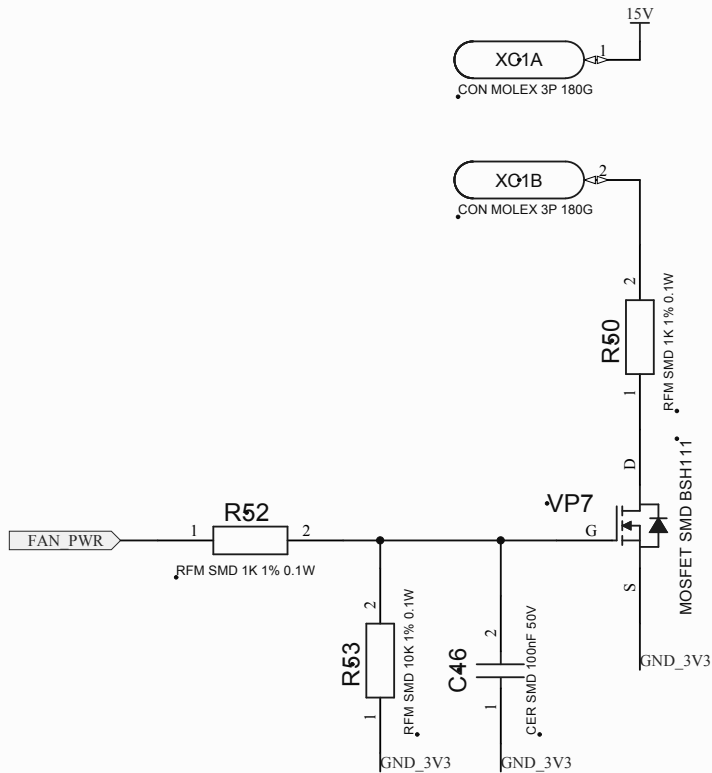



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Trocar por um Canal P, verificar se pinagem é a mesma.



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File: C:\Users\Naelton\Documents\Naelton\UDESC 2015-1\Mestrado\Projeto_PSE_Updated\trunk\Schematic\FAN.SchDoc				