
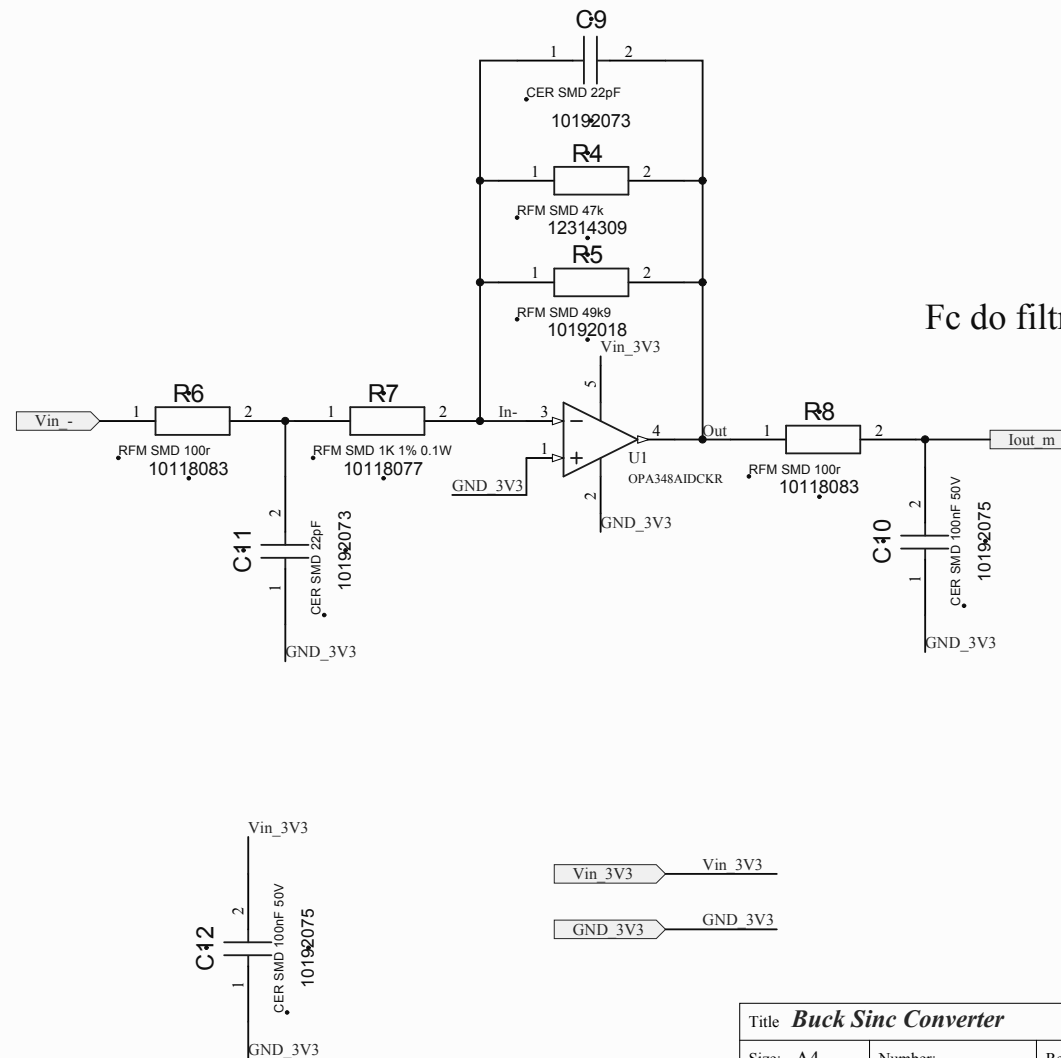

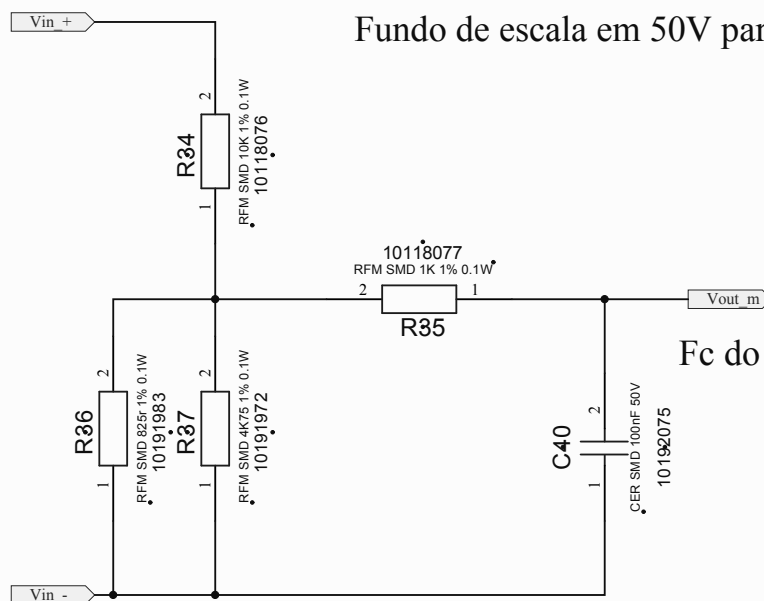


Title Buck Sinc Converter			UDESC	
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Date: 05/07/2015	Time: 18:42:33	Sheet 4 of 10		
File: C:\Users\Naelton\Documents\Naelton\UDESC 2015-1\Mestrado\Projeto_PSE_Updated\trunk\Schematic\Driver_rect.SchDoc				




F_c do filtro em $F_s * 2 = 120KHz$

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File: C:\Users\Naelton\Documents\Naelton\UDESC 2015-1\Mestrado\Projeto_PSE_Updated\trunk\Schematic\Current measure\SchDoc				



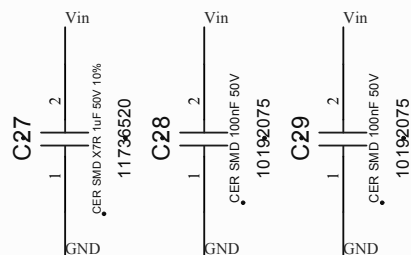
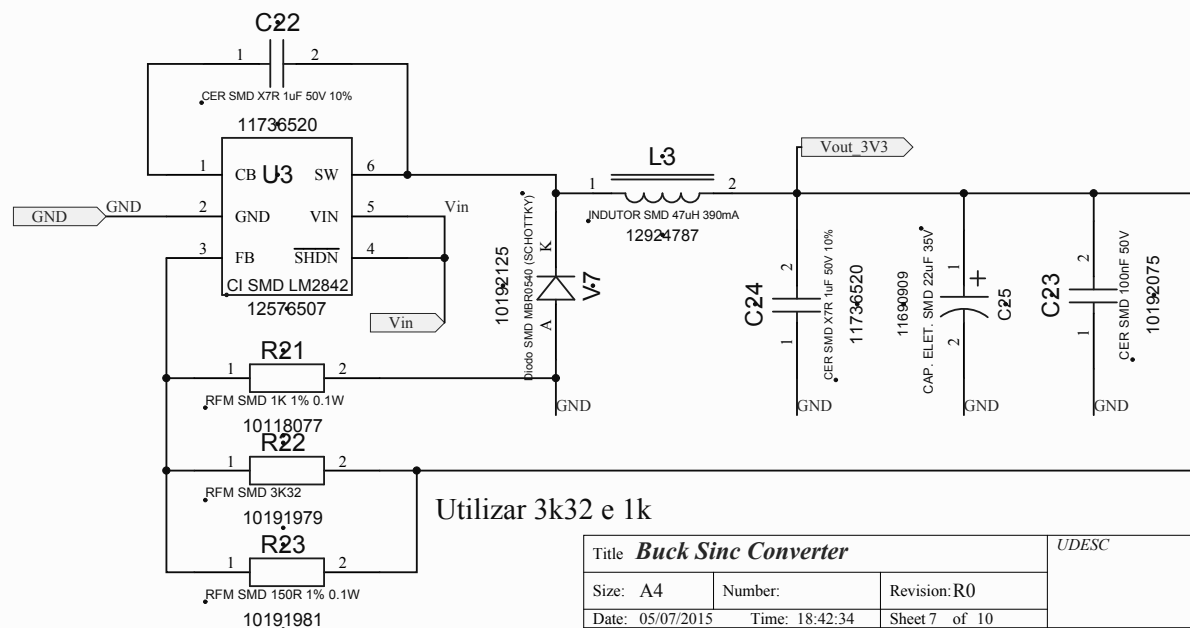
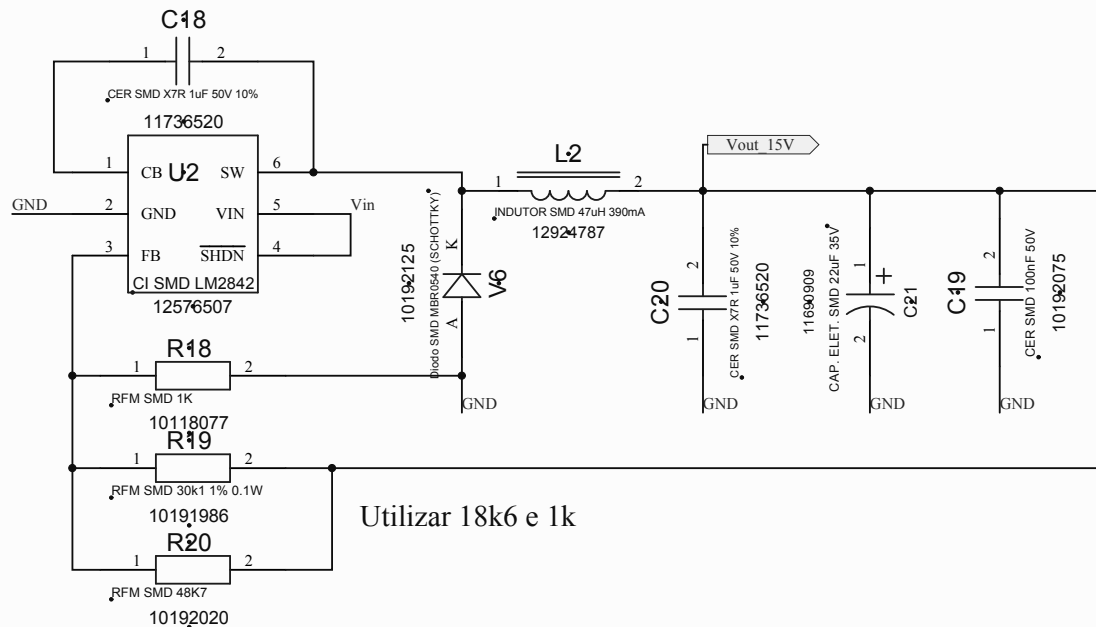
Fundo de escala em 50V para 3V3


F_c do filtro em $F_s/100 = 0.6KHz$

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File: C:\Users\Naelton\Documents\Naelton\UDESC 2015-1\Mestrado\Projeto_PSE_Updated\trunk\Schematic\Voltage_measur.SchDoc				

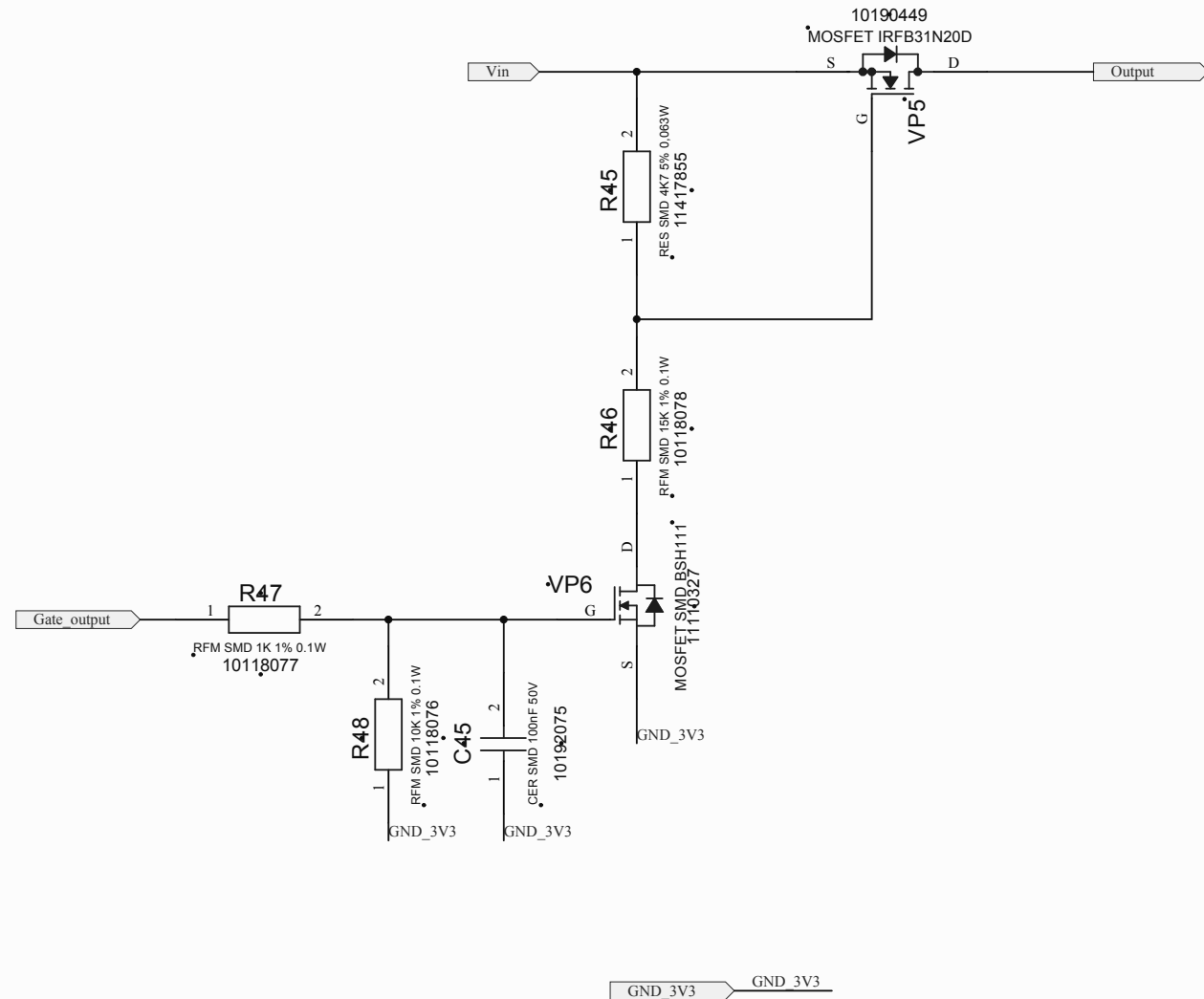
Avaliar de acordo com o posicionamento no layout se estes capacitores de filtro são necessários.


$$V_{OUT}=0,765V(1+(R1/R2))$$

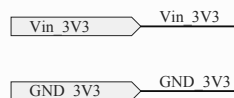
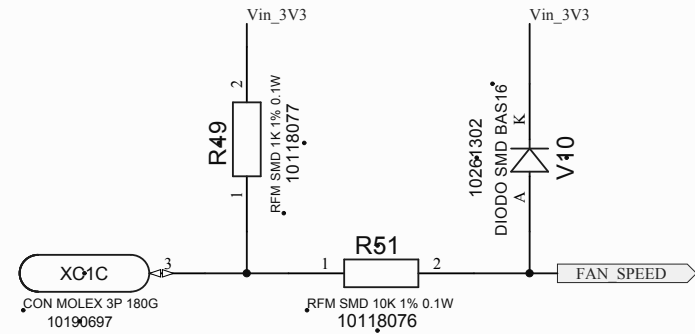
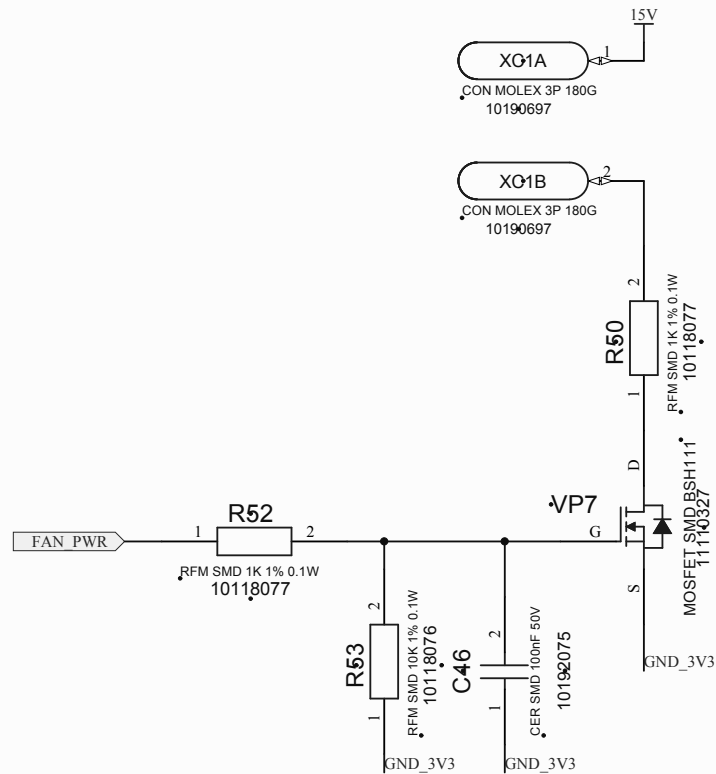



Title Buck Sinc Converter			UDESC	
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File: C:\Users\Naelton\Documents\Naelton\UDESC 2015-1\Mestrado\Projeto PSE Updated\trunk\Schematic\SMPS.SchDoc				

Trocar por um Canal P, verificar se pinagem é a mesma.



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File: C:\Users\Naelton\Documents\Naelton\UDESC 2015-1\Mestrado\Projeto_PSE_Updated\trunk\Schematic\FAN.SchDoc				