Master thesis

Automatic domain decomposition for HPC stencil codes in heterogeneous systems

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Abstract

Abstract

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1 Introduction 7

1 Introduction

1.1 Problem statement

The overarching problem addressed in this thesis is the decomposition of the computational domain for stencil based simulations. Domain decomposition is necessary to distribute and balance the computational load among distributed processing units. Additionally, including heterogeneous processing units adds further complications to the domain decomposition problem.

Solutions to the domain decomposition problem should be automatic, i.e. require a minimal amount of manual input. Automating domain decomposition helps separating concerns between domain scientists and computer scientists.

Therefore, the thesis focuses on the creation of an automatic domain decomposition library addressing these problems.

1.2 Background

Most scientific or engineering codes that model and simulate natural phenomena require more computational resources than a single processing unit can provide. Distributed systems, such as high performance clusters, are a common way to provide the necessary resources for complex simulations. However, porting such codes from their usually serial form to codes that run in parallel on distributed systems creates several challenges. These challenges are generally outside the scope of the domain specific developers.

Decomposing the computational domain and distributing it to the processing units is one of these challenges.

1.2.1 Distributed heterogeneous systems

Distributed systems have been used for decades in high performance computing (HPC) to compute simulations for various scientific fields. A more recent development is the introduction of accelerator devices e.g. graphics processing units (GPU) to the nodes in HPC clusters. These accelerator devices bring enormous computational power with their large number of cores for throughput-critical computations. But their architecture is different compared to CPUs for latency-critical applications.

Therefore, some HPC systems have a hybrid configuration of CPUs and GPUs to combine the advantages of both architectures. However, this also means that nodes in HPC clusters no longer have homogeneous processing units, which makes the distribution of the computational domain ever more complex. Mittal and Vetter [2015] provide a more detailed description of the evolution and motivation behind heterogeneous computing.

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1.2.2 Stencil codes

Domain decomposition is used in various type of simulations. This thesis focuses on domain decomposition for stencil codes.

Stencil codes are a family of codes commonly used in various scientific computing applications. The name "stencil codes" refers to computations using a fixed pattern of values from points on a mesh.

These fixed patterns often originate from explicit finite difference approximations on structured meshes. Structured meshes guarantee that every point in the mesh, except points on the boundary, has the same number of neighboring points. Explicit finite difference approximations use the values on the mesh at a given point in time to compute the values for the next small time step. Finite difference approximations are one common way to solve partial differential equations (PDEs). Numerous scientific fields - such as fluid dynamics or climate dynamics - model the natural phenomena of their studies as PDEs.

A single time step in a stencil code consists of iterating through every point on the mesh according to the fixed pattern, gathering values from other points, and using these values to compute the value for the next time step. Simulations of complex natural phenomena usually require very fine spatial and temporal resolutions as well as computations involving multiple different quantities on every grid point. Therefore, even theoretically simple stencil codes are in practice frequently more intricate.

Complex stencil codes usually require their large mesh to be decomposed into computational sub-domains for each processing unit to be solved in a reasonable amount of time. Therefore, domain decomposition is an important part of stencil codes.

1.2.3 Domain decomposition / graph partitioning

Domain decomposition can be seen as a specific form of load balancing for codes in which the load is roughly equal for each point in a domain. Stencil codes fulfill this characteristic.

The sub-domains that result from domain decomposition for stencil codes need to synchronize the values across their boundaries to be consistent. This synchronization requires communication between the different processing units.

The quality of a given domain decomposition is determined by the balance of computational load among all processors and the reduction of communication among different processors.

A common way to model and solve domain decomposition is through graph partitioning. When using this strategy, the elements of the discrete domain are represented by the vertices of a graph, with edges representing the connections between neighboring elements. The sum of all values on the edges between two sub-domains is called the edge cut. The edge cut in this model represents the total amount of communication between 1.3 Related works 9

the two sub-domains.

Therefore, graph partitioning algorithms that use a cost function to minimize such an edge cut, while maintaining an approximate uniformity of sub-domain size, provide domain decompositions that minimize the communication cost.

1.3 Related works

In literature, the process of creating sub-domains from a larger domain is referred to by different names: domain decomposition, static mapping, (graph) partitioning, topology mapping, or sometimes more general as a form of load balancing.

Domain decomposition is used to account for different computational load imbalances in various simulations. Common sources for such imbalances are irregular grids (e.g. finite element method), task-based applications, or heterogeneous communication costs. In Saxena et al. [2015] it is stated that heterogeneous architectures have not received as much attention as a source of imbalance. Nevertheless, heterogeneous architectures are a source of imbalance even for simulations that do not have any other source of imbalance.

However, even for different sources of imbalance, most often the problem is modeled as a graph, and a graph partitioning algorithm is used to solve for a domain decomposition.

In the general case, graph partitioning has been proven to be NP-complete (Feldmann and Foschini [2015]). However, relaxing the constraint to roughly equal instead of exactly equal partitions allows various different heuristics to generate a sub-optimal solution in reasonable time.

A general overview and comparison between different graph partitioning algorithms can be found in Karypis and Kumar [1998a]. Specifically, Table 9 illustrates different criteria for useful graph partitioning and how each algorithm ranks in them. Even though this paper is a few years old, both of the most common graph partitioning libraries, METIS ¹ and SCOTCH ², are based on the basic algorithms described in the paper.

While conceptually very similar the algorithms in these libraries differ in their mathematical description and their modeling approach.

1.3.1 SCOTCH library

The SCOTCH library models the problem as a set of two graphs: a valuated, undirected source graph S(V, E) representing the parallel processes (vertices) and communication channels (edges). And a not-valuated target graph T(V, E) representing the topology of the target machine. The target graph is not valuated because it assumes an homogeneous architecture.

To solve the graph partitioning on these two graphs, the SCOTCH library employs a dual recursive bipartitioning algorithm. This algorithm is formulated by Pellegrini [1994]

¹http://glaros.dtc.umn.edu/gkhome/views/metis

²https://gforge.inria.fr/projects/SCOTCH/

in the following way:

The goal of a mapping is then defined as: $\varphi: V(S) \to V(T)$ such that $\varphi(v_s) = v_t$ if v_s is mapped onto processor v_t .

And $\psi: E(S) \to E(T)$ such that $\psi(e_S) = \{e_T^1, e_T^2, \dots, e_T^n\}$ with $|\psi(e_S)|$ as the number of edges in the target graph that are used.

With these definitions, the cost function to be minimized is defined as the edge cut function f_c :

$$f_c(\varphi, \psi) = \sum_{e_s \in \text{cut}(E(S))} (c(e_S) \cdot |\psi(e_S)|)$$
(1)

where $c(e_S)$ is the value of the edge (i.e. the communication cost) and cut (E(S)) contains all edges belonging to the two subsets of the bipartition for which the cost function is computed.

1.3.2 METIS library

The older METIS library was developed on the basis of a multilevel bipartitioning algorithm. In their model, a single valuated, undirected source graph is partitioned, assuming that the target architecture has uniform cost for any communication between two processes. However, the graph partitioning algorithm in the METIS library has been expanded, as explained in Karypis and Kumar [1998b], to allow multiple constraints to be balanced.

1.4 Source graph generation for stencil codes

For graph partitioning to be used for domain decomposition, a source graph representing the domain is required.

Simple one and two dimensional stencils are often either visualized as points connected with lines or written in matrix form. For example the well known stencil used

in the computation of the Laplace equation can be expressed as $\begin{bmatrix} 1 \\ 1 & -4 & 1 \end{bmatrix}$, which is equivalent to writing the formula as $u_{i+1,j} + u_{i-1,j} + u_{i,j+1} + u_{i,j-1} - 4u_{i,j}$.

The matrix form also illustrates the communication pattern needed by the stencil. The position in the middle of the matrix represents the grid point itself. The other non-zero entries in the matrix represent the other grid points needed in the computation.

The computational cost can be approximated by the number of non-zero entries in the matrix e.g. the Laplace stencil requires 4 additions and one multiplication with a scalar constant.

A source graph to partition a given domain could be built by creating a vertex in the source graph for every grid point in the domain. In such a source graph, each vertex would have edges based on the communication patterns and a vertex weight based 1.5 Context 11

computational cost approximations of a stencil as previously described. However, even for simple stencils, like the one used in the Laplace equation, it becomes impractical to partition such a built source graph for meshes with several millions of elements due to the size of such a source graph.

Thus we will investigate models and methods to achieve the generation of a practical source graph to partition the domain.

1.5 Context

Domain decomposition is not the only concern that separates scientific models from well performing HPC code.

Complex scientific codes, like numerical weather prediction and climate simulation models, have grown over years to multiple hundred thousands lines of code written by a large community of scientific developers. Maintaining such large codes for the everchanging HPC environment is challenging.

The GridTools library provides a domain specific language (DSL) designed to reduce this effort for stencil codes. The main method of the GridTools DSL is to separate the concerns of the scientific model developers (domain experts) from the concerns of the computer scientists (HPC experts). Separation is accomplished by letting the domain experts use the DSL in their model development process without the need to think about performance. At the same time, computer scientist no longer need to understand the complete details of the model but instead can focus on optimizing the core computations used in stencil codes for different programming models and architectures as separate back-end of the GridTools DSL.

The GridTools DSL is written in C++, while domain scientist are often more familiar with higher level languages or simple Fortran as a language close to the mathematical formulations of their models. An additional DSL for Python (GT4Py) aims to overcome this programming language barrier for domain scientist. GT4Py adds an additional abstraction layer on top of the GridTools DSL so that domain scientists can use the simple and expressive Python syntax for model development without losing the advantages of the low-level optimizations of the GridTools back-end.

Within this context, the focus of this thesis is on the automation of the domain decomposition. The resulting automatic domain decomposition library will be part of the larger effort to separate HPC concerns from scientific modeling concerns.

1.6 Implementation and libraries

The focus of this thesis is the implementation of an automatic domain decomposition library to solve the problems described in the previous sections. The next few sections will describe some implementation aspects and existing libraries that will be used.

1.6.1 Implementation details

The implementation language will be Python. Python provides simple and expressive syntax and is increasingly used in the scientific community. Also, in the context of GT4Py, Python is the natural choice for the automatic domain decomposition library.

Standard practices for software development and testing will be employed. This includes unit tests, continuous integration (CI), version control system, code documentation, and adhering to standard style guide conventions.

1.6.2 Communication library

The communication between processors is a significant part of computations involving domain decomposition. Using a high quality communication library is therefore important. Message Passing Interface (MPI) is the leading standard for distributed systems. The communication model of MPI matches very well with finite difference computations and has been used in various finite difference implementations.

In the context of Python, this thesis will make use of the MPI for Python (MPI4Py) library, which provides bindings for MPI in Python. MPI4Py is open source, and as described in Dalcín et al. [2005] supports the most important MPI communications, namely point-to-point, as well as collective communications of general Python objects.

Furthermore MPI4Py supports one-sided MPI. One-sided MPI is the remote memory access (RMA) communication model of MPI. One-sided MPI allows processes to expose part of their memory to other processes, in order for them to directly write or read from this memory. In contrast to the two-sided communication model, this allows the decoupling of data movement and process synchronization.

Since one-sided communication models are a new trend in HPC, this thesis will explore the use of one-sided MPI in the context of domain decomposition and stencil computations.

1.6.3 Graph partitioning library

Solving the graph partitioning problem to determine a domain decomposition is a fundamental part of the proposed work. However, the focus of this thesis is automating the process and modeling heterogeneous systems. Therefore the automatic domain decomposition library will use another library to solve the underlying graph partitioning problem.

The reason for building on top of existing libraries is two-fold. Firstly, it saves time from implementing methods to solve the underlying basic graph partitioning problem. Secondly, building on top of popular libraries can make it easier in the future to keep up to date with the latest graph partitioning methods.

As mentioned in the state of the art review section 1.3 the two most popular graph partitioning libraries at the moment are METIS and SCOTCH.

1.7 Structure of the thesis

2 Domain decomposition

Domain decomposition in the context of this thesis refers to the process of dividing a computational domain into smaller parts in order to run on multiple processing units. Importantly, domain decomposition in this context is not be confused with the *domain decomposition method* in mathematics and numerical analysis. The *domain decomposition method* is used to solve boundary value problems by splitting them into smaller parts. Instead, domain decomposition in high performance computing and this thesis is the name of the process used to prepare a domain for distribution to multiple processing units. Specifically, domain decomposition in this thesis is used for solving partial differential equations using finite difference methods on multiple processing units.

The next sections outline the general approach to domain decomposition, the specifics of the model developed and used in this thesis, an overview of the graph partitioning method, and an example for the subdivision model and graph partitioning for domain decomposition.

2.1 General approach

Some form of domain decomposition is necessary for any distributed computation. However, the process of decomposing a domain can vary widely depending on the problem that is distributed i.e. the domain.

Many domain decomposition approaches have some overlap with load balancing approaches. For example, many approaches for both work by over-decomposing the domain into more parts than processing units. This over-decomposing allows in a second step to distribute the smaller parts to the processing units in a smart way to minimize a given set of cost factors.

For domain decomposition the two main cost factors are distributing the same number of grid points to each processing i.e. **computational cost** and minimizing the amount of communication between different processing units i.e. **communication cost**.

Given these smaller domain parts and their corresponding cost factors a graph partitioning algorithm can be applied to get a balanced distribution for each processing unit.

This two step process describes the general approach to domain decomposition and is described in more detail as pseudo-code in algorithm 1.

To note in algorithm 1 is that the over-decomposition method introduces one new parameter: the number of subdivisions per dimension. This parameter is further explained in section 2.2.

The flowchart in figure 1 gives a more abstract overview of the whole automatic domain decomposition process.

```
Input: Computational Domain, Number of Processing Units, Stencil Information
   Output: Decomposed Domain, Partitioning for each Processing Unit
 1 Datastructure DomainSubdivision
      Stores (id, size, boundaries, gridpoints, neighbors)
 3 Function DomainDecomposition(Domain, NumProcs, StencilExtent)
      /* Step 1: Over-decompose domain and store subdivisions in
          adjacency list.
                                                                                 */
      SubdivisionWeightedAdjacencyList, ListOfDomainSubdivisions \leftarrow
 5
       SubdivideDomain(Domain(SizePerDim, PeriodicityPerDim),
       NumSubdivPerDim, StencilExtent)
      /* Step 2: Partition subdivisions based on adjacency list.
 6
      PartitionList \leftarrow GraphPartitioning(SubdivisionWeightedAdjacencyList,
       NumProcs)
8 Function SubdivideDomain(SizePerDim, PeriodicityPerDim, NumSubdivPerDim,
    StencilExtent)
      /* Calculate derived information:
9
      TotalNumberOfSubdivisions \leftarrow NumSubdivPerDim
10
      SizeOfSubdivision \leftarrow Domain, NumSubdivPerDim
11
      SizeOfBoundaries \leftarrow SizeOfSubdivision, StencilExtent
12
13
      /* Create adjacency list:
                                                                                 */
      foreach Subdivision in TotalNumberOfSubdivisions do
14
         foreach Direction do
15
16
             Neighbors \leftarrow
              HandleDomainBoundaries(NumSubdivPerDim, PeriodicityPerDim)
         \mathbf{end}
17
         AppendToWeightedAdjacencyList \leftarrow Neighbors, SizeOfSubdivision,
18
          SizeOfBoundaries
         StoreSubdivisionInformation(id, size, boundaries, gridpoints,
19
           neighbors)
20
      end
      return SubdivisionWeightedAdjacencyList, ListOfDomainSubdivisions
\mathbf{21}
22 Function GraphPartitioning(SubdivisionWeightedAdjacencyList, NumProcs)
      if FileOutputIsEnabled then
23
         WriteAdjacancyListToFile(SubdivisionWeightedAdjacencyList,
\mathbf{24}
           FormatFlag)
25
      CallGraphPartitioningLibrary(SubdivisionWeightedAdjacencyList, NumProcs)
      return Partitioning
27
```

Algorithm 1: Pseudo-code description of the two-step process to decompose a domain.

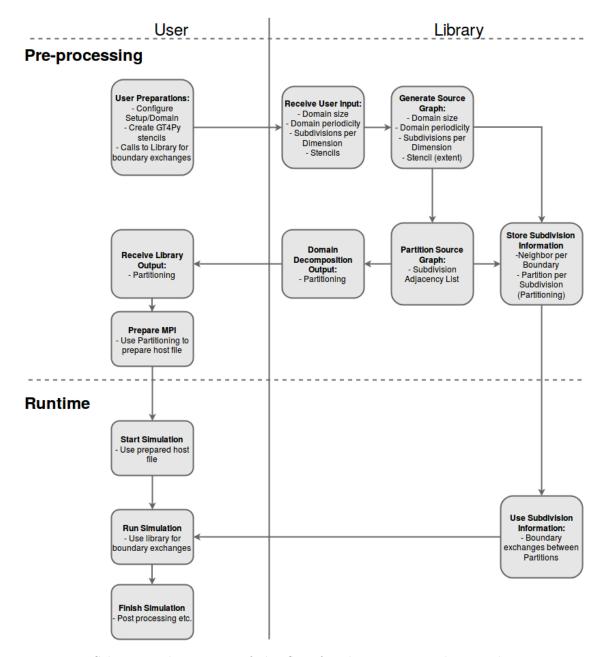


Figure 1: Schematic description of the flow for the automatic domain decomposition process.

2.2 Subdivision model for source graph generation

As mentioned before source graphs where every source graph node represents a single physical domain grid point would be impractical for large domains. Therefore, any alternative has to combine some of these domain grid points into separate divisions even before the domain decomposition occurs.

A model to simplify the source graph generation should

The simplest method of subdividing a regular grid is by splitting it across each dimension separately, i.e. creating a number of uniform subdivisions.

This guarantees that every subdivision has the same amount of neighbors, with the exception of sections at the boundary of the physical domain if the boundary is not periodic. Also every subdivision has the same size and therefore distributing them equally during the graph partitioning also means distributing the grid points equally.

For this kind of subdivisions it is simple to define the interior and boundary between subdivisions, as visualized for a two-dimensional and three-dimensional example in fig. 2 and fig. 3 respectively. This distinction is important for the calculations of the communication and computational cost as described in sections 2.2.1 and 2.2.2.

One limit to the minimal size of a subdivision in any direction is given by the corresponding stencil extend. Meaning a stencil can not extend into multiple subdivisions, but only the direct neighbor, otherwise subdivision would have multiple neighbors in the same direction. This should not be a problem, since usually stencils are not as large as any reasonable sized subdivision.

2.2.1 Communication cost

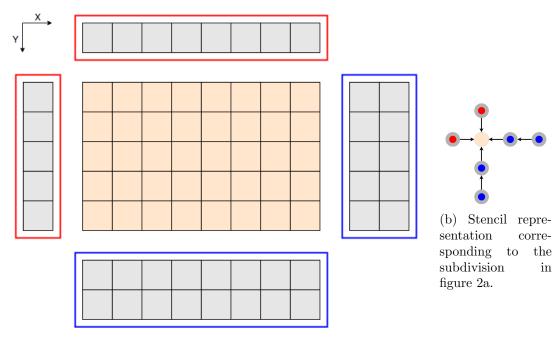
The communication cost between subdivisions is the main criteria being minimized in the graph partitioning algorithm. Splitting the domain into uniform subdivisions keeps the communication cost computation simple.

The communication cost for each neighbor of the subdivision is determined by the size of the boundary between the two and the extent of the stencil in the corresponding direction.

For three dimensional domains the communication cost can therefore be computed from the following formula:

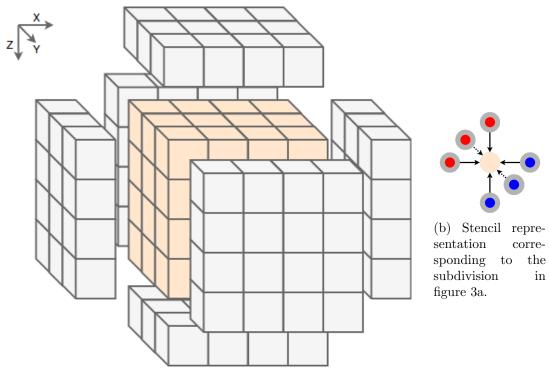
$$cc(i) = sds\left(\left(\left\lfloor \frac{i}{2} \right\rfloor - 1\right)\%3\right) \cdot sds\left(\left(\left\lfloor \frac{i}{2} \right\rfloor + 1\right)\%3\right) \cdot se(i)$$
 (2)

where cc(i) is the communication cost for side i. The index $i \in [0, 1, 2, 3, 4, 5]$ is combination of side and dimension, i.e. 0 is negative x direction, 1 is positive x-direction, 2 is negative y-direction and so on. sds(j) is the subdivision size of dimension $j \in [0, 1, 2]$. Lastly se(i) is the stencil extend of side and dimension i. The division, rounding and



(a) In the middle and in light red the grid points belonging to this subdivision. Outside in gray the four halo regions. The red and blue lines surrounding the halo region indicates negative and positive directions respectively.

Figure 2: Schematic view of a single two-dimensional domain subdivision.



(a) In the middle and in light red the grid points belonging to this subdivision. Outside in gray the six halo regions.

Figure 3: Schematic view of a single three-dimensional domain subdivision.

modulo in the formula is simply a short version of selecting the index of the other two dimensions for the computation of the area between two subdivisions.

2.2.2 Computational cost

The computational cost, corresponding also to the vertex weight in the source graph, is even easier to compute, since it is proportional to the number of grid points inside the subdivision.

2.3 Load balancing - graph partitioning method

Figure 4 shows three possible partitions for an example source graph. For visual simplicity a two-dimensional domain is used, however a three-dimensional domain would work in the same way. The source graph corresponds to a domain of size 30×40 with three subdivisions in x-direction and two subdivisions in y-direction. In this example these six subdivisions are partitioned to two partitions, represented by the color of the circle in the figures 4a, 4b, and 4c.

Figure 4a shows a case where a graph partitioning cut causes a computational imbalance between the partitions. Partitioning one subdivision more to one partition in such a small example causes a very large imbalance. Usually the graph partitioning method would not allow a result with this much imbalance. However, in larger graphs some computational imbalance up to a threshold is very common. Most graph partitioning methods allow manipulating the imbalance threshold.

The mixed direction cut shown in fig. 4b has the largest communication cost of the three partitions shown here. However, it is a fully valid partition and partition boundaries that look similar are common in partitioning of larger graphs. See the boundary between the brown and red partition in fig. 5a for example. This should highlight the importance of having defined clear and simple defined boundaries in the subdivision model, since the graph partitioning model already adds complexity to the partition boundaries.

Finally, fig. 4c shows the ideal case for this small example. Ideal cuts like this are only likely in such small and simple cases.

2.4 Example subdivision and domain decomposition

The following example should highlight some aspects of the discussed subdivision and domain decomposition.

All the following figures in this section use the same domain and stencil. The domain has size $2048 \times 1024 \times 40$ and the stencil extends are given as [1,1,1,1,0,0]. This means the stencil uses 1 neighboring grid point in negative and positive x- and y-direction. The boundary of the domain is also periodic in x-direction and non-periodic in y- and z-direction.

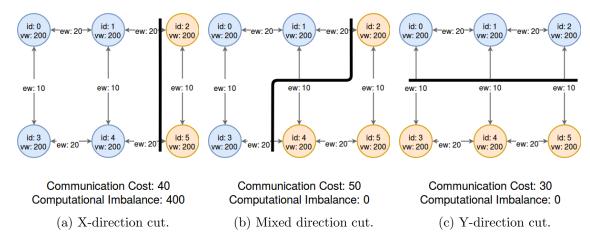


Figure 4: Visual representation for a small graph partitioning example. Each circle represents one subdivision of size 10×20 . The label "id" is the subdivision identification number. The label "vw" describes the vertex weight and is equal to the size of the subdivision. The label "ew" describes the edge weight and is equal to the size of the boundary between the corresponding subdivisions. The color of the circle depicts the corresponding partition. The thick, black line shows the boundary between the two partitions i.e. the cut chosen by the graph partitioning.

The figures 5a, 5c, and 5e show the results if the domain is split into $16 \times 8 \times 1$ subdivisions. This means every box in the figures represents $128 \times 128 \times 40$ grid points.

For comparison the figures 5b, 5d, and 5f show the results if the domain is split into $32 \times 16 \times 1$ subdivisions. This means every box in the figures represents $32 \times 32 \times 40$ grid points and 4 of these boxes correspond to one box in the figures on the left side.

This comparison should highlight the difference the number of subdivisions and their size can make for the overall domain decomposition. Also the difference the graph partitioning can make is shown in fig. 5.

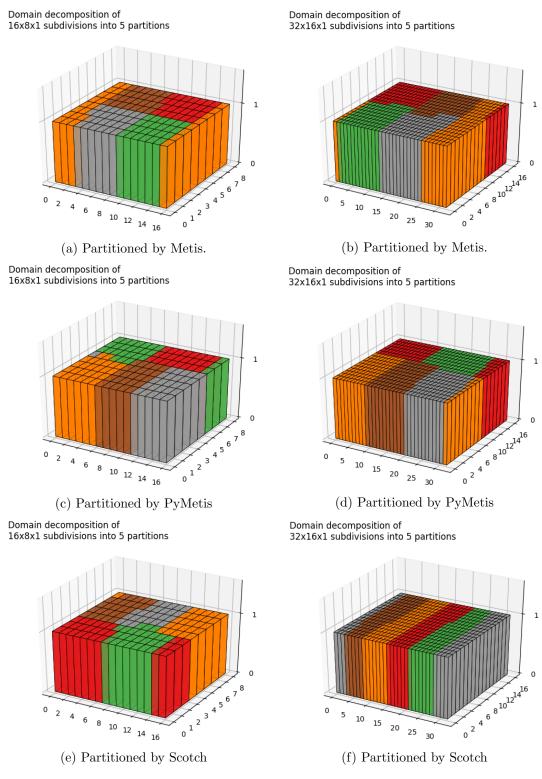


Figure 5: Domain subdivision and domain decomposition into 5 partitions. Each box represents one subdivision of grid points. Each color represents one partition. The domain has the size $2048 \times 1024 \times 40$ and a periodic boundary in x-direction. Even though, the domain is fully three dimensional the domain is only decomposed in the x-y plane i.e. the horizontal plane. Such a decomposition represents an often encountered use case for domains where the size of the horizontal dimensions are much larger than the size of the vertical dimension e.g. numerical weather prediction models. Not decomposing in the vertical dimension is important for locality and performance since the vertical dimension is usually the innermost loop dimension. This also fits with many parameterizations that use the single column model assumption.

3 Library design and implementation

3.1 Overview

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4 Case study

To show the functionality and applicability of the automatic domain decomposition library a few case studies were carried out. This chapter and the following sections will outline each case study and the corresponding experimental results.

The base implementation of each test case was provided by Stefano Ubbiali written in Python and using GT4Py stencils.

4.1 Case 1: Burger's equation

Burger's equation is a well-known nonlinear partial differential equation. Burger's equation can be used to model various physical phenomena. Most commonly it is used as a model for traffic flow or shock waves in a fluid. Additionally it is widely used to test numerical schemes since it has analytical solutions for a set of initial conditions.

4.1.1 Case description

The two-dimensional, viscid Burger's equation is given by the following system of equations as described in Zhao et al. [2011]:

$$\frac{\partial u}{\partial t} + u \frac{\partial u}{\partial x} + v \frac{\partial u}{\partial y} = \varepsilon \left(\frac{\partial^2 u}{\partial x^2} + \frac{\partial^2 u}{\partial y^2} \right)
\frac{\partial v}{\partial t} + u \frac{\partial v}{\partial x} + v \frac{\partial v}{\partial y} = \varepsilon \left(\frac{\partial^2 v}{\partial x^2} + \frac{\partial^2 v}{\partial y^2} \right)
\text{with } (x, y, t) \in D \times (0, T]$$
(3)

These two equations characterize the Burger equation as set of equations for the velocity in x and y direction. Both equations describe two parts. On the left hand side of the equation the advection of the velocity itself. On the right hand side of the equation the diffusion caused by viscosity.

To complete the full description of the Burger equation the initial and boundary conditions are generally given by the following equations:

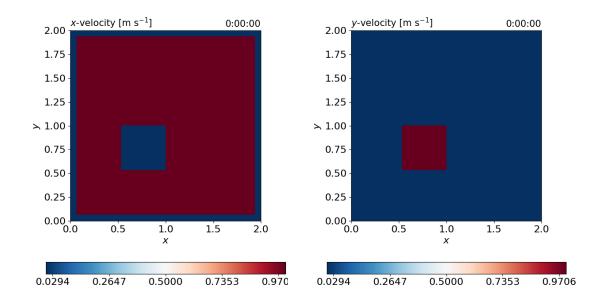
Initial conditions:

$$u(x, y, 0) = u_0(x, y), (x, y) \in D$$

$$v(x, y, 0) = v_0(x, y), (x, y) \in D$$
Boundary conditions:
$$u(x, y, t) = f(x, y, t), (x, y, t) \in \partial D \times (0, T]$$

$$v(x, y, t) = g(x, y, t), (x, y, t) \in \partial D \times (0, T]$$

$$(4)$$



- (a) X-velocity initial condition.
- (b) Y-Velocity initial condition.

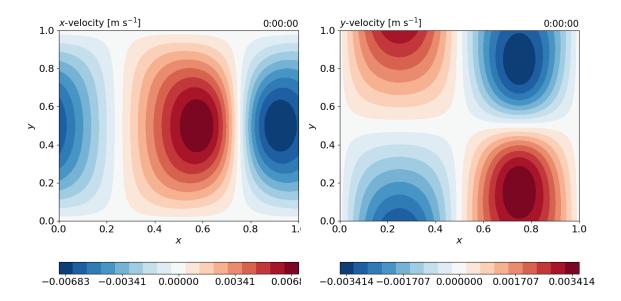
Figure 6: Initial condition for the Shankar test case.

Shankar conditions: The first set of initial and boundary conditions are the ones used by Shankar. 3

The following equations describe the Shankar test case fully and fig. 6a and fig. 6b visualizes the initial condition.

Boundary conditions: Initial conditions: $f(x,y,t)=0 \qquad u_0\left(x,y\right) = \begin{cases} 0, \text{ in } \left[0.5,1.0\right]\times\left[0.5,1.0\right]\\ 1, \text{ otherwise} \end{cases}$ $g(x,y,t)=0 \qquad v_0\left(x,y\right) = \begin{cases} 1, \text{ in } \left[0.5,1.0\right]\times\left[0.5,1.0\right]\\ 0, \text{ otherwise} \end{cases}$ Other parameters: Domain: $Viscosity: \ \varepsilon=0.01 \qquad D=\left[0,2\right]\times\left[0,2\right]$ T=0.6

³https://ch.mathworks.com/matlabcentral/fileexchange/38087-burgers-equation-in-1d-and-2d



- (a) X-velocity initial condition.
- (b) Y-Velocity initial condition.

Figure 7: Initial condition for the Zhao test case.

Zhao conditions: The second set of initial and boundary conditions are the same as used as example 1 in Zhao et al. [2011]:

Boundary conditions:

$$f(x,y,t) = \begin{cases} -2\varepsilon\pi \exp^{-5\pi^2\varepsilon t} \sin(\pi y), & \text{for } x = 0, y \in [0,1] \\ -2\varepsilon\pi \exp^{-5\pi^2\varepsilon t} \sin(\pi y), & \text{for } x = 1, y \in [0,1] \\ 0, & \text{for } x \in [0,1], y = 0 \\ 0, & \text{for } x \in [0,1], y = 1 \end{cases}$$

$$g(x,y,t) = \begin{cases} 0, & \text{for } x = 0, y \in [0,1] \\ 0, & \text{for } x = 1, y \in [0,1] \\ -\varepsilon\pi \exp^{-5\pi^2\varepsilon t} \sin(2\pi x), & \text{for } x \in [0,1], y = 0 \\ \varepsilon\pi \exp^{-5\pi^2\varepsilon t} \sin(2\pi x), & \text{for } x \in [0,1], y = 1 \end{cases}$$
Other parameters:

Other parameters:

Viscosity: $\varepsilon = 0.01$

Initial conditions:

$$\mathbf{u}_{0}(x,y) = \frac{-4\varepsilon\pi\cos(2\pi x)\sin(\pi y)}{2+\sin(2\pi x)\sin(\pi y)}$$

$$\mathbf{v}_{0}(x,y) = \frac{-2\varepsilon\pi\sin(2\pi x)\cos(\pi y)}{2+\sin(2\pi x)\sin(\pi y)}$$

Domain:

$$D = [0, 1] \times [0, 1]$$

$$T = 1$$
(6)

Notable about this set of initial and boundary condition is that they have exact solutions. The exact solutions as provided in Zhao et al. [2011] are:

$$u(x, y, t) = -2\varepsilon \frac{2\pi \exp^{-5\pi^2 \varepsilon t} \cos(2\pi x) \sin(\pi y)}{2 + \exp^{-5\pi^2 \varepsilon t} \sin(2\pi x) \sin(\pi y)}$$

$$v(x, y, t) = -2\varepsilon \frac{\pi \exp^{-5\pi^2 \varepsilon t} \sin(2\pi x) \cos(\pi y)}{2 + \exp^{-5\pi^2 \varepsilon t} \sin(2\pi x) \sin(\pi y)}$$
(7)

4.1.2 Implementation details

Stencil details: Both the Shankar and Zhao conditions can be solved using different numerical stencils for the computation. To show some variance and flexibility three stencils can be used to solve the test case.

The forward-backward stencil combines forward finite differences to compute the time derivatives with backward finite differences to compute the first-order space derivatives and a second-order centered scheme to compute the second-order derivatives. See fig. 8a for a visual representation of this stencil.

The other two stencil are called upwind schemes, since they use the values only from the direction the velocity is coming from. The first-order upwind scheme uses the directly neighboring grid points like the forward-backward stencil. The difference is that only the values from upwind are used in the actual computation for the next value. See fig. 8b for a visual representation of this stencil.

The third-order upwind stencil needs two neighboring grid points in each direction to increase the accuracy of the approximated space derivative. See fig. 8c for a visual representation of this stencil.

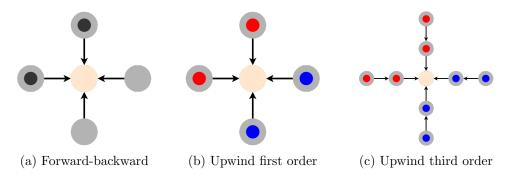


Figure 8: Schematic representation for three possible stencils for the Burger's equation. The large gray circle indicate the grid points necessary for the computation of the Laplacian for the diffusion part of the Burger's equation. The small dark gray circle indicate grid points necessary for the backward finite difference. The red and blue small circles indicate grid points needed for the upwind computations in the case of positive velocity or negative velocity respectively.

Domain decomposition details:

- 4.1.3 Experimental setup
- 4.1.4 Experimental results
- 4.2 Case 2: Shallow water equation
- 4.2.1 Case description
- 4.2.2 Implementation details
- 4.2.3 Experimental setup
- 4.2.4 Experimental results

References 28

References

Lisandro Dalcín, Rodrigo Paz, and Mario Storti. Mpi for python. *Journal of Parallel and Distributed Computing*, 65(9):1108–1115, 2005.

- Andreas Emil Feldmann and Luca Foschini. Balanced partitions of trees and applications. Algorithmica, 71(2):354–376, 2015.
- George Karypis and Vipin Kumar. A fast and high quality multilevel scheme for partitioning irregular graphs. SIAM Journal on scientific Computing, 20(1):359–392, 1998a.
- George Karypis and Vipin Kumar. Multilevel algorithms for multi-constraint graph partitioning. In *Proceedings of the 1998 ACM/IEEE conference on Supercomputing*, pages 1–13. IEEE Computer Society, 1998b.
- Sparsh Mittal and Jeffrey S Vetter. A survey of cpu-gpu heterogeneous computing techniques. ACM Computing Surveys (CSUR), 47(4):69, 2015.
- François Pellegrini. Static mapping by dual recursive bipartitioning of process architecture graphs. In Scalable High-Performance Computing Conference, 1994., Proceedings of the, pages 486–493. IEEE, 1994.
- Vaibhav Saxena, Thomas George, Yogish Sabharwal, and Lucas Villa Real. Architecture aware resource allocation for structured grid applications: flood modelling case. In Cluster, Cloud and Grid Computing (CCGrid), 2015 15th IEEE/ACM International Symposium on, pages 555–564. IEEE, 2015.
- Guozhong Zhao, Xijun Yu, and Rongpei Zhang. The new numerical method for solving the system of two-dimensional burgers' equations. *Computers & Mathematics with Applications*, 62(8):3279–3291, 2011.

5 Appendix 29

5 Appendix

The appendix contains:

• 1