

SUPRAGYA RAJ

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EXPERIENCE

Browserstack Mumbai, MH, India

Software Engineer (Desktop Platform team)

Dec 2019 - Present

- Developing for, managing and instrumenting (Dev + Ops) a highly heterogeneous cloud infrastructure with over 7000 active terminals consisting of machines running all macintosh and windows operating systems along with supporting subsystems such as KMS, reverse proxies, jump hosts, smokeping, DHCP etc.
- Hera SPOC framework:** Architected and developed a single point of contact framework + tool for internal engineering teams to access, debug, automate and run diagnostics parallelly on terminals. Proves vital for testing terminal health and uptime internally.

Cisco Systems Bangalore, KA, India

Software Engineer (Enterprise wireless controller team)

July 2019 - Dec 2019

Software Engineering Intern (Enterprise wireless controller team)

Jan 2019 - June 2019

- Fast wireless swarm upgrades:** Designed and implemented heirarchical (pre)download mechanism allowing enterprise grade access points to download device images in a peer to peer fashion which earlier used to be central download based architecture.
- The Fast wireless swarm upgrade mechanism reduced bandwidth load on Cisco wireless controllers and sped up download times from **O(n)** time to **O(logn)**, becoming highly effective in enterprise deployments as well as deployments with a remotely connected controller.
- Implementing controller side support for **802.11r (wireless fast transition)**, allowing fast roaming between Cisco enterprise grade WiFi access points, lowering roam times by around **10 times**.
- Configuration translator:** Developed mapping layer code to translate **openconfig** standard based wireless management to commands usable by Cisco devices.

Vicara Tech Vellore, TN, India

Software Developer Intern

April 2018 - July 2018

- Developed a Windows service to allow custom software (eg. AutoCAD) to be controlled using a gesture control device on Windows platform. The service module was lightweight and allowed very low latency inputs - **less than 10ms** to be provided to the operating system.

EDUCATION

Vellore Institute of Technology

Bachelor of Technology in Computer Science and Engineering **GPA: 9.02/10.0**

Chennai, India

July, 2019

Army Public School, Shankar Vihar

Senior Secondary, (PCM + CS), 12th CBSE: **95.6% (aggregate), 98% (CS)**
High School, 10th CBSE, **CGPA 10.0**

New Delhi, India

May 2015

May 2013

PROJECTS AND OPEN SOURCE CONTRIBUTIONS

- libfuse-FrameServer (Google Summer of Code 2018, 2019 Mentor):** A pseudo file system implementation based on libfuse to provide RGBA video output for applications such as VLC, Adobe Premiere Pro from a raw stream of camera sensor output voltages. The frameserver acts as a middleman which processes raw stream to RGBA values on the other end, allowing one to control elements of stream processing such as HW acceleration, denoising, demosaicing, downscaling etc.
- Raw video container format (Google Summer of Code 2018):** Extending Magic Lantern's MLV video container format (which existed for Canon cameras) to apertus open source camera. This allows video recording to be done straight onto a well supported video file format: MLV instead of only earlier option: image sequences.
- PiNG12RAW:** RGB image extraction (demosaicing) from cell matrix data of a camera sensor. **over 120 forks on github.**
- HLang Language & interpreter:** Developed a scripting language and its interpreter to provide functionalities of Bourne Shell (bash) on an opensource microkernel (HelenOS) operating system. The system included building AST, compile time optimisations etc.

PUBLICATIONS

- Raj S., Chodnekar S.P., Harish T., Sriraman H. (2019) **eMDPM: Efficient Multidimensional Pattern Matching Algorithm for GPU**. In: Tiwari S., Trivedi M., Mishra K., Misra A., Kumar K. (eds) Smart Innovations in Communication and Computational Sciences. Advances in Intelligent Systems and Computing, vol 851. Springer, Singapore.

AWARDS AND ACHIEVEMENTS

- Winner, Smart India Hackathon 2019, Goldman Sachs:** Mentored the team from IGDTUW, Delhi in GS problem set - smart transportation held at NIT, Calicut.
- Teaching Assistant, Fall 2019:** Taught Theory of Computation and Compiler Design to a class of over 50 students at VIT Chennai.
- Recommendation by MHRD, India:** Letter of Appreciation by Smt. Smriti Irani, former minister, Ministry of Human Resource and Development, Govt. of India for performance in senior secondary examinations.
- INSPIRE Fellowship:** Recipient of INSPIRE science fellowship (2013-2015), led by DST and DRDO, India.