Reversible motor driver BA6288FS

The BA6288FS is a reversible-motor driver with a maximum output current of 1.0A. Two logic inputs allow four output modes: forward, reverse, stop (idling), and brake. A built-in power saving circuit suppresses current consumption when the motor is in stop mode.

Applications

Compact, low-current DC motors and portable equipment such as camcorders

Features

- Two logic inputs allow four output modes: forward, reverse, stop (idling), and brake.
- Built-in power saving circuit suppresses the stop mode current dissipation.
- 3) Output voltage can be set arbitrarily with the VREF pin.
- 4) Interfaces with TTL devices.
- 5) Built-in thermal shutdown circuit turns off all circuits when high IC junction temperature is detected.

● Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Applied voltage	Vcc	18	V
Power dissipation	Pd	800*	mW
Operating temperature	Topr	−20~ +75	°C
Storage temperature	Tstg	−55∼+150	°C
Output current	Іоит	1000**	mA

^{*} When mounted on a glass epoxy board (50 imes 50 imes 1.6 mm).

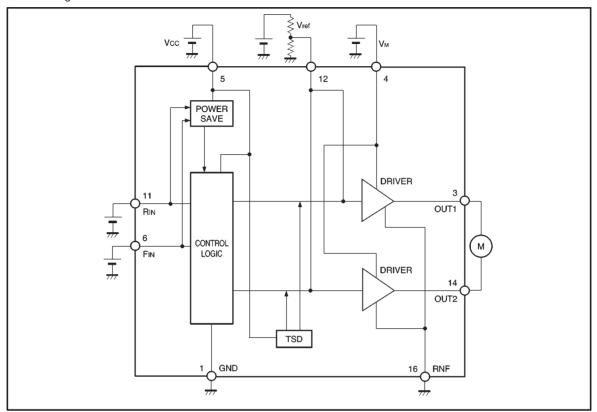
Recommended operating conditions

Parameter	Symbol	Limits	Unit
Operating power supply voltage	Vcc	3.5~15	٧
V _M pin applied voltage	Vм	0~15	V
VREF pin input voltage	Vref	0~ (Vcc-1V)	V

^{*} Reduced by 6.4 mW for each increase in Ta of 1°C over 25°C.

^{**} Should not exceed Pd or ASO values.

■Block diagram



Pin descriptions

Pin No.	Pin name	Function			
1	GND	Ground			
2	N.C.	_			
3	OUT1	Motor output			
4	VM	Motor power supply			
5	Vcc	Power supply			
6	Fin	Logic input			
7	N.C.	_			
8	N.C.	_			
9	N.C.	_			
10	N.C.	_			
11	Rin	Logic input			
12	V _{ref}	HIGH level output voltage setting			
13	N.C.	_			
14	OUT2	Motor output			
15	N.C.	_			
16	Rnf	Output section ground; resistor connection for detecting output current			

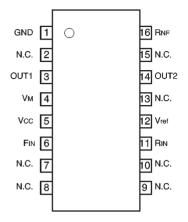


Fig.1 Pin assignments

●Electrical characteristics (unless otherwise noted, Ta = 25°C, Vcc = 6V, V_M = 6V)

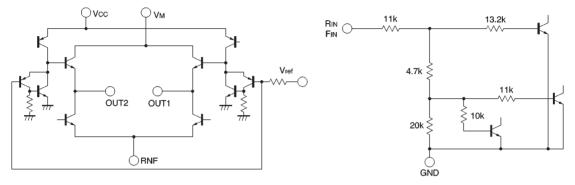
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Supply current 1	lcc1	24	39	56	mA	Forward or reverse mode, Io=200mA, Vref=Vcc
Supply current 2	lcc2	30	48	67	mA	Brake mode, V _{ref} = V _{CC}
Standby supply current	lsт	_	_	15	μА	Standby mode, V _{ref} =V _{CC}
Input high level voltage	ViH	2.0	_	_	٧	
Input low level voltage	VIL	_	_	0.8	٧	
Input high level current	Іін	45	90	135	μΑ	V _{IN} =2.0V
Output saturation voltage	Vce -		1.0	1.5	٧	Io=200mA, Vref = Vcc
		_	- 1.0			Sum of output transistor high- and low-side voltages
V _{ref} -pin source current	lref .	_	1.3	3.5	μΑ	Forward or reverse mode, V _{ref} =2.5V
Vour-Vret offset	Vouт	Vref	Vref	Vref	mV	V . —0. FV I - —1.00m A
	OFF	+0	+100	+200		V _{ret} =2.5V lo=100mA
Output transistor current capacity 1	IOMAX1 900					Vcc=3.5V
		_	-	mA	Low-side transistor, VcE=1.0V	
						V _{ref} =V _{CC}
Output transistor current capacity 2	2 IOMAX2 1000	_	-	mA	Vcc=4.0V	
					Low-side transistor, VcE=1.0V	
					V _{ref} =V _{CC}	
VM leakage current	ILM	_	_	5	μΑ	All pins except V _M and GND are OPEN

ONot designed for radiation resistance.

●Input / output truth table

Fin	Rin	OUT1	OUT2	Mode
Н	L	Н	L	Forward
L	Н	L	Н	Reverse
Н	Н	L	L	Brake
L	L	OPEN	OPEN	Standby

●Input / output circuits



Resistances are typical values.

Fig.2

Operation notes

(1) The quality of these products have been carefully checked; however, use of the products with applied voltages, operating temperatures, or other parameters that exceed the absolute maximum rating given may result in the damage of the IC and the product it is used in. If the IC is damaged, the short mode and open modes cannot be specified, so if the IC is to be used in applications where parameters may exceed the absolute maximum ratings, then be sure to incorporate fuses, or other physical safety measures.

(2) GND potential

The potential for pin 1 must be kept lower than the potensials of the other pins regardless of the circumstances.

(3) Input pins

Voltage should never be applied to the input pins when the $V_{\rm CC}$ voltage is not applied to the IC. Similarly, when $V_{\rm CC}$ is applied, the voltage on each input pin should be less than $V_{\rm CC}$ and within the guaranteed range for the electrical characteristics.

(4) Back-rush voltage

Depending on the ambient conditions, environment, or motor characteristics, the back-rush voltage may fluctuate. Be sure to confirm that the back-rush voltage will not adversely affect the operation of the IC.

(5) Large current line

Large currents are carried by the motor power supply and motor ground for these ICs.

Therefore, the layout of the pattern of the PC board and the constants of certain parameters for external components, such as the capacitor between the power supply and ground, may cause this large output current to flow back to the input pins, resulting in output oscillation or other malfunctions. To prevent this, make sure that the PC board layout and external circuit constants cause no problems with the characteristics of these ICs.

(6) Power dissipation

The power dissipation will fluctuate depending on the mounting conditions of the IC and the ambient environment. Make sure to carefully check the thermal design of the application where these ICs will be used.

(7) Power consumption

The power consumption by the IC varies widely with the power supply voltage and the output current. Give full consideration to the power dissipation rating and the thermal resistance data and transient thermal resistance data, to provide a thermal design so that none of the ratings for the IC are exceeded.

(8) ASO

Make sure that the output current and supply voltage do not exceed the ASO values.

(9) Precautions for input mode switching

To ensure reliability, it is recommended that the mode switching for the motor pass once through the open mode.

(10) In-rush current

There are no circuits built into these ICs that prevent inrush currents. Therefore, it is recommended to place a current limiting resistor or other physical countermeasure.

(11) Factors regarding the thermal, power supply, and motor conditions

If the potential of the output pin sways greatly and goes below the potential of ground, the operation of the IC may malfunction or be adversely affected. In such a case, place a diode between the output and ground, or other measure, to prevent this.

(12) HIGH level output voltage setting pin

The output voltage can be varied by controlling the V_{ref} voltage :

 $V_{OH}=V_{ref}+V_{BE}$ (PNP) - V_{BE} (NPN) + ($V_{OUT}-V_{ref}$ offset)

The voltage applied to the V_{ref} pin should not exceed the motor supply voltage (pin 4) or the V_{CC} voltage. The V_{ref} input range over which the HIGH level output voltage can be controlled according to the above equation is between 0V and ($V_{\text{CC}} - V_{\text{SAT}} - V_{\text{BE}}$).

The output may oscillate if the $V_{\rm ref}$ voltage is controlled by a low-impedance circuit. Set the voltage by either providing an impedance of about $10k\Omega$ or connecting a capacitor between the $V_{\rm ref}$ and GND pins. Because the optimum impedance and capacitance values depend on such factors as the type of motor, the PCB pattern, and the load current, the values must be determined separately for each application.

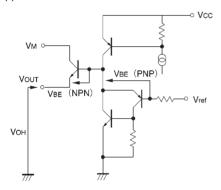


Fig.3

(13) Application circuit

Although the application circuit of Fig. 5 is recommendable, special care is required regarding the above precautions and parts characteristics when implementing this application. Also, note that we have not carried out extensive investigation of the patent right.

(14) Thermal shutdown circuit

When the thermal shutdown circuit is activated at the IC junction temperature of about 175°C (typical), all driver outputs are turned OFF. There is a temperature difference of about 15°C (typical) between the temperatures at which the circuit is activated and deactivated.

(15) The input pins (pins 6 and 11) have temperaturedependent characteristics. Take the temperature effect into consideration when using the IC. (16) To eliminate motor noise, connect a capacitor between OUT1 (pin 3) and GND and between OUT2 (pin 14) and GND. Alternatively, connect a capacitor between OUT1 and OUT2, and also a diode between OUT1 and GND and between OUT2 and GND.

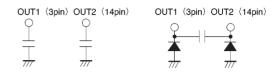


Fig.4

Application example

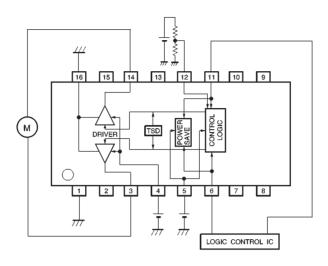


Fig.5

Electrical characteristic curves

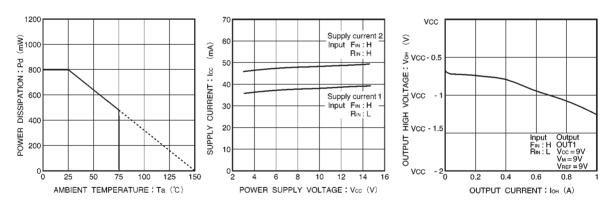
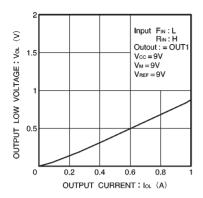
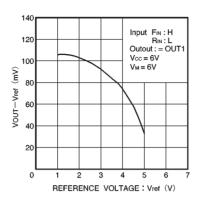


Fig.6 Power dissipation curve

ig.7 Supply current vs.power supply voltage

Fig.8 Output high level voltage vs. output current





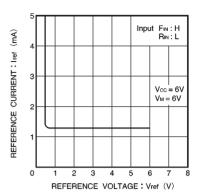


Fig.9 Output low level voltage vs. output current

Fig.10 Vout-V_{ref} voltage differential vs. V_{ref} reference voltage

Fig.11 V_{ref} reference current vs. V_{ref} reference voltage

External dimensions (Units: mm)

