

Byte Address	Contents	Description
7E	Stack Pointer ↑	The stack is init. At 30h
30		
2F		
2E	MSB ↑	PW storage from ext. RAM
2D		
2C		
2B	LSB ↑	User entered PW
2A		
29		
28	MSB ↑	Acceleration Setpoint
27		
26		
25	LSB	
24	0x##	Saves DPL during loops
23.1 (19h)	0 or 1	Limits a serial TX of Accel Alm to 1
23.0 (18h)	0 or 1	Alarm system armed
22	0 → 255	Current Acceleration from ADC
21	0 → 255	Current state for state machine
20.7 (07h)	0 or 1	Caps lock toggle bit
20.3 (03h)	0 or 1	Red function key
20.2 (02h)	0 or 1	Green function key
20.1 (01h)	0 or 1	Pink function key
20.0 (00h)	0 or 1	Blue function key
1F	Not Used	Not Used
1E	↑	↑
1D		
1C		
1B		
1A		
19		
18		
17		
16		
15		
14		
13		
12		
11		
10		
F		
E		
D		
C		
B		
A		
9		
8		
7		
6		
5		
4	Not Used	Not Used
3	B register	Used in conjunction w/ACC to inc DPTR for lookup table
2	00h	Limits BS/Non-funct. key presses
1	21h	Pointer for current state
0	2Bh	Pointer for MSB of user ent. PW