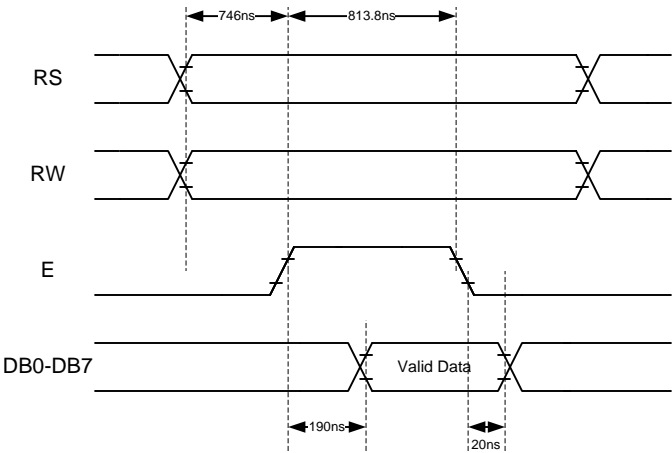
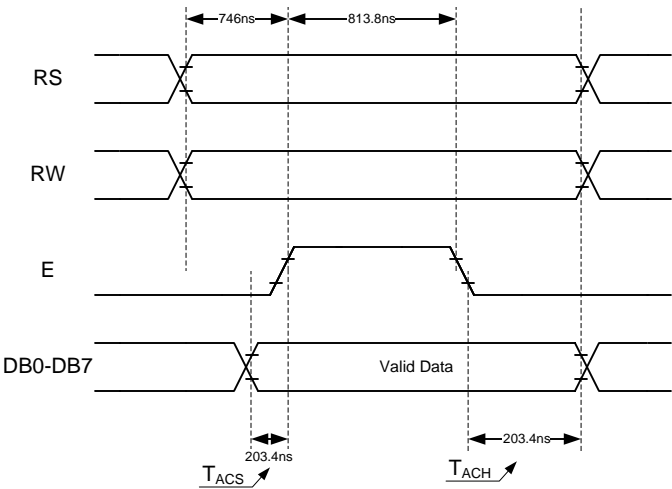


LCD Read Timing (max)



LCD Write Timing (max)



MCU External Memory Interface (EMI) Timing Parameters			
SYSCLK → 14.7456MHz		Period → 6.78168E-08	
EMI Parameter	Pulse Width Extension Factor	Total Pulse Width (sec.)	Description
T <sub>ALEH</sub>	4	2.71267E-07	Address/Control latch enable high time
T <sub>ALEL</sub>	4	2.71267E-07	Address/Control latch enable low time
T <sub>ACS</sub>	3	2.03451E-07	Address/Control setup time
T <sub>ACW</sub>	12	8.13802E-07	Address/Control pulse width for /WR & /RD
T <sub>ACH</sub>	3	2.03451E-07	Address/Control hold time

LCD Timing		SIZE	FSCM NO	DWG NO	REV
DRAWN	TBL			Figure 5: LCD Timing	1.0
ISSUED	7/27/2008	SCALE	1 : 1	SHEET	35 OF 45