

Real-time detection of memory-related voltage patterns in the brain through spatiotemporal filtering

Tomas Fiers

Thesis submitted for the degree of
Master of Science in
Biomedical Engineering

Thesis supervisors:

Prof. dr. ir. A. Bertrand
Prof. dr. F. Kloosterman

Assessors:

Prof. dr. ir. R. Puers
Dr. eng. J. Couto

Mentors:

Ir. J. Wouters
Dott. D. Ciliberti

© Copyright KU Leuven

Without written permission of the thesis supervisors and the author it is forbidden to reproduce or adapt in any form or by any means any part of this publication. Requests for obtaining the right to reproduce or utilize parts of this publication should be addressed to Faculteit Ingenieurswetenschappen, Kasteelpark Arenberg 1 bus 2200, B-3001 Heverlee, +32-16-321350.

A written permission of the thesis supervisors is also required to use the methods, products, schematics and programmes described in this work for industrial or commercial use, and for submitting this publication in scientific contests.