An Introduction to the TLA⁺ Language and its Tools

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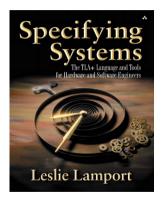
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TLA⁺ specification language



- describe and verify distributed and concurrent systems
- based on mathematical set theory plus temporal logic TLA
- TLA⁺ Video Course
- book: Addison-Wesley, 2003 (free download for personal use)
- Hillel Wayne: Practical TLA+, https://learntla.com/ (focuses on PlusCal algorithm language)
- tools: TLC and Apalache model checkers, TLA⁺ Proof System available from the TLA⁺ Toolbox and VS Code Extension

Objective of this presentation

Introduce basic concepts of TLA⁺

Model systems in TLA⁺

• Tool support for verification: model checking and proof

• Presentation by example: distributed termination detection

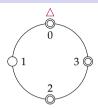
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Part I

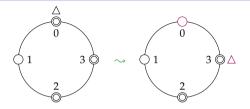
Modeling a Distributed Algorithm in TLA⁺



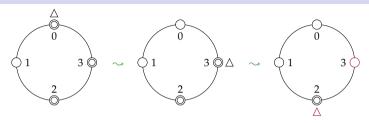
- Nodes arranged on a ring perform some computation
 - nodes can be active (double circle) or inactive (simple circle)
 - "master node" 0 wishes to detect when all nodes are inactive



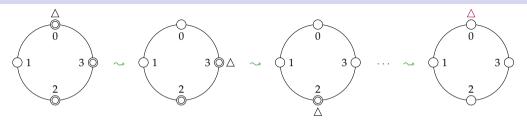
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 - initially, the master node holds the token



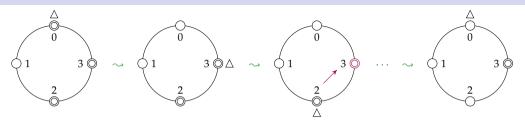
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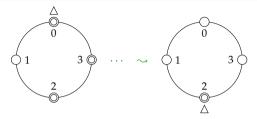
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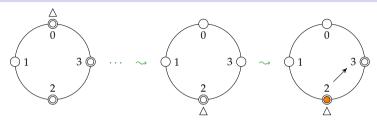
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 - termination detected when token returns to (inactive) master node



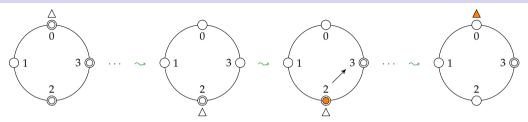
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 - when the node holding the token has terminated, the token moves on
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- Complication: nodes may send messages, activating receiver



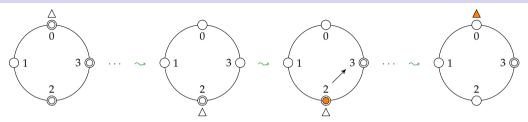
- Nodes and token colored orange or white
 - master node initiates probe by sending white token



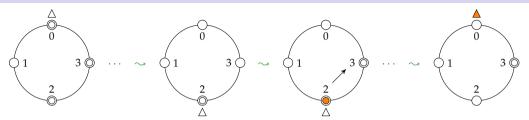
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- Master node detects termination when it is inactive, white, and holds white token



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 - master node initiates probe by sending white token
 - node becomes black when sending a message to a higher-numbered node
 - when passing the token, an orange node transfers the orange color to the token
- Master node detects termination when it is inactive, white, and holds white token
- Safety: termination detected only if all nodes are inactive
- Liveness: when all nodes inactive, termination will eventually be detected

Model-Based System Specifications in TLA⁺

- Describe the system configurations
 - state variables represent the state of the system
 - ► TLA⁺ encourages abstractions in terms of sets, functions, tuples etc.
 - data model: classical (untyped) mathematical set theory

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- State machine specification

▶ initial condition <i>Init</i> characterizes possible initial states $x = 0 \land 1$	$j \in Na$
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• actions describe effect of transitions
$$x' = x + y \land y' = y$$

- next-state relation Next disjunction of individual actions
- overall specification models all system executions $Init \wedge \Box [Next]_v$

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 $Init \wedge \Box [Next]_v$

Specifications and properties expressed in mathematical logic

TLA⁺ Specification of EWD 840: System Configurations

```
EXTENDS Naturals

CONSTANT N

ASSUME NAssumption \triangleq N \in Nat \setminus \{0\}

Nodes \triangleq 0..N-1

Color \triangleq \{\text{"white", "orange"}\}

VARIABLES active, color, tpos, tcolor

TypeOK \triangleq \land active \in [Nodes \rightarrow BOOLEAN] \land color \in [Nodes \rightarrow Color]

\land tpos \in Nodes \land tcolor \in Color
```

- Declaration of constants and variables
- Definition of operators
 - sets Nodes and Color
 - TypeOK documents expected values of variables
 - ▶ *active* and *color* are arrays, i.e. functions



TLA⁺ Specification of EWD 840: Initiation and System Transitions

$$\begin{array}{ll} \textit{Init} & \triangleq & \land \textit{active} \in [\textit{Nodes} \rightarrow \textit{BOOLEAN}] \land \textit{color} \in [\textit{Nodes} \rightarrow \textit{Color}] \\ & \land \textit{tpos} \in \textit{Nodes} \land \textit{tcolor} = "\texttt{orange}" \end{array}$$

• Initial condition: any "type-correct" values; token is initially orange

TLA⁺ Specification of EWD 840: Initiation and System Transitions

```
Init \stackrel{\triangle}{=} \land active \in [Nodes \rightarrow BOOLEAN] \land color \in [Nodes \rightarrow Color]
           \land tpos \in Nodes \land tcolor = "orange"
InitiateProbe \stackrel{\triangle}{=}
      \land tpos = 0 \land (tcolor = "orange" \lor color[0] = "orange")
      \land tpos' = N - 1 \land tcolor' = "white"
      \wedge color' = [color EXCEPT ! [0] = "white"]
      \wedge active' = active
PassToken(i) \triangleq
      \land tpos = i \land (\neg active[i] \lor color[i] = "orange" \lor tcolor = "orange")
      \land tpos' = i - 1
      \wedge tcolor' = IF color[i] = "orange" THEN "orange" ELSE tcolor
      \wedge color' = [color \ EXCEPT \ ![i] = "white"]
      \wedge active '= active
System \triangleq InitiateProbe \vee \exists i \in Nodes \setminus \{0\} : PassToken(i)
```

- Initial condition: any "type-correct" values; token is initially orange
- System transitions: token passing

TLA⁺ Specification of EWD 840: Environment Transitions

```
Terminate(i) \triangleq
      \land active[i] \land active' = [active EXCEPT ![i] = FALSE]
      ↑ UNCHANGED ⟨color, tpos, tcolor⟩
SendMsg(i) \triangleq
      \land active[i]
      \land \exists j \in Nodes \setminus \{i\} : \land active' = [active EXCEPT ! [i] = TRUE]
                                \land color' = [color \ EXCEPT \ ![i] = IF \ i > i \ THEN "orange" \ ELSE @]
      ∧ UNCHANGED ⟨tpos, tcolor⟩
Env \triangleq \exists i \in Nodes : Terminate(i) \lor SendMsg(i)
```

• Definition of actions not controlled by the algorithm



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      ∧ UNCHANGED ⟨tpos, tcolor⟩
Env \triangleq \exists i \in Nodes : Terminate(i) \lor SendMsg(i)
Next \triangleq System \lor Env
vars \triangleq \langle tpos, tcolor, active, color \rangle
Spec \stackrel{\Delta}{=} Init \wedge \Box [Next]_{vars}
```

- Definition of actions not controlled by the algorithm
- Possible executions: initial condition, interleaving of transitions

Part II

Verification By Model Checking

Formulation of Safety Properties in TLA⁺

- Check type correctness
 - invariant of the specification:

THEOREM $Spec \Rightarrow \Box TypeOK$

► *TypeOK* is true throughout any execution of *Spec*

Formulation of Safety Properties in TLA⁺

- Check type correctness
 - ▶ invariant of the specification:

THEOREM $Spec \Rightarrow \Box TypeOK$

- ► *TypeOK* is true throughout any execution of *Spec*
- All nodes are inactive when master node detects termination
 - master claims termination when it is white and inactive and holds a white token

```
terminated \triangleq \forall i \in Nodes : \neg active[i]
terminationDetected \triangleq tpos = 0 \land tcolor = \text{``white''} \land color[0] = \text{``white''} \land \neg active[0]
TerminationDetection \triangleq terminationDetected \Rightarrow terminated
Spec \Rightarrow \Box TerminationDetection
```

formally again expressed as an invariant

Model Checking Using TLC

- Create a model: finite instance of TLA⁺ specification
 - instantiate constant parameters by concrete values for example, create instance for N = 5
 - indicate operator corresponding to system specification heuristically set to *Spec* when that operator is defined in the module
 - indicate invariants to verify formulas TypeOK and TerminationDetection
 - ► TLC checks that these properties hold for this model
- TLC integrated into TLA⁺ Toolbox (Eclipse GUI)

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 - ► TLC checks that these properties hold for this model
- TLC integrated into TLA⁺ Toolbox (Eclipse GUI)
- Exploit the automation of TLC for validating the specification
 - check both properties you believe to be true and false
 - ▶ gain confidence in your model, remove modeling errors



```
Liveness \stackrel{\triangle}{=} terminated \sim terminationDetected THEOREM Spec \Rightarrow Liveness
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- ► TLC produces a counter-example that ends in infinite stuttering
- ▶ \Box [*Next*]_{vars} allows for steps that do not change vars
- we'll soon understand why TLA⁺ specifications allow for stuttering

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- Fairness conditions rule out infinite stuttering
 - assert that an action will be taken, provided it is "often" enabled
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- Fairness conditions rule out infinite stuttering
 - assert that an action will be taken, provided it is "often" enabled
 - abstractly represent assumptions about the "speed" of components
- Determining the right fairness conditions can be tricky



- Two standard concepts of fairness
 - weak fairness disallow behaviors where action is persistently enabled but never taken
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 - ▶ strong fairness disallow behaviors where action is repeatedly enabled but never taken
- Representation in temporal logic

$$\operatorname{WF}(A) \ \stackrel{\vartriangle}{=} \ \Box \big((\Box \operatorname{enabled} A) \Rightarrow \Diamond A \big) \qquad \operatorname{SF}(A) \ \stackrel{\vartriangle}{=} \ \Box \big((\Box \Diamond \operatorname{enabled} A) \Rightarrow \Diamond A \big)$$

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► TLA⁺ asserts fairness for non-stuttering actions

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- ► TLA⁺ asserts fairness for non-stuttering actions
- Fairness hypotheses for EWD 840
 - ► require fairness for the token-passing ("system") actions

$$Spec \stackrel{\Delta}{=} Init \wedge \Box [Next]_{vars} \wedge WF_{vars}(System)$$



Another Way of Specifying the Problem

- We have modeled a concrete algorithm and verified its properties
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- ullet Write a TLA⁺ specification \mathcal{TD} of termination detection
 - define the problem as an abstract state machine
 - then show that EWD 840 is an implementation of that problem
- TLA⁺ doesn't distinguish between specifications and properties
 - implementation: every execution is allowed by the high-level state machine

```
Theorem Spec \Rightarrow \mathcal{TD}!Spec
```

• insensitivity to stuttering is essential here: EWD 840 acts on variables (such as the token) that play no role in \mathcal{TD}



Specifying termination detection

MODULE SyncTerminationDetection —

```
VARIABLES active, termination Detected terminated \stackrel{\Delta}{=} \forall n \in Nodes : \neg active[n]
```

 $\textit{Init} \ \triangleq \ \textit{active} \in [\textit{Nodes} \rightarrow \textit{BOOLEAN}] \land \textit{terminationDetected} \in \{\textit{FALSE}, \textit{terminated}\}$

Specifying termination detection

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terminated \triangleq \forall n \in Nodes : \neg active[n]
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Terminate(i) \stackrel{\triangle}{=} \land active[i] \land active' = [active \ EXCEPT \,![i] = FALSE]
                     \land terminationDetected' \in {terminationDetected, terminated'}
Wakeup(i,j) \stackrel{\triangle}{=} active[i] \land active' = [active \ EXCEPT \ ![j] = TRUE] \land UNCHANGED termination Detected
DetectTermination \stackrel{\Delta}{=} terminated \land terminationDetected' = TRUE \land UNCHANGED active
Next \triangleq (\exists i \in Nodes : Terminate(i)) \lor (\exists i, j \in Nodes : Wakeup(i, j)) \lor DetectTermination
vars \triangleq \langle active, termination Detected \rangle
Spec \stackrel{\Delta}{=} Init \wedge \Box [Next]_{vars} \wedge WF_{vars}(DetectTermination)
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vars \triangleq \langle active, termination Detected \rangle
Spec \triangleq Init \wedge \Box [Next]_{vars} \wedge WF_{vars}(DetectTermination)
```

• Same overall structure as algorithm specification, can verify properties

```
Spec \Rightarrow \Box(terminationDetected \Rightarrow \Box terminated)
```

 $Spec \Rightarrow (terminated \rightarrow terminationDetected)$



Checking Refinement

• Within module *EWD840*, create an instance of *SyncTerminationDetection*

```
TD \stackrel{\Delta}{=} INSTANCE SyncTerminationDetection
THEOREM Spec \Rightarrow TD!Spec
```

- parameters instantiated by the operators of the same name in EWD840
- an instance may also substitute expressions for parameters ("refinement mapping")
- ▶ formula *Spec* refers to the specification of module *EWD840*

• Refinement can be verified in the same way as other properties

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- EWD840 assumes that messages between nodes are delivered instantaneously
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 - token may go around the ring twice while the message is in transit
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 - easy exercise: adapt the specification and detect the problem using model checking

Termination Detection When Communication is Asynchronous

- EWD840 assumes that messages between nodes are delivered instantaneously
- What if message delivery is asynchronous?
 - token may go around the ring twice while the message is in transit
 - master node may declare termination and receiver later becomes active
 - easy exercise: adapt the specification and detect the problem using model checking
- Adaptation suggested by Shmuel Safra, published as EWD998 (1987)
 - lacktriangle each node stores difference δ_i between the number of messages sent and received locally
 - the token sums up the differences δ_i of nodes it passes as tkn.q
 - master detects termination when it's inactive and $\delta_0 + tkn.q = 0$ (plus color conditions)

Part III

Deductive Verification Using the TLA⁺ Proof System

Using TLAPS to Prove Safety Properties

- TLAPS: proof assistant for verifying TLA⁺ specifications
 - \oplus verification is independent of the size of the model / state space
 - → interactive proof checker: user must guide the proof

Using TLAPS to Prove Safety Properties

- TLAPS: proof assistant for verifying TLA⁺ specifications
 - verification is independent of the size of the model / state space
- Proving a simple invariant in TLAPS (for arbitrary *N*)

```
THEOREM TypeCorrect \triangleq Spec \Rightarrow \BoxTypeOK \langle 1 \rangle 1. Init \Rightarrow TypeOK \langle 1 \rangle 2. TypeOK \wedge [Next]<sub>vars</sub> \Rightarrow TypeOK' \langle 1 \rangle 3. QED BY\langle 1 \rangle 1, \langle 1 \rangle 2, PTL DEF Spec
```

- hierarchical proof language represents proof tree
- ▶ individual steps can be proved in any order: usually start with QED step
- invariant follows from steps $\langle 1 \rangle 1$ and $\langle 1 \rangle 2$ by temporal logic

Simple Proofs

• Prove that *Init* implies *TypeOK*

```
\langle 1 \rangle 1. Init \Rightarrow TypeOK
BY NAssumption DEFS Init, TypeOK, Node, Color
```

- relevant definitions and facts must be cited explicitly
- this helps manage the size of the search space for proof tools

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- relevant definitions and facts must be cited explicitly
- this helps manage the size of the search space for proof tools
- Attempt similar proof for step $\langle 1 \rangle 2$

```
\langle 1 \rangle 2. TypeOK \wedge [Next]<sub>vars</sub> \Rightarrow TypeOK'
BY NAssumption DEFS TypeOK, Next, vars, InitiateProbe, . . .
```

decompose proof into smaller steps when brute force fails



Hierarchical Proofs

```
\langle 1 \rangle 2. TypeOK \wedge [Next]<sub>vars</sub> \Rightarrow TypeOK'
   (2) SUFFICES ASSUME TypeOK, [Next] vars
                     PROVE TupeOK'
      OBVIOUS
   \langle 2 \rangle USE NAssumption DEF TypeOK
   \langle 2 \rangle1. CASE InitiateProbe
      BY ⟨2⟩1 DEF InitiateProbe
   \langle 2 \rangle 2. ASSUME NEW i \in Node \setminus \{0\}, PassToken(i)
          PROVE TypeOK'
      BY (2)2 DEF PassToken
  ... similarly for the remaining actions ...
   \langle 2 \rangle QED BY \langle 2 \rangle 1, \langle 2 \rangle 2, \dots DEF Next
```

- SUFFICES steps represent backward chaining
- Toolbox IDE helps with hierarchical decomposition



Proof of Main Safety Property

- *TerminationDetection* is not preserved by the next-state relation
 - we need an inductive invariant that provides information about all reachable states

```
\begin{array}{ll} \mathit{Inv} \ \stackrel{\triangle}{=} \ \lor \ \forall i \in \mathit{Nodes} : i > \mathit{tpos} \Rightarrow \neg \mathit{active}[i] \\ \ \lor \ \exists j \in 0 \, .. \, \mathit{tpos} : \mathit{color}[j] = \text{``orange''} \end{array} \ \ \begin{array}{ll} \text{all nodes behind the token are inactive} \\ \text{some node ahead is dirty} \\ \text{the token is dirty} \end{array}
```

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```

▶ use TLC to check that *Inv* is inductive (relative to *TypeOK*)

```
TypeOK \wedge Inv \wedge \Box [Next]_{vars} \Rightarrow \Box Inv
```

Proof of Main Safety Property

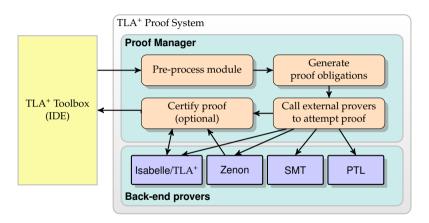
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```
TypeOK \wedge Inv \wedge \Box [Next]_{vars} \Rightarrow \Box Inv
```

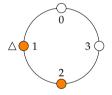
Proof of the theorem

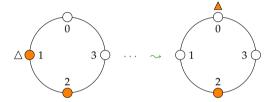
TLAPS Architecture

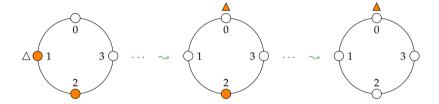


- Isabelle/TLA⁺: faithful encoding of TLA⁺ in Isabelle's meta-logic
- PTL: decision procedure for propositional temporal logic

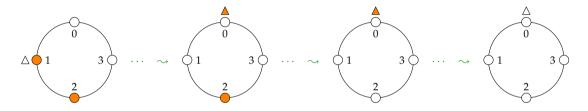






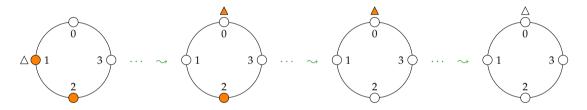


Liveness $\stackrel{\triangle}{=}$ terminated \sim terminationDetected THEOREM $Spec \Rightarrow Liveness$



Three rounds of the token may be required between termination and detection

Liveness $\stackrel{\Delta}{=}$ terminated \sim terminationDetected THEOREM Spec \Rightarrow Liveness



Three rounds of the token may be required between termination and detection

• Proof by contradiction: assume that termination is never detected

$$BSpec \ \stackrel{\vartriangle}{=} \ \Box TypeOK \land \Box (\neg terminationDetected) \land \Box [Next]_{vars} \land WF_{vars}(System)$$
 Theorem $BSpec \Rightarrow Liveness$



Reasoning about ENABLED

- ullet Liveness relies on the fairness hypothesis WF_{vars}(System)
 - ▶ remember: $WF_v(A) \stackrel{\Delta}{=} \Box ((\Box \text{ENABLED } \langle A \rangle_v) \Rightarrow \Diamond \langle A \rangle_v)$
 - reasoning about fairness requires reasoning about ENABLED
 - ▶ prove $ENABLED \langle A \rangle_v \equiv P$ for some state predicate P

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 - reasoning about fairness requires reasoning about ENABLED
 - ▶ prove $ENABLED \langle A \rangle_v \equiv P$ for some state predicate P
- Enabled $\langle A \rangle_v \stackrel{\triangle}{=} \exists v' : A \land v' \neq v$
 - expansion of ENABLED introduces quantifiers: problematic for automatic backends
 - non-stuttering conjunct adds extra complications
 - ▶ better: rely on specific rules for simplifying ENABLED



Computing ENABLED (System)_{vars}

• Observe that all *System* steps are non-stuttering

LEMMA
$$TypeOK \Rightarrow (\langle System \rangle_{vars} \equiv System)$$

established using standard action-level reasoning

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COROLLARY
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embodied in proof directive ENABLEDrules

Computing ENABLED (System)_{vars}

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LEMMA TypeOK \Rightarrow (\langle System \rangle_{vars} \equiv System)
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COROLLARY TypeOK \Rightarrow (ENABLED \langle System \rangle_{vars} \equiv ENABLED System)
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- embodied in proof directive ENABLEDrules
- ENABLEDrewrites pushes ENABLED across logical operators

```
ENABLED (A \lor B) \equiv (\text{ENABLED } A) \lor (\text{ENABLED } B)

ENABLED (A \land B) \equiv (\text{ENABLED } A) \land (\text{ENABLED } B) if A and B have disjoint primed variables

ENABLED (x' = t) \equiv \text{TRUE} if P is a state predicate etc.
```

Theorem on the Enabledness Condition

```
SystemEnabled \triangleq \lor tpos = 0 \land (tcolor = "orange" \lor color[0] = "orange") \\ \lor \exists i \in Nodes \land \{0\} : tpos = i \land (\neg active[i] \lor tcolor = "orange" \lor color[i] = "orange") \\ \mathsf{THEOREM} \quad \mathsf{ASSUME} \quad \mathsf{TypeOK} \\ \mathsf{PROVE} \quad \mathsf{ENABLED} \ \langle System \rangle_{vars} \equiv SystemEnabled \\ \langle 1 \rangle 1. \ System \equiv \langle System \rangle_{vars} \\ \mathsf{BY} \ \mathsf{DEF} \ \mathsf{TypeOK}, \ System, \ vars, \ \mathit{InitiateProbe}, \ \mathit{PassToken}, \ \mathit{Nodes} \\ \langle 1 \rangle 2. \ (\mathsf{ENABLED} \ System) \equiv \ \mathsf{ENABLED} \ \langle System \rangle_{vars} \\ \mathsf{BY} \ \langle 1 \rangle 1, \ \mathsf{ENABLED} \mathit{rules} \\ \langle 1 \rangle 3. \ \mathsf{QED} \\ \mathsf{BY} \ \langle 1 \rangle 2, \ \mathsf{ENABLED} \mathit{rwrites} \ \mathsf{DEF} \ System, \ \mathit{InitiateProbe}, \ \mathit{PassToken}, \ \mathit{SystemEnabled} \\ \end{cases}
```

Prove that token will return to node 0

LEMMA Round1 $\stackrel{\triangle}{=} BSpec \Rightarrow (terminated \rightsquigarrow (terminated \land tpos = 0))$

Prove that token will return to node 0

 $\texttt{LEMMA } \textit{Round} 1 \ \stackrel{\vartriangle}{=} \ \textit{BSpec} \Rightarrow \big(\textit{terminated} \sim (\textit{terminated} \land \textit{tpos} = 0)\big)$

▶ show by induction that token will travel from any node *i* to node 0

$$P(i) \stackrel{\triangle}{=} terminated \land i \in Nodes \land tpos = i$$

 $R(i) \stackrel{\triangle}{=} BSpec \Rightarrow (P(i) \leadsto P(0))$

Prove that token will return to node 0

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$$\begin{array}{ll} P(i) \ \stackrel{\triangle}{=} \ terminated \land i \in Nodes \land tpos = i \\ R(i) \ \stackrel{\triangle}{=} \ BSpec \Rightarrow (P(i) \leadsto P(0)) \end{array}$$

- ightharpoonup R(0) holds trivially
- for any $i \in Nodes$, prove that $R(i) \Rightarrow R(i+1)$

```
\langle 3 \rangle1. TypeOK \wedge P(i+1) \wedge [Next]_{vars} \Rightarrow P(i+1)' \vee P(i)'
\langle 3 \rangle2. TypeOK \wedge P(i+1) \wedge \langle System \rangle_{vars} \Rightarrow P(i)'
```

- $\langle 3 \rangle 3$. TypeOK $\wedge P(i+1) \Rightarrow \text{ENABLED } \langle \text{System} \rangle_{vars}$
- $\langle 3 \rangle 4. \ BSpec \Rightarrow (P(i+1) \leadsto P(i))$ By $\langle 3 \rangle 1, \ \langle 3 \rangle 2, \ \langle 3 \rangle 3, \ PTL \ DEF \ BSpec$
- $\langle 3 \rangle$. QED BY $\langle 3 \rangle 4$, PTL



Finishing the Liveness Proof

• Prove two similar lemmas about the two remaining rounds

```
 allWhite \stackrel{\triangle}{=} \forall i \in Nodes : color[i] = \text{``white''}  LEMMA Round2 \stackrel{\triangle}{=} BSpec \Rightarrow (terminated \land tpos = 0 \rightsquigarrow (terminated \land tpos = 0 \land allWhite)) LEMMA Round3 \stackrel{\triangle}{=} BSpec \Rightarrow (terminated \land tpos = 0 \land allWhite \rightsquigarrow (terminated \land tpos = 0 \land allWhite \land tcolor = \text{``white''}))
```

- proofs of these lemmas obtained by copy and paste from that of Round1
- ▶ clearly: $terminated \land tpos = 0 \land allWhite \land tcolor = "white" \Rightarrow terminationDetected$

Finishing the Liveness Proof

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allWhite \stackrel{\triangle}{=} \forall i \in Nodes : color[i] = \text{``white''}
LEMMA \ Round2 \stackrel{\triangle}{=} \ BSpec \Rightarrow \big(terminated \land tpos = 0 \leadsto (terminated \land tpos = 0 \land allWhite \hookrightarrow \big)
LEMMA \ Round3 \stackrel{\triangle}{=} \ BSpec \Rightarrow \big(terminated \land tpos = 0 \land allWhite \leadsto \big)
(terminated \land tpos = 0 \land allWhite \land tcolor = \text{``white''})\big)
```

- proofs of these lemmas obtained by copy and paste from that of Round1
- ▶ clearly: $terminated \land tpos = 0 \land allWhite \land tcolor = "white" \Rightarrow terminationDetected$
- Putting everything together

```
THEOREM Spec \Rightarrow Liveness By TypeCorrect, Round1, Round2, Round3, PTL DEF Spec, BSpec, Liveness
```



Part IV

Conclusion

Summing Up

- TLA⁺: formal language for specifying systems based on mathematics
 - highly expressive and flexible language favors abstraction
 - state machines for representing system behavior
 - no distinction between systems and properties
 - refinement (and composition) reflected in logic

Support tools

- ► TLA⁺ Toolbox: editor, syntax/semantic analysis, pretty printer
- ▶ TLC: explicit-state model checker, checkpointing, parallelization
- ► TLAPS: interactive proof platform, automatic proof back-ends
- Apalache: bounded model checking based on SMT encoding
- ▶ PlusCal: front-end for generating TLA⁺ from "pseudo code" language

More information

http://lamport.azurewebsites.net/tla/tla.html

Google discussion group