

FREE FOR NON-COMMERCIAL USE

**TOM STOREY**

Sheet: /

File: VME\_Backplane\_3ActPas.kicad\_sch

**Title: 3 SLOT ACTIVE/PASSIVE TERMINATION VMEBUS BACKPLANE**

Size: A4

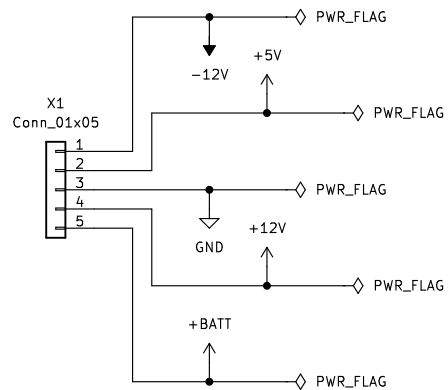
Date: 2024-01-03

Rev: 1

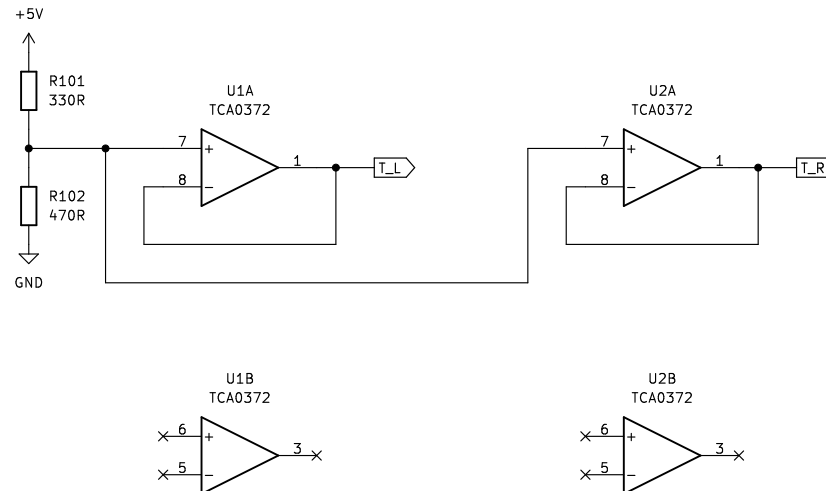
KiCad E.D.A. kicad (6.0.10-0)

Id: 1/6

## POWER INPUT TERMINALS



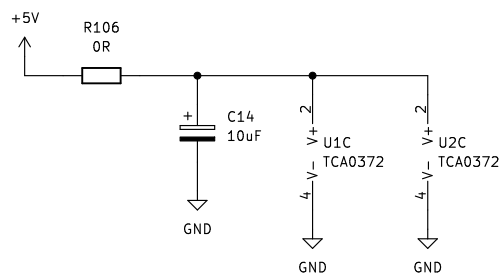
## ACTIVE TERMINATION VOLTAGE GENERATION



### OPAMP SUBSTITUTION TABLE

MFG	P/N
MOTOROLA	TCA0372 **
ST	L272M

\*\* REFERENCE PART



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Sheet: /Power/

File: power.kicad\_sch

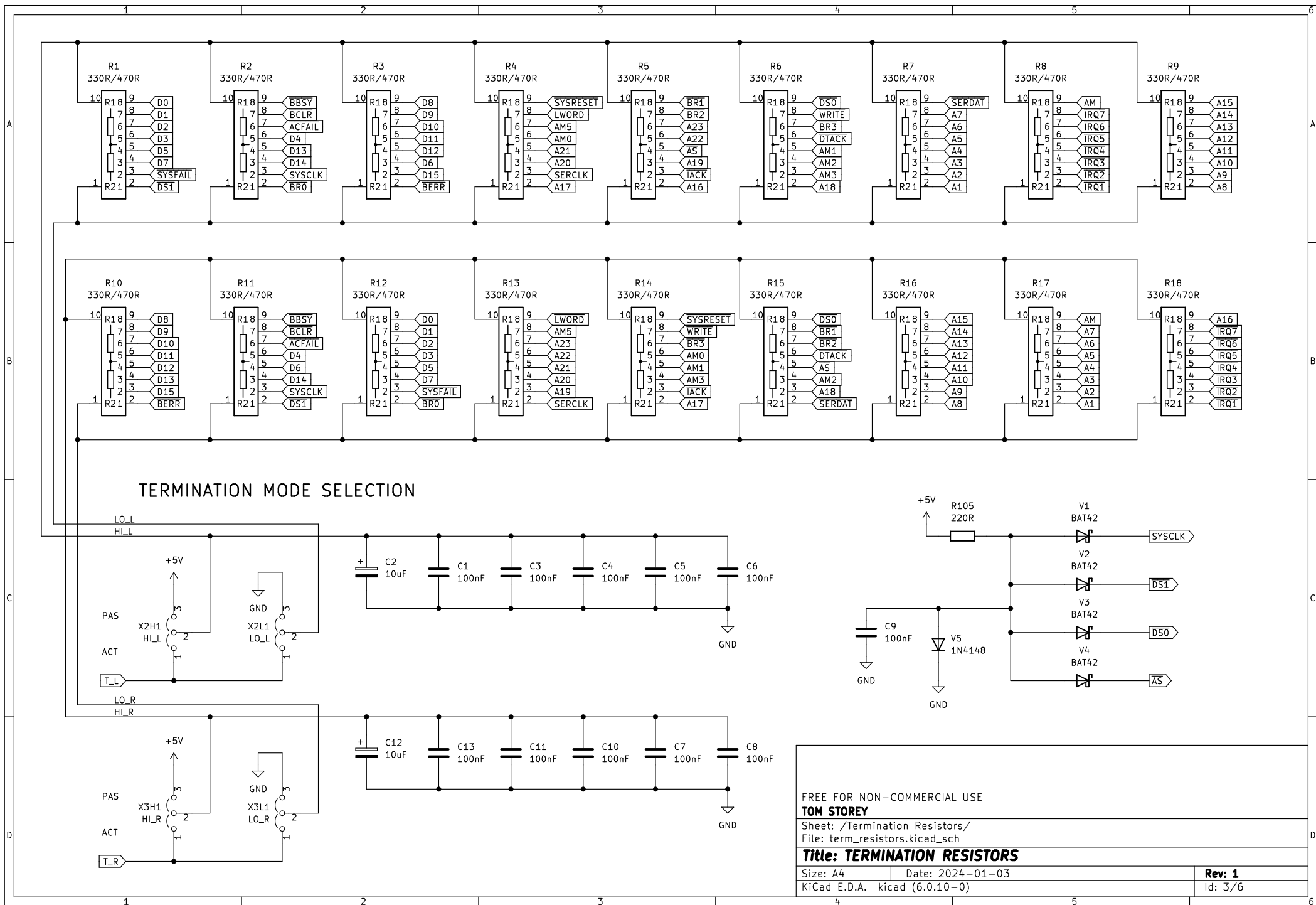
**Title: POWER INPUT AND TERMINATION VOLTAGES**

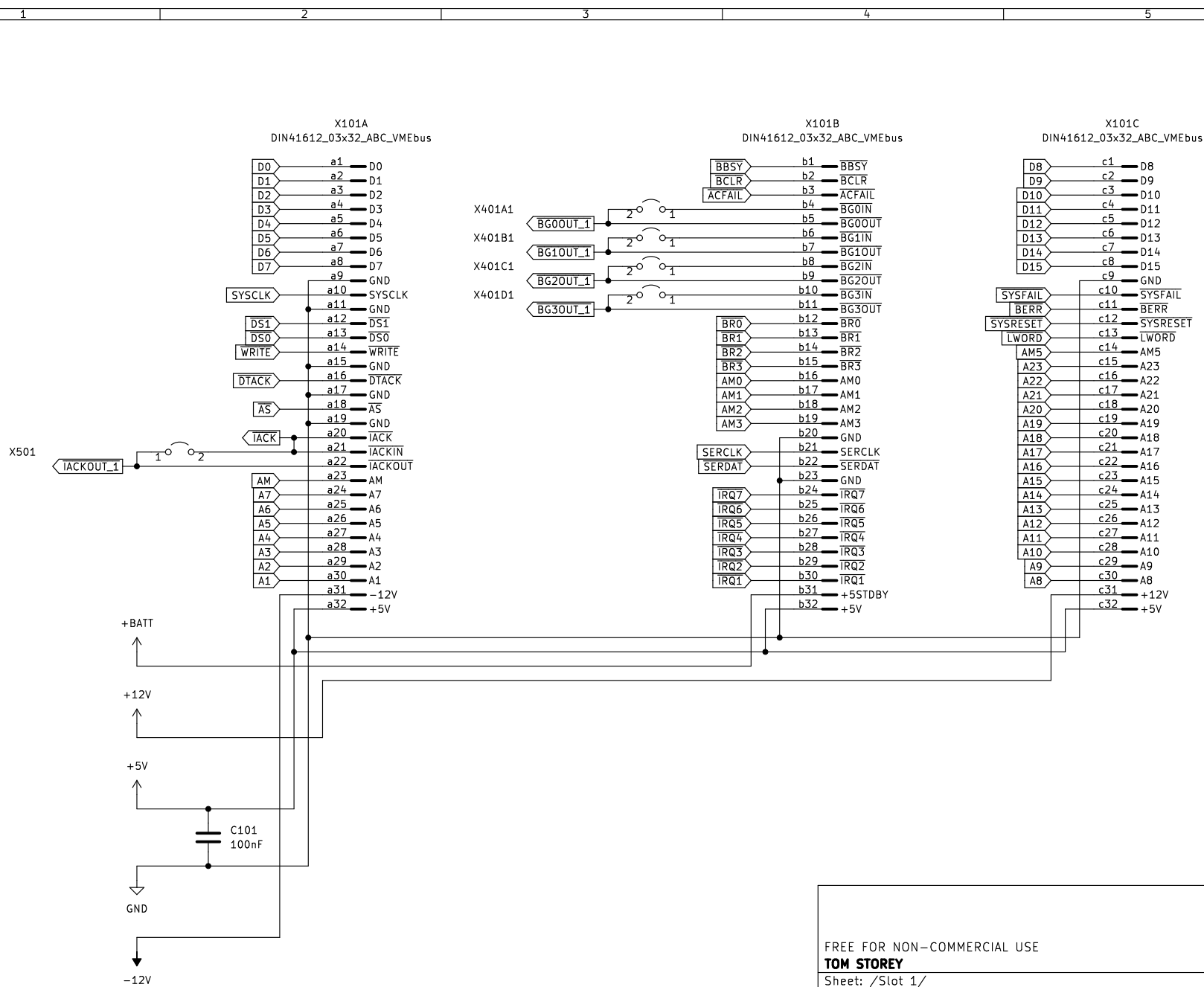
Size: A4 Date: 2024-01-03

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**Rev: 1**

Id: 2/6





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**TOM STOREY**

Sheet: /Slot 1/

File: slot1.kicad\_sch

**Title: SLOT 1**

Size: A4

Date: 2024-01-03

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**Rev: 1**

Id: 4/6

