

--	--

File: expansion_bus.kicad_sch

--

File: buffers_transceivers.kicad_sch

--

File: addr_decode.kicad_sch

--	--

File: cpld.kicad_sch

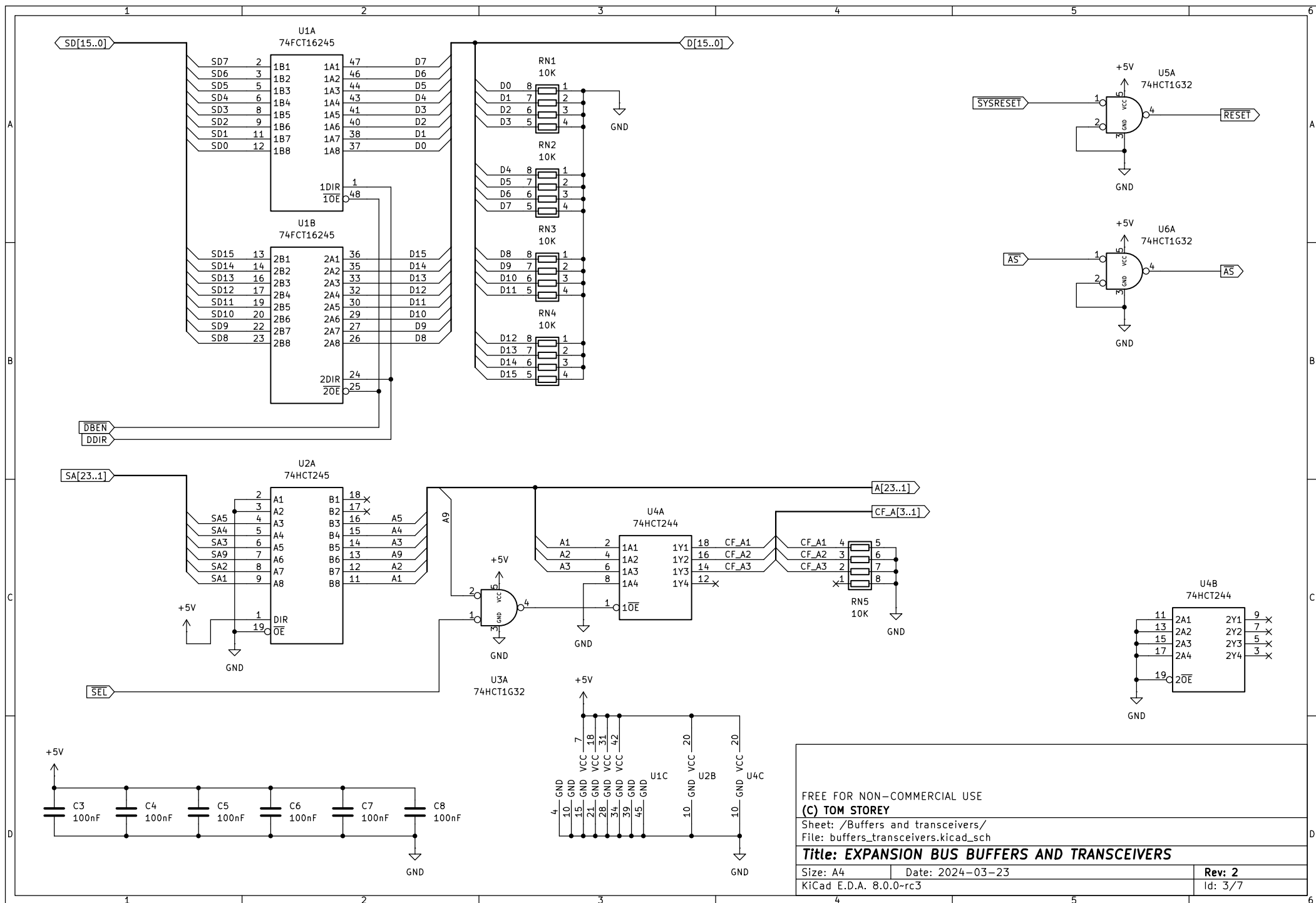
--	--

File: con_stat.kicad_sch

--	--

File: cf_socket.kicad_sch

Id: 1/7



FREE FOR NON-COMMERCIAL USE

(C) TOM STOREY

Sheet: /Buffers and transceivers/
File: buffers_transceivers.kicad_sch

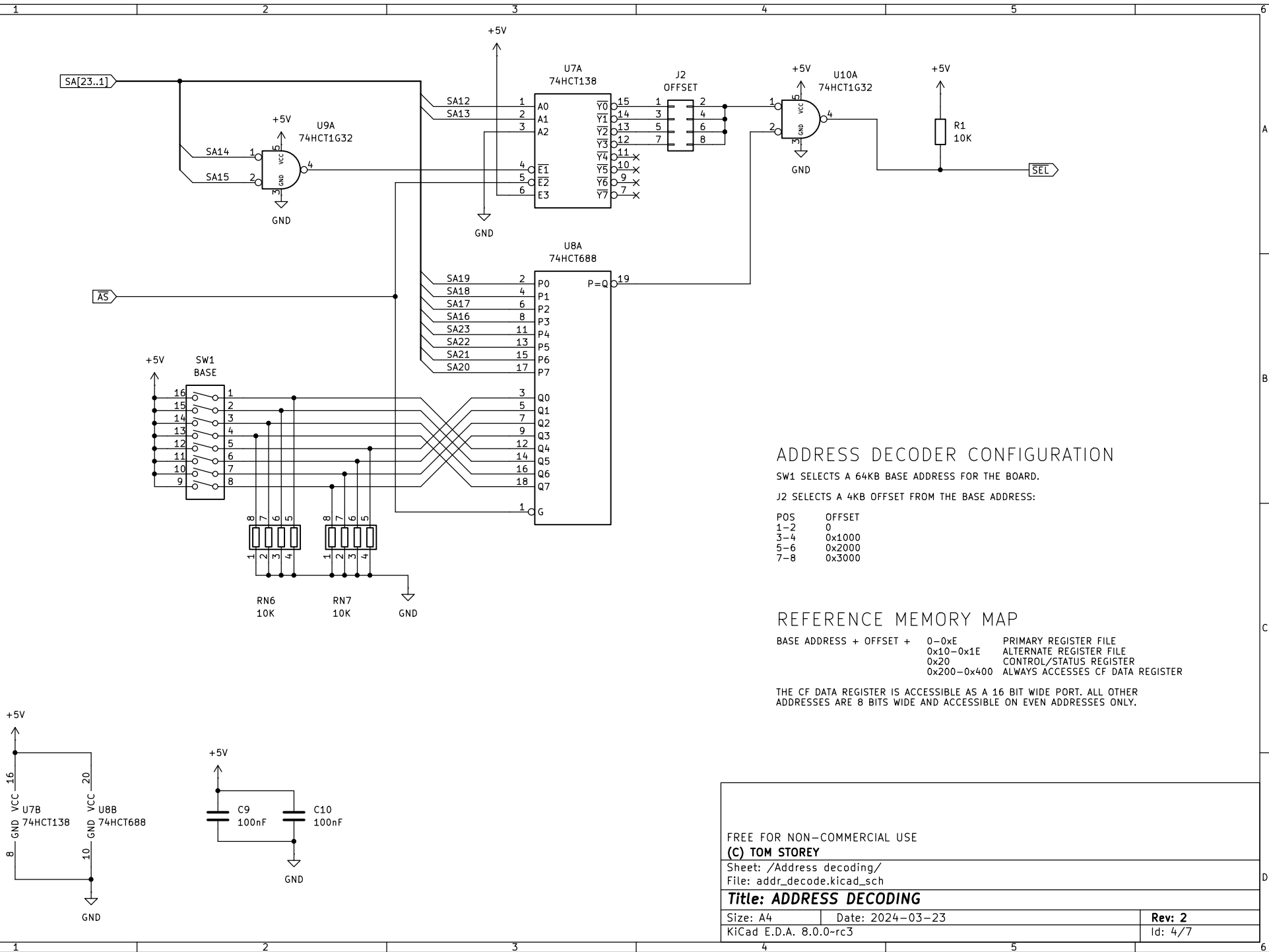
Title: EXPANSION BUS BUFFERS AND TRANSCEIVERS

Size: A4 Date: 2024-03-23

KiCad E.D.A. 8.0.0-rc3

Rev: 2

Id: 3/7



ADDRESS DECODER CONFIGURATION

SW1 SELECTS A 64KB BASE ADDRESS FOR THE BOARD.

J2 SELECTS A 4KB OFFSET FROM THE BASE ADDRESS:

P05	OFFSET
1-2	0
3-4	0x1000
5-6	0x2000
7-8	0x3000

REFERENCE MEMORY MAP

BASE ADDRESS + OFFSET +	0-0xE	PRIMARY REGISTER FILE
	0x10-0x1E	ALTERNATE REGISTER FILE
	0x20	CONTROL/STATUS REGISTER
	0x200-0x400	ALWAYS ACCESSES CF DATA REGISTER

THE CF DATA REGISTER IS ACCESSIBLE AS A 16 BIT WIDE PORT. ALL OTHER ADDRESSES ARE 8 BITS WIDE AND ACCESSIBLE ON EVEN ADDRESSES ONLY.

FREE FOR NON-COMMERCIAL USE

(C) TOM STOREY

Sheet: /Address decoding/

File: addr_decode.kicad_sch

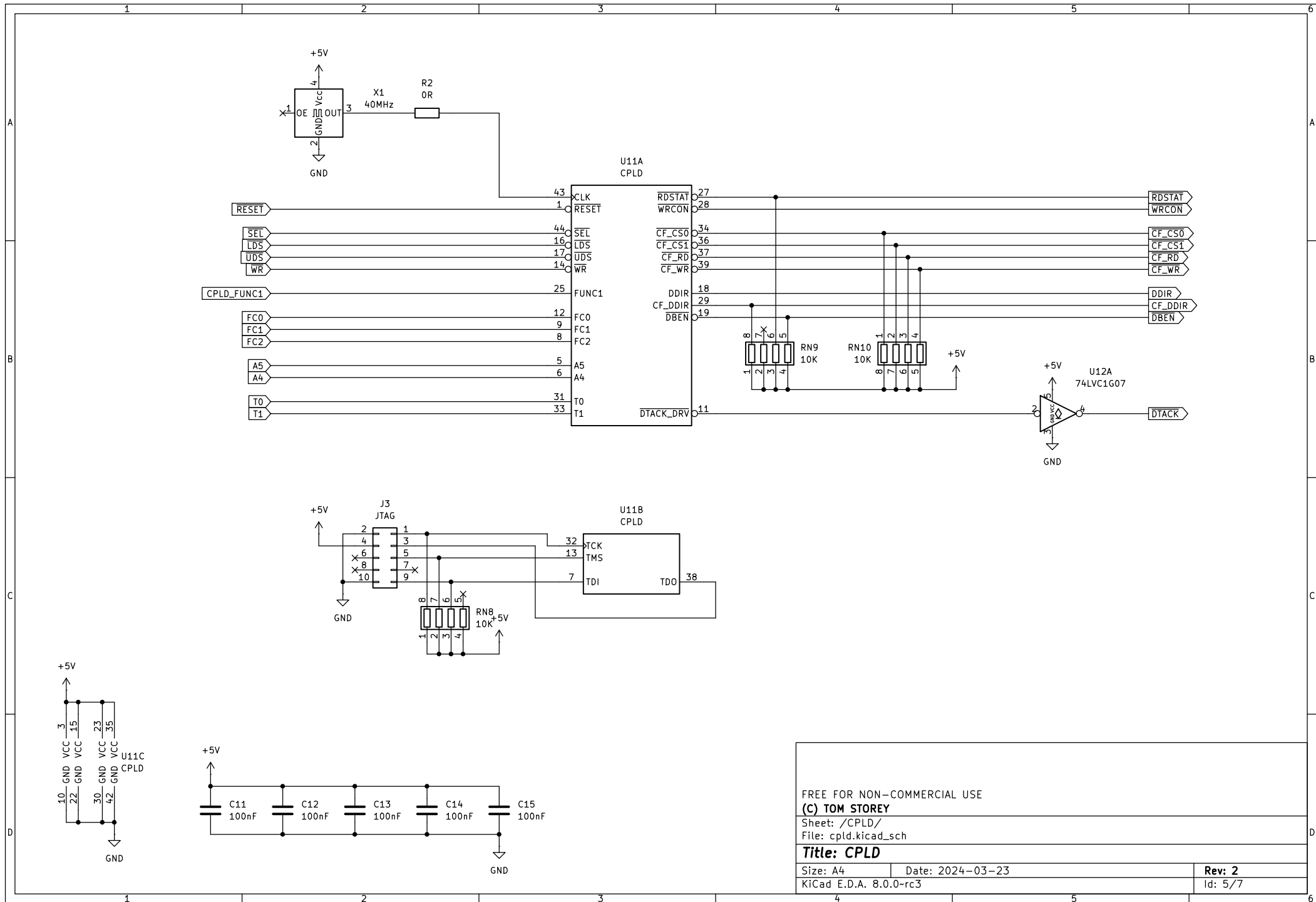
Title: ADDRESS DECODING

Size: A4 Date: 2024-03-23

KiCad E.D.A. 8.0.0-rc3

Rev: 2

Id: 4/7



FREE FOR NON-COMMERCIAL USE

(C) TOM STOREY

Sheet: /CPLD/

File: cpld.kicad_sch

Title: CPLD

Size: A4 Date: 2024-03-23

KiCad E.D.A. 8.0.0-rc3

Rev: 2

Id: 5/7

