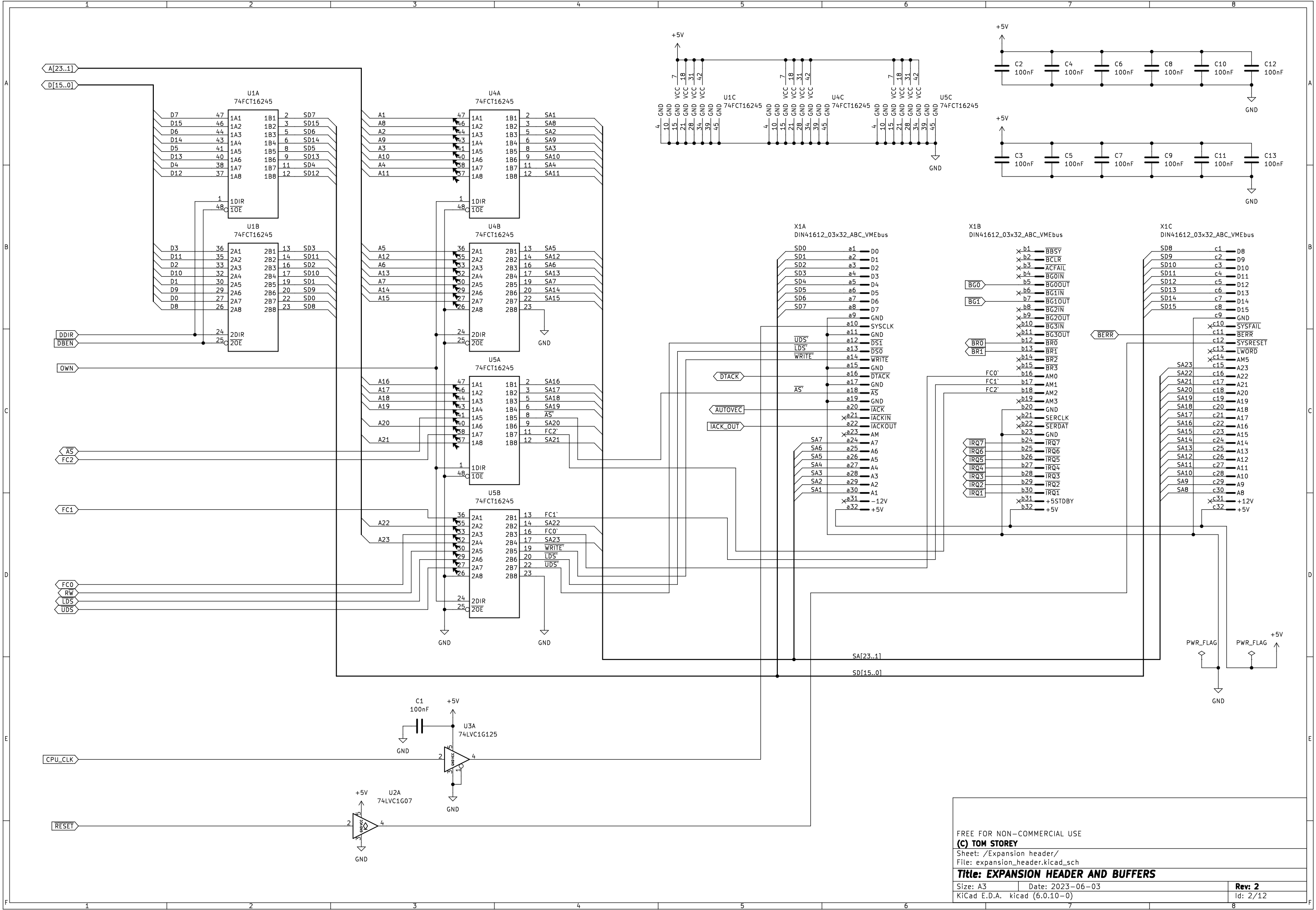


	1	2	3	4	5	6	7	8
A	<div>Expansion header</div> <div>File: expansion_header.kicad_sch</div>	<div>CPU</div> <div>File: cpu.kicad_sch</div>	<div>Memory</div> <div>File: memory.kicad_sch</div>	<div>Ethernet</div> <div>File: ethernet.kicad_sch</div>	<div>UART</div> <div>File: uart.kicad_sch</div>	<div>Timer &amp; RTC</div> <div>File: timer_rtc.kicad_sch</div>		
B	<div>Reset</div> <div>File: reset.kicad_sch</div>	<div>CPLD</div> <div>File: cpld.kicad_sch</div>	<div>Onboard IO</div> <div>File: onboard_io.kicad_sch</div>	<div>X busses</div> <div>File: xbusses.kicad_sch</div>	<div>ROM</div> <div>File: rom.kicad_sch</div>			
C								
D								
E								
F								<div>FREE FOR NON-COMMERCIAL USE</div> <div>(C) TOM STOREY</div> <div>Sheet: /</div> <div>File: COMET68k.kicad_sch</div> <div>Title: <b>COMET68k</b></div> <div>Size: A3Date: 2023-06-03KiCad E.D.A. kicad (6.0.10-0)</div> <div>Rev: 2Id: 1/12</div>
	1	2	3	4	5	6	7	8



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(C) TOM STOREY

Sheet: /Expansion header/

File: expansion\_header.kicad\_sch

Title: EXPANSION HEADER AND BUFFERS

Size: A3

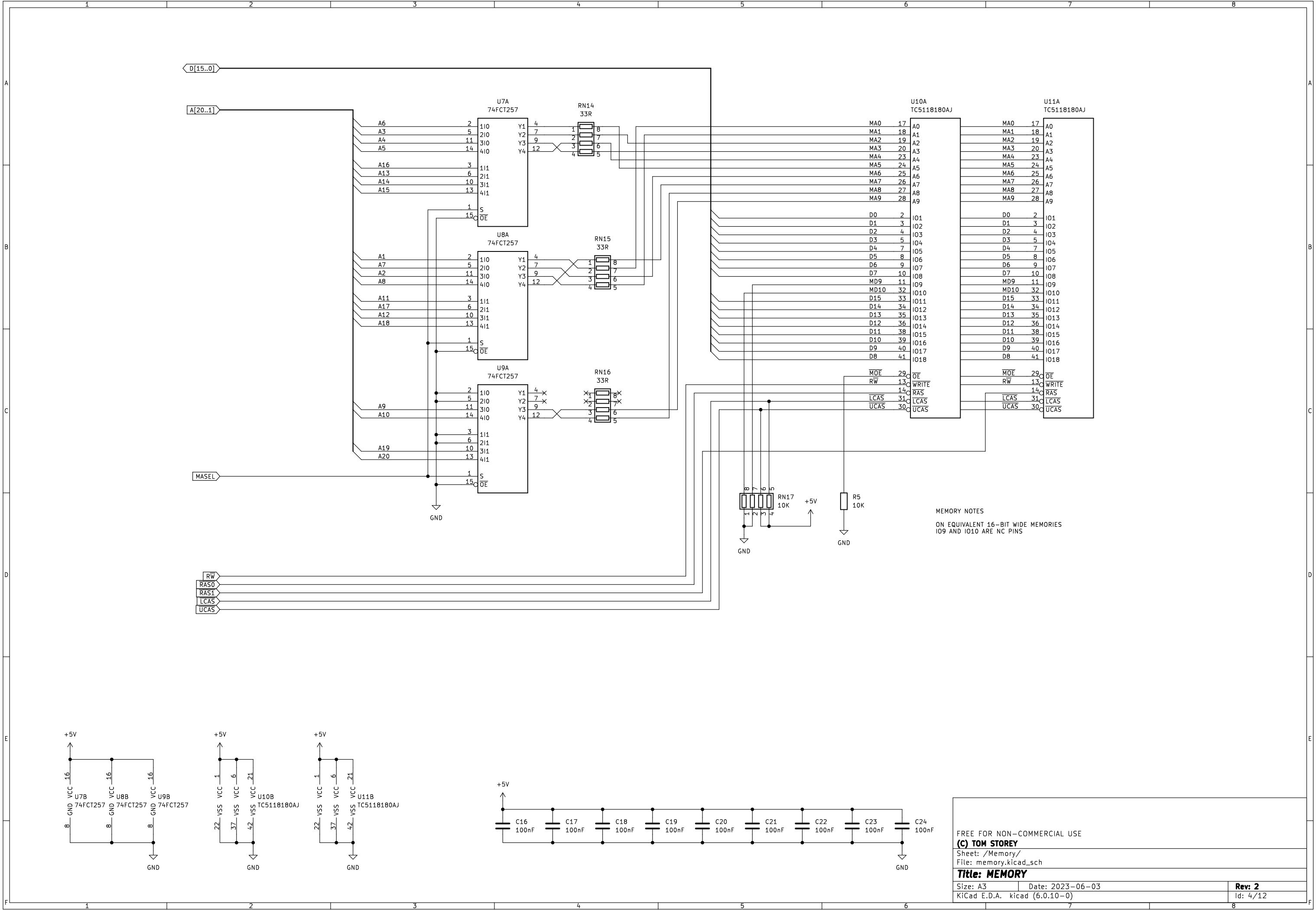
Date: 2023-06-03

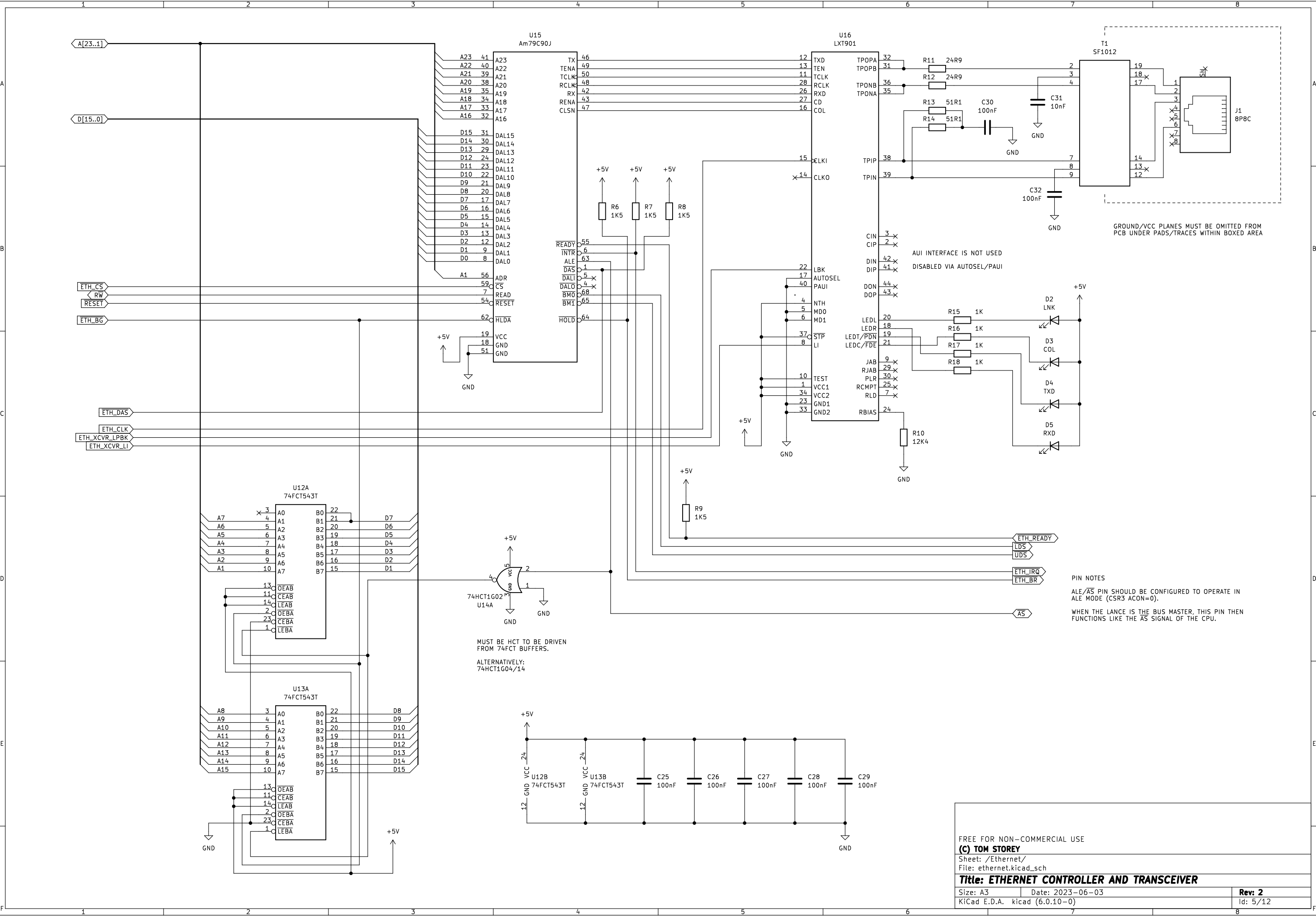
Rev: 2

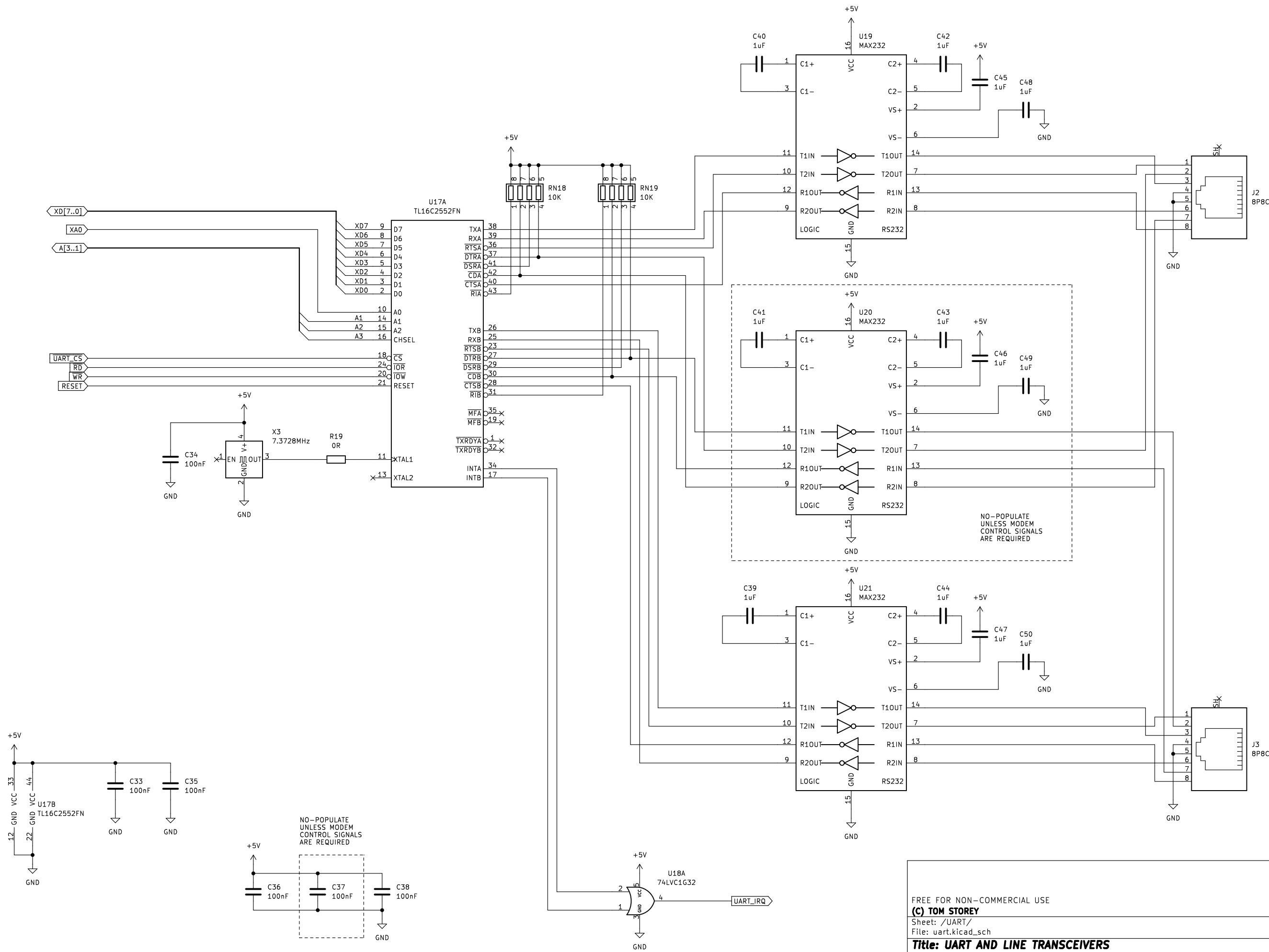
KiCad E.D.A. kicad (6.0.10-0)

Id: 2/12

KiCad E.D.A. kicad (6.0.10-0)	Id: 3/12
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(C) TOM STOREY

Sheet: /UART/

File: uart.kicad\_sch

**Title: UART AND LINE TRANSCEIVERS**

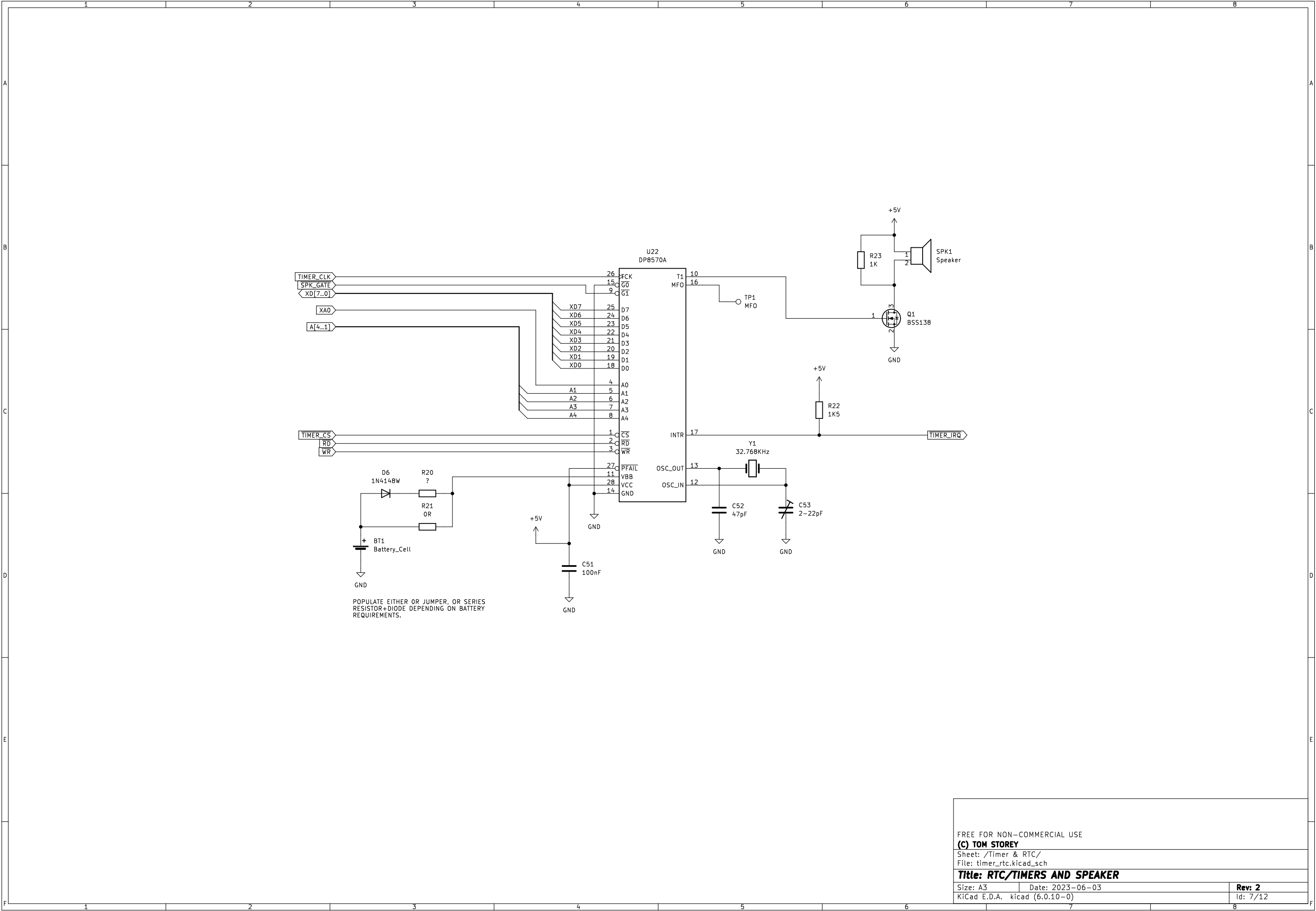
Size: A3

Date: 2023-06-03

Rev: 2

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Id: 6/12



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(C) TOM STOREY

Sheet: /Timer & RTC/

File: timer\_rtc.kicad\_sch

Title: **RTC/TIMERS AND SPEAKER**

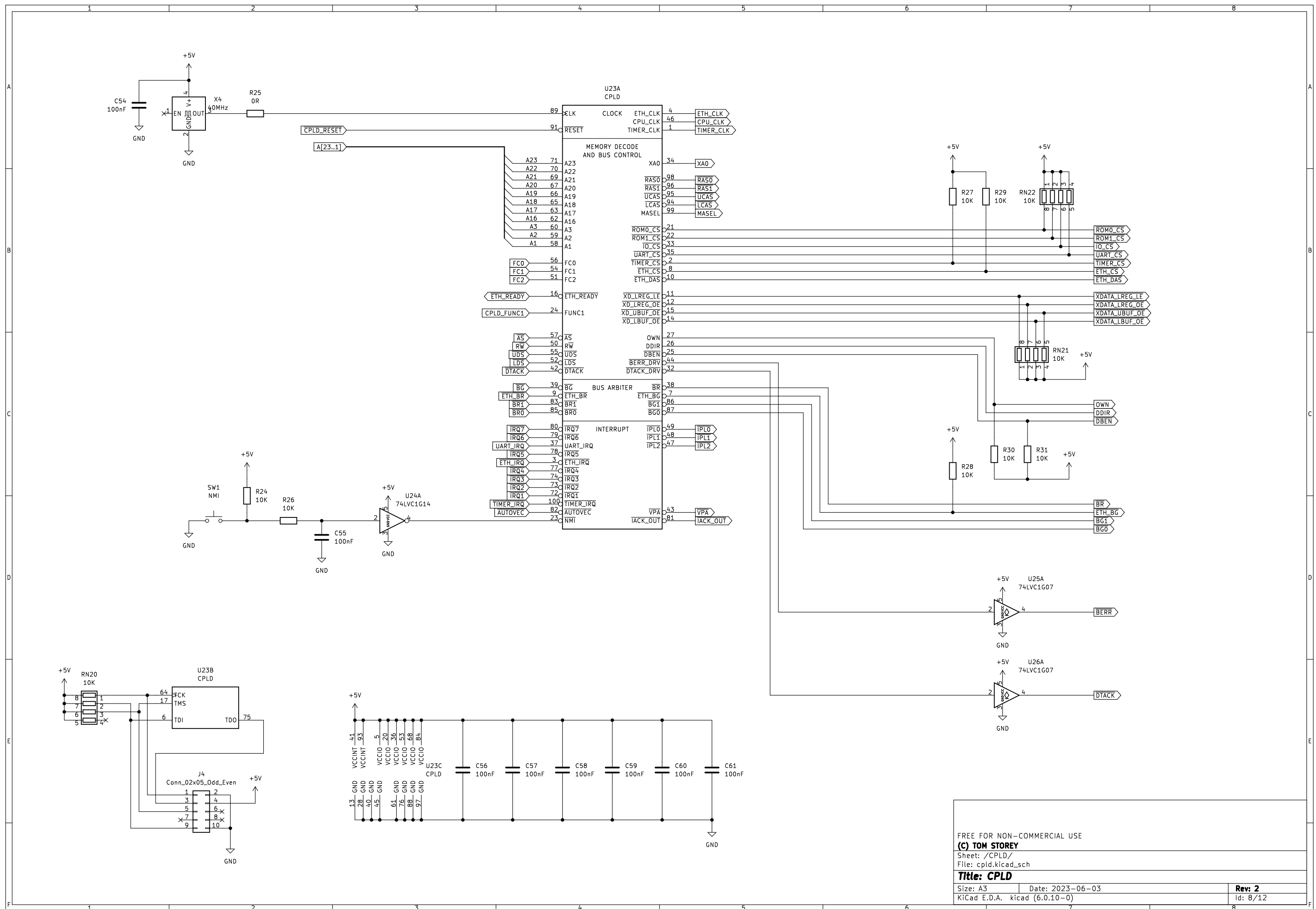
Size: A3

Date: 2023-06-03

Rev: 2

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Id: 7/12





## IO DECODE SUMMARY

OFFSET	WRITE	READ	FUNCTION
0	X	X	READ/WRITE CONTROL REGISTER 1
1	X	X	READ/WRITE CONTROL/STATUS REGISTER 2
2		X	WATCHDOG TIMER RESET
3		X	SOFTWARE RESET (IF ENABLED VIA CSR 2)

## CONTROL REGISTER 1

RW-x	RW-x	RW-x	RW-x	RW-x	RW-x	RW-x	RW-x
CPLD_FUNC1	SPK_GATE	ETH_LI	ETH_LPBK	LED_D	LED_C	LED_B	LED_A
BIT 7							BIT 0

BIT 7	CPLD_FUNC1: CPLD FUNCTION 1 0 = LOGIC LOW TO CPLD PIN 1 = LOGIC HIGH TO CPLD PIN	BIT 3	LED_D: LED D CONTROL 1 = LED OFF 0 = LED ON
BIT 6	SPK_GATE: SPEAKER GATE 1 = SPEAKER TIMER IS GATED 0 = SPEAKER TIMER IS RUNNING	BIT 2	LED_C: LED C CONTROL 1 = LED OFF 0 = LED ON
BIT 5	ETH_LI: ETHERNET LINK INTEGRITY TEST 1 = LINK INTEGRITY TEST ENABLED 0 = LINK INTEGRITY TEST DISABLED	BIT 1	LED_B: LED B CONTROL 1 = LED OFF 0 = LED ON
BIT 4	ETH_LPBK: ETHERNET LOOPBACK 1 = LOOPBACK ENABLED 0 = LOOPBACK DISABLED	BIT 0	LED_A: LED A CONTROL 1 = LED OFF 0 = LED ON

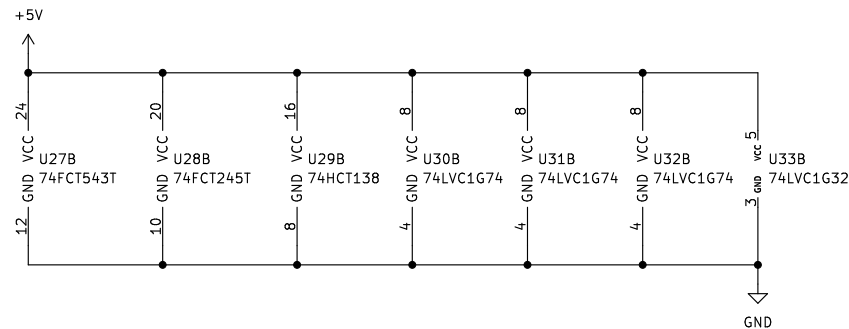
## CONTROL/STATUS REGISTER 2

R-1	R-0	RW-0	RW-0	R-x	R-x	R-x	R-x
POR	WDTO	SOFT_RST_EN	WDT_EN	CONFIG3	CONFIG2	CONFIG1	CONFIG0
BIT 7							BIT 0

BIT 7	POR: POWER ON RESET FLAG (1)(3) 1 = POWER ON RESET OCCURRED 0 = NORMAL RESET	BIT 4	WDT_EN: WATCHDOG TIMER ENABLE (4) 1 = WATCHDOG IS ENABLED 0 = WATCHDOG IS DISABLED
BIT 6	WDTO: WATCHDOG TIMEOUT FLAG (2)(3) 1 = WATCHDOG TIMEOUT CAUSED RESTART 0 = NORMAL RESET	BIT 3-0	CONFIG3..0: CONFIGURATION JUMPERS 1 = OPEN, JUMPER NOT INSTALLED 0 = CLOSED, JUMPER INSTALLED
BIT 5	SOFT_RST_EN: SOFTWARE RESET ENABLE (4) 1 = SOFTWARE RESET MAY BE INITIATED 0 = SOFTWARE RESET IS INHIBITED		

NOTE 1: BIT IS SET DURING POWER UP, OR BROWNOUT IF VOLTAGE DROPS TO 4V OR LESS.  
NOTE 2: BIT IS SET IN THE EVENT OF A WATCHDOG TIMEOUT. BIT IS CLEARED BY POR OR BROWNOUT.  
NOTE 3: BIT IS CLEARED AFTER CONTROL/STATUS REGISTER 2 IS READ.  
NOTE 4: BIT IS CLEARED FOLLOWING ANY RESET CAUSE.

LEGEND:  
R = READABLE BIT      W = WRITABLE BIT      U = UNIMPLEMENTED BIT  
-n = VALUE AT POR      1 = BIT IS SET      0 = BIT IS CLEARED      x = BIT IS UNKNOWN



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Sheet: /Onboard IO/

File: onboard\_io.kicad\_sch

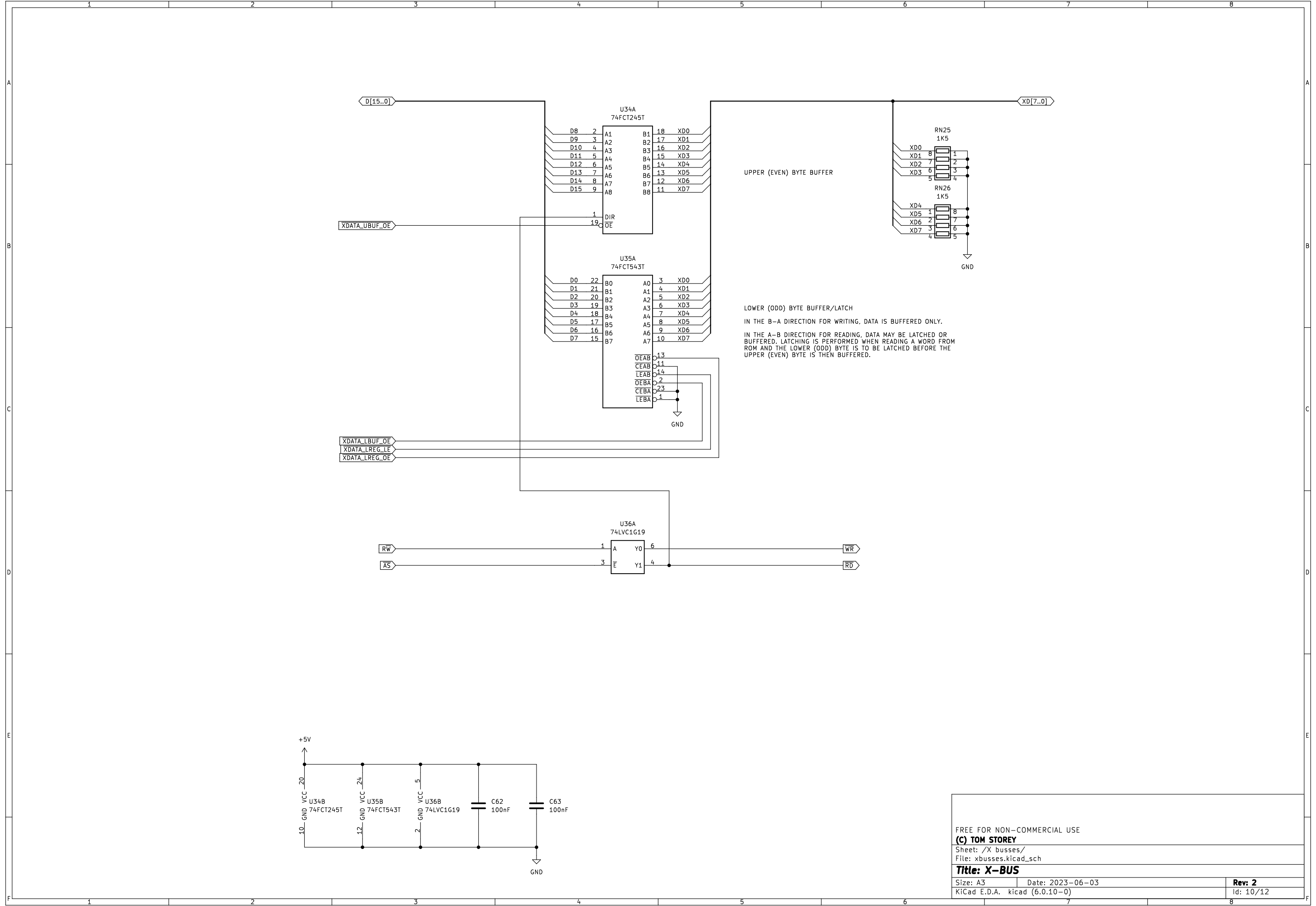
Title: ON-BOARD I/O PORTS

Size: A3      Date: 2023-06-03

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Rev: 2

Id: 9/12



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(C) TOM STOREY

Sheet: /X busses/

File: xbusses.kicad\_sch

**Title: X-BUS**

Size: A3

Date: 2023-06-03

Rev: 2

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Id: 10/12

