|   |                                      | 2  | 3  | 4   |   | 5                    | 6      |
|---|--------------------------------------|--|--|---|---|----------------------|--------|
| A | Expansion bus connecto               |  | Address decoding  Sch File: Sheetfile: addr_decode.kicad_sch | GAL File: gal.kicad_sch   | Interrupt logic                         | ad_sch               | A      |
| В | Registers  File: registers.kicad_sch | Printer port  File: printer_port.kicad_sch |  |   |   |                      | E      |
| c |                                      |  |  |   |   |                      | C      |
|   |                                      |  |  |   |   |                      |        |
| D |                                      |  | 7  | (C) TOM<br>Sheet: ,<br>File: Pr<br>Title:<br>Size: A <sup>f</sup> | inter_Interface.kicad_<br>PARALLEL PRIN | sch<br>TER INTERFACE | Rev: 1 |













