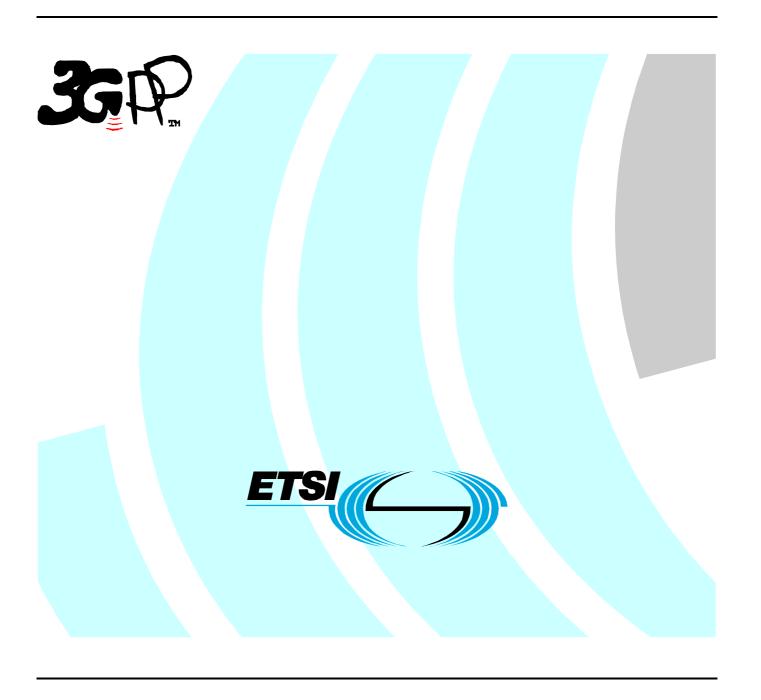
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Contents

Intelle	ectual Property Rights	2
Forew	vord	2
Forew	vord	5
1	Scope	6
2	References	6
3	Symbols and abbreviations	6
3.1	Symbols	
3.2	Abbreviations	
4	General	7
5	Data modulation for the 3.84 Mcps and 7.68Mcps options	8
5.1	Symbol rate	8
5.2	Mapping of bits onto signal point constellation	
5.2.1	Mapping for burst type 1 and 2	
5.2.1.1	11 0 11	
5.2.1.2		
5.2.2	Mapping for burst type 3	
5.2.3	Mapping for 3.84 Mcps MBSFN IMB	
5.2.3.1	•	
5.2.3.2		
5 A	Data madulation for the 1.20 Mana anti-	11
5A	Data modulation for the 1.28 Mcps option	
5A.1	Symbol rate	
5A.2	Mapping of bits onto signal point constellation	
5A.2.1		
5A.2.2		
5A.2.3 5A.2.4	· ·	
JA.2.4	•	
6	Spreading modulation	
6.1	Basic spreading parameters	
6.2	Channelisation codes	
6.3	Channelisation Code Specific Multiplier	
6.4	Scrambling codes for the 3.84Mcps and 1.28Mcps options	17
6.4a	Scrambling codes for the 7.68Mcps option	
6.4a.1	Generation of binary scrambling codes	
6.5	Spread signal of data symbols and data blocks	
6.6	Modulation for the 3.84Mcps and 7.68Mcps options	
6.6.1	Combination of physical channels in uplink	
6.6.1a	·	
6.6.2	Combination of physical channels in downlink	
6.6.3	Combination of signature sequences for E-HICH	
6.7	Modulation for the 1.28 Mcps option	
6.7.1	Combination of physical channels in uplink	
6.7.1a		
6.7.2	Combination of physical channels in downlink	
6.7.3	Combination of signature sequences for Scheduled E-HICH	
6.7.3a		
6.8	Spreading modulation for the 3.84 Mcps MBSFN IMB option	
6.8.1	Spreading	
6.8.2	Code generation and allocation	
6.8.2.1		
6.8.2.2		
6.8.3	Modulation	27

7	Synchronisation cod	les for the 3.84 Mcps option	28	
7.1				
7.2	Code Allocation	28		
7.2.1	Code allocation	n for Case 1	30	
7.2.2	Code allocation	n for Case 2	31	
7.3		hronisation codes		
7.4		odes for 3.84 Mcps MBSFN IMB		
7.4.1		n		
7.4.2	Code allocation	n of SSC	33	
7A	Synchronisation cod	les for the 7.68 Mcps option	33	
7A.1				
7A.2	Code Allocation		34	
7A.2.1	Code allocation	n for Case 1	36	
7A.2.2	Code allocation	n for Case 2	37	
7A.3	Evaluation of sync	hronisation codes	38	
8	Synchronisation cod	les for the 1.28 Mcps option	39	
8.1				
8.1.1		the SYNC-DL		
8.2		nannel (UpPCH)		
8.3				
8.3Aa				
9	Cell synchronisation	codes	41	
Anne	x A (normative):	Scrambling Codes	43	
Anne	x AA (normative):	Synchronisation sequence	46	
AA.1		equence		
AA.2		Codes		
Anne	x B (informative):	Generalised Hierarchical Golay Sequences	53	
		on		
	x C (informative):	Change history		
	· · · · · · · · · · · · · · · · · · ·	Change motory		
Histor	v		55	

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1 Scope

The present document describes spreading and modulation for UTRA Physical Layer TDD mode.

2 References

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication, edition number, version number, etc.) or non-specific.
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- 3GPP TS 25.201: "Physical layer general description". [1] 3GPP TS 25.211: "Physical channels and mapping of transport channels onto physical channels [2] (FDD)". 3GPP TS 25.212: "Multiplexing and channel coding (FDD)". [3] 3GPP TS 25.213: "Spreading and modulation (FDD)". [4] 3GPP TS 25.214: "Physical layer procedures (FDD)". [5] 3GPP TS 25.215: "Physical layer – Measurements (FDD)". [6] 3GPP TS 25.221: "Physical channels and mapping of transport channels onto physical channels [7] (TDD)". 3GPP TS 25.222: "Multiplexing and channel coding (TDD)". [8] [9] 3GPP TS 25.102: "UTRA (UE) TDD; Radio Transmission and Reception". 3GPP TS 25.105: "UTRA (BS) TDD; Radio Transmission and Reception". [10] 3GPP TS25.308: "High Speed Downlink Packet Access (HSDPA); Overall description; Stage 2". [11] 3GPP TS25.224: 'Physical Layer Procedures (TDD)' [12]

3 Symbols and abbreviations

3.1 Symbols

[13]

For the purposes of the present document, the following symbols apply:

 C_p : PSC

C_i: i:th secondary SCH code

 $C_{CSC,m}^{(k)}$: CSC derived as k:th offset version from m:th applicable constituent Golay complementary pair

3GPP TS25.321: 'Medium Access Control (MAC) protocol specification'

3.2 Abbreviations

For the purposes of the present document, the following abbreviations apply:

16QAM
CCTrCH
Coded Composite Transport Channel
CDMA
Code Division Multiple Access
CSC
Cell Synchronisation Code
DPCH
Dedicated Physical Channel
FDD
Frequency Division Duplex

HS-PDSCH High Speed Physical Downlink Shared Channel

IMB Integrated Mobile Broadcast

MBSFN MBSM over a Single Frequency Network

MIB Master Information Block

MU-MIMO Multi-User Multiple Input Multiple Output
OVSF Orthogonal Variable Spreading Factor
P-CCPCH Primary Common Control Physical Channel

PN Pseudo Noise

PRACH Physical Random Access Channel Primary Synchronisation Code **PSC QPSK** Quadrature Phase Shift Keying **RACH** Random Access Channel Synchronisation Channel SCH **Spreading Factor** SF SFN System Frame Number Time Division Duplex TDD

TFC Transport Format Combination

UE User Equipment

UL Uplink

4 General

In the following, a separation between the data modulation and the spreading modulation has been made. The data modulation for 3.84Mcps TDD (including 3.84 Mcps MBSFN IMB) and 7.68Mcps TDD is defined in clause 5 'Data modulation for the 3.84 Mcps and 7.68Mcps options', the data modulation for 1.28Mcps TDD is defined in clause 5A 'Data modulation for the 1.28 Mcps option' and the spreading modulation in clause 6 'Spreading modulation'.

Table 1 shows the basic modulation parameters for the 7.68Mcps, 3.84Mcps (including 3.84 Mcps MBSFN IMB) and 1.28Mcps TDD options.

Table 1: Basic modulation parameters

Chip rate	7.68 Mchip/s	same as FDD basic chiprate:	Low chiprate:
		3.84 Mchip/s and 3.84 Mcps	1.28 Mchip/s
		MBSFN IMB	
Data modulation	QPSK,16QAM (HS-	QPSK,16QAM (HS-PDSCH,	QPSK, 8PSK,16QAM (HS-
	PDSCH, MBSFN S-	MBSFN S-CCPCH and E-	PDSCH, E-PUCH, MBSFN
	CCPCH and E-PUCH only)	PUCH only)	S-CCPCH only)
Spreading characteristics	Orthogonal	Orthogonal	Orthogonal
	Q chips/symbol,	Q chips/symbol,	Q chips/symbol,
	where $Q = 2^p$, $0 \le p \le 5$	where $Q = 2^p$, $0 \le p \le 4$	where $Q = 2^p$, $0 \le p \le 4$
		(For 3.84 Mcps MBSFN IMB	
		$Q = 2^p$, where $p = 4$ or 8 only)	

Data modulation for the 3.84 Mcps and 7.68Mcps options

5.1 Symbol rate

The symbol duration T_S depends on the spreading factor Q and the chip duration T_C : $T_s = Q \times T_c$, where $T_c = \frac{1}{\text{chiprate}}$.

5.2 Mapping of bits onto signal point constellation

5.2.1 Mapping for burst type 1 and 2

5.2.1.1 QPSK modulation

The data modulation is performed to the bits from the output of the physical channel mapping procedure in [8] and combines always 2 consecutive binary bits to a complex valued data symbol. Each user burst has two data carrying parts, termed data blocks:

$$\underline{\mathbf{d}}^{(k,i)} = \left(\underline{d}_1^{(k,i)}, \underline{d}_2^{(k,i)}, \dots, \underline{d}_{N_k}^{(k,i)}\right)^T, \quad i = 1, 2; k = 1, \dots, K_{Code}$$

$$\tag{1}$$

 K_{Code} is the number of used codes in a time slot: for 3.84Mcps, max K_{Code} =16; for 7.68Mcps, max K_{Code} =32. N_k is the number of symbols per data field for the code k. This number is linked to the spreading factor Q_k [7].

Data block $\underline{\mathbf{d}}^{(k,1)}$ is transmitted before the midamble and data block $\underline{\mathbf{d}}^{(k,2)}$ after the midamble. Each of the N_k data symbols $\underline{d}_n^{(k,i)}$; i=1, 2; k=1,..., K_{Code} ; n=1,..., N_k ; of equation 1 has the symbol duration $T_s^{(k)} = Q_k T_c$ as already given.

The data modulation is QPSK, thus the data symbols $\underline{d}_n^{(k,i)}$ are generated from two consecutive data bits from the output of the physical channel mapping procedure in [8]:

$$b_{l,n}^{(k,i)} \in \left\{0,1\right\}, \quad l = 1,2; k = 1,..., K_{Code}; n = 1,..., N_k; i = 1,2 \tag{2}$$

using the following mapping to complex symbols:

consecutive binary bit pattern	complex symbol
$b_{l,n}^{(k,i)} b_{2n}^{(k,i)}$	$\underline{d}_{n}^{(k,i)}$
00	+j
01	+1
10	-1
11	-j

The mapping corresponds to a QPSK modulation of the interleaved and encoded data bits $b_{l,n}^{(k,i)}$ of equation 2.

5.2.1.2 16QAM modulation

The data modulation is performed to the bits from the output of the physical channel mapping procedure. In case of 16QAM, modulation 4 consecutive binary bits are represented by one complex valued data symbol. Each user burst has two data carrying parts, termed data blocks:

$$\underline{\mathbf{d}}^{(k,i)} = (\underline{d}_1^{(k,i)}, \underline{d}_2^{(k,i)}, \dots, \underline{d}_{N_k}^{(k,i)})^{\mathrm{T}} \quad i = 1, 2; k = 1, \dots, K.$$
(2b)

 N_k is the number of symbols per data field for the user k. This number is linked to the spreading factor Q_k .

Data block $\frac{\mathbf{d}}{s}^{(k,1)}$ is transmitted before the midamble and data block $\frac{\mathbf{d}}{s}^{(k,2)}$ after the midamble. Each of the N_k data symbols $\frac{d}{s}^{(k,i)}$; $i=1,2; k=1,...,K; n=1,...,N_k$; of equation 2b has the symbol duration $T_s^{(k)} = Q_k T_c$ as already given.

The data modulation is 16QAM, thus the data symbols $\frac{d^{(k,i)}}{n}$ are generated from 4 consecutive data bits from the output of the physical channel mapping procedure in [8]:

$$b_{l,n}^{(k,i)} \in \{0,1\}, \quad l = 1,2,3,4; \ k = 1,..., K_{code}; \ n = 1,...N_k; \ i = 1,2 \tag{2c}$$

using the following mapping to complex symbols:

Consecutive binary bit pattern	complex symbol
$b_{1,n}^{(k,i)} b_{2,n}^{(k,i)} b_{3,n}^{(k,i)} b_{4,n}^{(k,i)}$	$\underline{d}_{n}^{(k,i)}$
0000	$j\frac{1}{\sqrt{5}}$
0001	$-\frac{1}{\sqrt{5}} + j\frac{2}{\sqrt{5}}$
0010	$\frac{1}{\sqrt{5}} + j\frac{2}{\sqrt{5}}$
0011	$j\frac{3}{\sqrt{5}}$
0100	$ \frac{1}{\sqrt{5}} + j \frac{2}{\sqrt{5}} $ $ \frac{1}{\sqrt{5}} + j \frac{2}{\sqrt{5}} $ $ \frac{1}{\sqrt{5}} + j \frac{2}{\sqrt{5}} $ $ j \frac{3}{\sqrt{5}} $ $ \sqrt{\frac{1}{5}} $
0101	$\frac{2}{\sqrt{5}} - j\frac{1}{\sqrt{5}}$ $\frac{2}{\sqrt{5}} + j\frac{1}{\sqrt{5}}$ $\frac{3}{\sqrt{5}}$ $-\frac{1}{\sqrt{5}}$
0110	$\frac{2}{\sqrt{5}} + j\frac{1}{\sqrt{5}}$
0111	$\frac{3}{\sqrt{5}}$
1000	$-\frac{1}{\sqrt{5}}$
1001	$-\frac{2}{\sqrt{5}} + j\frac{1}{\sqrt{5}}$
1010	$-\frac{2}{\sqrt{5}}-j\frac{1}{\sqrt{5}}$
1011	$-\frac{3}{\sqrt{5}}$
1100	$-\frac{2}{\sqrt{5}} + j\frac{1}{\sqrt{5}}$ $-\frac{2}{\sqrt{5}} - j\frac{1}{\sqrt{5}}$ $-\frac{3}{\sqrt{5}}$ $-j\frac{1}{\sqrt{5}}$ $\frac{1}{\sqrt{5}} - j\frac{2}{\sqrt{5}}$ $1 \qquad 2$
1101	$\frac{1}{\sqrt{5}} - j\frac{2}{\sqrt{5}}$
1110	$-\frac{1}{\sqrt{5}} - j\frac{2}{\sqrt{5}}$ $-j\frac{3}{\sqrt{5}}$
1111	$-j\frac{3}{\sqrt{5}}$

The mapping corresponds to a 16QAM modulation of the interleaved and encoded data bits $b_{l,n}^{(k,i)}$ of the table above and $\frac{d^{(k,i)}}{n}$ of equation 2b.

5.2.2 Mapping for burst type 3

In case of burst type 3, the definitions in subclause 5.2.1.1 and subclause 5.2.1.2 apply with a modified number of symbols in the second data block. For the burst type 3, the number of symbols in the second data block $\mathbf{d}^{(k,2)}$ is

decreased by
$$\frac{96}{Q_{\kappa}}$$
 symbols for 3.84Mcps TDD and is decreased by $\frac{192}{Q_{k}}$ symbols for 7.68Mcps TDD.

5.2.3 Mapping for 3.84 Mcps MBSFN IMB

5.2.3.1 Modulation mapping for data

Mapping of data bits onto a QPSK or 16-QAM signal point constellation shall be accomplished as described in subclause 5.1.1.1 or 5.1.1.2 of [4] respectively.

5.2.3.2 Modulation mapping for TFCI

In the case of S-CCPCH frame type 1 and S-CCPCH frame type 2 using QPSK modulation for data, TFCI bits shall be QPSK modulated according to subclause 5.1.1.1 of [4].

In the case of S-CCPCH frame type 2 using 16-QAM modulation for data, each consecutive pair of binary-valued TFCI bits $\{b_{2q},b_{2q+1}\}$, with $q=\{0,1,2,\ldots\}$ shall be mapped according to the rotated QPSK constellation given by the following table.

$\{b_{2q},b_{2q+1}\}$	I branch	Q branch
{0,0}	0.4472	1.3416
{0,1}	1.3416	-0.4472
{1,0}	-1.3416	0.4472
{1,1}	-0.4472	-1.3416

5A Data modulation for the 1.28 Mcps option

5A.1 Symbol rate

The symbol duration T_S depends on the spreading factor Q and the chip duration T_C : $T_s = Q \times T_c$, where $T_c = \frac{1}{chiprate}$.

5A.2 Mapping of bits onto signal point constellation

5A.2.1 QPSK modulation

The mapping of bits onto the signal point constellation for QPSK modulation is the same as in the 3.84Mcps TDD cf. [5.2.1.1 QPSK modulation].

5A.2.2 8PSK modulation

The data modulation is performed to the bits from the output of the physical channel mapping procedure. In case of 8PSK modulation 3 consecutive binary bits are represented by one complex valued data symbol. Each user burst has two data carrying parts, termed data blocks:

$$\underline{\mathbf{d}}^{(k,i)} = \left(\underline{d}_1^{(k,i)}, \underline{d}_2^{(k,i)}, \dots, \underline{d}_{N_k}^{(k,i)}\right)^T, \quad i = 1, 2; k = 1, \dots, K_{Code}$$
(1a)

 N_k is the number of symbols per data field for the code k. This number is linked to the spreading factor Q_k .

Data block $\frac{\mathbf{d}^{(k,1)}}{i}$ is transmitted before the midamble and data block $\frac{\mathbf{d}^{(k,2)}}{i}$ after the midamble. Each of the N_k data symbols $\frac{d^{(k,i)}}{i}$; i=1, 2; k=1,..., K_{Code} ; n=1,..., N_k ; of equation 1 has the symbol duration $T_s^{(k)} = Q_k T_c$ as already given.

The data modulation is 8PSK, thus the data symbols $\frac{d^{(k,i)}}{d^n}$ are generated from 3 consecutive data bits from the output of the physical channel mapping procedure in [8]:

$$b_{l,n}^{(k,i)} \in \{0,1\} \quad l = 1,2,3; \ k = 1,..., K_{Code}; \ n = 1,..., N_k; \ i = 1,2 \tag{2a}$$

using the following mapping to complex symbols:

Consecutive binary bit pattern	complex symbol
$b_{l,n}^{(k,i)} b_{2,n}^{(k,i)} b_{3n}^{(k,i)}$	$\underline{d}_{n}^{(k,i)}$
000	cos(11pi/8)+ jsin(11pi/8)
001	cos(9pi/8)+ jsin(9pi/8)
010	cos(5pi/8)+ jsin(5pi/8)
011	cos(7pi/8)+ jsin(7pi/8)
100	cos(13pi/8)+ jsin(13pi/8)
101	cos(15pi/8)+ jsin(15pi/8)
110	cos(3pi/8)+ jsin(3pi/8)
111	cos(pi/8)+ jsin(pi/8)

The mapping corresponds to a 8PSK modulation of the interleaved and encoded data bits $b_{l,n}^{(k,i)}$ of the table above and $\underline{d}_{n}^{(k,i)}$ of equation 1a.

5A.2.3 16QAM modulation

The mapping of bits onto the signal point constellation for 16QAM modulation is the same as in the 3.84Mcps TDD cf. [5.2.1.2 16QAM modulation].

5A.2.4 64QAM modulation

The data modulation is performed to the bits from the output of the physical channel mapping procedure. In case of 64QAM, modulation 6 consecutive binary bits are represented by one complex valued data symbol. Each user burst has two data carrying parts, termed data blocks:

$$\underline{\mathbf{d}}^{(k,i)} = (\underline{d}_1^{(k,i)}, \underline{d}_2^{(k,i)}, \dots, \underline{d}_{N_k}^{(k,i)})^{\mathrm{T}} \quad i = 1, 2; k = 1, \dots, K.$$
 (1c)

 N_k is the number of symbols per data field for the user k. This number is linked to the spreading factor Q_k .

Data block $\frac{\mathbf{d}^{(k,1)}}{}$ is transmitted before the midamble and data block $\frac{\mathbf{d}^{(k,2)}}{}$ after the midamble. Each of the N_k data symbols $\frac{d^{(k,i)}}{}{}$; i=1, 2; k=1,...,K; n=1,...,N_k; of equation 1c has the symbol duration $T_s^{(k)} = Q_k T_c$ as already given.

The data modulation is 64QAM, thus the data symbols $\frac{d^{(k,i)}}{n}$ are generated from 6 consecutive data bits from the output of the physical channel mapping procedure in [8]:

$$b_{l,n}^{(k,i)} \in \left\{0,1\right\}, \quad l = 1,2,3,4,5,6; \;\; k = 1,...,K_{code}; \;\; n = 1,...N_k; \;\; i = 1,2 \eqno(2c)$$

using the following mapping to complex symbols:

Consecutive binary bit pattern	complex symbol	Consecutive binary bit pattern	complex symbol
$b_{1,n}^{(k,i)} b_{2,n}^{(k,i)} b_{3,n}^{(k,i)} b_{4,n}^{(k,i)} b_{5,n}^{(k,i)} b_{6,n}^{(k,i)}$	$\underline{d}_{n}^{(k,i)}$	$b_{1,n}^{(k,i)} b_{2,n}^{(k,i)} b_{3,n}^{(k,i)} b_{4,n}^{(k,i)} b_{5,n}^{(k,i)} b_{6,n}^{(k,i)}$	$\underline{d}_n^{(k,i)}$
000000	, 3	100000	$-\frac{3}{\sqrt{21}}$
	$j\frac{3}{\sqrt{21}}$		$-\frac{1}{\sqrt{21}}$
000001	$\frac{1}{\sqrt{21}} + j\frac{2}{\sqrt{21}}$	100001	$-\frac{2}{1}$
	$\frac{1}{\sqrt{21}} + j\frac{2}{\sqrt{21}}$		$-\frac{2}{\sqrt{21}} - j\frac{1}{\sqrt{21}}$
000010	$-\frac{1}{\sqrt{21}} + j\frac{2}{\sqrt{21}}$	100010	$-\frac{2}{\sqrt{21}} - j\frac{1}{\sqrt{21}} - \frac{2}{\sqrt{21}} + j\frac{1}{\sqrt{21}}$
000011	$j\frac{1}{\sqrt{21}}$	100011	$-\frac{1}{\sqrt{21}}$
000100		100100	A 1
000101	$-\frac{2}{\sqrt{21}} + j\frac{5}{\sqrt{21}}$	100101	$-\frac{5}{\sqrt{21}} + j\frac{2}{\sqrt{21}}$
000110	$-\frac{1}{\sqrt{21}} + j\frac{4}{\sqrt{21}} - \frac{2}{\sqrt{21}} + j\frac{5}{\sqrt{21}} - \frac{2}{\sqrt{21}} + j\frac{3}{\sqrt{21}} - \frac{3}{\sqrt{21}} + j\frac{4}{\sqrt{21}}$	100110	$-\frac{4}{\sqrt{21}} + j\frac{1}{\sqrt{21}} - \frac{5}{\sqrt{21}} + j\frac{2}{\sqrt{21}} - \frac{3}{\sqrt{21}} + j\frac{2}{\sqrt{21}} - \frac{4}{\sqrt{21}} + j\frac{3}{\sqrt{21}}$
000111	$-\frac{3}{\sqrt{21}} + j\frac{4}{\sqrt{21}}$	100111	$-\frac{4}{\sqrt{21}} + j\frac{3}{\sqrt{21}}$
001000	$\frac{1}{\sqrt{21}} + j \frac{4}{\sqrt{21}}$	101000	$-\frac{4}{\sqrt{21}} - j\frac{1}{\sqrt{21}}$
001001	$ \frac{1}{\sqrt{21}} + j \frac{4}{\sqrt{21}} $ $ \frac{2}{\sqrt{21}} + j \frac{3}{\sqrt{21}} $ $ \frac{2}{\sqrt{21}} + j \frac{5}{\sqrt{21}} $ $ \frac{3}{\sqrt{21}} + j \frac{4}{\sqrt{21}} $	101001	3 . 2
001010	$\frac{2}{\sqrt{21}} + j \frac{5}{\sqrt{21}}$	101010	$-\frac{\sqrt{21}}{\sqrt{21}} - j\frac{\sqrt{21}}{\sqrt{21}}$ $-\frac{5}{\sqrt{21}} - j\frac{2}{\sqrt{21}}$
001011	$\frac{3}{\sqrt{21}} + j\frac{4}{\sqrt{21}}$	101011	$-\frac{4}{\sqrt{21}} - j\frac{3}{\sqrt{21}}$
001100	$j\frac{3}{\sqrt{2}}$	101100	$-\frac{5}{\sqrt{21}}$
001101	$-\frac{1}{\sqrt{21}} + j\frac{6}{\sqrt{21}}$	101101	$-\frac{6}{\sqrt{21}} + j\frac{1}{\sqrt{21}}$
001110	$\frac{1}{\sqrt{21}} + j \frac{6}{\sqrt{21}}$	101110	$-\frac{6}{\sqrt{21}}-j\frac{1}{\sqrt{21}}$
001111	$j\frac{7}{\sqrt{21}}$	101111	7
010000	$ \frac{\sqrt{21}}{-\frac{1}{\sqrt{21}} + j \frac{6}{\sqrt{21}}} $ $ \frac{1}{\sqrt{21}} + j \frac{6}{\sqrt{21}} $ $ j \frac{7}{\sqrt{21}} $ $ \frac{3}{\sqrt{21}} $	110000	$-\frac{7}{\sqrt{21}}$ $-j\frac{3}{\sqrt{21}}$
010001	$\frac{2}{\sqrt{21}} + j \frac{1}{\sqrt{21}}$	110001	$-\frac{1}{\sqrt{21}} - j\frac{2}{\sqrt{21}}$
010010	$\frac{2}{\sqrt{21}} + j\frac{1}{\sqrt{21}}$ $\frac{2}{\sqrt{21}} - j\frac{1}{\sqrt{21}}$	110010	$-\frac{1}{\sqrt{21}} - j\frac{2}{\sqrt{21}} - j\frac{2}{\sqrt{21}} - j\frac{2}{\sqrt{21}}$

040044		440044	
010011	$\frac{1}{\sqrt{21}}$	110011	$-j\frac{1}{\sqrt{21}}$
010100	1 1	110100	1 /
	$\frac{4}{\sqrt{21}} - j\frac{1}{\sqrt{21}}$		$\int \frac{1}{\sqrt{21}} - j \frac{4}{\sqrt{21}}$
010101	$\frac{4}{\sqrt{21}} - j\frac{1}{\sqrt{21}}$ $\frac{5}{\sqrt{21}} - j\frac{2}{\sqrt{21}}$ $\frac{3}{\sqrt{21}} - j\frac{2}{\sqrt{21}}$ $\frac{4}{\sqrt{21}} - j\frac{3}{\sqrt{21}}$	110101	$\frac{1}{\sqrt{21}} - j \frac{4}{\sqrt{21}}$ $\frac{2}{\sqrt{21}} - j \frac{5}{\sqrt{21}}$ $\frac{2}{\sqrt{21}} - j \frac{3}{\sqrt{21}}$ $\frac{3}{\sqrt{21}} - j \frac{4}{\sqrt{21}}$
010110	$\frac{3}{\sqrt{21}} - j\frac{2}{\sqrt{21}}$	110110	$\frac{2}{\sqrt{21}} - j\frac{3}{\sqrt{21}}$
010111	$\frac{4}{\sqrt{21}} - j\frac{3}{\sqrt{21}}$	110111	$\frac{3}{\sqrt{21}} - j\frac{4}{\sqrt{21}}$
011000	$\frac{4}{\sqrt{21}} + j\frac{1}{\sqrt{21}}$ $\frac{3}{\sqrt{21}} + j\frac{2}{\sqrt{21}}$ $\frac{5}{\sqrt{21}} + j\frac{2}{\sqrt{21}}$ $\frac{4}{\sqrt{21}} + j\frac{3}{\sqrt{21}}$	111000	$-\frac{1}{\sqrt{21}}-j\frac{4}{\sqrt{21}}$
011001	$\frac{3}{\sqrt{21}} + j\frac{2}{\sqrt{21}}$	111001	$-\frac{2}{\sqrt{21}} - j\frac{3}{\sqrt{21}} - \frac{2}{\sqrt{21}} - j\frac{5}{\sqrt{21}} - \frac{3}{\sqrt{21}} - j\frac{4}{\sqrt{21}}$
011010	$\frac{5}{\sqrt{21}} + j\frac{2}{\sqrt{21}}$	111010	$-\frac{2}{\sqrt{21}} - j\frac{5}{\sqrt{21}}$
011011	$\frac{4}{\sqrt{21}} + j\frac{3}{\sqrt{21}}$	111011	$-\frac{3}{\sqrt{21}} - j\frac{4}{\sqrt{21}}$
011100	$\frac{5}{\sqrt{21}}$	111100	$-j\frac{5}{\sqrt{21}}$
011101	$\frac{6}{\sqrt{21}} - j \frac{1}{\sqrt{21}}$	111101	$\sqrt{21}$ $J\sqrt{21}$
011110	$\frac{6}{\sqrt{21}} + j\frac{1}{\sqrt{21}}$	111110	$-\frac{1}{\sqrt{21}} - j\frac{6}{\sqrt{21}}$
011111	$\frac{6}{\sqrt{21}} + j\frac{1}{\sqrt{21}}$ $\frac{7}{\sqrt{21}}$	111111	$-j\frac{7}{\sqrt{21}}$

The mapping corresponds to a 64QAM modulation of the interleaved and encoded data bits $b_{l,n}^{(k,i)}$ of the table above and $\frac{d^{(k,i)}}{d^n}$ of equation 2c.

6 Spreading modulation

Sub-clauses 6.1 to 6.7 do not apply to 3.84 Mcps MBSFN IMB. Spreading modulation for 3.84 Mcps MBSFN IMB is described in clause 6.8.

6.1 Basic spreading parameters

Spreading of data consists of two operations: Channelisation and Scrambling. Firstly, each complex valued data symbol $\underline{d}_n^{(k,i)}$ of equation 1 (or $e_n^{(k,i)}$ of equation 8 in the case of E-HICH) is spread with a real valued channelisation code $\mathbf{c}^{(k)}$ of length Q_k : for 3.84Mcps TDD and 1.28Mcps TDD, $Q_k \in \{1,2,4,8,16\}$; for 7.68Mcps TDD,

 $Q_k \in \{1,2,4,8,16,32\}$. The resulting sequence is then scrambled by a complex sequence $\underline{\mathbf{v}}$: the sequence is $\underline{\mathbf{v}}$ of length 16 for the 3.84Mcps and 1.28Mcps options; it is of length 32 for the 7.68Mcps option.

6.2 Channelisation codes

The elements $c_q^{(k)}$; k=1,...,K_{Code}; q=1,...,Q_k; of the real valued channelisation codes

$$\mathbf{c}^{(k)} = (c_1^{(k)}, c_2^{(k)}, ..., c_{Q_k}^{(k)})$$
; $k=1,...,K_{Code}$

shall be taken from the set

$$V_{c} = \{1, -1\} \tag{3}$$

The $\mathbf{c}_{Q_k}^{(k)}$ are Orthogonal Variable Spreading Factor (OVSF) codes, allowing to mix in the same timeslot channels with different spreading factors while preserving the orthogonality. The OVSF codes can be defined using the code tree of figure 1.

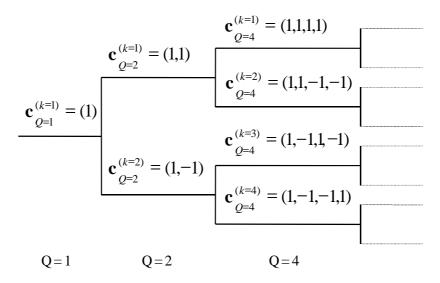


Figure 1: Code-tree for generation of Orthogonal Variable Spreading Factor (OVSF) codes for Channelisation Operation

Each level in the code tree defines a spreading factor indicated by the value of Q in the figure. All codes within the code tree cannot be used simultaneously in a given timeslot. A code can be used in a timeslot if and only if no other code on the path from the specific code to the root of the tree or in the sub-tree below the specific code is used in this timeslot. This means that the number of available codes in a slot is not fixed but depends on the rate and spreading factor of each physical channel.

For the 3.84Mcps and 1.28Mcps TDD options, the spreading factor goes up to Q_{MAX} =16; for the 7.68Mcps TDD option, the spreading factor goes up to Q_{MAX} =32.

6.3 Channelisation Code Specific Multiplier

Associated with each channelisation code is a multiplier $w_{Q_k}^{(k)}$ taking values from the set $\{e^{j\pi/2 \cdot p_k}\}$, where p_k is a permutation of the integer set $\{0, ..., Q_k-1\}$ and Q_k denotes the spreading factor. The multiplier is applied to the data sequence modulating each channelisation code. The values of the multiplier for each channelisation code are given in the table below:

k	$w_{Q=1}^{(k)}$	$W_{Q=2}^{(k)}$	$w_{Q=4}^{(k)}$	$w_{Q=8}^{(k)}$	$W_{Q=16}^{(k)}$	$w_{Q=32}^{(k)}$
1	1	1	-j	1	-1	-j
2 3		+j	1	+j	-j	-1
3		•	+j	+j	1	-1
4			-1	-1	1	1
5				-j	+j	-1
6				-1	-1	-j
7				-j	-1	j
8				1	1	1
9					-j	-1
10					+j	1
11					1	1
12					+j	-j
13					-j	j
14					-j	-1
15					+j	j
16					-1	-j
17						-j
18						-j
19						1
20						j
21						-1
22						-j
23						-j
24						-j
25						-1
26						-1
27						j
28						-1
29						-j
30						1
31						-1
32					_	-1

NOTE: the multiplier $W_{O=32}^{(k)}$ may only be applied in the 7.68Mcps TDD option.

If the UE autonomously changes the SF, as described in [7], it shall always use the multiplier associated with the channelisation code allocated by higher layers.

6.4 Scrambling codes for the 3.84Mcps and 1.28Mcps options

The spreading of data by a real valued channelisation code $\mathbf{c}^{(k)}$ of length Q_k is followed by a cell specific complex scrambling sequence $\underline{\mathbf{v}} = (\underline{v}_1, \underline{v}_2, ..., \underline{v}_{16})$. The elements $\underline{v}_i; i = 1, ..., 16$ of the complex valued scrambling codes shall be taken from the complex set

$$\underline{\mathbf{V}}_{\underline{\mathbf{v}}} = \{1, \mathbf{j}, -1, -\mathbf{j}\}\tag{4}$$

In equation 4 the letter j denotes the imaginary unit. A complex scrambling code $\underline{\mathbf{v}}$ is generated from the binary scrambling codes $\mathbf{v} = (v_1, v_2, \dots, v_{16})$ of length 16 shown in Annex A. The relation between the elements $\underline{\mathbf{v}}$ and $\underline{\mathbf{v}}$ is given by:

$$\underline{\mathbf{v}}_{i} = (\mathbf{j})^{i} \cdot \mathbf{v}_{i} \qquad \mathbf{v}_{i} \in \{1, -1\}, \ i = 1, ..., 16$$
 (5)

Hence, the elements $\[\underline{v}_i\]$ of the complex scrambling code $\[\underline{\mathbf{v}}\]$ are alternating real and imaginary.

The length matching is obtained by concatenating Q_{MAX}/Q_k spread words before the scrambling. The scheme is illustrated in figure 2 and is described in more detail in subclause 6.5.

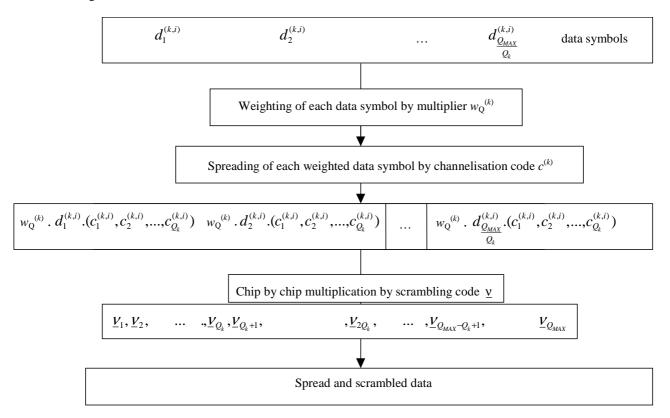


Figure 2: Spreading of data symbols

6.4a Scrambling codes for the 7.68Mcps option

The spreading of data by a real valued channelisation code $\mathbf{c}^{(k)}$ of length Q_k is followed by a cell specific complex scrambling sequence $\underline{v} = (\underline{v}_1, \underline{v}_2, ... \underline{v}_{32})$. The elements \underline{v}_i ; i = 1,...,32 of the complex valued scrambling codes shall be taken from the complex set

$$\underline{\mathbf{V}}_{\underline{\mathbf{V}}} = \{1, \mathbf{j}, -1, -\mathbf{j}\} \tag{4a}$$

In equation 4a the letter j denotes the imaginary unit. A complex scrambling code $\underline{\mathbf{v}}$ is generated from the binary scrambling codes $v = (v_1, v_2, ... v_{32})$ of length 32 that are generated according to the method described in section 6.4a.1. The relation between the elements $\underline{\mathbf{v}}$ and $\underline{\mathbf{v}}$ is given by:

$$\underline{v}_i = (j)^i \cdot v_i \quad v_i \in \{1, -1\}, \quad i = 1, ..., 32$$
 (5a)

Hence, the elements \underline{V}_i of the complex scrambling code $\underline{\mathbf{v}}$ are alternating real and imaginary.

The length matching is obtained by concatenating Q_{MAX}/Q_k spread words before the scrambling. The scheme is illustrated in figure 2 and is described in more detail in subclause 6.5.

6.4a.1 Generation of binary scrambling codes

The binary scrambling code, $c_{7.68}^n$, for cell parameter n in the 7.68Mcps TDD option is formed from the concatenation of the binary scrambling codes $c_{3.84}^n$ and $c_{3.84}^{(n+2) \mod 128}$ shown in Annex A:

$$v = (v_1, v_2, ... v_{32}) = c_{7.68}^n = \{c_{3.84}^n, c_{3.84}^{(n+2) \mod 128}\}$$

6.5 Spread signal of data symbols and data blocks

The combination of the user specific channelisation and cell specific scrambling codes can be seen as a user and cell specific spreading code $\mathbf{s}^{(k)} = (s_p^{(k)})$ with

$$s_p^{(k)} = c_{1 + [(p-1) \bmod Q_k]}^{(k)} \cdot \mathcal{V}_{1 + [(p-1) \bmod Q_{MAX}]}, k=1,...,K_{\text{Code}}, p=1,...,N_kQ_k.$$

With the root raised cosine chip impulse filter $Cr_0(t)$ the transmitted signal belonging to the data block $\underline{\mathbf{d}}^{(k,1)}$ of equation 1 transmitted before the midamble is

$$d^{(k,1)}(t) = \sum_{n=1}^{N_k} d_n^{(k,1)} w_{Q_k}^{(k)} \sum_{q=1}^{Q_k} s_{(n-1)Q_k+q}^{(k)} . Cr_0(t - (q-1)T_c - (n-1)Q_k T_c)$$
(6)

and for the data block $\underline{\mathbf{d}}^{(k,2)}$ of equation 1 transmitted after the midamble

$$d^{(k,2)}(t) = \sum_{n=1}^{N_k} d_n^{(k,2)} w_{Q_k}^{(k)} \sum_{q=1}^{Q_k} s_{(n-1)Q_k+q}^{(k)} . Cr_0(t - (q-1)T_c - (n-1)Q_k T_c - N_k Q_k T_c - L_m T_c)$$
(7)

where L_m is the number of midamble chips.

6.6 Modulation for the 3.84Mcps and 7.68Mcps options

The complex-valued chip sequence is modulated as shown in figure 3.

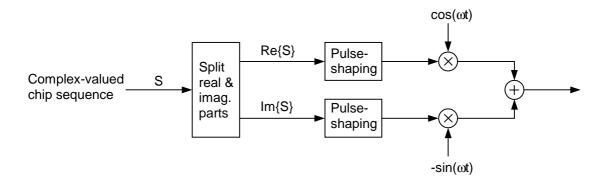


Figure 3: Modulation of complex valued chip sequences

The pulse-shaping characteristics are described in [9] and [10].

6.6.1 Combination of physical channels in uplink

Figure 4 illustrates the principle of combination of two different physical uplink channels within one timeslot. In the case of E-PUCH, only a single uplink physical channel is transmitted per timeslot and the procedures of subclause 6.6.1a shall instead apply).

The DPCHs to be combined belong to same CCTrCH, did undergo spreading as described in sections before and are thus represented by complex-valued sequences. First, the amplitude of all DPCHs is adjusted according to UL open loop power control as described in [10]. Each DPCH is then separately weighted by a weight factor γ_i and combined using complex addition. After combination of Physical Channels the gain factor β_j is applied, depending on the actual TFC as described in [10].

In case of different CCTrCH, principle shown in Figure 4 applies to each CCTrCH separately.

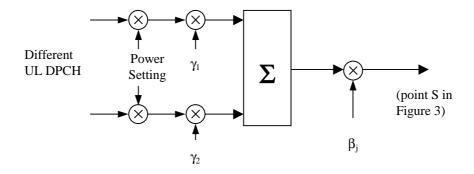


Figure 4: Combination of different physical channels in uplink

The values of weight factors γ_i are depending on the spreading factor SF of the corresponding DPCH:

SF of DPCH _i	γι
32	$\sqrt{2}/8$
16	1/4
8	$\sqrt{2}/4$
4	1/2
2	$\sqrt{2}/2$
1	1

NOTE: in the above table, SF = 32 is only supported in the 7.68Mcps TDD option.

In the case that β_j (corresponding to the *j*-th TFC) has been explicitly signalled to the UE, the possible values that β_j can assume are listed in the table below. In the case that β_j has been calculated by the UE from a reference TFC, β_j shall not be restricted to the quantised values.

Signalling value for β_j	Quantized value β _j
15	16/8
14	15/8
13	14/8
12	13/8
11	12/8
10	11/8
9	10/8
8	9/8
7	8/8
6	7/8
5	6/8
4	5/8
3	4/8
2	3/8
1	2/8
0	1/8

6.6.1a Physical channel transmission for E-PUCH

Figure 4a illustrates the principle of E-PUCH transmission. In a timeslot in which an E-PUCH is transmitted by a UE, no other physical channels may be transmitted by the same UE.

The amplitude of the E-PUCH is adjusted in accordance with the E-PUCH UL power control procedure described in [12]. The power setting procedure of [12] includes appropriate power adjustment factors for the E-PUCH spreading factor and for the E-TFC selected by higher layers [13]. Quantisation of the gain factor used to set the E-PUCH power is not specified.

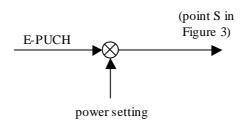


Figure 4a: Combination of different physical channels in uplink

6.6.2 Combination of physical channels in downlink

Figure 5 illustrates how different physical downlink channels are combined within one timeslot. Each complex-valued spread channel is separately weighted by a weight factor G_i . If a timeslot contains the SCH, the complex-valued SCH, as described in [7] is separately weighted by a weight factor G_{SCH} . All downlink physical channels are then combined using complex addition.

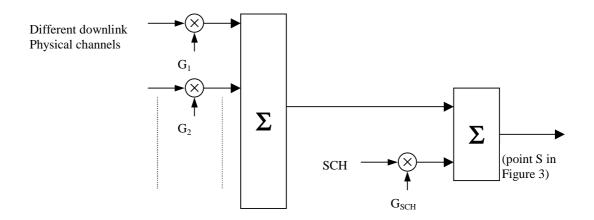


Figure 5: Combination of different physical channels in downlink in case of SCH timeslot

6.6.3 Combination of signature sequences for E-HICH

Multiple HARQ acknowledgement indicator signature sequences may be mapped onto the same channelisation code. Each signature sequence (described in [8]) is first subjected to QPSK modulation as described in subclause 5.2.1.1 to form the output sequence $d_{n,h}^{(k,i)}$ for the h^{th} indicator sequence, where $n=1,2,...,N_k$ and i=1,2. Code k is the same value for all signature sequences mapped to the same channelisation code.

When multiple signature sequences are to be transmitted on the same channelisation code, the following procedure shall be applied prior to spreading.

Each QPSK-modulated stream $d_{n,h}^{(k,i)}$ is amplitude-weighted by a factor g_h according to the desired signature sequence power. A summation is then performed across all H signature sequences mapped to the same channelisation code as shown in figure 5a. The output of the summation block is the sequence:

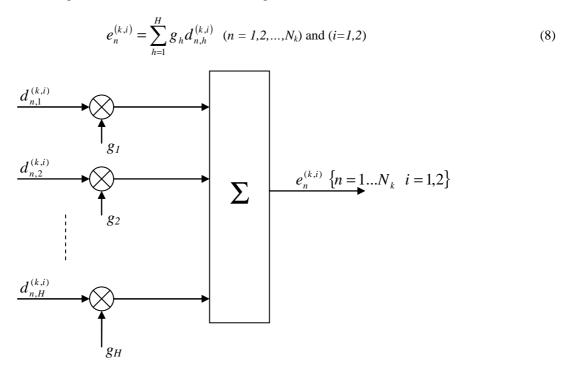


Figure 5a: Combination of HARQ acknowledgement indicator sequences prior to spreading

The sequence $e_n^{(k,i)}$ is mapped to a single channelisation code and subject to spreading at SF=16 (for 3.84Mcps) and at SF=32 (for 7.68Mcps) in accordance with the general method of subclause 6.

6.7 Modulation for the 1.28 Mcps option

The complex-valued chip sequence in uplink or downlink on one carrier within one timeslot is modulated as shown in figure 6.

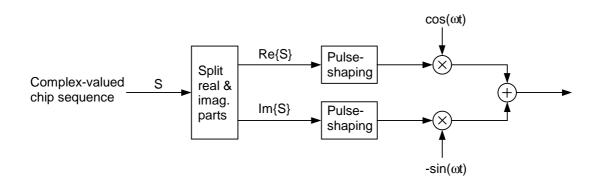


Figure 6: Modulation of complex valued chip sequences

The pulse-shaping characteristics are described in [9] and [10].

6.7.1 Combination of physical channels in uplink

The principle of combination of two different physical uplink channels within one timeslot is the same as in the 3.84 Mcps TDD cf. [6.6.1 Combination of physical channels in uplink] In the case of E-PUCH, the procedures of subclause 6.7.1a shall instead apply).

6.7.1a Physical channel transmission for E-PUCH

Figure 6a illustrates the principle of E-PUCH transmission when one uplink physical channel is transmitted.

The amplitude of the E-PUCH is adjusted in accordance with the E-PUCH UL power control procedure described in [12]. The power setting procedure of [12] includes appropriate power adjustment factors for the E-PUCH spreading factor and for the E-TFC selected by higher layers [13]. Quantisation of the gain factor used to set the E-PUCH power is not specified.

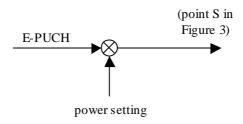


Figure 6a: Combination of different physical channels in uplink

6.7.2 Combination of physical channels in downlink

Figure 7 illustrates how different physical downlink channels are combined within one timeslot. Each spread channel is separately weighted by a weight factor G_i . All downlink physical channels are then combined using complex addition.

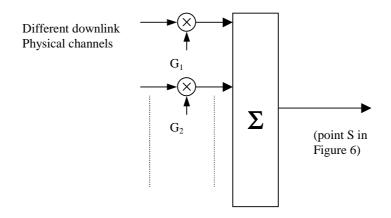


Figure 7: Combination of different physical channels in downlink

6.7.3 Combination of signature sequences for Scheduled E-HICH

For Scheduled E-HICH, every scheduled user is assigned one signature sequence which is related to the E-DCH resources allocated by Node-B to indicate ACK/NACK. But for the user configured in MU-MIMO mode by higher layers, in case the special default midamble allocation scheme is taken, the signature sequence allocated to the user is related to both the E-DCH resources allocated by Node-B and the variable 'offset' which is determined by the special default midamble pattern indicator [7] signalled on E-AGCH. Multiple users" HARQ acknowledgement indicator signature sequences may be mapped onto the same channelisation code. Each signature sequence (described in [8]) is first subjected to QPSK modulation as described in subclause 5.2.1.1 to form the output sequence $d_{n,h}^{(k,i)}$ for the h^{th} indicator sequence, where $n=1,2,...,N_k$ and i=1,2. Code k is the same value for all signature sequences mapped to the same channelisation code.

When multiple signature sequences are to be transmitted on the same channelisation code, the following procedure shall be applied prior to spreading.

Each QPSK-modulated stream $d_{n,h}^{(k,i)}$ is amplitude-weighted by a factor g_h according to the desired signature sequence power. Each E-HICH physical channel may carry ACK/NACK signature sequence(s) for one UE or multiple UEs decided by Node-B. A summation is then performed across M signature sequences mapped to the same channelisation code as shown in figure 8. The output of the summation block is the sequence:

$$e_n^{(k,i)} = \sum_{h=1}^{M} g_h d_{n,h}^{(k,i)} \quad (n = 1, 2, ..., N_k) \text{ and } (i=1, 2)$$
 (9)

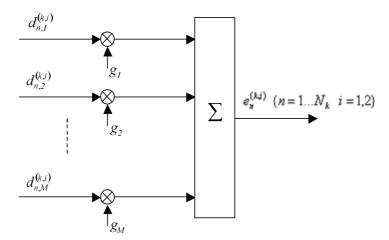


Figure 8: Combination of HARQ acknowledgement indicator sequences prior to spreading for Scheduled E-HICH

The sequence $e_n^{(k,i)}$ is mapped to a single channelisation code and subject to spreading at SF=16 in accordance with the general method of subclause 6.

6.7.3a Combination of signature sequences for Non-Scheduled E-HICH

For Non-Scheduled E-HICH, the 80 signature sequences are divided into 20 groups while each group includes 4 sequences. Every non-scheduled user is assigned one group by higher layer, from that two sequences are selected to indicate ACK/NACK and TPC/SS command. Multiple users" signature sequences may be mapped onto the same channelisation code. Each user"s two signature sequences (described in [8]) are first subjected to QPSK modulation as described in subclause 5.2.1.1 to form the two output sequences $d_{n,h_1}^{(k,i)}$ and $d_{n,h_2}^{(k,i)}$ for the h^{th} user, where $n=1,2,...,N_k$ and i=1,2. Code k is the same value for all signature sequences mapped to the same channelisation code.

When multiple users" signature sequences are to be transmitted on the same channelisation code, the following procedure shall be applied prior to spreading.

Firstly, each user"s QPSK-modulated stream $d_{n,h_2}^{(k,i)}$ corresponding to TPC/SS signature sequence is amplitude-weighted by a factor f_h and added to the QPSK-modulated stream $d_{n,h_1}^{(k,i)}$ corresponding to ACK/NACK signature sequence; Secondly, each user"s combined stream $d_{n,h}^{(k,i)}$ is amplitude-weighted by a factor g_h according to the desired user power. A summation is then performed across M users" signature sequences mapped to the same channelisation code as shown in figure 8a. The output of the summation block is the sequence:

$$e_n^{(k,i)} = \sum_{h=1}^M g_h d_{n,h}^{(k,i)} \quad (n = 1, 2, ..., N_k) \text{ and } (i=1, 2)$$
 (9a)

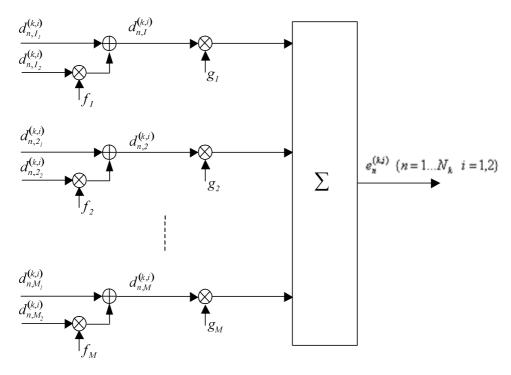


Figure 8a: Combination of ACK/NACK and TPC/SS sequences prior to spreading for Non-Scheduled E-HICH

The sequence $e_n^{(k,i)}$ is mapped to a single channelisation code and subject to spreading at SF=16 in accordance with the general method of subclause 6.

6.8 Spreading modulation for the 3.84 Mcps MBSFN IMB option

6.8.1 Spreading

The spreading operation includes a modulation mapper stage successively followed by a channelisation stage, an IQ combining stage and a scrambling stage as illustrated by figure 9.

Modulation mapping is described in subclause 5.2.3.

For all physical channels, except for the Synchronisation Channel (SCH), the I and Q branches shall be spread to the chip rate by the same real-valued channelisation code $C_{\text{ch,SF,m}}$, i.e. the output for each input symbol on the I and the Q branches shall be a sequence of SF chips corresponding to the channelisation code chip sequence multiplied by the real-valued symbol. The channelisation code sequence shall be aligned in time with the symbol boundary. The real-valued chip sequence on the Q-branch shall be complex multiplied with j and summed with the corresponding real-valued chip sequence on the I-branch, resulting in a single complex-valued chip sequence I+jQ.

The sequence of complex-valued chips output from the spreading stage shall be scrambled (complex chip-wise multiplication) by a complex-valued scrambling code $S_{\text{dl,n}}$.

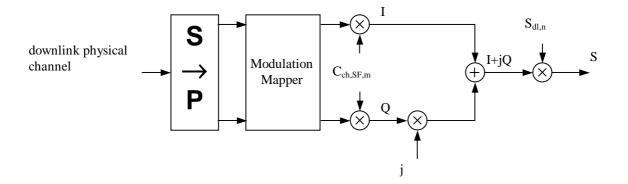


Figure 9: Spreading for all downlink physical channels except SCH

All complex-valued spread channels are separately weighted and then combined, together with separately weighted Primary SCH and Secondary SCH, into one complex-valued chip sequence by using complex addition, as illustrated by figure 9 in subclause 5.1.5 of [4]. The resulting signal is modulated prior to transmission as described in subclause 6A.3.

6.8.2 Code generation and allocation

6.8.2.1 Channelisation codes

The channelisation codes are OVFS codes that preserve the orthogonality between downlink channels of different rates and spreading factors. The channelisation codes are defined in figure 4 of subclause 4.3.1.1 of [3] and are uniquely described as $C_{ch,SF,m}$, where SF is the spreading factor of the code and m is the code number, $0 \le m \le SF-1$.

The following applies to the MBSFN IMB physical channels:

- The channelisation code for the Primary CPICH is fixed to $C_{ch,256,0}$;
- The channelisation code for the Primary CCPCH is fixed to C_{ch.256.1}:
- The channelisation codes for the Secondary CCPCH frame type 1 and MICH are assigned by UTRAN from the codes $C_{ch,256,m}$ $m \in \{2,3,...,15\}$;
- The channelisation codes for the Secondary CCPCH frame type 2 are assigned by UTRAN from the codes $C_{ch,16,m}$ $m \in \{1,2,...,15\}$;
- The channelisation codes for the T-CPICH are C_{ch,16,1}, C_{ch,16,2}, ..., C_{ch,16,15}.

6.8.2.2 Scrambling codes

The scrambling codes shall be generated as described in subclause 5.2.2 in [4]. For MBSFN IMB operation, only primary scrambling codes shall be used. Out of all possible primary scrambling codes with index n=16*i where i=0...511 as defined in [4] the following subset shall be supported for the MBSFN option:

 $n \in \{0, 128, 256, 384, 512, 640, 768, 896\}$. No two members of set n belong to the same scrambling code group.

Cells that belong to a certain MBSFN IMB cluster shall use the same primary scrambling code.

The primary scrambling code for all physical channels shall be applied aligned with the start of the Primary CCPCH frame. This also applies in the case of a Secondary CCPCH frame type 2 associated with the k^{th} sub-frame of a radio frame (k = 0, 1, ... 4) [7], such that the start of the scrambling code is always aligned with the start of sub-frame k = 0.

6.8.3 Modulation

Modulation of the complex-valued chip sequence generated by the spreading process is performed according to subclause 6.6. The modulation chip rate is 3.84 Mcps.

7 Synchronisation codes for the 3.84 Mcps option

Sub-clauses 7.1, 7.2 and 7.3 do not apply for 3.84 Mcps MBSFN IMB operation. Synchronisation codes for 3.84 Mcps MBSFN IMB are described in sub-clause 7.4.

7.1 Code Generation

The primary synchronisation code (PSC), C_p , is constructed as a so-called generalised hierarchical Golay sequence. The PSC is furthermore chosen to have good aperiodic auto correlation properties.

Define
$$a = \langle x_1, x_2, x_3, ..., x_{16} \rangle = \langle 1, 1, 1, 1, 1, 1, -1, -1, 1, -1, 1, -1, 1, -1, 1 \rangle$$

The PSC is generated by repeating the sequence 'a' modulated by a Golay complementary sequence and creating a complex-valued sequence with identical real and imaginary components.

The PSC,
$$C_p$$
 , is defined as $C_p = \langle y(0), y(1), y(2), ..., y(255) \rangle$

and the left most index corresponds to the chip transmitted first in time.

The 12 secondary synchronization codes, $\{C_0, C_1, C_3, C_4, C_5, C_6, C_8, C_{10}, C_{12}, C_{13}, C_{14}, C_{15}\}$ are complex valued with identical real and imaginary components, and are constructed from the position wise multiplication of a Hadamard sequence and a sequence z, defined as

$$b = \langle x_1, x_2, x_3, x_4, x_5, x_6, x_7, x_8, -x_9, -x_{10}, -x_{11}, -x_{12}, -x_{13}, -x_{14}, -x_{15}, -x_{16} \rangle$$

and $x_1, x_2, x_3, ..., x_{16}$ are the same as in the definition of the sequence 'a' above.

The Hadamard sequences are obtained as the rows in a matrix H_8 constructed recursively by:

$$H_{0} = (1)$$

$$H_{k} = \begin{pmatrix} H_{k-1} & H_{k-1} \\ H_{k-1} & -H_{k-1} \end{pmatrix}, \quad k \ge 1$$

The rows are numbered from the top starting with row θ (the all ones sequence).

Denote the n:th Hadamard sequence h_n as a row of H_8 numbered from the top, n = 0, 1, 2, ..., 255, in the sequel.

Furthermore, let $h_m(l)$ and z(l) denote the lth symbol of the sequence h_m and z, respectively where l = 0, 1, 2, ..., 255 and l = 0 corresponds to the leftmost symbol.

The i:th secondary SCH code word, C_i , i = 0, 1, 3, 4, 5, 6, 8, 10, 12, 13, 14, 15 is then defined as

$$C_i = (1 + j) \times \langle h_m(0) \times z(0), h_m(1) \times z(1), h_m(2) \times z(2), ..., h_m(255) \times z(255) \rangle$$

where $m = (16 \times i)$ and the leftmost chip in the sequence corresponds to the chip transmitted first in time.

7.2 Code Allocation

Three secondary SCH codes are QPSK modulated and transmitted in parallel with the primary synchronization code. The QPSK modulation carries the following information:

- the code group that the base station belongs to (32 code groups:5 bits; Cases 1, 2);

- the position of the frame within an interleaving period of 20 msec (2 frames:1 bit, Cases 1, 2);
- the position of the SCH slot(s) within the frame (2 SCH slots:1 bit, Case 2).

The modulated secondary SCH codes are also constructed such that their cyclic-shifts are unique, i.e. a non-zero cyclic shift less than 2 (Case 1) and 4 (Case 2) of any of the sequences is not equivalent to some cyclic shift of any other of the sequences. Also, a non-zero cyclic shift less than 2 (Case 1) and 4 (Case 2) of any of the sequences is not equivalent to itself with any other cyclic shift less than 8. The secondary synchronization codes are partitioned into two code sets for Case 1 and four code sets for Case 2. The set is used to provide the following information:

Case 1:

Table 2: Code Set Allocation for Case 1

Code Set	Code Group
1	0-15
2	16-31

The code group and frame position information is provided by modulating the secondary codes in the code set.

Case 2:

Table 3: Code Set Allocation for Case 2

Code Set	Code Group
1	0-7
2	8-15
3	16-23
4	24-31

The slot timing and frame position information is provided by the comma free property of the code word and the Code group is provided by modulating some of the secondary codes in the code set.

The following SCH codes are allocated for each code set:

Case 1

Code set 1: C_1 , C_3 , C_5 .

Code set 2: C₁₀, C₁₃, C₁₄.

Case 2

Code set 1: C_1 , C_3 , C_5 .

Code set 2: C₁₀, C₁₃, C₁₄.

Code set 3: C_0 , C_6 , C_{12} .

Code set 4: C_4 , C_8 , C_{15} .

The following subclauses 7.2.1 to 7.2.2 refer to the two cases of SCH/P-CCPCH usage as described in [7].

Note that in the tables 4 and 5 corresponding to Cases 1 and 2, respectively, Frame 1 implies the frame with an odd SFN and Frame 2 implies the frame with an even SFN.

7.2.1 Code allocation for Case 1

Table 4: Code Allocation for Case 1

Code Group	Code Set		Frame 1			Frame 2		Associated toffset
0	1	C ₁	C ₃	C ₅	C ₁	C ₃	-C ₅	t_0
1	1	C ₁	-C ₃	C_5	C ₁	-C ₃	-C ₅	t ₁
2	1	-C ₁	C ₃	C ₅	-C ₁	C ₃	-C ₅	t ₂
3	1	-C ₁	-C ₃	C ₅	-C ₁	-C ₃	-C ₅	t ₃
4	1	jC₁	jC₃	C_5	jC₁	jC₃	-C ₅	t_4
5	1	jC₁	-jC₃	C ₅	jC₁	-jC₃	-C ₅	t 5
6	1	-jC₁	jC ₃	C ₅	-jC₁	jC ₃	-C ₅	t_6
7	1	-jC₁	-jC₃	C ₅	-jC₁	-jC₃	-C ₅	t ₇
8	1	jC₁	jC ₅	C ₃	jC₁	jC₅	-C ₃	t ₈
9	1	jC₁	-jC₅	C ₃	jC₁	-jC₅	-C ₃	t ₉
10	1	-jC₁	jC₅	C ₃	-jC₁	jC₅	-C ₃	t ₁₀
11	1	-jC₁	-jC₅	C ₃	-jC₁	-jC₅	-C ₃	t ₁₁
12	1	jC₃	jC₅	C ₁	jC₃	jC₅	-C ₁	t ₁₂
13	1	jC₃	-jC₅	C ₁	jC₃	-jC₅	-C ₁	t ₁₃
14	1	-jC₃	jC ₅	C ₁	-jC₃	jC₅	-C ₁	t ₁₄
15	1	-jC₃	-jC₅	C ₁	-jC₃	-jC₅	-C ₁	t ₁₅
16	2	C ₁₀	C ₁₃	C ₁₄	C ₁₀	C ₁₃	-C ₁₄	t ₁₆
17	2	C ₁₀	-C ₁₃	C ₁₄	C ₁₀	-C ₁₃	-C ₁₄	t ₁₇
	•••					•••		
20	2	jC ₁₀	jC ₁₃	C ₁₄	jC ₁₀	jC ₁₃	-C ₁₄	t ₂₀
				•••				
24	2	jC ₁₀	jC ₁₄	C ₁₃	jC ₁₀	jC ₁₄	-C ₁₃	t ₂₄
				•••				
31	2	-jC ₁₃	-jC ₁₄	C ₁₀	-jC ₁₃	-jC ₁₄	-C ₁₀	t ₃₁

NOTE: The code construction for code groups 0 to 15 using only the SCH codes from code set 1 is shown. The construction for code groups 16 to 31 using the SCH codes from code set 2 is done in the same way.

7.2.2 Code allocation for Case 2

Table 5: Code Allocation for Case 2

Code	Code	Frame 1				Frame 2					Associated toffset			
Group	Set		Slot k		,	Slot k+8		Slot k Slot l			Slot k+8			
0	1	C ₁	C ₃	C ₅	C ₁	C ₃	-C ₅	-C ₁	-C ₃	C ₅	-C ₁	-C ₃	-C ₅	t ₀
1	1	C ₁	-C ₃	C_5	C ₁	-C ₃	-C ₅	-C ₁	C ₃	C_5	-C ₁	C ₃	-C ₅	t ₁
2	1	jC₁	jC ₃	C ₅	jC₁	jC ₃	-C ₅	-jC₁	-jC₃	C ₅	-jC₁	-jC₃	-C ₅	t ₂
3	1	jC₁	-jC₃	C_5	jC₁	-jC₃	-C ₅	-jC₁	jC ₃	C ₅	-jC₁	jC₃	-C ₅	t_3
4	1	jC₁	jC₅	C ₃	jC₁	jC₅	-C ₃	-jC₁	-jC₅	C ₃	-jC₁	-jC₅	-C ₃	t ₄
5	1	jC₁	-jC₅	C ₃	jC₁	-jC₅	-C ₃	-jC₁	jC₅	C ₃	-jC₁	jC₅	-C ₃	t_5
6	1	jC₃	jC ₅	C_1	jC₃	jC₅	-C ₁	-jC₃	-jC₅	C_1	-jC₃	-jC₅	-C ₁	t_6
7	1	jC₃	-jC₅	C ₁	jC₃	-jC₅	-C ₁	-jC₃	jC₅	C ₁	-jC₃	jC₅	-C ₁	t ₇
8	2	C ₁₀	C ₁₃	C ₁₄	C ₁₀	C ₁₃	-C ₁₄	-C ₁₀	-C ₁₃	C ₁₄	-C ₁₀	-C ₁₃	-C ₁₄	t ₈
9	2	C ₁₀	-C ₁₃	C ₁₄	C ₁₀	-C ₁₃	-C ₁₄	-C ₁₀	C ₁₃	C ₁₄	-C ₁₀	C ₁₃	-C ₁₄	t ₉
10	2	jC ₁₀	jC ₁₃	C ₁₄	jC ₁₀	jC ₁₃	-C ₁₄	-jC ₁₀	-jC ₁₃	C ₁₄	-jC ₁₀	-jC ₁₃	-C ₁₄	t ₁₀
11	2	jC ₁₀	-jC ₁₃	C ₁₄	jC ₁₀	-jC ₁₃	-C ₁₄	-jC ₁₀	jC ₁₃	C ₁₄	-jC ₁₀	jC ₁₃	-C ₁₄	t ₁₁
12	2	jC ₁₀	jC ₁₄	C ₁₃	jC ₁₀	jC ₁₄	-C ₁₃	-jC ₁₀	-jC ₁₄	C ₁₃	-jC ₁₀	-jC ₁₄	-C ₁₃	t ₁₂
13	2	jC ₁₀	-jC ₁₄	C ₁₃	jC₁0	-jC ₁₄	-C ₁₃	-jC ₁₀	jC ₁₄	C ₁₃	-jC ₁₀	jC ₁₄	-C ₁₃	t ₁₃
14	2	jC ₁₃	jC ₁₄	C ₁₀	jC ₁₃	jC ₁₄	-C ₁₀	-jC ₁₃	-jC ₁₄	C ₁₀	-jC ₁₃	-jC ₁₄	-C ₁₀	t ₁₄
15	2	jC ₁₃	-jC ₁₄	C ₁₀	jC ₁₃	-jC ₁₄	-C ₁₀	-jC ₁₃	jC ₁₄	C ₁₀	-jC ₁₃	jC ₁₄	-C ₁₀	t ₁₅
16	3	Co	C_6	C ₁₂	C_0	C_6	-C ₁₂	-C ₀	-C ₆	C ₁₂	-C ₀	-C ₆	-C ₁₂	t ₁₆
						•••			•••			•••		
23	3	jC ₆	-jC ₁₂	Co	jC ₆	-jC ₁₂	-C ₀	-jC ₆	jC ₁₂	Co	-jC ₆	jC ₁₂	-C ₀	t ₂₀
24	4	C ₄	C ₈	C ₁₅	C ₄	C ₈	-C ₁₅	-C ₄	-C ₈	C ₁₅	-C ₄	-C ₈	-C ₁₅	t ₂₄
31	4	jC ₈	-jC ₁₅	C ₄	jC ₈	-jC ₁₅	-C ₄	-jC ₈	jC ₁₅	C_4	-jC ₈	jC ₁₅	-C ₄	t ₃₁

NOTE: The code construction for code groups 0 to 15 using the SCH codes from code sets 1 and 2 is shown. The construction for code groups 16 to 31 using the SCH codes from code sets 3 and 4 is done in the same way.

7.3 Evaluation of synchronisation codes

The evaluation of information transmitted in SCH on code group and frame timing is shown in table 6, where the 32 code groups are listed. Each code group is containing 4 specific scrambling codes (cf. subclause 6.4), each scrambling code associated with a specific short and long basic midamble code.

Each code group is additionally linked to a specific t_{Offset} , thus to a specific frame timing. By using this scheme, the UE can derive the position of the frame border due to the position of the SCH sequence and the knowledge of t_{Offset} . The complete mapping of Code Group to Scrambling Code, Midamble Codes and t_{Offset} is depicted in table 6.

Table 6: Mapping scheme for Cell Parameters, Code Groups, Scrambling Codes, Midambles and t_{Offset}

CELL	Code	Α	Associat			
PARA- METER	Group	Scrambling Code	Long Basic Midamble Code	Short Basic Midamble Code	ed t _{Offset}	
0	Group 0	Code 0	m _{PL0}	m _{SL0}	t ₀	
1		Code 1	m _{PL1}	m _{SL1}		
2		Code 2	m _{PL2}	m _{SL2}		
3		Code 3	m _{PL3}	m _{SL3}		
4	Group 1	Code 4	m _{PL4}	m _{SL4}	t ₁	
5		Code 5	m _{PL5}	m _{SL5}		
6		Code 6	m _{PL6}	m _{SL6}]	
7		Code 7	m _{PL7}	m _{SL7}		
· · ·						
124	Group 31	Code 124	m _{PL124}	m _{SL124}	t ₃₁	
125		Code 125	m _{PL125}	m _{SL125}]	
126		Code 126	m _{PL126}	m _{SL126}]	
127		Code 127	m _{PL127}	m _{SL127}		

For basic midamble codes m_P cf. [7], annex A 'Basic Midamble Codes'.

Each cell shall cycle through two sets of cell parameters in a code group with the cell parameters changing each frame. Table 7 shows how the cell parameters are cycled according to the SFN.

Table 7: Alignment of cell parameter cycling and SFN

Initial Cell Parameter Assignment	Code Group	Cell Parameter used when SFN mod 2 = 0	Cell Parameter used when SFN mod 2 = 1
0	Group 0	0	1
1		1	0
2		2	3
3		3	2
4	Group 1	4	5
5		5	4
6		6	7
7		7	6
		•	
124	Group 31	124	125
125		125	124
126		126	127
127		127	126

7.4 Synchronisation codes for 3.84 Mcps MBSFN IMB

7.4.1 Code generation

The primary synchronisation code (PSC), C_{psc} , is constructed to be orthogonal to both the primary and secondary SCH codes defined in subclause 7.1. The PSC is furthermore chosen to have good aperiodic auto correlation properties and low aperiodic cross correlations with the primary SCH defined in sub-clause 7.1.

Define:

-
$$a = \langle 1, -1, -1, 1, 1, -1, 1, 1, -1, 1, 1, -1, 1, 1, 1, 1, 1 \rangle$$

The PSC is generated by repeating the sequence a modulated by a Golay complementary sequence, and creating a complex-valued sequence with identical real and imaginary components. The PSC C_{psc} is defined as:

where the leftmost chip in the sequence corresponds to the chip transmitted first in time.

The 16 secondary synchronization codes, $\{C_{ssc,1},...,C_{ssc,16}\}$, are complex-valued with identical real and imaginary components, and are constructed from position wise multiplication of a Hadamard sequence and a sequence z, defined as:

- $b = \langle 1, 1, 1, 1, 1, 1, 1, -1, -1, 1, -1, 1, -1, 1, 1, -1 \rangle$.

The Hadamard sequences are obtained as the rows in a matrix H_8 constructed recursively by:

$$H_{0} = (1)$$

$$H_{k} = \begin{pmatrix} H_{k-1} & H_{k-1} \\ H_{k-1} & -H_{k-1} \end{pmatrix}, k \ge 1$$

The rows are numbered from the top starting with row θ (the all ones sequence).

Denote the n:th Hadamard sequence as a row of H_8 numbered from the top, n = 0, 1, 2, ..., 255, in the sequel.

Furthermore, let $h_n(i)$ and z(i) denote the i:th symbol of the sequence h_n and z, respectively where i = 0, 1, 2, ..., 255 and i = 0 corresponds to the leftmost symbol.

The k:th secondary synchronization code, $C_{ssc,k}$, k = 1, 2, 3, ..., 16 is then defined as:

-
$$C_{ssc,k} = (1+j) \times \langle h_m(0) \times z(0), h_m(1) \times z(1), h_m(2) \times z(2), ..., h_m(255) \times z(255) \rangle;$$

where $m = 16 \times (k - 1)$ and the leftmost chip in the sequence corresponds to the chip transmitted first in time.

7.4.2 Code allocation of SSC

The secondary synchronisation code sequences shall be constructed as described in subclause 5.2.3.2 in [4]. For MBSFN IMB operation, only the first 8 scrambling code groups are utilised.

7A Synchronisation codes for the 7.68 Mcps option

7A.1 Code Generation

The primary synchronisation code (PSC), C_p , is constructed as a so-called generalised hierarchical Golay sequence. The PSC is furthermore chosen to have good aperiodic auto correlation properties.

Define
$$a = \langle x_1, x_2, x_3, ..., x_{16} \rangle = \langle 1, 1, 1, 1, 1, 1, -1, -1, 1, -1, 1, -1, 1, -1, 1 \rangle$$

The PSC of length 512 chips is generated by repetition coding and repeating the sequence 'a' modulated by a Golay complementary sequence and creating a complex-valued sequence with identical real and imaginary components.

The PSC,
$$C_p$$
, is defined as $C_p = \langle y(0), y(0), y(1), y(1), y(2), y(2), \dots, y(255), y(255) \rangle$

and the left most index corresponds to the chip transmitted first in time.

The 12 secondary synchronization codes, $\{C_0, C_1, C_3, C_4, C_5, C_6, C_8, C_{10}, C_{12}, C_{13}, C_{14}, C_{15}\}$ are complex valued with identical real and imaginary components, and are constructed from repetition coding of the position wise multiplication of a Hadamard sequence and a sequence z, defined as

$$b = \langle x_1, x_2, x_3, x_4, x_5, x_6, x_7, x_8, -x_9, -x_{10}, -x_{11}, -x_{12}, -x_{13}, -x_{14}, -x_{15}, -x_{16} \rangle$$

and $x_1, x_2, x_3, ..., x_{16}$ are the same as in the definition of the sequence 'a' above.

The Hadamard sequences are obtained as the rows in a matrix H_8 constructed recursively by:

$$H_{0} = (1)$$

$$H_{k} = \begin{pmatrix} H_{k-1} & H_{k-1} \\ H_{k-1} & -H_{k-1} \end{pmatrix}, \quad k \ge 1$$

The rows are numbered from the top starting with row θ (the all ones sequence).

Denote the n:th Hadamard sequence h_n as a row of H_8 numbered from the top, n = 0, 1, 2, ..., 255, in the sequel.

Furthermore, let $h_m(l)$ and z(l) denote the lth symbol of the sequence h_m and z, respectively where l = 0, 1, 2, ..., 255 and l = 0 corresponds to the leftmost symbol.

The i:th secondary SCH code word, C_i , i = 0, 1, 3, 4, 5, 6, 8, 10, 12, 13, 14, 15 is of length 512 chips and is then defined as

$$C_i = (1+j) \times \langle h_m(0) \times z(0), h_m(0) \times z(0), h_m(1) \times z(1), h_m(1) \times z(1), \dots, h_m(255) \times z(255), h_m(255) \times z(255) \rangle$$

where $m = (16 \times i)$ and the leftmost chip in the sequence corresponds to the chip transmitted first in time.

7A.2 Code Allocation

Three secondary SCH codes are QPSK modulated and transmitted in parallel with the primary synchronization code. The QPSK modulation carries the following information:

- the code group that the base station belongs to (32 code groups:5 bits; Cases 1, 2);
- the position of the frame within an interleaving period of 20 msec (2 frames:1 bit, Cases 1, 2);
- the position of the SCH slot(s) within the frame (2 SCH slots:1 bit, Case 2).

The QPSK modulation sequences for the 7.68Mcps TDD option are unique to the modulation sequences for the 3.84Mcps TDD option.

The modulated secondary SCH codes are also constructed such that their cyclic-shifts are unique, i.e. a non-zero cyclic shift less than 2 (Case 1) and 4 (Case 2) of any of the sequences is not equivalent to some cyclic shift of any other of the sequences. Also, a non-zero cyclic shift less than 2 (Case 1) and 4 (Case 2) of any of the sequences is not equivalent to itself with any other cyclic shift less than 8. The secondary synchronization codes are partitioned into two code sets for Case 1 and four code sets for Case 2. The set is used to provide the following information:

<u>Case 1:</u>

Table 7A: Code Set Allocation for Case 1

Code Set	Code Group
1	0-15
2	16-31

The code group and frame position information is provided by modulating the secondary codes in the code set.

Case 2:

Table 7B: Code Set Allocation for Case 2

Code Set	Code Group
1	0-7
2	8-15
3	16-23
4	24-31

The slot timing and frame position information is provided by the comma free property of the code word and the Code group is provided by modulating some of the secondary codes in the code set.

The following SCH codes are allocated for each code set:

Case 1

Code set 1: C_1 , C_3 , C_5 .

Code set 2: C₁₀, C₁₃, C₁₄.

Case 2

Code set 1: C_1 , C_3 , C_5 .

Code set 2: C₁₀, C₁₃, C₁₄.

Code set 3: C_0 , C_6 , C_{12} .

Code set 4: C_4 , C_8 , C_{15} .

The following subclauses 7A.2.1 to 7A.2.2 refer to the two cases of SCH/P-CCPCH usage as described in [7].

Note that in the tables 7C and 7D corresponding to Cases 1 and 2, respectively, Frame 1 implies the frame with an odd SFN and Frame 2 implies the frame with an even SFN.

7A.2.1 Code allocation for Case 1

Table 7D: Code Allocation for Case 1

Code Group	Code Set		Frame 1			Frame 2		Associated toffset
0	1	C ₁	C ₃	jC ₅	C ₁	C ₃	-jC₅	t_0
1	1	C ₁	-C ₃	jC ₅	C ₁	-C ₃	-jC₅	t ₁
2	1	-C ₁	C ₃	jC ₅	-C ₁	C ₃	-jC₅	t ₂
3	1	-C ₁	-C ₃	jC ₅	-C ₁	-C ₃	-jC₅	t ₃
4	1	jC₁	jC₃	jC ₅	jC₁	jC ₃	-jC₅	t ₄
5	1	jC₁	-jC₃	jC₅	jC₁	-jC₃	-jC₅	t ₅
6	1	-jC₁	jC ₃	jC₅	-jC₁	jC ₃	-jC₅	t ₆
7	1	-jC₁	-jC₃	jC ₅	-jC₁	-jC₃	-jC₅	t ₇
8	1	jC₁	C ₅	C ₃	jC₁	C ₅	-C ₃	t ₈
9	1	jC₁	-C ₅	C ₃	jC₁	-C ₅	-C ₃	t ₉
10	1	-jC₁	C ₅	C ₃	-jC₁	C ₅	-C ₃	t ₁₀
11	1	-jC₁	-C ₅	C ₃	-jC₁	-C ₅	-C ₃	t ₁₁
12	1	jC₃	C ₅	C ₁	jC₃	C ₅	-C ₁	t ₁₂
13	1	jC₃	-C ₅	C ₁	jC₃	-C ₅	-C ₁	t ₁₃
14	1	-jC₃	C ₅	C ₁	-jC₃	C ₅	-C ₁	t ₁₄
15	1	-jC₃	-C ₅	C ₁	-jC₃	-C ₅	-C ₁	t ₁₅
16	2	C ₁₀	C ₁₃	jC ₁₄	C ₁₀	C ₁₃	-jC ₁₄	t ₁₆
17	2	C ₁₀	-C ₁₃	jC ₁₄	C ₁₀	-C ₁₃	-jC ₁₄	t ₁₇
20	2	jC ₁₀	jC ₁₃	jC ₁₄	jC ₁₀	jC ₁₃	-jC ₁₄	t ₂₀
24	2	jC ₁₀	C ₁₄	C ₁₃	jC ₁₀	C ₁₄	-C ₁₃	t ₂₄
31	2	-jC ₁₃	-C ₁₄	C ₁₀	-jC ₁₃	-C ₁₄	-C ₁₀	t ₃₁

NOTE: The code construction for code groups 0 to 15 using only the SCH codes from code set 1 is shown. The construction for code groups 16 to 31 using the SCH codes from code set 2 is done in the same way.

7A.2.2 Code allocation for Case 2

Table 7C: Code Allocation for Case 2

Code	Code			Fra	me 1					Fran	ne 2			Associated toffset
Group	Set		Slot k		,	Slot k+8			Slot k		,	Slot k+8		
0	1	C ₁	C ₃	jC ₅	C ₁	C ₃	-jC₅	-C ₁	-C ₃	jC ₅	-C ₁	-C ₃	-jC₅	t_0
1	1	C ₁	-C ₃	jC ₅	C ₁	-C ₃	-jC₅	-C ₁	C_3	jC ₅	-C ₁	C ₃	-jC₅	t ₁
2	1	jC₁	jC ₃	jC ₅	jC₁	jC ₃	-jC₅	-jC₁	-jC₃	jC ₅	-jC₁	-jC₃	-jC₅	t_2
3	1	jC₁	-jC₃	jC ₅	jC₁	-jC₃	-jC₅	-jC₁	jC ₃	jC ₅	-jC₁	jC ₃	-jC₅	t_3
4	1	jC₁	C ₅	C ₃	jC₁	C ₅	-C ₃	-jC₁	-C ₅	C ₃	-jC₁	-C ₅	-C ₃	t ₄
5	1	jC₁	-C ₅	C_3	jC₁	-C ₅	-C ₃	-jC₁	C_5	C_3	-jC₁	C ₅	-C ₃	t ₅
6	1	jC₃	C ₅	C ₁	jC₃	C ₅	-C ₁	-jC₃	-C ₅	C ₁	-jC₃	-C ₅	-C ₁	t ₆
7	1	jC₃	-C ₅	C ₁	jC₃	-C ₅	-C ₁	-jC₃	C ₅	C ₁	-jC₃	C ₅	-C ₁	t ₇
8	2	C ₁₀	C ₁₃	jC ₁₄	C ₁₀	C ₁₃	-jC ₁₄	-C ₁₀	-C ₁₃	jC ₁₄	-C ₁₀	-C ₁₃	-jC ₁₄	t ₈
9	2	C ₁₀	-C ₁₃	jC ₁₄	C ₁₀	-C ₁₃	-jC ₁₄	-C ₁₀	C ₁₃	jC ₁₄	-C ₁₀	C ₁₃	-jC ₁₄	t ₉
10	2	jC ₁₀	jC ₁₃	jC ₁₄	jC ₁₀	jC ₁₃	-jC ₁₄	-jC ₁₀	-jC ₁₃	jC ₁₄	-jC ₁₀	-jC ₁₃	-jC ₁₄	t ₁₀
11	2	jC ₁₀	-jC ₁₃	jC ₁₄	jC ₁₀	-jC ₁₃	-jC ₁₄	-jC ₁₀	jC ₁₃	jC ₁₄	-jC ₁₀	jC ₁₃	-jC ₁₄	t ₁₁
12	2	jC ₁₀	C ₁₄	C ₁₃	jC ₁₀	C ₁₄	-C ₁₃	-jC ₁₀	-C ₁₄	C ₁₃	-jC ₁₀	-C ₁₄	-C ₁₃	t ₁₂
13	2	jC ₁₀	-C ₁₄	C ₁₃	jC ₁₀	-C ₁₄	-C ₁₃	-jC ₁₀	C ₁₄	C ₁₃	-jC ₁₀	C ₁₄	-C ₁₃	t ₁₃
14	2	jC ₁₃	C ₁₄	C ₁₀	jC ₁₃	C ₁₄	-C ₁₀	-jC ₁₃	-C ₁₄	C ₁₀	-jC ₁₃	-C ₁₄	-C ₁₀	t ₁₄
15	2	jC ₁₃	-C ₁₄	C ₁₀	jC ₁₃	-C ₁₄	-C ₁₀	-jC ₁₃	C ₁₄	C ₁₀	-jC ₁₃	C ₁₄	-C ₁₀	t ₁₅
16	3	C ₀	C ₆	jC ₁₂	C ₀	C ₆	-jC ₁₂	-C ₀	-C ₆	jC ₁₂	-C ₀	-C ₆	-jC ₁₂	t ₁₆
•••					• • •						•••			
23	3	jC ₆	-C ₁₂	C_0	jC ₆	-C ₁₂	-C ₀	-jC ₆	C ₁₂	C_0	-jC ₆	C ₁₂	-C ₀	t ₂₀
24	4	C ₄	C ₈	jC ₁₅	C ₄	C ₈	-jC ₁₅	-C ₄	-C ₈	jC ₁₅	-C ₄	-C ₈	-jC ₁₅	t ₂₄
														•••
31	4	jC ₈	-C ₁₅	C ₄	jC ₈	-C ₁₅	-C ₄	-jC ₈	C ₁₅	C ₄	-jC ₈	C ₁₅	-C ₄	t ₃₁

NOTE: The code construction for code groups 0 to 15 using the SCH codes from code sets 1 and 2 is shown. The construction for code groups 16 to 31 using the SCH codes from code sets 3 and 4 is done in the same way.

7A.3 Evaluation of synchronisation codes

The evaluation of information transmitted in SCH on code group and frame timing is shown in table 7E, where the 32 code groups are listed. Each code group contains 4 specific scrambling codes, each scrambling code associated with a specific short and long basic midamble code.

Each code group is additionally linked to a specific t_{Offset} , thus to a specific frame timing. By using this scheme, the UE can derive the position of the frame border due to the position of the SCH sequence and the knowledge of t_{Offset} . The complete mapping of Code Group to Scrambling Code, Midamble Codes and t_{Offset} is depicted in table 7E.

Table 7E: Mapping scheme for Cell Parameters, Code Groups, Scrambling Codes, Midambles and t_{Offset}

CELL	Code	Α	ssociated Code	s	Associat
PARA- METER	Group	Scrambling Code	Long Basic Midamble Code	Short Basic Midamble Code	ed t _{Offset}
0	Group 0	Code 0	m _{PL0}	m _{SL0}	t_0
1		Code 1	m _{PL1}	m_{SL1}	
2		Code 2	m_{PL2}	m_{SL2}	
3		Code 3	m _{PL3}	m_{SL3}	
4	Group 1	Code 4	m_{PL4}	m_{SL4}	t ₁
5		Code 5	m _{PL5}	m _{SL5}	
6		Code 6	m _{PL6}	m _{SL6}	<u> </u>
7		Code 7	m _{PL7}	m _{SL7}	
			· .		
124	Group 31	Code 124	m _{PL124}	m _{SL124}	t ₃₁
125		Code 125	m _{PL125}	m _{SL125}	ļ
126		Code 126	m _{PL126}	m _{SL126}	_
127		Code 127	m _{PL127}	m _{SL127}	

Each cell shall cycle through two sets of cell parameters in a code group with the cell parameters changing each frame. Table 7F shows how the cell parameters are cycled according to the SFN.

Table 7F: Alignment of cell parameter cycling and SFN

Initial Cell Parameter Assignment	Code Group	Cell Parameter used when SFN mod 2 = 0	Cell Parameter used when SFN mod 2 = 1
0	Group 0	0	1
1		1	0
2		2	3
3		3	2
4	Group 1	4	5
5		5	4
6		6	7
7		7	6
		· · ·	
124	Group 31	124	125
125		125	124
126		126	127
127		127	126

8 Synchronisation codes for the 1.28 Mcps option

8.1 The downlink pilot channel (DwPCH)

The contents of DwPCH is composed of 64 chips of a SYNC-DL sequence, cf.[AA.1 Basic SYNC-DL sequence] and 32 chips of guard period (GP). The SYNC-DL code is not scrambled

There should be 32 different basic SYNC-DL codes for the whole system.

For the generation of the complex valued SYNC-DL codes of length 64, the basic binary SYNC-DL codes

 $\mathbf{s} = (s_1, s_2, \dots, s_{64})$ of length 64 shown in Table AA.1 are used. The relation between the elements \mathbf{s} and \mathbf{s} is given by:

$$\underline{s}_{i} = (j)^{i} \cdot s_{i} \qquad s_{i} \in \{1, -1\}, i = 1, ..., 64$$
 (1)

Hence, the elements \mathbf{S}_i of the complex SYNC-DL code \mathbf{S}_i are alternating real and imaginary.

The SYNC-DL is QPSK modulated and the phase of the SYNC-DL is used to signal the presence of the P-CCPCH in the multi-frame of the resource units of code $c_{Q=16}^{(k=1)}$ and $c_{Q=16}^{(k=2)}$ in time slot #0.

8.1.1 Modulation of the SYNC-DL

The SYNC-DL sequences are modulated with respect to the midamble $(m^{(1)})$ in time slot #0.

Four consecutive phases (phase quadruple) of the SYNC-DL are used to indicate the presence of the P-CCPCH in the following 4 sub-frames. In case the presence of a P-CCPCH is indicated, the next following sub-frame is the first sub-frame of the interleaving period. As QPSK is used for the modulation of the SYNC-DL, the phases 45, 135, 225, and 315° are used.

The total number of different phase quadruples is 2 (S1 and S2). A quadruple always starts with an even system frame number ((SFN mod 2) =0). Table 8 is showing the quadruples and their meaning.

Table 8: Sequences for the phase modulation for the SYNC-DL

Name	Phase quadruple	Meaning
S1	135, 45, 225, 135	There is a P-CCPCH in the next 4 sub-frames
S2	315, 225, 315, 45	There is no P-CCPCH in the next 4 sub-frames

8.2 The uplink pilot channel (UpPCH)

The contents in UpPCH is composed of 128 chips of a SYNC-UL sequence, cf. [AA.2 Basic SYNC-UL sequence] and 32chips of guard period (GP) .The SYNC-UL code is not scrambled.

There should be 256 different basic SYNC-UL codes (see Table AA.2) for the whole system.

For the generation of the complex valued SYNC-UL codes of length 128, the basic binary SYNC-UL codes

 $\mathbf{s} = (s_1, s_2, \dots, s_{128})$ of length 128 shown in Table AA.2 are used. The relation between the elements \mathbf{s} and \mathbf{s} is given by:

$$\underline{s}_{i} = (j)^{i} \cdot s_{i} \qquad s_{i} \in \{1, -1\}, i = 1, ..., 128$$
 (2)

Hence, the elements \mathbf{S}_i of the complex SYNC-UL code \mathbf{S}_i are alternating real and imaginary.

8.3 Code Allocation

Relationship between the SYNC-DL and SYNC-UL sequences, the scrambling codes and the midamble codes

Code Group			Associated Codes	
	SYNC-DL ID	SYNC-UL ID	Scrambling Code ID	Basic Midamble Code ID
Group 1	0	07	0	0
			1	1
			2	2
			3	3
Group 2	1	815	4	4
			5	5
			6	6
			7	7
			· ·	
Group 32	31	248255	124	124
-			125	125
			126	126
			127	127

Note: In a multi-frequency cell, primary frequency and secondary frequency use the same scrambling code and basic midamble code.

8.3Aa Code Allocation

For the dedicated carrier MBSFN, the basic preamble codes are segemted into two groups, even group and odd group, and the Basic preamble Code is described in [7].

Relationship between the scrambling codes and the preamble codes

Code subgroup		Associ	ated Codes							
	$P_{P,ev}$	ven group	P _{P,odd} group							
	Scrambling Code	Basic preamble Code	Scrambling Code	Basic preamble Code						
	ID	ID	ID	ID						
Sub-g1	0	0	4	1						
Sub-g2	8	2	12	3						
Sub-g3	16	4	20	5						
Sub-g4	24	6	28	7						

		·		
		-0		-0
Sub-g15	112	28	116	29
Sub-g16	120	30	124	31

9 Cell synchronisation codes

The cell synchronisation codes (CSCs) are constructed as so-called CEC sequences, i.e. concatenated and periodically extended complementary sequences. They are complex-valued sequences that are derived as cyclically offset versions from a set of possible constituent Golay complementary pairs.

The CSCs are chosen to have good aperiodic auto correlation properties. The aperiodic auto correlations of the applicable constituent Golay complementary pairs and every pair of their derived cyclically offset versions are complementary. Furthermore, orthogonality is preserved for all CSCs which are derived from the same constituent Golay complementary pair due to this complementary property.

The delay and weight matrices for the set of M=8 possible constituent Golay complementary pairs are listed in the table below:

Code ID m	Delay matrices D_m and weight matrices W_m of constituent Golay complementary pairs
0	$D_0 = \langle 512, 64, 128, 1, 16, 4, 256, 32, 8, 2 \rangle, W_0 = \langle 1, 1, 1, 1, -1, -1, 1, 1, 1, 1 \rangle$
1	$D_1 = \langle 2, 16, 32, 256, 1, 8, 128, 4, 512, 64 \rangle, W_1 = \langle 1, -1, 1, -1, 1, -1, -1, 1, -1, -1 \rangle$
2	$D_2 = \langle 16, 512, 32, 256, 4, 1, 64, 8, 2, 128 \rangle, W_2 = \langle -1, 1, 1, -1, -1, 1, -1, 1, -1, -1 \rangle$
3	$D_3 = \langle 512, 16, 8, 4, 2, 256, 128, 64, 32, 1 \rangle, W_3 = \langle -1, -1, -1, -1, -1, 1, -1, 1, 1, 1 \rangle$
4	D ₄ = <512, 128, 256, 32, 2, 4, 64, 1, 16, 8>, W ₄ = <1, -1, 1, -1, -1, -1, -1, -1, 1>
5	$D_5 = <1,2,4,64,512,16,32,256,128,8>,W_5 = <-1,1,1,1,1,-1,-1,1,-1,1>$
6	$D_6 = \langle 8, 16, 128, 2, 32, 1, 256, 512, 4, 64 \rangle, W_6 = \langle -1, -1, 1, 1, 1, 1, -1, -1, -1, 1 \rangle$
7	$D_7 = <1,2,128,16,256,32,8,512,64,4>,W_7 = <1,1,-1,-1,-1,-1,-1,-1,-1,-1>$

A constituent Golay complementary pair of length N = 1024, defined as:

$$s_m = \langle s_m(0), s_m(1), s_m(2), ..., s_m(1023) \rangle$$
 and $g_m = \langle g_m(0), g_m(1), g_m(2), ..., g_m(1023) \rangle$

shall be derived from the selected delay and weight matrices:

$$D_m = \langle D_m(0), D_m(1), D_m(2), ..., D_m(9) \rangle$$
 and $W_m = \langle W_m(0), W_m(1), W_m(2), ..., W_m(9) \rangle$

as follows.

Define:

$$a^{(0)} = \langle a^{(0)}(0), a^{(0)}(1), a^{(0)}(2), \dots, a^{(0)}(1023) \rangle = \langle 1, 0, 0, \dots, 0 \rangle$$
 and

$$b^{(0)} = \langle b^{(0)}(0), b^{(0)}(1), b^{(0)}(2), \dots, b^{(0)}(1023) \rangle = \langle 1, 0, 0, \dots, 0 \rangle.$$

Then, the elements of the set of auxiliary sequences:

$$a^{(n)} = \langle a^{(n)}(0), a^{(n)}(1), a^{(n)}(2), \dots, a^{(n)}(1023) \rangle$$
 and $b^{(n)} = \langle b^{(n)}(0), b^{(n)}(1), b^{(n)}(2), \dots, b^{(n)}(1023) \rangle$

are given by the recursive relations:

$$a^{(n+1)}(i) = a^{(n)}(i) + W_m(n) \times b^{(n)}(i - D_m(n))$$
 and

$$b^{(n+1)}(i) = a^{(n)}(i) - W_m(n) \times b^{(n)}(i - D_m(n))$$

with element index i = 0, 1, 2, ..., 1023 and iteration index n = 0, 1, 2, ..., 9. Operations on the element index shall be performed modulo 1024.

The elements of the constituent Golay complementary pairs s_m and g_m are then obtained from the output of the last iteration step using:

$$s_m(i) = a^{(10)}(i)$$
 and $g_m(i) = b^{(10)}(i)$ for $i = 0, 1, 2, ..., 1023$

From each applicable constituent Golay complementary pair s_m and g_m , up to K = 8 different cyclically offset pairs $s_m^{(k)}$ and $g_m^{(k)}$, with offset index k = 0, 1, 2, ..., K-1, of length 1152 chips can be derived. The complementary property of the respective aperiodic auto correlation is preserved for each particular pair of sequences $s_m^{(k)}$ and $g_m^{(k)}$. The generation of the K cyclically offset pairs from s_m and g_m is done in a similar way as the generation of the user midambles from a periodic basic midamble sequence as described in [7].

With N = 1024, K = 8, W = 128, the elements of a cyclically offset pair:

$$s_m^{(k)} = \langle s_m^{(k)}(0), s_m^{(k)}(1), s_m^{(k)}(2), \dots, s_m^{(k)}(1151) \rangle$$
 and $g_m^{(k)} = \langle g_m^{(k)}(0), g_m^{(k)}(1), g_m^{(k)}(2), \dots, g_m^{(k)}(1151) \rangle$

for a particular offset k, with k = 0, 1, 2, ..., K-1, shall be derived from the elements of the constituent Golay complementary pairs s_m and g_m using:

$$s_m^{(k)}(i) = (j)^i \times s_m(i+k \times W)$$
 and $g_m^{(k)}(i) = (j)^i \times g_m(i+k \times W)$ for $i = 0, 1, 2, ..., N-k \times W-1$,

$$s_m^{(k)}(i) = (j)^i \times s_m(i - N + k \times W)$$
 and $g_m^{(k)}(i) = (j)^i \times g_m(i - N + k \times W)$ for $i = N - k \times W$, $N - k \times W + 1$, ..., 1151.

Hence, the elements of $s_m^{(k)}$ and $g_m^{(k)}$ are alternating real and imaginary.

Note that both $s_m^{(0)}$ and $g_m^{(0)}$ simply correspond to s_m and g_m respectively, followed by its first W elements as post extension and that both $s_m^{(7)}$ and $g_m^{(7)}$ simply correspond to the last W elements of s_m and g_m in form of a pre extension, followed by s_m and g_m respectively.

Finally, the CSC $C_{CSC, m}^{(k)}$ derived from the m:th applicable constituent Golay complementary pair s_m and g_m , and for the k:th offset is then defined as a concatenation of $s_m^{(k)}$ and $g_m^{(k)}$ by:

$$C_{CSC,m}^{(k)} = \langle s_m^{(k)}(0), s_m^{(k)}(1), s_m^{(k)}(2), ..., s_m^{(k)}(1151), g_m^{(k)}(0), g_m^{(k)}(1), g_m^{(k)}(2), ..., g_m^{(k)}(1151) \rangle$$

where the leftmost element $s_m^{(k)}(0)$ in the sequence corresponds to the chip to be first transmitted in time. An CSC has therefore length 2304 chips.

Note that due to this construction method, the auto correlations for all CSCs derived from one particular constituent Golay complementary pair s_m and g_m can be obtained simultaneously and in sequential order from the sum of partial correlations with s_m and g_m , these CSCs remaining orthogonal.

CSCs derived according to above have complex values and shall not be subject to the channelisation or scrambling process, i.e. its elements represent complex chips for usage in the pulse shaping process at modulation.

Annex A (normative): Scrambling Codes

The applicable scrambling codes are listed below. Code numbers are referring to table 6 'Mapping scheme for Cell Parameters, Code Groups, Scrambling Codes, Midambles, Preambles and t_{offset} ' in subclause 6.3 'Evaluation of synchronisation codes'.

 $Code\{0 \square 4 \square 8 \square 12 \square ... \square 120 \square 124\}$ are used for downlink MBSFN operation.

Scrambling Code	ν ₁	ν ₂	ν ₃	V4	ν ₅	ν ₆	ν ₇	ν ₈	ν ₉	ν ₁₀	V ₁₁	V ₁₂	V ₁₃	V ₁₄	V ₁₅	V ₁₆
Code 0	-1	1	-1	-1	-1	1	-1	-1	1	-1	1	1	-1	1	-1	-1
Code 1	1	1	1	1	1	-1	1	-1	1	-1	-1	1	1	1	-1	-1
Code 2	1	-1	1	1	1	-1	1	1	-1	1	1	1	1	-1	-1	-1
Code 3	1	1	1	-1	-1	-1	-1	1	-1	-1	1	-1	-1	-1	1	-1
Code 4	1	1	1	-1	-1	-1	-1	1	1	1	1	-1	1	1	1	-1
Code 5	-1	1	1	-1	-1	-1	1	1	1	1	1	1	1	-1	1	-1
Code 6	-1	1	-1	-1	-1	1	-1	-1	-1	1	1	1	1	-1	-1	-1
Code 7	1	-1	1	-1	-1	-1	-1	-1	1	1	-1	-1	-1	1	1	-1
Code 8	1	1	1	-1	-1	-1	1	-1	1	1	-1	1	1	1	1	-1
Code 9	1	1	-1	1	1	1	1	-1	1	1	1	-1	-1	-1	1	-1
Code 10	1	-1	1	-1	1	1	1	1	-1	-1	1	1	-1	1	1	-1
Code 11	-1	1	1	1	1	-1	-1	-1	-1	1	-1	-1	-1	1	-1	-1
Code 12	-1	-1	1	-1	-1	-1	1	-1	-1	-1	-1	1	1	1	1	-1
Code 13	1	-1	1	1	1	-1	-1	-1	1	-1	-1	-1	-1	1	-1	-1
Code 14	1	-1	-1	-1	-1	1	-1	-1	1	-1	1	1	1	-1	-1	-1
Code 15	1	1	-1	-1	-1	1	1	-1	1	-1	1	-1	-1	-1	-1	-1
Code 16	1	-1	-1	1	-1	1	-1	1	-1	-1	-1	-1	1	1	-1	-1
Code 17	1	1	1	-1	1	1	1	-1	1	1	-1	1	-1	-1	1	-1
Code 18	-1	1	1	1	-1	1	-1	-1	-1	1	-1	-1	1	-1	-1	-1
Code 19	-1	1	-1	-1	1	-1	-1	-1	-1	1	1	1	-1	1	-1	-1
Code 20	-1	-1	-1	-1	1	-1	1	-1	-1	1	1	-1	1	1	-1	-1
Code 21	1	1	1	1	-1	-1	1	1	-1	1	1	-1	1	-1	1	-1
Code 22	1	-1	-1	-1	-1	1	1	1	-1	1	-1	-1	-1	1	-1	-1
Code 23	-1	1	1	1	-1	1	1	1	1	-1	1	1	-1	1	-1	-1
Code 24	-1	-1	1	-1	1	1	1	-1	-1	-1	-1	1	-1	-1	1	-1
Code 25	1	-1	1	1	1	-1	1	1	1	-1	1	1	-1	1	-1	-1
Code 26	1	-1	-1	-1	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1
Code 27	-1	1	-1	-1	-1	1	1	1	1	-1	-1	-1	-1	1	-1	-1
Code 28	-1	-1	-1	1	-1	-1	-1	1	-1	-1	-1	1	1	1	1	-1
Code 29	1	-1	1	1	-1	1	-1	-1	-1	1	-1	-1	-1	1	-1	-1
Code 30	-1	-1	-1	-1	-1	-1	1	1	1	-1	-1	1	1	-1	1	-1
Code 31	1	1	-1	-1	1	1	1	1	-1	1	-1	1	-1	1	1	-1
Code 32	1	-1	-1	-1	1	-1	1	1	-1	1	-1	-1	1	-1	-1	-1
Code 33	-1	-1	-1	1	1	1	1	-1	1	1	1	-1	1	1	1	-1
Code 34	1	-1	-1	-1	1	-1	-1	-1	1	-1	1	1	-1	1	-1	-1
Code 35	1	-1	1	1	-1	1	-1	-1	1	-1	-1	-1	1	-1	-1	-1
Code 36	1	1	-1	1	1	1	-1	1	-1	-1	-1	1	1	1	1	-1
Code 37	-1	-1	-1	1	-1	-1	1	-1	-1	-1	1	-1	1	1	1	-1
Code 38	-1	1	-1	-1	1	-1	1	1	1	-1	-1	-1	1	-1	-1	-1
Code 39	-1	1	1	1	1	-1	-1	-1	1	-1	-1	-1	1	-1	-1	-1
Code 40	-1	1	-1	1	-1	-1	-1	-1	-1	-1	1	1	-1	1	1	-1
Code 41	1	1	-1	1	-1	-1	1	-1	-1	-1	1	-1	-1	-1	1	-1

Scrambling Code	V 1	ν ₂	V3	V4	ν ₅	ν ₆	V 7	ν ₈	V 9	ν ₁₀	V ₁₁	V ₁₂	V ₁₃	V ₁₄	V ₁₅	V 16
Code 42	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1	1	-1	-1	-1
Code 43	-1	-1	1	1	-1	-1	-1	-1	-1	1	-1	1	-1	1	1	-1
Code 44	-1	-1	1	-1	-1	-1	-1	1	1	1	1	-1	-1	-1	1	-1
Code 45	-1	-1	1	-1	1	1	-1	1	1	1	1	-1	1	1	1	-1
Code 46	-1	1	1	-1	1	1	-1	-1	-1	-1	-1	-1	1	-1	1	-1
Code 47	1	-1	-1	1	1	1	-1	-1	1	1	1	1	1	-1	1	-1
Code 48	1	1	-1	1	1	1	-1	1	1	1	-1	1	-1	-1	1	-1
Code 49	-1	-1	1	1	-1	1	1	-1	-1	1	-1	1	-1	-1	-1	-1
Code 50	1	1	-1	1	-1	-1	1	-1	1	1	1	-1	1	1	1	-1
Code 51	1	-1	-1	1	1	1	-1	-1	1	-1	1	-1	-1	-1	-1	-1
Code 52	1	1	1	-1	1	1	1	-1	-1	-1	-1	1	1	1	1	-1
Code 53	-1	1	1	1	-1	-1	-1	1	-1	1	1	1	1	1	1	-1
Code 54	-1	-1	1	-1	-1	-1	1	-1	1	1	-1	1	-1	-1	1	-1
Code 55	-1	1	1	-1	-1	-1	-1	-1	1	-1	1	-1	1	1	-1	-1
Code 56	-1	1	1	1	-1	1	1	1	-1	1	1	1	1	-1	-1	-1
Code 57	-1	1	1	-1	-1	-1	1	1	-1	1	-1	1	-1	-1	-1	-1
Code 58	-1	1	-1	1	-1	-1	-1	-1	-1	1	1	-1	1	1	-1	-1
Code 59	1	1	-1	-1	-1	-1	-1	-1	1	-1	1	-1	-1	1	1	-1
Code 60	-1	1	1	-1	1	1	1	1	-1	1	-1	1	1	1	-1	-1
Code 61	-1	-1	1	1	1	-1	-1	1	1	-1	1	-1	-1	-1	-1	-1
Code 62	-1	1	-1	-1	1	1	1	-1	1	-1	-1	-1	-1	-1	1	-1
Code 63	-1	1	-1	1	-1	-1	1	1	1	-1	-1	1	-1	-1	-1	-1
Code 64	1	-1	-1	1	-1	-1	1	1	-1	-1	-1	-1	1	-1	1	-1
Code 65	-1	-1	-1	1	1	1	1	-1	-1	-1	1	-1	-1	-1	1	-1
Code 66	-1	-1	-1	-1	1	-1	-1	1	1	1	-1	-1	1	-1	1	-1
Code 67	-1	-1	-1	1	1	1	-1	1	1	1	-1	1	1	1	1	-1
Code 68	1	-1	1	1	-1	-1	-1	1	1	-1	-1	-1	-1	-1	1	-1
Code 69	-1	-1	1	-1	1	-1	-1	-1	1	1	1	-1	-1	1	-1	-1
Code 70	1	1	-1	1	-1	-1	-1	1	-1	-1	-1	1	-1	-1	1	-1
Code 71	1	-1	-1	1	-1	-1	-1	-1	-1	1	-1	1	1	1	-1	-1
Code 72	1	1	1	1	-1	1	1	-1	1	1	-1	-1	1	-1	1	-1
Code 73	-1	1	1	1	-1	-1	-1	1	-1	1	-1	-1	-1	-1	1	-1
Code 74	1	1	-1	1	-1	1	-1	-1	-1	-1	-1	1	1	-1	-1	-1
Code 75	1	1	-1	-1	1	-1	-1	1	-1	1	-1	1	-1	-1	-1	-1
Code 76	-1	1	-1	-1	-1	-1	-1	1	-1	1	1	1	-1	-1	1	-1
Code 77	-1	1	-1	1	1	1	1	1	-1	1	1	-1	1	1	-1	-1
Code 78	-1	1	-1	1	-1	1	1	-1	-1	-1	1	1	-1	-1	-1	-1
Code 79	-1	1	-1	1	1	1	-1	-1	-1	1	1	-1	-1	-1	-1	-1
Code 80	1	1	-1	1	-1	1	-1	-1	-1	-1	1	-1	-1	1	-1	-1
Code 81	1	1	1	1	1	-1	1	-1	-1	-1	1	1	-1	1	1	-1
Code 82	-1	1	-1	1	1	1	1	1	1	1	-1	-1	-1	1	1	-1
Code 83	1	1	-1	-1	1	-1	1	-1	-1	-1	-1	-1	-1	1	1	-1
Code 84	-1	-1	1	-1	1	1	-1	1	-1	-1	1	-1	-1	-1	1	-1
Code 85	-1	1	1	-1	-1	1	-1	1	1	1	1	1	1	1	-1	-1
Code 86	-1	-1	-1	1	-1	-1	-1	1	1	1	-1	1	-1	-1	1	-1
Code 87	1	1	-1	-1	-1	1	-1	1	1	1	1	1	-1	1	1	-1
Code 88	-1	1	1	-1	1	1	-1	-1	1	-1	1	-1	-1	-1	-1	-1
Code 89	-1	1	-1	-1	1	-1	1	1	-1	1	-1	-1	-1	1	-1	-1
Code 90	1	-1	-1	-1	-1	-1	-1	1	1	-1	1	1	-1	-1	1	-1
Code 91	-1	1	-1	-1	-1	-1	1	-1	1	-1	1	1	-1	-1	1	-1
Code 92	-1	1	1	-1	1	-1	1	-1	-1	-1	-1	-1	1	1	-1	-1
Code 93	-1	-1	<u>'</u> -1	-1	-1	1	1	-1	-1	-1	1	1	1	-1	1	-1
Code 94	1	-1	1	-1 -1	-1 -1	1	1	-1 -1	1	1	<u>'</u> -1	-1	<u>'</u> -1	-1 -1	<u>'</u> -1	-1
0000 0 1	<u>'</u>	'	'	_ '	<u>'</u>	_ '	_ '	'	_ '	'	'	'	'	'	'	'

Scrambling Code	ν ₁	V ₂	V 3	V4	V 5	ν ₆	ν ₇	ν ₈	V 9	V ₁₀	V 11	V 12	V 13	V 14	V 15	V 16
Code 95	1	1	1	1	1	-1	-1	1	-1	-1	1	1	1	-1	1	-1
Code 96	1	1	-1	-1	-1	1	1	-1	-1	-1	-1	-1	1	-1	1	-1
Code 97	1	1	-1	-1	1	-1	-1	1	1	1	1	1	1	-1	1	-1
Code 98	1	1	-1	1	1	-1	1	1	1	1	1	-1	1	-1	-1	-1
Code 99	1	-1	1	-1	1	-1	-1	1	-1	-1	1	1	-1	-1	-1	-1
Code 100	1	-1	1	1	-1	-1	1	-1	-1	1	-1	-1	-1	-1	1	-1
Code 101	1	1	1	1	-1	1	-1	1	1	1	-1	-1	-1	1	1	-1
Code 102	1	-1	1	-1	1	1	1	1	-1	1	1	-1	1	1	-1	-1
Code 103	-1	-1	1	-1	-1	1	-1	-1	1	1	1	-1	1	-1	-1	-1
Code 104	1	-1	1	1	-1	1	1	1	-1	1	1	1	-1	1	-1	-1
Code 105	1	1	1	1	1	1	-1	-1	1	-1	-1	1	1	-1	1	-1
Code 106	1	1	-1	-1	-1	1	-1	1	-1	-1	-1	-1	-1	1	1	-1
Code 107	-1	-1	-1	-1	1	1	-1	-1	-1	1	1	-1	1	-1	1	-1
Code 108	-1	-1	-1	1	-1	1	-1	-1	1	1	-1	1	1	-1	-1	-1
Code 109	-1	1	-1	1	1	-1	-1	1	1	1	-1	-1	-1	-1	-1	-1
Code 110	-1	-1	1	1	-1	1	-1	1	1	1	1	1	-1	1	1	-1
Code 111	1	1	1	-1	-1	1	1	1	1	1	-1	1	-1	1	-1	-1
Code 112	-1	-1	1	1	1	-1	1	-1	1	1	1	1	-1	1	1	-1
Code 113	1	1	-1	-1	1	-1	1	-1	1	1	1	1	-1	1	1	-1
Code 114	-1	-1	-1	1	1	-1	-1	-1	1	1	-1	1	-1	1	-1	-1
Code 115	1	-1	-1	1	1	1	1	1	1	-1	1	-1	1	1	-1	-1
Code 116	-1	1	1	1	1	-1	1	1	1	-1	1	1	1	-1	-1	-1
Code 117	1	1	1	-1	1	1	-1	1	-1	-1	1	-1	1	1	1	-1
Code 118	-1	-1	-1	-1	-1	1	-1	1	1	-1	-1	1	1	1	-1	-1
Code 119	-1	-1	-1	1	-1	1	1	1	-1	-1	1	-1	-1	1	-1	-1
Code 120	-1	-1	1	-1	1	-1	1	1	-1	-1	1	-1	-1	1	-1	-1
Code 121	-1	1	1	1	1	1	1	-1	1	-1	1	1	-1	-1	1	-1
Code 122	-1	-1	-1	1	1	-1	1	1	-1	-1	1	-1	1	-1	-1	-1
Code 123	1	-1	1	-1	1	1	-1	-1	1	-1	-1	1	-1	-1	-1	-1
Code 124	-1	-1	1	1	1	1	1	1	1	-1	1	-1	-1	1	1	-1
Code 125	1	-1	-1	1	1	-1	1	-1	1	1	1	1	1	1	-1	-1
Code 126	1	1	1	1	-1	1	-1	1	-1	1	1	-1	1	1	-1	-1
Code 127	1	-1	1	-1	-1	-1	-1	-1	1	-1	-1	1	1	1	-1	-1

Annex AA (normative): Synchronisation sequence

AA.1 Basic SYNC-DL sequence

Table AA.1: Basic SYNC-DL Codes

Code ID	SYNC-DL Codes of length 64
0	B3A7CC05A98688E4
1	9D559BD290606791
2	2CE7BA12A017C3A2
3	34511D20672F4712
4	9A772841474603F2
5	9109B1A5CE01F228
6	8FD429B3594501C0
7	25251354AA3F8C19
8	C9A3B8E0C043EA56
9	BA04B888E5BC1802
10	A735354299370207
11	74C3C8DA4415AE51
12	F4FD0458A0124663
13	A011D4E16C3D6064
14	BDA0661B0CAA8C68
15	8E31123F28928698
16	F095C1632E2906AB
17	B60B4A8A664071CF
18	AA094DCCE91E041A
19	C0C31CDA8A256807
20	D516964FB18C1890
21	30DE01834F4AACCE
22	8F700323BA5CAD34
23	1B50F4DEE0C1380C
24	443382164F56F2D1
25	E1E4005D49B846B4
26	040A97165330BFAA
27	C48E26881693AD78
28	D4354B2FE02361CC
29	5383AB6C8A10CE84
30	D417A730F2F12244
31	ABF0A0D905A939C4

AA.2 Basic SYNC-UL Codes

Table AA.2: Basic SYNC-UL Codes

0 C11C20F0D1807DB8859175B798EC094A 1 91278068081EC8E74543DBC1C9AD4235 2 38F5AEE2E513DB12A663BA04160103E5 3 7AA8A0A210F12A1E4332F2EDD33011FC 4 C180EA3B9BA1774EB9611BD249C4A508 5 B072A2C839489D496B98CE9D0132FBC9 6 B2723EAC6EB01667F2B33961C8074234 7 C4144AD060F0EC095E227B92CF7C8280 8 653036A10D3054146FCF815986C63A14 9 F899CA61435D64DC07FDF04C4A0C053A	1
2 38F5AEE2E513DB12A663BA04160103E5 3 7AA8A0A210F12A1E4332F2EDD33011FC 4 C180EA3B9BA1774EB9611BD249C4A508 5 B072A2C839489D496B98CE9D0132FBC9 6 B2723EAC6EB01667F2B33961C8074234 7 C4144AD060F0EC095E227B92CF7C8280 8 653036A10D3054146FCF815986C63A14 9 F899CA61435D64DC07FDF04C4A0C053A	
3 7AA8A0A210F12A1E4332F2EDD33011FC 4 C180EA3B9BA1774EB9611BD249C4A508 5 B072A2C839489D496B98CE9D0132FBC9 6 B2723EAC6EB01667F2B33961C8074234 7 C4144AD060F0EC095E227B92CF7C8280 8 653036A10D3054146FCF815986C63A14 9 F899CA61435D64DC07FDF04C4A0C053A	
4 C180EA3B9BA1774EB9611BD249C4A508 5 B072A2C839489D496B98CE9D0132FBC9 6 B2723EAC6EB01667F2B33961C8074234 7 C4144AD060F0EC095E227B92CF7C8280 8 653036A10D3054146FCF815986C63A14 9 F899CA61435D64DC07FDF04C4A0C053A	
5 B072A2C839489D496B98CE9D0132FBC9 6 B2723EAC6EB01667F2B33961C8074234 7 C4144AD060F0EC095E227B92CF7C8280 8 653036A10D3054146FCF815986C63A14 9 F899CA61435D64DC07FDF04C4A0C053A	;
6 B2723EAC6EB01667F2B33961C8074234 7 C4144AD060F0EC095E227B92CF7C8280 8 653036A10D3054146FCF815986C63A14 9 F899CA61435D64DC07FDF04C4A0C053A	3
7 C4144AD060F0EC095E227B92CF7C8280 8 653036A10D3054146FCF815986C63A14 9 F899CA61435D64DC07FDF04C4A0C053A)
8 653036A10D3054146FCF815986C63A14 9 F899CA61435D64DC07FDF04C4A0C053A	
9 F899CA61435D64DC07FDF04C4A0C053A)
	4
10 B56F2D6893A8051407F4C341D88DC7DC	;
11 DC0BE838242142EDE6413A72C88D74AA	١.
12 22A2FD86E4086C70A4860B13C76E579F	
13 A3CBC21322C97D2A02728E7875F39588	
14 D4EC4F694A082CB38E3B1558A0FCC89F	=
15 CC891141C4E216D235C15CF5D3F9B002	2
16 A1993114C50B77CB0C0725D1E22FD016	;
17 24F73A979DE52F82E8800CCB93842A59	
18 8F878FA04659842E294D8DEAB20BA2FD)
19 AC90B0442D70662B028CF76A6BECDF09)
20 D94A284DF64D7B0102F0E084C29C88C8	3
21 8603200C7596F24E865FD3815693358D	
22 B466B12CF433642BD8B08F1F452E0550	
23 86A3A1772C1C99FCA7DBBA0C312E34A0)
24 622A1889F72A9A2C042D46F08EFEE1AC	;
25 BF220A362BC0D3B0D7CE400954C6CFAB	
26 D28D73C52E89CF57905C502244F63616	
27 AD4E1C2103697D64D8B9D4C035D90548	3
28 8F081A9BA12B6C6BD024531AA984D21C	;
29 E4092429BE82988E1E3585BF6A6AE550	
30 08BD36E0A9C061782CB38B35B335CA56	3
31 1CDFF3CC2685D1C44F4A1059AB03F40A	١
32 506ED4E88FB1CECE3243F2A27A0221A4	ļ
33 846CF58A7AB613C83A24130B5778C0E2	
34 A2711A99E26A0C75AC026F4CFAECE893	3
35 D846EEBA2432AC05A01043C62579DCF	=
36 6B16B4E851CAF2121FC4CF88820C89E7	,
37 AA4889A78207674A74E10C6F2BE11D48	
38 8534CF8145BC991052814ED5C72709EE	
39 01AEF15D2290A84A607425746D9963C7	

40	999188F758245D5164FE16D852942C71					
41	CF71C008599287E446E30745BD56E2D2					
42	248414BA0DF8CDC4711FE7C8707ED0AD					
43	EB2E263EC016191C81AB714BFE4D2B30					
44	862082A7482FAC1C499793A0D8CED670					
45	DE2C22B2783AB75A7342608DE413840A					
46	E31AA60B727F2CA2A78DAAC10665011D					
47	CEF6CD06509870AC9E0177ACD550921D					
48	E52C84D499FFCDC287581691471540F2					
49	B33BF6551A4322504BEE0930BCA1EC68					
50	555BE6886D0FC43D72315E6C6D384148					
51	8444F67451EE23CE1240C90F0B52A492					
52	5C290D28E84060E69D09788A261B10FF					
53	337E0C35E83CD38CCC5D45804241F952					
54	A7879F0D31A8982A01EE6AC4952984DC					
55	A37F506508928C70A83D69A2373781B9					
56	42F55208EE12909803A7CBEB19B5419E					
57	57E5E268A328FCC9ED04B9E5420AC702					
58	EB033AD1222F84D8642C4E3FAAD28206					
59	98EE1415F026AC0E862C520451697DD0					
60	6A0528AEA4B7CD6702660D81F8821E19					
61	763D626A87C603BCB09E1A4C800A378F					
62	EEA61897879289340C23F669D6A03762					
63	A6571B3CC2D0E04F017ACC808B92DCE7					
64	DDF88B52EA1831D293A803CF23C8C471					
65	6CA4D333A2684140475DAB491F61C17A					
66	A7D2AD23043989A13289F7C3E135580A					
67	B1C752FA66B41C81904EDE27EA000E2E					
68	8694BE3CC1CB36BE2A095F89CC619080					
69	9C20334E1BBC596B25E151180BF99940					
70	484256214F81070DD9C49A2B05A43DCE					
71	401A20BCBE29B7438A7AEE44635A9E23					
72	8858585C3239CBF628033FA0DF189378					
73	EFA36404C1BA5118CC5F9052FD28D9C3					
74	155609873D8A042D496E6477B747C4F8					
75	8446077883A6D7D2549CC9742E3FD023					
76	E630142B189AA209371A6F0FFDBC30A7					
77	C46060535AC6DBB2095F1D7826D0CD5C					
78	E00D19E48797148B28DEDA9D429362E2					
79	645DE447E938485489416CAFCC1C571F					
80	DA10AFBF2AE61C593A1D88584DE30598					
81	BB248AEA5FD3FE210CD48FC401E1A686					
82	A89F146BD9191F445301C081CB6F5625					
83	15BBF04F247C59150208949EB6B9CC58					

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237 A06A0D822165D3F3416B47419ECCB547	
238 1D2068039A32B7EF728914ECE07CB416	
239 64C0CF81F78E8823ECC8661A5295422A	
240 902A7243F593F2180E5A306A8438E6A9	
241 A4CCED356D56BF1B41C28E1504301FE8	
242 82AE90E2F76B3055A2E3A966025CC01A	
243 8B90D5A62364E18574145C5895CEFF60	
244 43F7EA1AB0D19032551AD9DE21307353	
245 DD5D8424AC60360B1C14E65815C9B15E	
246 C632A67382ECB2681DFB8525140E2878	
247 3A6ACF212B6F8B9C53FF224C2E00C16C	
248 86A90C267B1171093F362FE5CB14E3A0	
249 EA262EC36E6589C3BB005426AF2590F4	
250 200F03126C5B0D7B901128E7757C5F70	
251 68FC090C2221AA98BF0D24E85066EFC2	
252 9E26CEC67832FC42A87E92FA1015212E	
253 ACD889634F79506F2582EA03240F2A07	
254 AA65407E1F4A33BF9A62860A3D6A4CC0	
255 B1B950AC76A608AA32D04B03C7FF24D3	

Annex B (informative): Generalised Hierarchical Golay Sequences

B.1 Alternative generation

The generalised hierarchical Golay sequences for the PSC described in 7.1 may be also viewed as generated (in real valued representation) by the following methods:

Method 1.

The sequence y is constructed from two constituent sequences x_1 and x_2 of length n_1 and n_2 respectively using the following formula:

- $y(i) = x_2(i \mod n_2) * x_1(i \operatorname{div} n_2), i = 0 ... (n_1 * n_2) - 1.$

The constituent sequences x_1 and x_2 are chosen to be the following length 16 (i.e. $n_1 = n_2 = 16$) sequences:

- x_1 is defined to be the length 16 (N⁽¹⁾=4) Golay complementary sequence obtained by the delay matrix D⁽¹⁾ = [8, 4, 1,2] and weight matrix W⁽¹⁾ = [1, -1, 1,1].
- x₂ is a generalised hierarchical sequence using the following formula, selecting s=2 and using the two Golay complementary sequences x₃ and x₄ as constituent sequences. The length of the sequence x₃ and x₄ is called n₃ respectively n₄.
- $x_2(i) = x_4(i \mod s + s*(i \operatorname{div} sn_3)) * x_3((i \operatorname{div} s) \mod n_3), i = 0 \dots (n_3*n_4) 1.$
- x_3 and x_4 are defined to be identical and the length 4 ($N^{(3)} = N^{(4)} = 2$) Golay complementary sequence obtained by the delay matrix $D^{(3)} = D^{(4)} = [1, 2]$ and weight matrix $W^{(3)} = W^{(4)} = [1, 1]$.

The Golay complementary sequences x₁,x₃ and x₄ are defined using the following recursive relation:

$$a_0(k) = \delta(k) \text{ and } b_0(k) = \delta(k);$$

$$a_n(k) = a_{n-1}(k) + W^{(j)}_{n} \cdot b_{n-1}(k - D^{(j)}_{n});$$

$$b_n(k) = a_{n-1}(k) - W^{(j)}_{n} \cdot b_{n-1}(k - D^{(j)}_{n});$$

$$k = 0, 1, 2, ..., 2^{**}N^{(j)} - 1;$$

$$n = 1, 2, ..., N^{(j)}.$$

The wanted Golay complementary sequence x_j is defined by a_n assuming $n=N^{(j)}$. The Kronecker delta function is described by δ , k,j and n are integers.

Method 2

The sequence y can be viewed as a pruned Golay complementary sequence and generated using the following parameters which apply to the generator equations for a and b above:

(a) Let
$$j = 0$$
, $N^{(0)} = 8$.

(b)
$$[D_1^0, D_2^0, D_3^0, D_4^0, D_5^0, D_6^0, D_7^0, D_8^0] = [128, 64, 16, 32, 8, 1, 4, 2].$$

(c)
$$[W_1^0, W_2^0, W_3^0, W_4^0, W_5^0, W_6^0, W_7^0, W_8^0] = [1, -1, 1, 1, 1, 1, 1, 1, 1]$$
.

(d) For
$$n = 4$$
, 6, set $b_4(k) = a_4(k)$, $b_6(k) = a_6(k)$.

Annex C (informative): Change history

					Change history		
Date	TSG #	TSG Doc.	CR	Rev	Subject/Comment	Old	New
14/01/00	RAN_05	RP-99593	-		Approved at TSG RAN #5 and placed under Change Control	-	3.0.0
14/01/00	RAN_06	RP-99696	001	01	Primary and Secondary CCPCH in TDD	3.0.0	3.1.0
14/01/00	RAN_06	RP-99695	003	1	Alignment of Terminology Regarding Spreading for TDD Mode	3.0.0	3.1.0
14/01/00	RAN 06	RP-99696	004	-	Code allocation for Case 3	3.0.0	3.1.0
14/01/00	-	-	-		Change history was added by the editor	3.1.0	3.1.1
31/03/00	RAN_07	RP-000069	002	3	Cycling of cell parameters	3.1.1	3.2.0
31/03/00	RAN 07	RP-000069	005	-	Removal of Synchronisation Case 3 in TDD	3.1.1	3.2.0
31/03/00	RAN_07	RP-000069	006	1	Signal Point Constellation	3.1.1	3.2.0
03/05/00	-	-	-	-	Revision marks accepted to create clean version	3.2.0	3.2.1
26/06/00	RAN_08	RP-000273	800	-	Editorial Modifications for 25.223	3.2.1	3.3.0
26/06/00		RP-000273		-	Editorial modification of 25.223	3.2.1	3.3.0
26/06/00		RP-000273		-	Editorial modification of 25.223	3.2.1	3.3.0
26/06/00	RAN_08	RP-000273		2	Editorial modification of 25.223	3.2.1	3.3.0
26/06/00	RAN_08	RP-000273		2	Modified code sets on SCH for cell search in UTRA TDD	3.2.1	3.3.0
26/06/00	RAN 08	RP-000273		1	Editorial update of TS25.223	3.2.1	3.3.0
23/09/00	RAN_09	RP-000346		1	Gain Factors for TDD Mode	3.3.0	3.4.0
23/09/00	RAN_09	RP-000346	014	-	Synchronisation codes	3.3.0	3.4.0
16/03/01	RAN 11		-	-	Approved as Release 4 specification (v4.0.0) at TSG RAN #11	3.4.0	4.0.0
16/03/01		RP-010064		1	Code specific phase offsets for TDD	3.4.0	4.00
16/03/01	RAN_11	RP-010073		-	Cell synchronisation codes for R'4 Node B sync over air interface in	3.4.0	4.0.0
10/03/01	IXAIN_III	KF-010073	010	-	UTRA TDD	3.4.0	4.0.0
16/03/01	RAN_11	RP-010071	017	1	Inclusion of 1.28Mcps TDD in TS 25.223	3.4.0	4.0.0
15/06/01	RAN_12	RP-010337	019	-	Addition to the abbreviation list and definition of a constant	4.0.0	4.1.0
21/09/01	RAN_13	RP-010524	021	1	Clarification of notations in TS25.221 and TS25.223	4.1.0	4.2.0
21/09/01	RAN_13	RP-010530	022	1	Clarification of notations in TS25.221 and TS25.223	4.1.0	4.2.0
14/12/01	RAN_14	RP-010748	023	-	A correction of Figure 7 in subclause 7.7.2 of TS 25.223	4.2.0	4.3.0
08/03/03	RAN_15	RP-020051	025	1	Removal of quantisation of bj gain factor when calculated from a reference TFC	4.3.0	4.4.0
08/03/03	RAN_15	RP-020051	028	-	Channelisation code-specific multiplier operation under autonomous SF change	4.3.0	4.4.0
08/03/03	RAN_15	RP-020051	030	-	Alignment of gamma(i) gains of 25.223 with SIR target of WG2 25.331	4.3.0	4.4.0
08/03/03	DAN 15	RP-020058	026	1	CR to include HSDPA in TS25.223	4.4.0	5.0.0
07/06/02		RP-0200317	031	-	Correction of SPC for 16QAM in TDD	5.0.0	5.1.0
22/12/02	RAN_18	RP-020852	033	-	Editorial modification to the section numberings	5.1.0	5.2.0
25/03/03	RAN_19	RP-030140		3	Miscellaneous Corrections	5.2.0	5.3.0
13/01/04	RAN 22	1030140	-	-	Created for M.1457 update	5.3.0	6.0.0
12/12/05		RP-050728			Correction to 16QAM modulation function	6.0.0	6.1.0
20/03/06	RAN_31	RP-060079		-	Introduction of 7.68Mcps TDD option	6.1.0	7.0.0
12/06/06		RP-060295		-	Correction of the values of weight factors	7.0.0	7.1.0
29/09/06		RP-060492			Introduction of E-DCH for 3.84Mcps and 7.68Mcps TDD	7.1.0	7.1.0
13/03/07		RP-070118		1	Introduction of E-DCH for 1.28Mcps TDD	7.2.0	7.3.0
30/05/07		RP-070385		1	Support for MBSFN operation	7.3.0	
30/05/07		RP-070386		-	Support for 1.28Mcps TDD MBSFN operation	7.3.0	7.4.0
11/09/07		RP-070560		1	Introduction of multi-frequency operation for 1.28Mcps TDD	7.4.0	7.5.0
		RP-070650		1	More improvement on dedicated carrier for 1.28Mcps TDD MBMS	7.5.0	
27/11/07 04/03/08	RAN_39		-		Creation of Release 8 further to RAN_39 decision	7.6.0	7.6.0 8.0.0
28/05/08		RP-080356	0052	-	Introduction the 64QAM constellation for 1.28 Mcps TDD HSDPA	8.0.0	8.1.0
28/05/08	RAN_40			-	Correction of uplink multicode capability for 1.28 Mcps TDD FISDEA	8.0.0	8.1.0
03/12/08	RAN 42	RP-080347		1	Support for 3.84 Mcps MBSFN IMB operation	8.1.0	8.2.0
03/12/08	RAN_42 RAN_43	RP-090239		<u> </u>	TFCI for Secondary CCPCH frame type 2 with 16QAM	8.2.0	8.3.0
03/03/09	RAN_43			-	Specification of scrambling codes and code groups for MBSFN IMB	8.2.0	
	RAN_44	RP-090239 RP-090531		-	Minor correction for MBSFN IMB		8.3.0
26/05/09		NF-090531	0059	-		8.3.0	8.4.0
01/12/09	RAN_46	- DD 101017	0060	2	Creation of Release 9 (v9.0.0) at RAN#46	8.4.0	9.0.0
07/12/10	RAN_50	RP-101317			Introduction of MC-HSUPA for 1.28Mcps TDD	9.0.0	10.0.0
07/12/10	RAN_50	RP-101319	0063	1	Introduction of MU-MIMO for 1.28Mcps TDD	9.0.0	10.0.0

History

Document history					
V10.0.0	May 2011	Publication			