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ANSI-C code for the floating-point Adaptive Multi-Rate (AMR) speech codec

(3GPP TS 26.104 version 16.0.0 Release 16)





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Foreword

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1 Scope

This Technical Standard (TS) contains an electronic copy of the ANSI-C code for a floating-point implementation of the Adaptive Multi-Rate codec. This floating-point codec specification is mainly targeted to be used in multimedia applications such as the 3G-324M terminal specified in 3GPP TS 26.110, or in packet-based (e.g., H.323) applications. The bit-exact fixed-point ANSI-C code in 3GPP TS 26.073 remains the preferred implementation for all applications, but the floating-point codec may be used instead of the fixed-point codec when the implementation platform is better suited for a floating-point implementation. It has been verified that the fixed-point and floating-point codecs interoperate with each other without any artefacts.

The floating-point ANSI-C code in this specification is the only standard conforming non-bit-exact implementation of the Adaptive Multi Rate speech transcoder (3GPP TS 26.090 [2]), Voice Activity Detection (3GPP TS 26.094 [6]), comfort noise generation (3GPP TS 26.092 [4]), and source controlled rate operation (3GPP TS 26.093 [5]). The floating-point code also contains example solutions for substituting and muting of lost frames (3GPP TS 26.091 [3]).

The fixed-point specification in 26.073 shall remain the only allowed implementation for the 3G mandatory speech service and the use of the floating-point codec is strictly limited to other services.

The floating-point encoder in this specification is a non-bit-exact implementation of the fixed-point encoder producing quality indistinguishable from that of the fixed-point encoder. The decoder in this specification is functionally a bit-exact implementation of the fixed-point decoder, but the code has been optimized for speed and the standard fixed-point libraries are not used as such.

2 Normative references

The following documents contain provisions which, through reference in this text, constitute provisions of the present document.

- References are either specific (identified by date of publication, edition number, version number, etc.) or non-specific.
- For a specific reference, subsequent revisions do not apply.
- For a non-specific reference, the latest version applies. In the case of a reference to a 3GPP document (including a GSM document), a non-specific reference implicitly refers to the latest version of that document *in the same Release as the present document*.
- [1] 3GPP TS 26.074: "AMR Speech Codec; Test sequences". [2] 3GPP TS 26.090: "AMR Speech Codec; Speech transcoding". [3] 3GPP TS 26.091: "AMR Speech Codec; Substitution and muting of lost frames". 3GPP TS 26.092: "AMR Speech Codec; Comfort noise aspects". [4] [5] 3GPP TS 26.093: "AMR Speech Codec; Source controlled rate operation". 3GPP TS 26.094: "AMR Speech Codec; Voice Activity Detection". [6] [7] 3GPP TS 26.073: "ANSI-C code for the Adaptive Multi Rate speech codec". [8] 3GPP TS 26.101: "AMR Speech Codec Frame Structure". RFC 3267: "A Real-Time Transport Protocol (RTP) Payload Format and File Storage Format for [9] Adaptive Multi-Rate (AMR) and Adaptive Multi-Rate Wideband (AMR-WB) Audio Codecs", June 2002.

3 Definitions and abbreviations

3.1 Definitions

Definition of terms used in the present document, can be found in 3GPP TS 26.090 [2], 3GPP TS 26.091 [3], 3GPP TS 26.092 [4], 3GPP TS 26.093 [5], and 3GPP TS 26.094 [6].

3.2 Abbreviations

For the purpose of the present document, the following abbreviations apply:

ANSI American National Standards Institute
ETS European Telecommunication Standard
GSM Global System for Mobile communications

I/O Input/Output

RAM Random Access Memory ROM Read Only Memory

4 C code structure

This clause gives an overview of the structure of the floating-point C code and provides an overview of the contents and organization of the C code attached to this document. The basic structure of the floating-point C code follows that of the bit-exact fixed-point code [7].

The C code has been verified on the following systems:

- IBM PC/AT compatible computers with Windows NT40 and Microsoft Visual C++ v.5.0 compiler;
- HP workstations and GNU gcc compiler;
- IBM PC/AT compatible computers with Linux operating system and GNU gcc compiler;

ANSI-C 9899 was selected as the programming language because portability was desirable

4.1 Contents of the C source code

The C code distribution has all files in the root level.

The files with suffix "c" contain the source code and the files with suffix "h" are the header files. The ROM data is contained in "rom" files with suffix "h".

The C code does not contain any speech coder installation verification data files. Verification for the bit-exact decoder is defined in specification 3GPP TS 26.073 [7].

Makefiles are provided for the platforms in which the C code has been verified (listed above). Once the software is installed, this directory will have a compiled version of encoder and decoder and all the object files.

4.2 Program execution

The Adaptive Multi-Rate codec is implemented in two programs:

- (encoder) speech encoder;
- (decoder) speech decoder.

The programs should be called like:

encoder [-dtx] mode speech_file bitstream_file

or

encoder [-dtx] -modefile=mode_file speech_file bitstream_file

decoder <parameter file> <speech output file>

The speech files contain 16-bit linear encoded PCM speech samples and the parameter files contain encoded speech data and some additional flags.

See the file readme.txt for more information on how to run the *encoder* and *decoder* programs.

4.3 Coding style

The C code has been written according to structuring conventions used in 3GPP TS 26.073 [7]. Encoder and decoder state structures are allocated and initialized with special initializing functions. There are no separate functions for each module, as opposed to the fixed-point implementation in 3GPP TS 26.073 [7].

4.4 Code hierarchy

The code hierarchy follows the one specified in 3GPP TS 26.073 [7].

Figures 1 to 4 are call graphs that show the functions used in the speech codec, including the functions of VAD, DTX, and comfort noise generation.

Each column represents a call level and each cell a function. The functions contain calls to the functions in rightwards neighbouring cells. The time order in the call graphs is from the top downwards as the processing of a frame advances. All standard C functions, such as printf(), fwrite(), etc., have been omitted.

The encoder call graph is broken down into three separate call graphs, shown in Tables 1 to 3.

Table 1: Speech encoder call structure

Speech_Encode_Frame	Pre_Process				
	cod_amr	vad	filter_bank	first_filter_stage	
				filter5	
				filter3	
				level_calculation	
			vad_decision	complex_estimate_adapt	
				complex_vad	
				noise_estimate_update	update_cntrl
				hangover_addition	
		tx_dtx_handler		_	
		lpc	Autocorr		
			Levinson		
		Isp	Az_lsp	Chebps	
			Q_plsf_5	Lsp_lsf	
				Lsf_wt	
				Vq_subvec	
				Vq_subvec_s	
				Reorder_lsf	
				Lsf_lsp	
			Int_lpc_1and3_2	Lsp_az	Get_lsp_pol
			Int_lpc_1and3	Lsp_az	Get_lsp_pol
			Q_plsf_3	Lsp_lsf	
				Lsf_wt	
				Vq_subvec3	
				Vq_subvec4	
				Reorder_lsf	
				Lsf_lsp	
			Int_lpc_1to3_2	Lsp_az	Get_lsp_pol
			Int_lpc_1to3	Lsp_az	Get_lsp_pol
		dtx_buffer	Dotproduct40		
		dtx_enc	Lsp_lsf		
			Reorder_lsf		
			Lsf_lsp		
			Q_plsf_3	Lsp_lsf	
				Lsf_wt	
				Vq_subvec3	
				Vq_subvec4	
				Reorder_lsf	
				Lsf_lsp	
		check_lsp			
		pre_big	Weight_Ai		
			Residu		
			Syn_filt		
		ol_ltp	Pitch_ol	vad_tone_detection_update	
				Lag_max	vad_tone_detection
				comp_corr	
				hp_max	
			Pitch_ol_wgh	comp_corr	
				Lag_max_wght	vad_tone_detection_update
					vad_tone_detection
				gmed_n	
				hp_max ²	
		vad_pitch_detection		1 1 =	
		subframePreProc	Weight_Ai	7	
			Syn_filt	7	
			Residu	7	
		cl_ltp	Pitch_fr	getRange	
				Norm_Corr	Dotproduct40
				searchFrac	Interpol_3or6
				Enc_lag3	
				Enc_lag6	
			Pred_lt_3or6		
			G_pitch	Dotproduct40	
			check_gp_clipping	Эзргодосто	
			q_gain_pitch	┥	
		cbsearch	see Table 2	\dashv	
		gainQuant	see Table 3	Ⅎ	
				⊢	
		update_gp_clipping	Copy Syn, filt	\dashv	
		subframePostProc	Syn_filt	∃	

Table 2: cbsearch call structure

cbsearch	code 2i40 9bits	cor_h_x	Dotproduct40
CD3Carcii	code_zi+o_sbits	set_sign	Dotproduct40
		cor h	Dotproduct40
			Dotproduct40
		search_2i40_9bits	4
	1 0:40 441:	build_code_2i40_9bits	D : 1 :10
	code_2i40_11bits	cor_h_x	Dotproduct40
		set_sign	<u> </u>
		cor_h	Dotproduct40
		search_2i40_11bits	1
		build_code_2i40_11bits	
	code_3i40_14bits	cor_h_x	Dotproduct40
		set_sign	
		cor_h	Dotproduct40
		search_3i40	-
		build_code_3i40_14bits	
	code_4i40_17bits	cor_h_x	Dotproduct40
		set_sign	
		cor_h	Dotproduct40
		search_4i40	
		build_code_4i40	1
	code_8i40_31bits	cor_h_x	Dotproduct40
		set_sign12k2	Dotproduct40
		cor_h	Dotproduct40
		search 8i40	
		build_code_8i40_31bits	
		compress_code	compress10
	code 10i40 35bits	cor h x	Dotproduct40
		set_sign12k2	Dotproduct40
		cor h	Dotproduct40
		search 10i40	
		build code 10i40 35bits	1
		q_p	1
	1	[M_P	4

Table 3: gainQuant call structure

gainQuant	gc_pred	Dotproduct40	
	calc_filt_energies	Dotproduct40	
	Dotproduct40		
	MR475_update_unq_pred		
	MR475_gain_quant	gc_pred	Dotproduct40
	q_gain_code		
	MR795_gain_quant	q_gain_pitch	
		MR795_gain_code_quant3	
		calc_unfilt_energies	Dotproduct40
		gain_adapt	Gmed_n_f
		MR795_gain_code_quant_mod	
	Qua_gain		

Speech_Decode_Frame rx_dtx_handler Decoder_amr_reset dtx dec Сору Lsf Isp D_plsf_3 Lsf_lsp pseudonois Lsp_lsf Reorder_lsf Lsp_Az Get_lsp_pol A Refl Log2_norm Log2 Pow2 Build_CN_code pseudonoise Syn_filt Lsf_lsp lsp_avg Build_CN_param Lsf_lsp D_plsf_3 Int_lpc_1to3 Get_lsp_pol Lsp_Az D_plsf_5 Reorder_lsf Lsf_lsp Int_lpc_1and3 Dec_lag3 Get_lsp_pol Lsp_Az Pred_lt_3or6_40 Dec_lag6 decode 2i40 9bits decode_2i40_11bits decode_3i40_14bits decode 4i40 17bits decode_8i40_31bits decompress10 decompress_codewords ec_gain_pitch gmed_n d_gain_pitch ec_gain_pitch_update decode_10i40_35bits Log2 Log2_norm gc_pred Log2 Log2_norm Log2 norm Pow2 gc_pred_update ec_gain_code gmed_n gc_pred_average_limited gc_pred_update ec_gain_code_update d_gain_code Log2_norm ac pred Log2 Log2_norm Pow2 gc_pred_update Int_lsf Cb_gain_average ph_disp sqrt_l_exp gmed_n Ex_ctrl agc2 Syn_filt gmed_n Bgn_scd dtx_dec_activity_update Copy Log2_norm Log2 lsp_avg Post_Filter Residu40 Syn_filt energy_nev energy_old Post_Process

Table 4: Speech decoder call structure

4.5 Variables, constants and tables

The data types of variables and tables used in the floating-point implementation are signed integers in 2's complement representation, defined by:

Word8 8 bit variable

UWord8 8 bit unsigned variable

Word16 16 bit variable Word32 32 bit variable

Floating-point numbers use the IEEE (Institute of Electrical and Electronics Engineers) format:

Float32 8 bit exponent, 23 bit mantissa, 1 bit sign

Float64 11 bit exponent, 52 bit mantissa, 1 bit sign

Furthermore some **enum** types are used, all possible to represent with one byte, and a Boolean **Flag**.

4.5.1 Description of constants used in the C code

Constants for the codec are defined in rom (h) files.

4.5.2 Description of fixed tables used in the C code

This section contains a listing of all fixed tables sorted by source file name and table name.

Table 5: Speech encoder fixed tables

File	Table name	Type[Length]	Description
rom_enc.h	trackTable	Word8[4*5]	track table for algebraic code book search (MR475, MR515)
rom_enc.h	gamma1	Float32[10]	spectral expansion factors
rom_enc.h	gamma1_12k2	Float32[10]	spectral expansion factors
rom_enc.h	gamma2	Float32[10]	spectral expansion factors
rom_enc.h	b60	Float32]61]	interpolation filter coefficients
rom_enc.h	startPos1	Word16[2]	track start search position for first pulse
rom_enc.h	startPos2	Word16[4]	track start search position for second pulse
rom_enc.h	startPos	Word16[16]	track start search position
rom_enc.h	corrweight	Float32[251]	weighting of the correlation function in open loop LTP search (MR102)
rom_enc.h	qua_gain_pitch	Float32[16]	adaptive codebook gain quantization table (MR795)
rom_enc.h	qua_gain_pitch_MR12	Float32[16]	adaptive codebook gain quantization table (MR122)
	2		
rom_enc.h	qua_gain_code	Float32[64]	fixed codebook gain quantization table (MR122, MR795)
rom_enc.h	gray	Word8[8]	gray coding table
rom_enc.h	grid	Float32[61]	grid points at which Chebyshev polynomials are evaluated
rom_enc.h	b24	Float32[25]	interpolation filter coefficients
rom_enc.h	lag_wind	Float32[10]	lag window table
rom_enc.h	lsp_init_data	Float32[10]	initialization table for lsp history in DTX
rom_enc.h	past_rq_init	Float32[80]	initialization table for the MA predictor in DTX
rom_enc.h	mean_lsf_3	Float32[10]	LSF means (not in MR122)
rom_enc.h	mean_lsf_5	Float32[10]	LSF means (MR122)
rom_enc.h	pred_fac	Float32[10]	LSF prediction factors (not in MR122)
rom_enc.h	dico1_lsf_3	Float32[3*256]	1st LSF quantizer (not in MR122 and MR795)
rom_enc.h	dico2_lsf_3	Float32[3*512]	2 nd LSF quantizer (not in MR122)
rom_enc.h	dico3_lsf_3	Float32[4*512]	3rd LSF quantizer (not in MR122, MR515 and MR475)
rom_enc.h	mr515_3_lsf	Float32[4*128]	3 rd LSF quantizer (MR515 and MR475)
rom_enc.h	mr795_1_lsf	Float32[3*512]	1st LSF quantizer (MR795)
rom_enc.h	dico1_lsf_5	Float32[4*128]	1st LSF quantizer (MR122)
rom_enc.h	dico2_lsf_5	Float32[4*256]	2 nd LSF quantizer (MR122)
rom_enc.h	dico3_lsf_5	Float32[4*256]	3 rd LSF quantizer (MR122)
rom_enc.h	dico4_lsf_5	Float32[4*256]	4 th LSF quantizer (MR122)
rom_enc.h	dico5_lsf_5	Float32[4*64]	5 th LSF quantizer (MR122)
rom_enc.h	table_gain_MR475	Float32[4*256]	gain quantization table (MR475)
rom_enc.h	table_gain_highrates	Float32[128*3]	gain quantization table (MR67, MR74 and MR102)
rom_enc.h	table_gain_lowrates	Float32[64*3]	gain quantization table (MR515 and MR59)
rom_enc.h	window_200_40	Float32[240]	LP analysis window (not in MR122)
rom_enc.h	window_160_80	Float32[240]	1 st LP analysis window (MR122)
rom_enc.h	window_232_8	Float32[240]	2 nd LP analysis window (MR122)
rom_enc.h	corrweight	Float32[251]	correlation weights
rom_enc.h	mode_dep_parm	Word8[8*9]	parameters defining the adaptive codebook search per mode

Table 6: Speech decoder fixed tables

rom., dech. ho. down dxx.log, en. adjust Word16[9] rom. dech. podown Word32[7] rom. dech. pred level adjustments for ech mode rom., dech. pred pred Word32[7] rom. dech. pred JMR122 Word32[7] word32[4] algebraic code book gain MA predictor coefficients rom., dech. gamma3, JMR122 Word32[10] word32[10] rom., dech. gamma4, JMR122 Word32[10] word32[10] word32[10] word32[10] word32[10] word32[10] spectral expansion factors rom., dech. bitro, JMR475 Word32[10] word16[7] rom., dech. bitro, JMR59 Word16[7] rom.dech. bitro, JMR59 Word16[7] rom.dech. bitro, JMR67 Word16[7] rom.dech. bitro, JMR67 Word16[7] rom.dech. bitro, JMR67 Word16[7] rom.dech. bitro, JMR74 Word16[7] rom.dech. bitro, JMR74 Word16[7] rom.dech. bitro, JMR74 Word16[7] rom.dech. bitro, JMR122 Word16[7] rom.dech. bitro, JMR122 Word16[7] rom.dech. bitro, JMR122 Word16[7] rom.dech. bitro, JMR122 Word16[7] rom.dech. pitro, JMR122 Word32[7] rom.dech. pitro, JMR122 Word32[7] rom.dech. pitro, JMR122 Word32[7] rom.dech. pitro, JMR122 Word32[7] rom.dech. pitro, JMR122 </th <th>File</th> <th>Table name</th> <th>Type[Length]</th> <th>Description</th>	File	Table name	Type[Length]	Description
rom_dec.h podwn Word32[7] attenuation factors for adaptive codebook gain rom_dec.h pred_MR122 Word32[4] sigbbraic code book gain MA predictor coefficients rom_dec.h gamma3_MR122 Word32[10] rom_dec.h gamma4_MR122 Word32[10] rom_dec.h gamma4_MR122 Word32[10] spectral expansion factors rom_dec.h bitno_MR515 Word16[17] number of bits per parameter to transmit (MR475) rom_dec.h bitno_MR519 Word16[19] number of bits per parameter to transmit (MR516) rom_dec.h bitno_MR67 Word16[19] number of bits per parameter to transmit (MR516) rom_dec.h bitno_MR74 Word16[19] number of bits per parameter to transmit (MR74) rom_dec.h bitno_MR122 Word16[39] number of bits per parameter to transmit (MR74) rom_dec.h bitno_MR122 Word16[39] number of bits per parameter to transmit (MR72) rom_dec.h qua_gain_pitch Word32[4] number of bits per parameter to transmit (MR72) rom_dec.h gray Word32[4] number of bits per parameter to transmit (MR122) rom_dec.h	rom_dec.h	dtx_log_en_adjust	Word16[9]	level adjustments for ech mode
rom_dec.h pred_MR122 Word32[4] algebraic code book gain MA predictor coefficients (MR122) rom_dec.h gamma3_MR122 Word32[10] spectral expansion factors rom_dec.h gamma4_MR122 Word32[10] spectral expansion factors rom_dec.h gamma4_MR122 Word32[10] spectral expansion factors rom_dec.h bitno_MR475 Word16[19] number of bits per parameter to transmit (MR475) rom_dec.h bitno_MR67 Word16[19] number of bits per parameter to transmit (MR75) rom_dec.h bitno_MR74 Word16[19] number of bits per parameter to transmit (MR74) rom_dec.h bitno_MR795 Word16[19] number of bits per parameter to transmit (MR74) rom_dec.h bitno_MR102 Word16[23] number of bits per parameter to transmit (MR74) rom_dec.h bitno_MR102 Word16[39] number of bits per parameter to transmit (MR710) rom_dec.h gua_gain_pitch Word32[49] word16[59] number of bits per parameter to transmit (MR710) rom_dec.h gray Word8[8] gray word16[59] rumber of bits per parameter to transmit (MR710) <	rom_dec.h	cdown	Word32[7]	attenuation factors for codebook gain
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rom_dec.h acos_slope Word32[44] table to compute acos(x) in Lsp_lsf() rom_dec.h ph_imp_low_MR795 Word32[40] phase dispersion impulse response (MR795) rom_dec.h ph_imp_mid_MR795 Word32[40] phase dispersion impulse response (MR795) rom_dec.h ph_imp_low Word32[40] phase dispersion impulse response (MR475 - MR67) rom_dec.h ph_imp_mid Word32[40] phase dispersion impulse response (MR475 - MR67) rom_dec.h past_rq_init Word32[40] phase dispersion impulse response (MR475 - MR67) rom_dec.h mean_lsf_3 Word32[80] initialization table for the MA predictor in DTX rom_dec.h mean_lsf_5 Word32[10] LSF means (not in MR122) rom_dec.h pred_fac Word32[10] LSF means (MR122) rom_dec.h dico1_lsf_3 Word32[3*256]] 1** LSF quantizer (not in MR122) rom_dec.h dico3_lsf_3 Word32[3*512] 2** LSF quantizer (not in MR122) rom_dec.h dico3_lsf_3 Word32[4*512] 3** LSF quantizer (MR515 and MR475) rom_dec.h mr515_3_lsf Word32[4*512] 3** LSF quantizer (MR515 and MR475) rom_dec.h mr795_1_lsf Word32[3*512] 1** LSF quantizer (MR795) rom_dec.h dico1_lsf_5 Word32[4*128] 1** LSF quantizer (MR795) rom_dec.h dico2_lsf_5 Word32[4*256] 2** LSF quantizer (MR122) rom_dec.h dico3_lsf_5 Word32[4*256] 2** LSF quantizer (MR122) rom_dec.h dico4_lsf_5 Word32[4*256] 2** LSF quantizer (MR122) rom_dec.h dico5_lsf_5 Word32[4*256] 3** LSF quantizer (MR122) rom_dec.h dico5_lsf_5 Word32[4*256] 4** LSF quantizer (MR122) rom_dec.h dico5_lsf_5 Word32[4*256] 3** LSF quantizer (MR122) rom_dec.h table_gain_MR475 Word32[4*256] gain quantization table (MR475) rom_dec.h table_gain_lowrates Word32[4*256] gain quantization table (MR67, MR74 and MR102) rom_dec.h table_gain_lowrates Word32[64*4] gain quantization table (MR515 and MR59) interpolation filter coefficients rom_dec.h window_200_40 Word32[44] LP analysis window (not in MR122)	rom_dec.h	pow2_table	Word32[33]	table used in 2 to the power computation
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rom_dec.h table_SID Uword8[9] comparison optimisation table in DTX		•		·
rom_dec.h table_DTX Uword8[9] comparison optimisation table in DTX				·
rom_dec.h table_mute				·

4.5.3 Static variables used in the C code

In this section, two tables that specify the static variables for the speech encoder and decoder, respectively, are shown. All static variables are declared within a C **struct.**

Table 7: Speech encoder static variables

Speech Encode	Struct name	Variable	Type[Length]	Description
pre_state dx	Speech_Encode_	cod_amr_state	cod_amrState	see below in this table
dtx				
Pre_ProcessState y2		pre_state	Pre_ProcessState	see below in this table
y1 Word16 Float32 filter state x0 Float32 filter state cod_amrState c		dtx	Word32	Is set if DTX functionality is used
y1 Word16 Float32 filter state x0 Float32 filter state cod_amrState c				·
y1 Word16 Float32 filter state x0 Float32 filter state cod_amrState c	Pre ProcessState	v2	Float32	filter state
x0 x1 Float32 filter state filter state filter state filter state speech p. window Float32 p. window f				
x1 Float32 filter state speech Float32 speech buffer spointer to LPC analysis window in old_speech spointer to LPC analysis window in old_speech spointer to LPC analysis window with no lookahead in old_speech (MR122) spointer to the last 160 speech samples in old_speech speech sp		y1	Word16 Float32	filter state
x1 Float32 filter state speech Float32 speech buffer spointer to LPC analysis window in old_speech spointer to LPC analysis window in old_speech spointer to LPC analysis window with no lookahead in old_speech (MR122) spointer to the last 160 speech samples in old_speech speech sp				
cod_amrState cod_speech Float32 speech buffer pointer to current frame in old_speech pointer to LPC analysis window with no lookahead in lod_speech pointer to LPC analysis window with no lookahead in lod_speech pointer to LPC analysis window with no lookahead in lod_speech pointer to LPC analysis window with no lookahead in lod_speech pointer to LPC analysis window with no lookahead in lod_speech pointer to LPC analysis window with no lookahead in lod_speech pointer to the last 160 speech samples in old_speech pointer to the current frame pointer to the current frame in old_speech pointer to the curren		x0	Float32	filter state
speech p_window pointer to LPC analysis window with no lookahead in old_speech (MR122) pointer to the last 160 speech samples in old_speech pointer to tpre the tast 160 speech samples in old_speech pointer to tpre the tast 160 speech samples in old_speech pointer to tpre the tast 160 speech samples in old_speech pointer to tpre analts pointer to		x1	Float32	filter state
p_window p_window_12k2 Float32* pointer to LPC analysis window in lol speech pointer to LPC analysis window with no lookahead in old_speech (MR122) pointer to LPC analysis window with no lookahead in old_speech (MR122) pointer to the last 160 speech samples in old_speech old	cod_amrState	old_speech	Float32 [320]	speech buffer
p_window_12k2 lnew_speech lold_wsp lold_lags lold_gas lold_gas lold_gas lold_gas lold_wc lold_		speech		
new_speech old_wsp old_wsp old_wsp old_wsp old_lags ol_gain_lfg old_exc exc Float32 Float32 enables open loop bitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old_exc exc Float32 enables open loop pitch lag weighting (MR102) old exc exc Float32 initiation vector removed to weighted synthesis filter impulse response of weighted synthesis filter genovector followed by impulse response see below in this table see this control table see the control table see the control ta				
new_speech old_wsp		p_window_12k2	Float32*	
old_wsp wsp Float32				
wsp old_lags old_lags old_lags old_lags old_lags old_lags old_sinfig old_exc exc Float32 [2] enables open loop LTP states exc Float32 [314] excitation vector current excitation vector lag. 2ero Float32* current excitation history of weighted synth. filter followed by zero vector lag. 2ero Float32* impulse response of weighted synthesis filter zero vector followed by impulse response of weighted synthesis filter zero vector followed by impulse response levels in this table see below				
old_lags ol_gain_fig old_exc exc Float32 [2] excesc ai_zero Float32 [51] hvec Float32 [61] Float32 [61] Float32 [7] hvec Float32 [80] F				0 1
ol. gain_flg old_exc exc Float32 [314] excitation vector exc Float32 [51] ai_zero Float32 [51] history of weighted synth. filter followed by zero vector zero Float32* tero vector float32* hvec Float32* Float32 [80] pcSt pcSt pcState lspSt clt.pSt gainQuantSt gainQuantSt pitchOLWghtSt tonStabSt vadSt vadSt dtv_encSt dtv_encSt mem_yo mem_w0 Float32 [10] mem_w1 Float32 [10] mem_w1 Float32 [10] mem_w2 Float32 [10] mem_w2 Float32 [10] mem_w6 Float32 [10] mem_w6 Float32 [10] mem_w7 Float32 [10] mem_w8 Float32 [10] mem_w8 Float32 [10] mem_w9 Float32 [10] mem_w9 Float32 [10] mem_w1 Float32 [10] mem_w1 Float32 [10] mem_w6 Float32 [10] mem_w6 Float32 [10] mem_w7 Float32 [10] mem_w8 Float32 [10] mem_w9 Float32 [10] mem_w1 Float32 [10] mem_w6 Float32 [10] mem_w6 Float32 [10] mem_w7 Float32 [10] mem_w8 Float32 [10] mem_w8 Float32 [10] mem_w9 Float32 [10] mem_w9 Float32 [10] mem_w9 Float32 [10] mem_w1 Float32 [10] mem_w6 Float32 [10] mem_w6 Float32 [10] mem_w6 Float32 [10] mem_w7 Float32 [10] mem_w8 Float32 [10] mem_w8 Float32 [10] mem_w8 Float32 [10] mem_w9 Float32 [10] mem_w9 Float32 [10] mem_w9 Float32 [10] mem_w9 Float32 [10] mem_w1 Float32 [10] mem_w1 Float32 [10] mem_w1 Float32 [10] mem_w2 Float32 [10] mem_w6 Float32 [10] mem_w7 Float32 [10] mem_w8 Float32 [10] mem_w8 Float32 [10] mem_w8 Float32 [10] mem_w9 Float32 [10] mem_w9 Float32 [10] mem_w1 Floa				
old_exc exc Float32 [314] excitation vector exc exc Float32 [51] history of weighted synth. filter followed by zero vector zero Float32 float32 zero vector flowed by impulse response float32 zero vector followed by impulse response float32 see below in this table zero vector this table zero vector xero vector x				
exc ai_zero Float32' current excitation history of weighted synth. filter followed by zero vector zero Float32' zero vector float32' zero vector h1 Float32' impulse response of weighted synthesis filter zero vector plocSt lpcState see below in this table see below in th				
ai_zero				
Zero Float32* Zero vector h1				
h1 hvec Float32 80 zero vector followed by impulse response lpcState lpcState lpcState lpcState lspSt lspState see below in this table see below in this table gainQuantSt gainQuantState pitchOLWghtState tonStabSt vadState vadStat				, , ,
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lpcSt lspSt lspState see below in this table clLtpSt see below in this table clLtpSt gainQuantSt gainQuantState see below in this table tonStabSt tonStabState see below in this table see below in this table vadSt vadState see below in this table				
IspSt clLtpSt clLtpState see below in this table see b				
clLtpSt gainQuantSt gainQuantState pitchOLWghtSt pitchOLWghtSt tonStabSt tonStabState vadSt vadState see below in this table see below				
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mem_syn mem_w0 Float32 [10] synthesis filter memory (applied to error signal) mem_w Float32 [10] weighting filter memory (applied to error signal) mem_w Float32 [50] filter memory for production of error vector error Float32* error signal (input minus synthesized speech) pitch sharpening gain pitch sharpening gain vadState bckr_est ploat32 [9] background noise estimate ave_level float32 [9] averaged input components for stationary estimation old_level float32 [9] input levels of the previous frame sub_level float32 [9] input levels calculated at the end of a frame (lookahead) memory for the filter bank a_data5 float32 [6] memory for the filter bank burst_count Word16 counts length of a speech burst hang_count stat_count Word16 stationary counter stat_count vadreg Word32 to flags for intermediate VAD decisions pitch word16 word16 flags for tone detection flags for complex_high Word16 flags for complex detection		dtx	Word32	is set if DTX functionality is used
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mem_err error sloat32 [50] filter memory for production of error vector error sharp Float32* error signal (input minus synthesized speech) pitch sharpening gain vadState bckr_est ploat32 [9] background noise estimate ave_level ploat32 [9] averaged input components for stationary estimation old_level ploat32 [9] input levels of the previous frame sub_level ploat32 [9] input levels calculated at the end of a frame (lookahead) a_data5 ploat32 [6] memory for the filter bank a_data3 ploat32 [5] memory for the filter bank burst_count word16 counts length of a speech burst hang_count word16 hangover counter stat_count word16 stationary counter vadreg word32 pitch word32 pitch word32 pitch grid flags for intermediate VAD decisions tone complex_high word16 flags for complex detection		mem_w0		
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complex_high Word16 flags for complex detection		I!		
oldlag_count Word32 variables for pitch detection				
oldlag Word32 variables for pitch detection		_		
complex_hang_count Word16 complex hangover counter, used by VAD				
complex_hang_timer Word16 hangover initiator, used by CAD				

Struct name	Variable	Type[Length]	Description
	best_corr_hp	Float32	filtered value
	speech_vad_decision	Word16	final decision
	complex_warning	Word16	complex background warning
	sp_burst_count	Word16	counts length of a speech burst incl HO addition
	corr_hp_fast	Word16	filtered value
dtx_encState	Isp_hist	Float32[80]	LSP history (8 frames)
	log_en_hist	Float32 [8]	logarithmic frame energy history (8 frames)
	hist_ptr	Word16	pointer to the cyclic history vectors
	log_en_index	Word16	Index for logarithmic energy
	init_lsf_vq_index	Word32	initial index for lsf predictor
	lsp_index	Word16[3]	Isp indecies to the three code books
	dtxHangoverCount	Word16	is decreased in DTX hangover period
	decAnaElapsedCount	Word16	counter for elapsed speech frames in DTX
IpcState	LevinsonSt	LevinsonState	see below
LevinsonState	old_A	Float32[11]	last frames direct form coefficients
IspState	lsp_old	Float32 [10]	old LSP vector
'	lsp_old_q	Float32 [10]	old quantized LSP vector
	qSt	Q_plsfState	see below in this table
Q_plsfState	past_rq	Float32[10]	past quantized LSF prediction error
clLtpState	pitchSt	Pitch_frState	see below in this table
tonStabState	count	Word16	count consecutive (potential) resonance frames
	gp	Float32[7]	pitch gain history
Pitch_frState	T0_prev_subframe	Word32	integer. pitch lag of previous subframe
gainQuantState	sf0_ gcode0	Float32	subframe 0/2 codebook gain
	sf0_ target_en	Float32	subframe 0/2 target energy
	sf0_ coeff	Float32 [5]	subframe 0/2 energy coefficient
	gain_idx_ptr	Word16*	pointer to gain index value in parameter frame
	gc_predSt	gc_predState	see below in this table
	gc_predUncSt	gc_predState	see below in this table
	adaptSt	GainAdaptState	see below in this table
gc_predState	past_qua_en	Float32[4]	MA predictor memory (20*log10(pred. error))
GainAdaptState	onset	Word16	onset counter
	prev_alpha	Float32	previous adaptor output
	prev_gc	Float32	previous codebook gain
	ltpg_mem	Float32 [5]	pitch gain history
pitchOLWghtState	old_T0_med	Word32	weighted open loop pitch lag
	ada_w	Float32	weigthing level depeding on open loop pitch gain
	wght_flg	Word16	switches lag weighting on and off

Table 8: Speech decoder static variables

Struct name	Variable	Type[Length]	Description
Speech_Decode_FrameSt		Decoder_amrState	see below in this table
ate			
	post_state	Post_FilterState	see below in this table
	postHP_state	Post_ProcessState	see below in this table
Decoder_amrState	old_exc	Word32[194]	excitation vector
	exc	Word32* Word32[10]	current excitation LSP vector of previous frame
	lsp_old mem_syn	Word32[10]	synthesis filter memory
	sharp	Word32	pitch sharpening gain
	old_T0	Word32	pitch sharpening lag
	prev_bf	Word16	previous value of "bad frame" flag
	prev_pdf	Word16	previous value of "pot. dangerous frame" flag
	state	Word16	ECU state (06)
	excEnergyHist	Word32[9]	excitation energy history
	T0_lagBuff	Word32 Word32	received pitch lag for ECU
	inBackgroundNoise voicedHangover	Word32	background noise flag hangover flag
	ItpGainHistory	Word32[9]	pitch gain history
	background_state	Bgn_scdState	see below in this table
	Cb_gain_averState	Cb_gain_averageState	see below in this table
	lsp_avg_st	lsp_avgState	see below in this table
	IsfState	D_plsfState	see below in this table
	ec_gain_p_st	ec_gain_pitchState	see below in this table
	ec_gain_c_st	ec_gain_codeState gc_predState	see below in this table see table 7
	pred_state nodataSeed	gc_predState Word16	seed for CN generator
	ph_disp_st	ph_dispState	see below in this table
	dtxDecoderState	dtx_decState	see below in this table
dtx_decState	since_last_sid	Word16	number of frames since last SID frame
	true_sid_period_inv	Word16	inverse of true SID update rate
	log_en	Word32	logarithmic frame energy
	old_log_en	Word32	previous value of log_en
	pn_seed_rx lsp	Word32 Word32[10]	random number generator seed LSP vector
	lsp_old	Word32[10]	previous LSP vector
	lsf_hist	Word32[80]	LSF vector history (8 frames)
	lsf_hist_ptr	Word16	index to beginning of LSF history
	lsf_hist_mean	Word32[80]	mean-removed LSF history (8 frames)
	log_pg_mean	Word16	mean-removed logarithmic prediction gain
	log_en_hist	Word32[8]	logarithmic frame energy history
	log_en_hist_ptr log_en_adjust	Word16 Word16	index to beginning of log, frame energy history mode-dependent frame energy adjustment
	dtxHangoverCount	Word16	counts down in hangover period
	decAnaElapsedCount	Word16	counts elapsed speech frames after DTX
	sid_frame	Word16	flags SID frames
	valid_data	Word16	flags SID frames containing valid data
	dtxHangoverAdded	Word16	flags hangover period at end of speech
	dtxGlobalState	enum DTXStateType	DTX state flags
Dan codetata	data_updated	Word16	flags CNI updates
Bgn_scdState	frameEnergyHist bgHangover	Word32[60] Word16	history of synthesis frame energy number of frames since last speech frame
Cb_gain_averageState	cbGainHistory	Word32[7]	codebook gain history
ob_gam_avoragootato	hangVar	Word16	counts length of talkspurt in subframes
	hangCount	Word16	number of subframes since last talkspurt
lsp_avgState	lsp_meanSave	Word32[10]	averaged LSP vector
D_plsfState	past_r_q	Word32[10]	past quantized LSF prediction vector
an main mitab Ctata	past_lsf_q	Word32[10]	past dequantized LSF vector
ec_gain_pitchState	pbuf past_gain_pit	Word32[5] Word32	pitch gain history previous pitch gain (limited to 1.0)
	prev_gp	Word32	previous good pitch gain
ec_gain_codeState	gbuf	Word32[5]	codebook gain history
	past_gain_code	Word32	previous codebook gain
	prev_gc	Word32	previous good codebook gain
ph_dispState	gainMem	Word32[5]	pitch gain history
	prevState	Word32	previously used impulse response
	prevCbGain lockFull	Word32 Word16	previous codebook gain force maximum phase dispersion
	onset	Word16	onset counter
Post_FilterState	res2	Word32[40]	LP residual
. Joi_i morotato	mem_syn_pst	Word32[10]	synthesis filter memory
	synth_buf	Word16[170]	synthesis filter work area
	agc_state	agcState	see below in this table
	preemph_state	preemphasisState	see below in this table
agcState	past_gain	Word16	past agc gain
preemphasisState	mem_pre	Word16	filter state

Struct name	Variable	Type[Length]	Description
Post_ProcessState	y2_hi	Word32	filter state, upper word
	y2_lo	Word32	filter state, lower word
	y1_hi	Word32	filter state, upper word
	y1_lo	Word32	filter state, lower word
	x0	Word32	filter state
	x1	Word32	filter state

5 Homing procedure

The principles of the homing procedures are described in 3GPP TS 06.090 [2]. This specification only includes a detailed description of the 8 decoder homing frames. For each AMR codec mode, the corresponding decoder homing frame has a fixed set of speech parameters shown in table 9a-9h. The bit allocation within these parameters is identical to the corresponding bit allocation of the source encoder output parameters given in 3GPP TS 06.090 [2].

In the following tables, the following naming convention is used for the individual parameters. Letters in *italics* indicate numbers.

- LPC_nindex of nth LSF submatrix
- LTP-LAG m adaptive codebook index for subframe m
- LTP-GAIN madaptive codebook gain index in subframe m
- FCB-GAIN m fixed codebook gain index in subframe m
- GAIN VQ m codebook gain VQ index in subframe m (subframe m and m+1 for MR475)
- POS m_n position index of nth pulse in subframe m
- POS $m_n k$ position index of nth and kth pulse in subframe m
- POS m_n_k_l_j position index of nth, kth, lth, and jth pulse in subframe m
- SIGN m_n_k sign information for nth and kth pulse in subframe m
- SIGN $m_n_k_l$ sign information for nth, kth, lth, and jth pulse in subframe m
- SIGN_m_n_k_POS_m_n sign information for *n*th and *k*th pulse and position index for *n*th pulse in subframe *m*

Table 9a: Parameter values for the decoder homing frame (MR475)

Parameter	Value (LSB=b0)
LPC 1	0x00F8
LPC 2	0x009D
LPC 3	0x001C
LTP-LAG 1	0x0066
POS 1_1_2	0x0000
SIGN_1_1_2	0x0003
GAIN-VQ 1	0x0028
LTP-LAG 2	0x000F
POS 2_1_2	0x0038
SIGN_2_1_2	0x0001
LTP-LAG 3	0x000F
POS 3_1_2	0x0031
SIGN_3_1_2	0x0002
GAIN-VQ 3	0x0008
LTP-LAG 4	0x000F
POS 4_1_2	0x0026
SIGN_4_1_2	0x0003

Table 9b: Parameter values for the decoder homing frame (MR515)

Parameter	Value (LSB=b0)
LPC 1	0x00F8
LPC 2	0x009D
LPC 3	0x001C
LTP-LAG 1	0x0066
POS 1_1_2	0x0000
SIGN_1_1_2	0x0003
GAIN-VQ 1	0x0037
LTP-LAG 2	0x000F
POS 2_1_2	0x0000
SIGN_2_1_2	0x0003
GAIN-VQ 2	0x0005
LTP-LAG 3	0x000F
POS 3_1_2	0x0037
SIGN_3_1_2	0x0003
GAIN-VQ 3	0x0037
LTP-LAG 4	0x000F
POS 4_1_2	0x0023
SIGN_4_1_2	0x0003
GAIN-VQ 4	0x001F

Table 9c: Parameter values for the decoder homing frame (MR59)

Parameter	Value (LSB=b0)
LPC 1	0x00F8
LPC 2	0x00E3
LPC 3	0x002F
LTP-LAG 1	0x00BD
POS 1_1_2	0x0000
SIGN_1_1_2	0x0003
GAIN-VQ 1	0x0037
LTP-LAG 2	0x000F
POS 2_1_2	0x0001
SIGN_2_1_2	0x0003
GAIN-VQ 2	0x000F
LTP-LAG 3	0x0060
POS 3_1_2	0x00F9
SIGN_3_1_2	0x0003
GAIN-VQ 3	0x0037
LTP-LAG 4	0x000F
POS 4_1_2	0x0000
SIGN_4_1_2	0x0003
GAIN-VQ 4	0x0037

Table 9d: Parameter values for the decoder homing frame (MR67)

Parameter	Value (LSB=b0)
LPC 1	0x00F8
LPC 2	0x00E3
LPC 3	0x002F
LTP-LAG 1	0x00BD
POS 1_1_2_3	0x0002
SIGN_1_1_2_3	0x0007
GAIN-VQ 1	0x0000
LTP-LAG 2	0x000F
POS 2_1_2_3	0x0098
SIGN_2_1_2_3	0x0007
GAIN-VQ 2	0x0061
LTP-LAG 3	0x0060
POS 3_1_2_3	0x05C5
SIGN_3_1_2_3	0x0007
GAIN-VQ 3	0x0000
LTP-LAG 4	0x000F
POS 4_1_2_3	0x0318
SIGN_4_1_2_3	0x0007
GAIN-VQ 4	0x0000

Table 9e: Parameter values for the decoder homing frame (MR74)

Parameter	Value (LSB=b0)
LPC 1	0x00F8
LPC 2	0x00E3
LPC 3	0x002F
LTP-LAG 1	0x00BD
POS 1_1_2_3_4	0x0006
SIGN_1_1_2_3_4	0x000F
GAIN-VQ 1	0x0000
LTP-LAG 2	0x001B
POS 2_1_2_3_4	0x0208
SIGN_2_1_2_3_4	0x000F
GAIN-VQ 2	0x0062
LTP-LAG 3	0x0060
POS 3_1_2_3_4	0x1BA6
SIGN_3_1_2_3_4	0x000F
GAIN-VQ 3	0x0000
LTP-LAG 4	0x001B
POS 4_1_2_3_4	0x0006
SIGN_4_1_2_3_4	0x000F
GAIN-VQ 4	0x0000

Table 9f: Parameter values for the decoder homing frame (MR795)

•	
Parameter	Value (LSB=b0)
LPC 1	0x00C2
LPC 2	0x00E3
LPC 3	0x002F
LTP-LAG 1	0x00BD
POS_1_1_2_3_4	0x0006
SIGN_1_1_2_3_4	0x000F
LTP-GAIN 1	0x000A
FCB-GAIN 1	0x0000
LTP-LAG 2	0x0039
POS_2_1_2_3_4	0x1C08
SIGN_2_1_2_3_4	0x0007
LTP-GAIN 2	0x000A
FCB-GAIN 2	0x000B
LTP-LAG 3	0x0063
POS_3_1_2_3_4	0x11A6
SIGN_3_1_2_3_4	0x000F
LTP-GAIN 3	0x0001
FCB-GAIN 3	0x0000
LTP-LAG 4	0x0039
POS_4_1_2_3_4	0x09A0
SIGN_4_1_2_3_4	0x000F
LTP-GAIN 4	0x0002
FCB-GAIN 4	0x0001

Table 9g: Parameter values for the decoder homing frame (MR102)

Parameter	Value (LSB=b0)
LPC 1	0x00F8
LPC 2	0x00E3
LPC 3	0x002F
LTP-LAG 1	0x0045
SIGN_1_1_5	0x0000
SIGN_1_2_6	0x0000
SIGN_1_3_7	0x0000
SIGN_1_4_8	0x0000
POS_1_1_2_5	0x0000
POS_1_3_6_7	0x0000
POS_1_4_8	0x0000
GAIN-VQ_1	0x0000
LTP-LAG 2	0x001B
SIGN_2_1_5	0x0000
SIGN_2_2_6	0x0001
SIGN_2_3_7	0x0000
SIGN_2_4_8	0x0001
POS_2_1_2_5	0x0326
POS_2_3_6_7	0x00CE
POS_2_4_8	0x007E
GAIN-VQ_2 LTP-LAG 3	0x0051
	0x0062 0x0000
SIGN_3_1_5	0x0000
SIGN_3_2_6 SIGN_3_3_7	0x0000
SIGN_3_4_8	0x0000
POS_3_1_2_5	0x015A
POS 3 3 6 7	0x0359
POS_3_4_8	0x0076
GAIN-VQ_3	0x0000
LTP-LAG 4	0x001B
SIGN_4_1_5	0x0000
SIGN_4_2_6	0x0000
SIGN_4_3_7	0x0000
SIGN_4_4_8	0x0000
POS_4_1_2_5	0x017C
POS_4_3_6_7	0x0215
POS_4_4_8	0x0038
GAIN-VQ_4	0x0030

Table 9h: Parameter values for the decoder homing frame (MR122)

Parameter	Value (LSB=b0)
LPC1	0x0004
LPC2	0x002A
LPC3	0x00DB
LPC4	0x0096
LPC5	0x002A
LTP-LAG 1	0x0156
LTP-GAIN 1	0x000B
SIGN_1_1_6_POS_1_1	0x0000
SIGN_1_2_7_POS_1_2	0x0000
SIGN_1_3_8_POS_1_3	0x0000
SIGN_1_4_9_POS_1_4	0x0000
SIGN_1_5_10_POS_1_5	0x0000
POS 1_6	0x0000
POS 1_7	0x0000
POS 1_8	0x0000
POS 1_9	0x0000
POS 1_10	0x0000
FCB-GAIN 1	0x0000
LTP-LAG 2	0x0036
LTP-GAIN 2	0x000B
SIGN_2_1_6_POS_2_1	0x0000
SIGN_2_2_7_POS_2_2	0x000F
SIGN_2_3_8_POS_2_3	0x000E
SIGN_2_4_9_POS_2_4	0x000C
SIGN_2_5_10_POS_2_5	0x000D
POS 2_6	0x0000
POS 2_7	0x0001
POS 2_8	0x0005
POS 2_9	0x0007
POS 2_10	0x0001
FCB-GAIN 2	0x0008
LTP-LAG 3	0x0024
LTP-GAIN 3	0x0000
SIGN_3_1_6_POS_3_1	0x0001
SIGN_3_2_7_POS_3_2	0x0000
SIGN_3_3_8_POS_3_3	0x0005
SIGN_3_4_9_POS_3_4	0x0006
SIGN_3_5_10_POS_3_5	0x0001
POS 3_6	0x0002
POS 3_7	0x0004
POS 3_8	0x0007
POS 3_9	0x0004
POS 3_10	0x0002
FCB-GAIN 3	0x0003
LTP-LAG 4	0x0036
LTP-GAIN 4	0x000B
SIGN_4_1_6_POS_4_1	0x0000
SIGN_4_2_7_POS_4_2	0x0002
SIGN_4_3_8_POS_4_3	0x0004
SIGN_4_4_9_POS_4_4	0x0000
SIGN_4_5_10_POS_4_5	0x0003
POS 4_6	0x0006
POS 4_7	0x0001
POS 4_8	0x0007
POS 4_9	0x0006
POS 4_10	0x0005
FCB-GAIN 4	0x0000

File formats

This section describes the file formats used by the encoder and decoder programs. The test sequences defined in [2] also use the file formats described here.

Speech file (encoder input / decoder output) 6.1

Speech files read by the encoder and written by the decoder consist of 16-bit words where each word contains a 13-bit, left aligned speech sample. The byte order depends on the host architecture (e.g. MSByte first on SUN workstations, LSByte first on PCs etc.). Both the encoder and the decoder program process complete frames (of 160 samples) only.

This means that the encoder will only process n frames if the length of the input file is n*160 + k words, while the files produced by the decoder will always have a length of n*160 words.

Mode control file (encoder input) 6.2

The encoder program can optionally read in a mode control file which specifies the encoding mode for each frame of speech processed. The file is a text file containing one line per speech frame. Each line contains one of the mode names from the list {MR475, MR515, MR59, MR67, MR74, MR795, MR102, MR122}.

Parameter bitstream file (encoder output / decoder input) 6.3

The files produced by the speech encoder/expected by the speech decoder contain an arbitrary number of frames in the format described in RFC 3267 [9], sections 5.1 and 5.3.

By using preprocessor definition encoder/decoder can optionally use AMR Interface Format 2. The format is described in TS 26.101 [8] Annex A.

By using another preprocessor definition encoder/decoder can optionally use format compatible with the existing AMR fixed-point C-code. Frame format is following.

FRAME_T	YPE B1	B2		B244	MODE_INFO	unused1		unused4	Ī
---------	--------	----	--	------	-----------	---------	--	---------	---

Each box corresponds to one Word16 value in the bitstream file, for a total of 250 words or 500 bytes per frame. The fields have the following meaning:

FRAME_TYPE transmit frame type, which is one of

TX_SPEECH (0x0000)

TX_SID_FIRST(0x0001)

TX SID UPDATE (0x0002)

TX_NO_DATA (0x0003)

B0...B244 speech encoder parameter bits (i.e. the bitstream itself). Each Bx either has the value 0x0000

or 0x0001. Only mode MR122 really uses all 244 bits; for the other modes, only the first n

bits are used (35 \leq n \leq 204). The remaining bits are unused (written as 0x0000)

MODE_INFO encoding mode information, which is one of

MR475 (0x0000)

MR515 (0x0001)

MR59 (0x0002)

MR67 (0x0003)

MR74 (0x0004) MR795 (0x0005)

MR102 (0x0006)

MR122 (0x0007)

unused1...4 unused, written as 0x0000

As indicated in section 6.1 above, the byte order depends on the host architecture.

Annex A (informative): Change History

TSG SA#	Tdoc	CR	Rev	Cat	PH	Vers	New Vers	Subject	
10	SP-000577	002		Α	Rel-4	3.0.0	4.0.0	AMR Core Frame bit ordering (AMR speech Codec; Floating point C-Code	
12	SP-010306	004	1	Α	Rel-4	4.0.0	4.1.0	Limiting predicted codebook gain computing in encoder	
12	SP-010306	006	1	А	Rel-4	4.0.0	4.1.0	Correction of decoder operation in error concealment of lost frames	
12	SP-010306	800	1	Α	Rel-4	4.0.0	4.1.0	Correction of mode state bug in AMR decoder	
12	SP-010306	012	1	Α	Rel-4	4.0.0	4.1.0	Correction of decoder Reset	
12	SP-010306	014	1	А	Rel-4	4.0.0	4.1.0	Correction of comfort noise parameter interpolation bug of AMR decoder	
12	SP-010306	016	1	Α	Rel-4	4.0.0	4.1.0	Correction of the TX_TYPE and RX_TYPE identifiers	
	MCC				Rel-4	4.1.0	4.1.1	Correction of bugs in code	
13	SP-010452	010	1	Α	Rel-4	4.1.1	4.2.0	Correction to make encoder and decoder memories independent	
13	SP-010452	018		Α	Rel-4	4.1.1	4.2.0	Correction of decoder operation in error concealment of lost frames	
15	SP-020079	019		Α	Rel-4	4.2.0	4.3.0	Maintaining bit-exactness with TS 26.073	
16							5.0.0	Version for Release 5	
19	SP-030088	21	1	F	Rel-5	5.0.0	5.1.0	MMS compatible i/o format option	
19	SP-030088	24		Α	Rel-5	5.0.0	5.1.0		
20	SP-030214	26		Α	Rel-5	5.1.0	5.2.0	Correction on codec mode handling during DTX	
22	SP-030681	29	1	F	Rel-5	5.2.0	5.3.0	Correction on the implementation of the interface of decoder.c	
22	SP-030682	30	1	D	Rel-6	5.3.0	6.0.0	Correction on the default behaviour of the unix makefile	
23	SP-040198	32		Α	Rel-6	6.0.0	6.1.0	Correction of floating point AMR DTX functionality	
36	SP-070321	0033	1	F	Rel-7	6.1.0	7.0.0	Bit order of Mode Indication in AMR comfort noise frames	
42					Rel-8		8.0.0	Version for Release 8	
46					Rel-9		9.0.0	Version for Release 9	
51					Rel-10		10.0.0	Version for Release 10	
57					Rel-11		11.0.0	Version for Release 11	
65					Rel-12		12.0.0	Version for Release 12	
70					Rel-13		13.0.0	Version for Release 13	

	Change history								
Date	Meeting	TDoc	CR	Rev	Cat	Subject/Comment	New version		
2017-03	75					Version for Release 14	14.0.0		
2018-06	80					Version for Release 15	15.0.0		
2018-09	81	SP-180657	0035	2	F	Corrections to AMR Floating-Point Code	16.0.0		

History

	Document history								
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