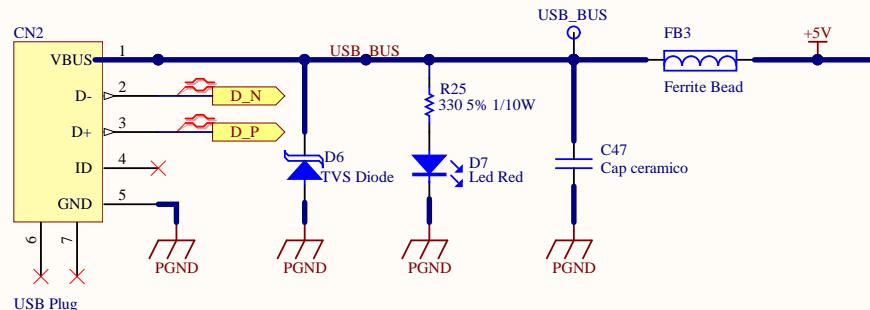
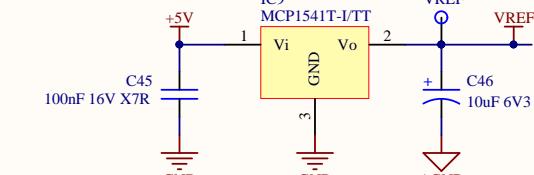


Power Stage

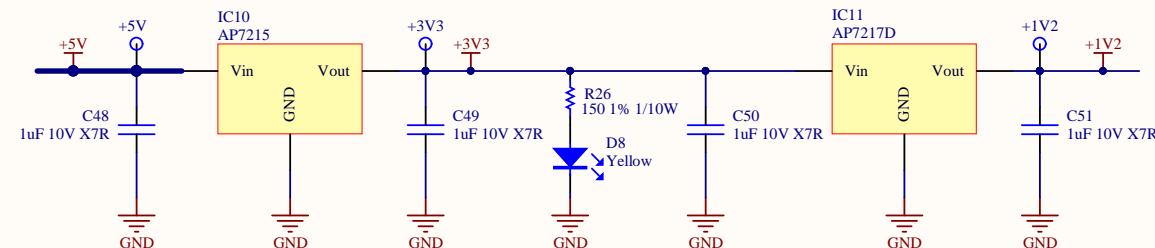
USB to +3V3 and +1V2



Reference Voltage

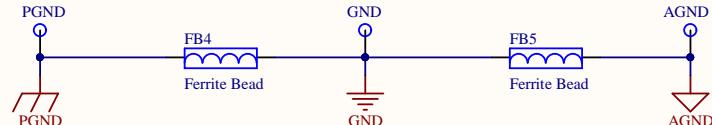


Inverter Voltage



Es muy importante el ESR de los capacidores para el ripple

Ground Connections



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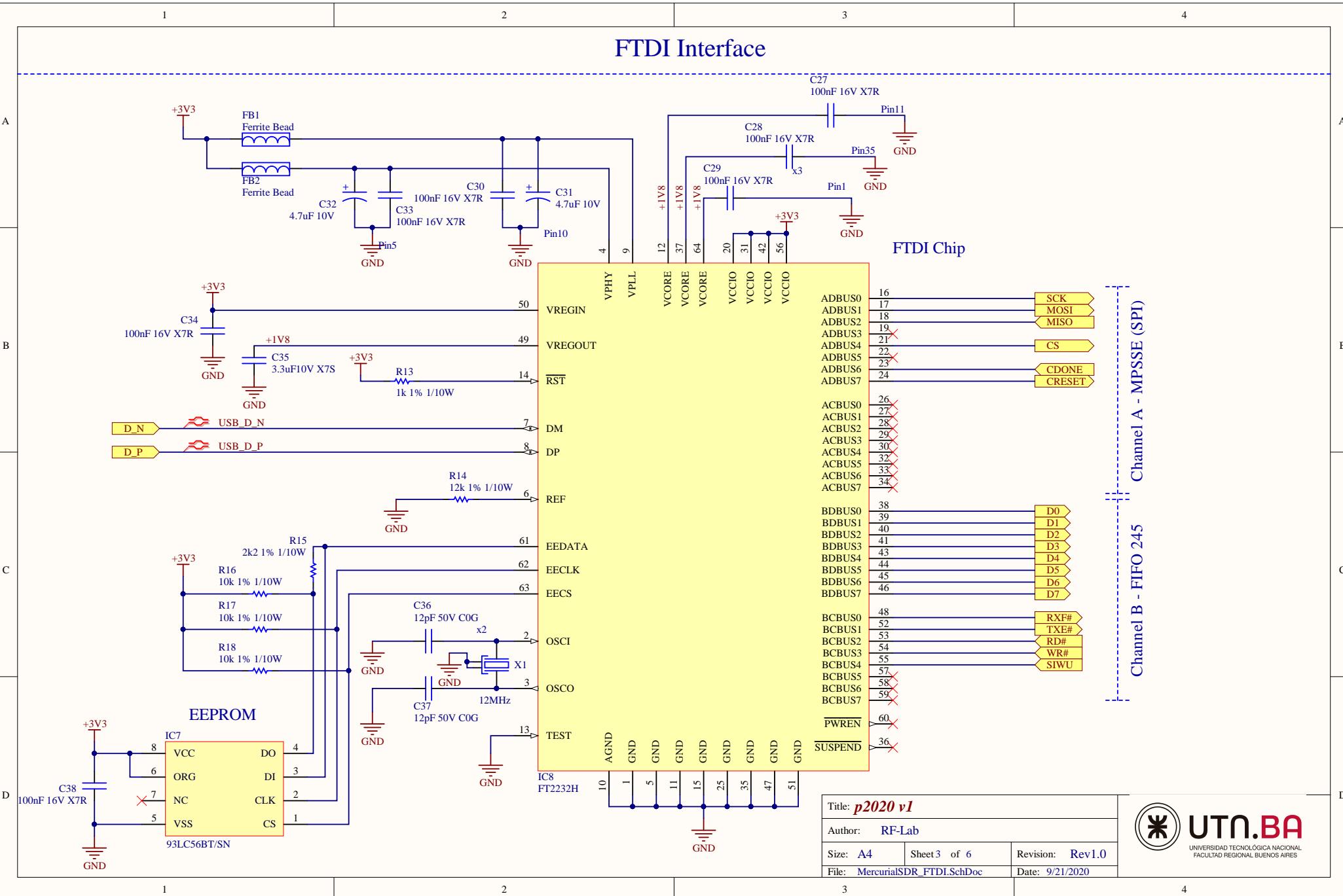
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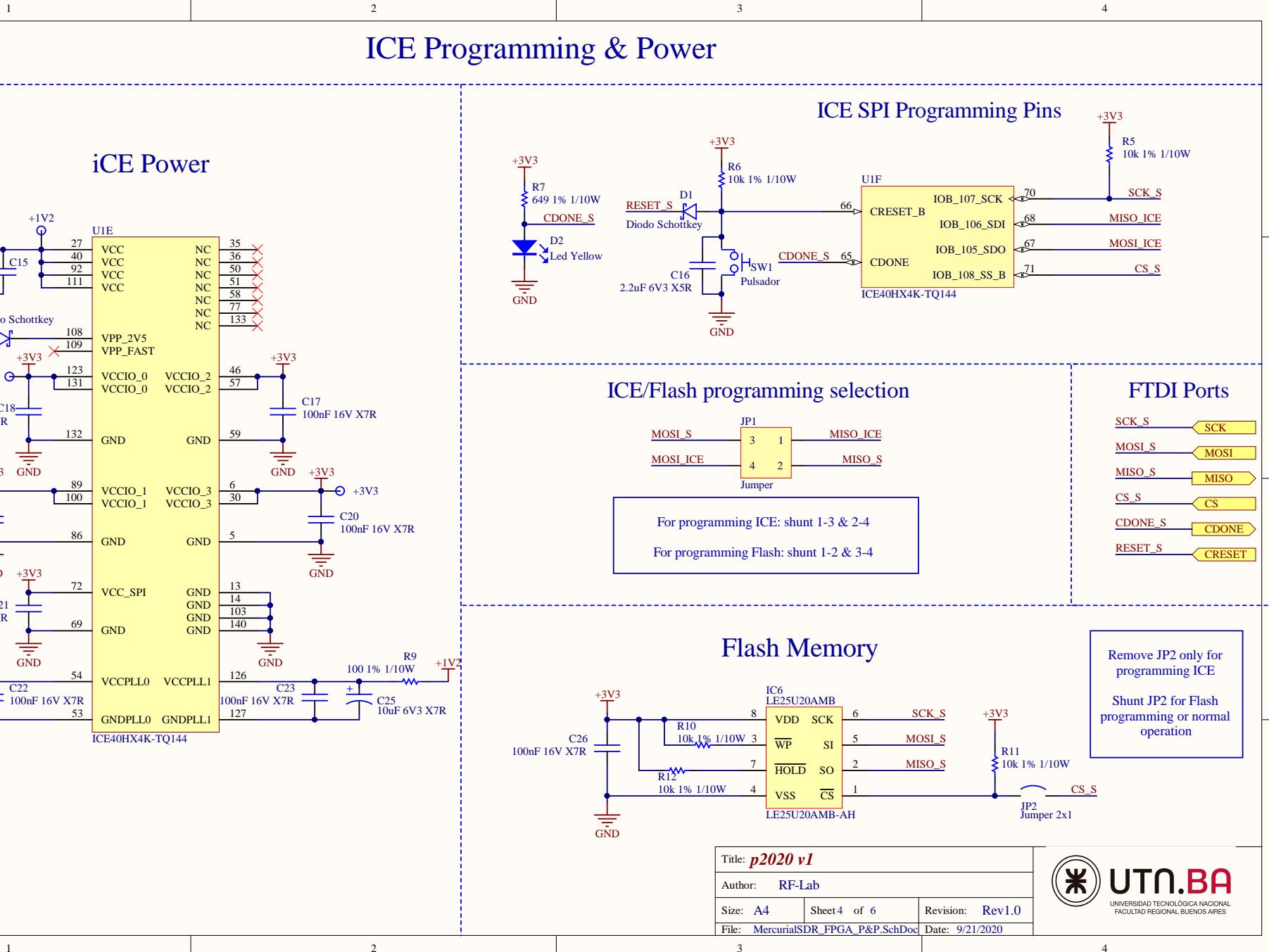
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FPGA Banks

PIN93y94 son GlobalPins, pero se pueden usar como GPIOs

BANK 1

IOR_109
IOR_110
IOR_111
IOR_112
IOR_113
IOR_114
IOR_115
IOR_116
IOR_117
IOR_118
IOR_119
IOR_120
IOR_121
IOR_122
IOR_123
IOR_124
IOR_125
IOR_126
IOR_127
IOR_128
IOR_129
IOR_130
IOR_131
IOR_132
IOR_133
IOR_134
IOR_135
IOR_136
IOR_137
IOR_138
IOR_139
IOR_140_GBIN3
IOR_141_GBIN2
IOR_144
IOR_146
IOR_147
IOR_148
IOR_149
IOR_150
IOR_160
IOR_161
IOR_162
IOR_164
IOR_165
IOR_166
IOR_167

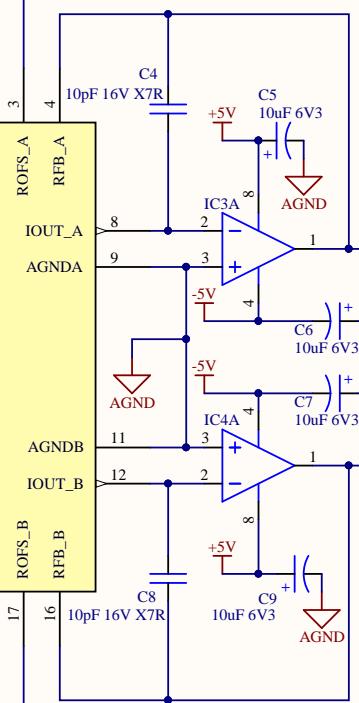
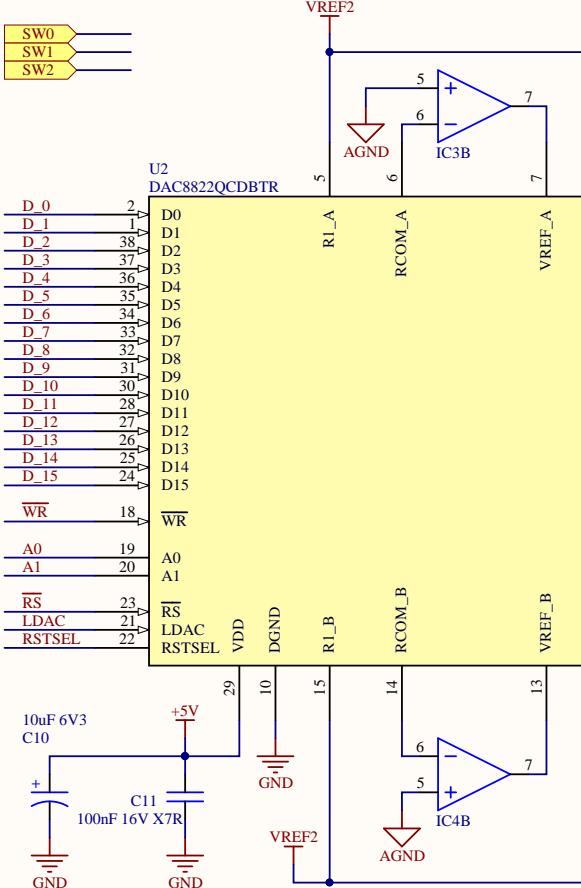
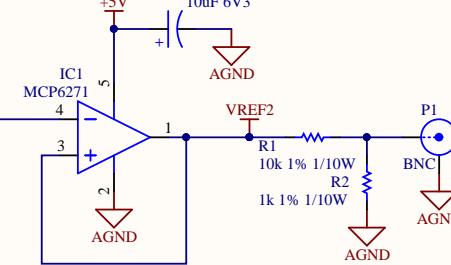
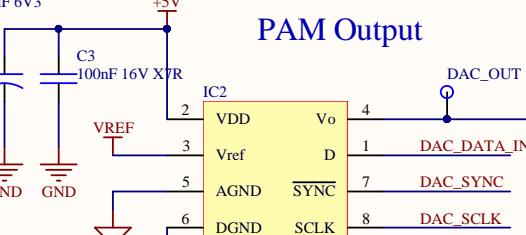
DAC_DATA_IN
DAC_SYNC
DAC_SCLK

BANK 3

IOL_2A
IOL_2B
IOL_3A
IOL_3B
IOL_4A
IOL_4B
IOL_5A
IOL_5B
IOL_8A
IOL_8B
IOL_10A
IOL_10B
IOL_12A
IOL_12B
IOL_13A
IOL_13B
IOL_14A_GBIN7
IOL_14A_GBIN6
iCE_OSC
IOL_14B
IOL_17A
IOL_17B
IOL_18A
IOL_18B
IOL_23A
IOL_23B
IOL_24A
IOL_24B
IOL_25A
IOL_25B

iCE40HX4K-TQ144

PAM Output



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Author: RF-Lab

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BANK 0

U1A
D_0
D_1
D_2
D_3
D_4
D_5
D_6
D_7
D_8
D_9
D_10
D_11
D_12
D_13
D_14
D_15
RS
RSTSEL
LDAC
A1
A0
WR

IOT_168
IOT_169
IOT_170
IOT_171
IOT_172
IOT_173
IOT_174
IOT_175
IOT_176
IOT_177
IOT_178
IOT_179
IOT_180
IOT_190
IOT_191
IOT_192
IOT_197_GBIN1
IOT_198_GBIN0
IOT_206
IOT_212
IOT_213
IOT_214
IOT_215
IOT_216
IOT_217
IOT_219
IOT_220
IOT_221
IOT_222

iCE40HX4K-TQ144

BANK 2

U1C
SIWU
WR#
RD#
TXE#
RXF#
D7
D6
D5
D4
D3
D2
D1
D0

IOB_56
IOB_57
IOB_61
IOB_63
IOB_64
IOB_71
IOB_72
IOB_73
IOB_79
IOB_80
IOB_81_GBIN5
IOB_82_GBIN4
IOB_91
IOB_94
IOB_95
IOB_96
IOB_102
IOB_104_CBSEL0
IOB_104_CBSEL1

iCE40HX4K-TQ144



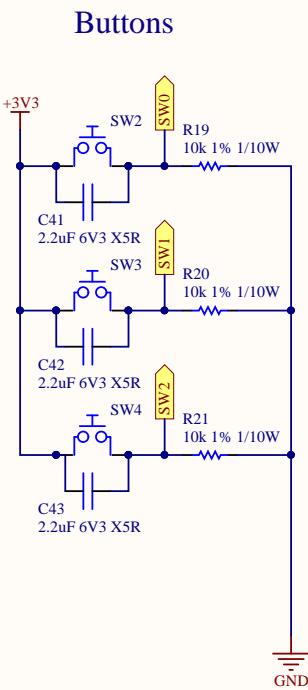
UTN.BA

UNIVERSIDAD TECNOLÓGICA NACIONAL

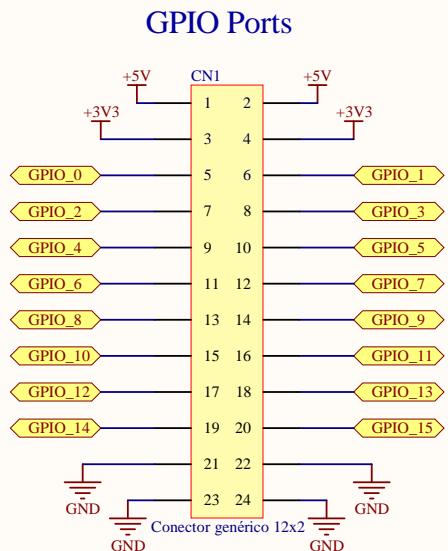
FACULTAD REGIONAL BUENOS AIRES

Miscellaneous

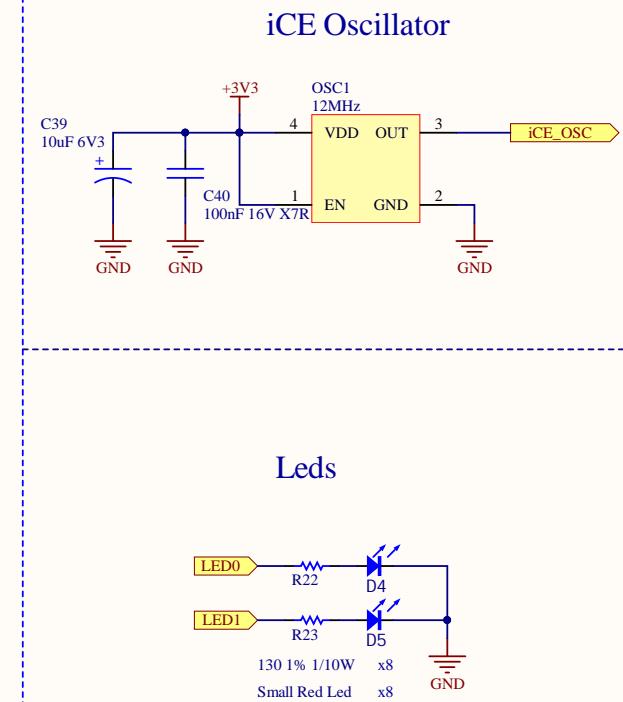
A



B



C



D

Title: **p2020 v1**

Author: **RF-Lab**

Size: **A4** Sheet 6 of 6 Revision: **Rev1.0**

File: **MercurialSDR_MISC.SchDoc** Date: **9/21/2020**

