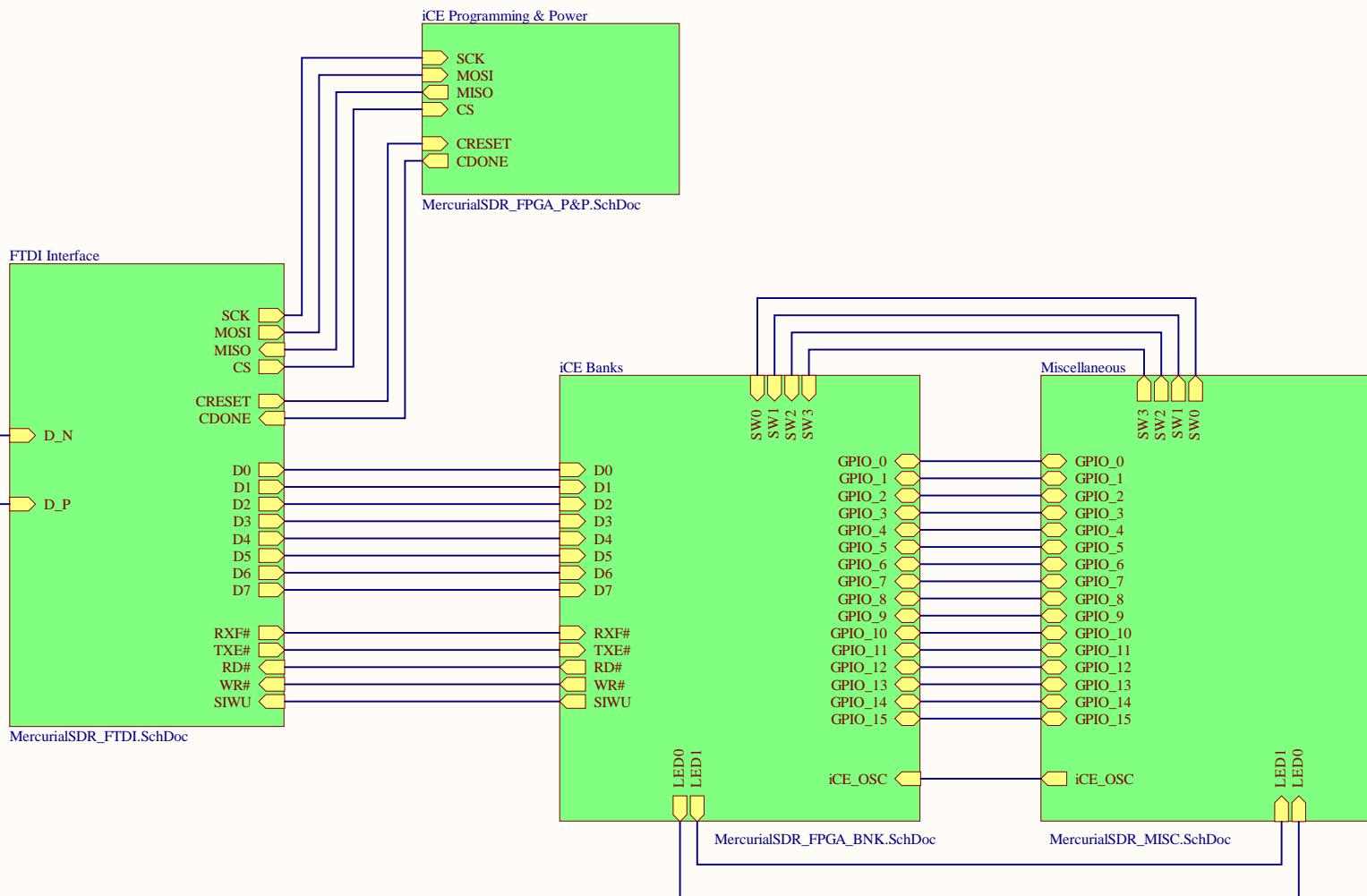


A

A



B

B

C

C

D

D

Title: *p2020 v1*

Author: RF-Lab

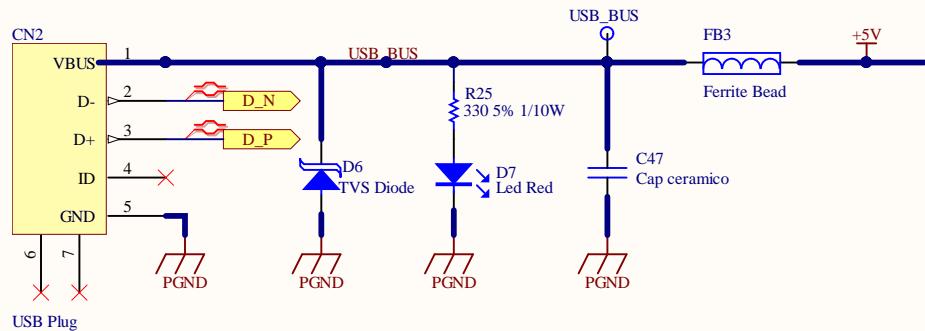
Size: A4 | Sheet 1 of 6 | Revision: Rev1.0

File: MercurialSDR_BLK.SchDoc | Date: 27/8/2020

Power Stage

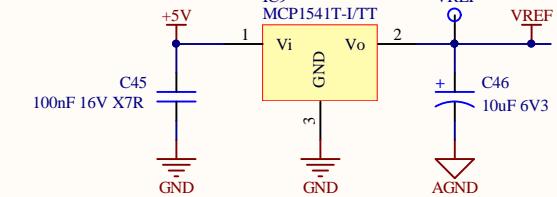
A

USB to +3V3 and +1V2

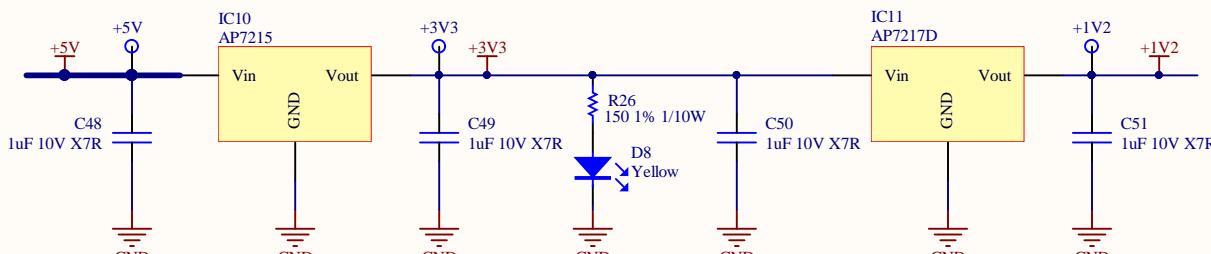


B

Reference Voltage

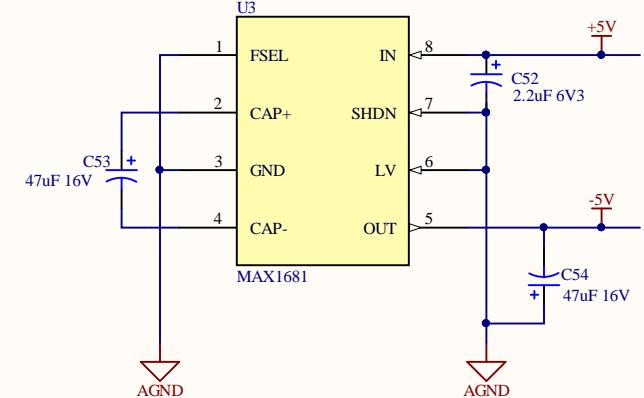


C



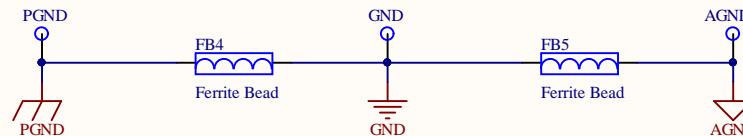
Es muy importante el ESR de los
capacitores para el ripple

Inverter Voltage



D

Ground Connections



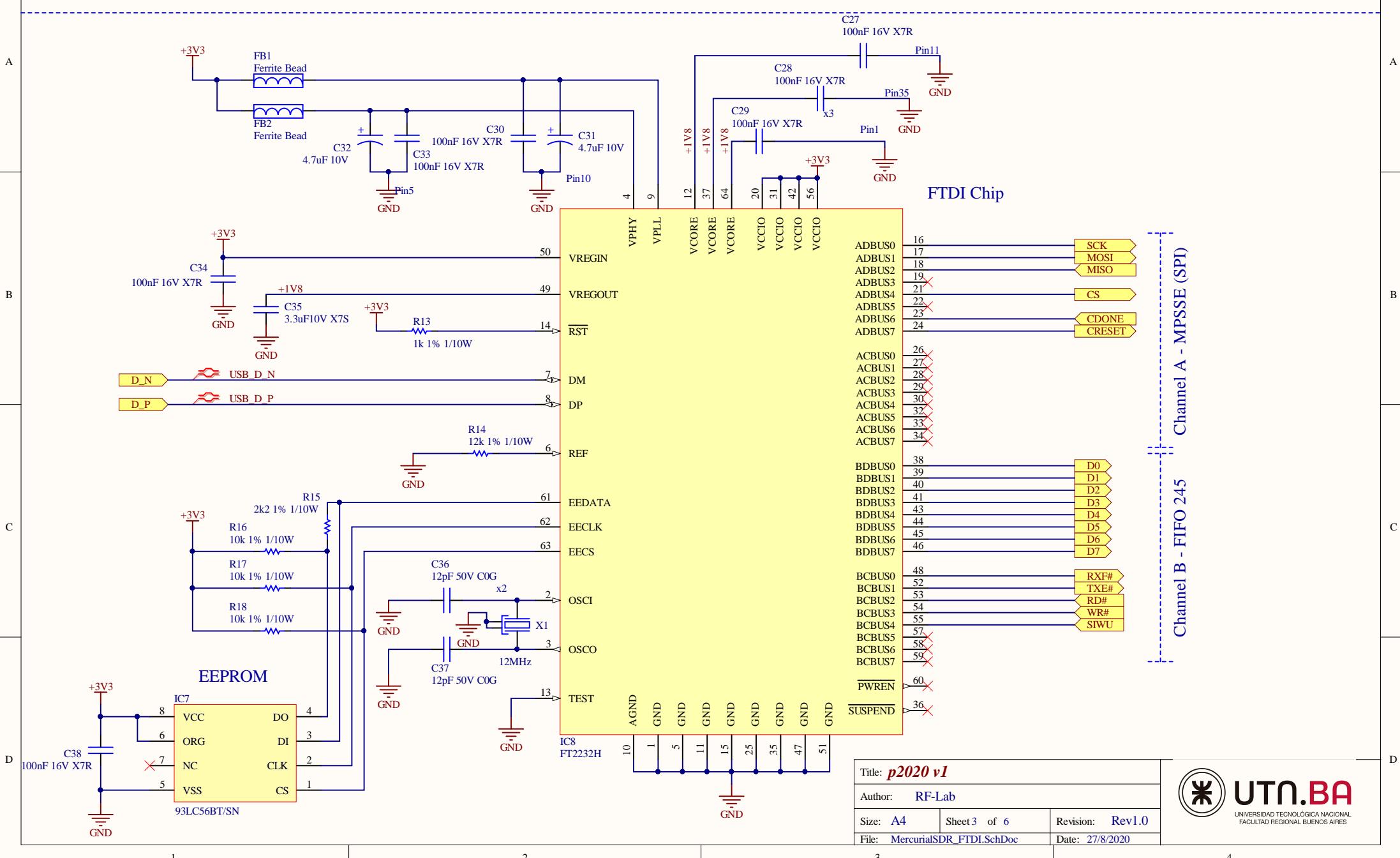
Title: *p2020 v1*

Author: RF-Lab

Size: A4 | Sheet 2 of 6 | Revision: Rev1.0

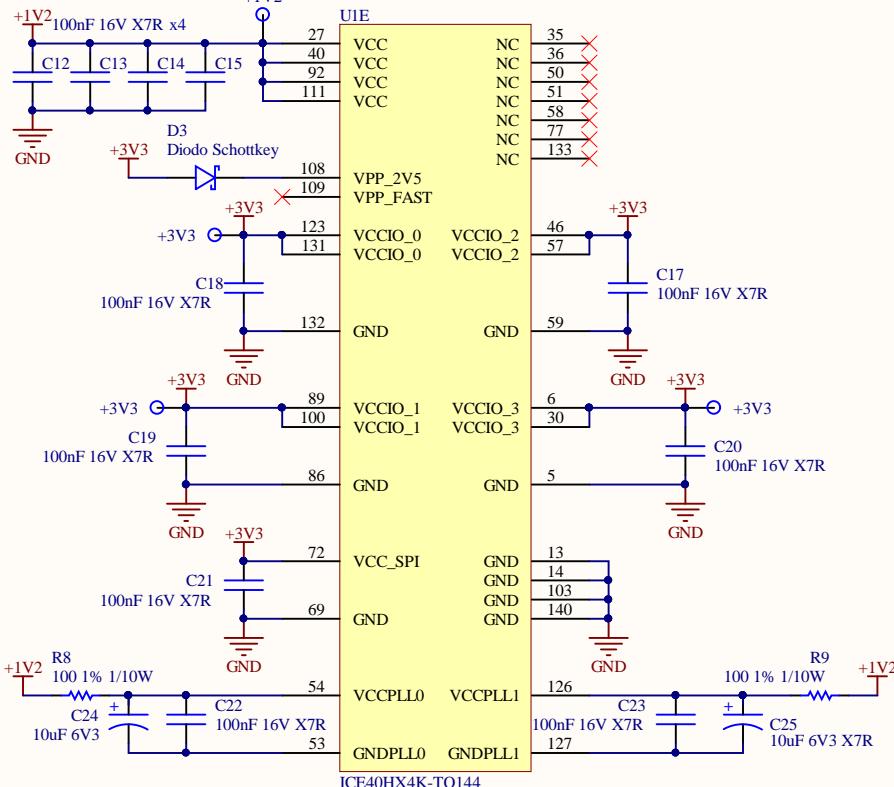
File: MercurialSDR_PWR.SchDoc | Date: 27/8/2020

FTDI Interface

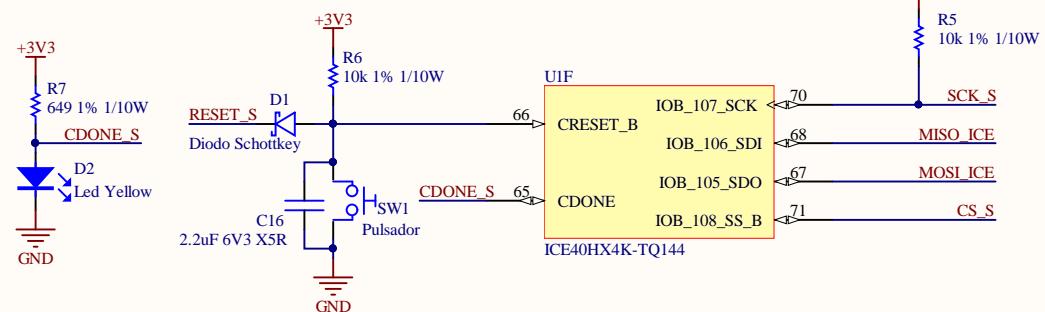


ICE Programming & Power

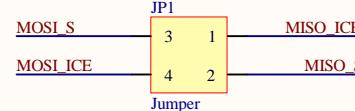
iCE Power



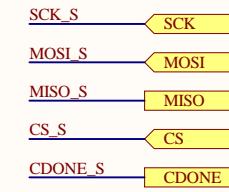
ICE SPI Programming Pins



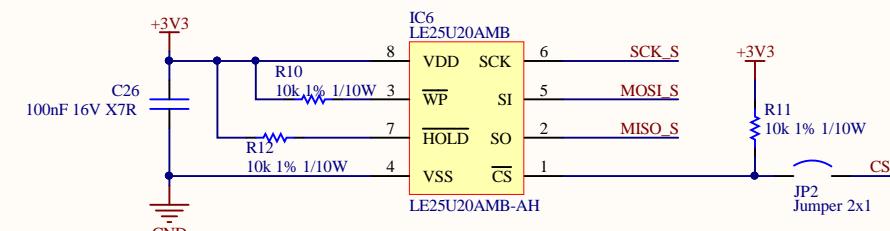
ICE/Flash programming selection



FTDI Ports



Flash Memory



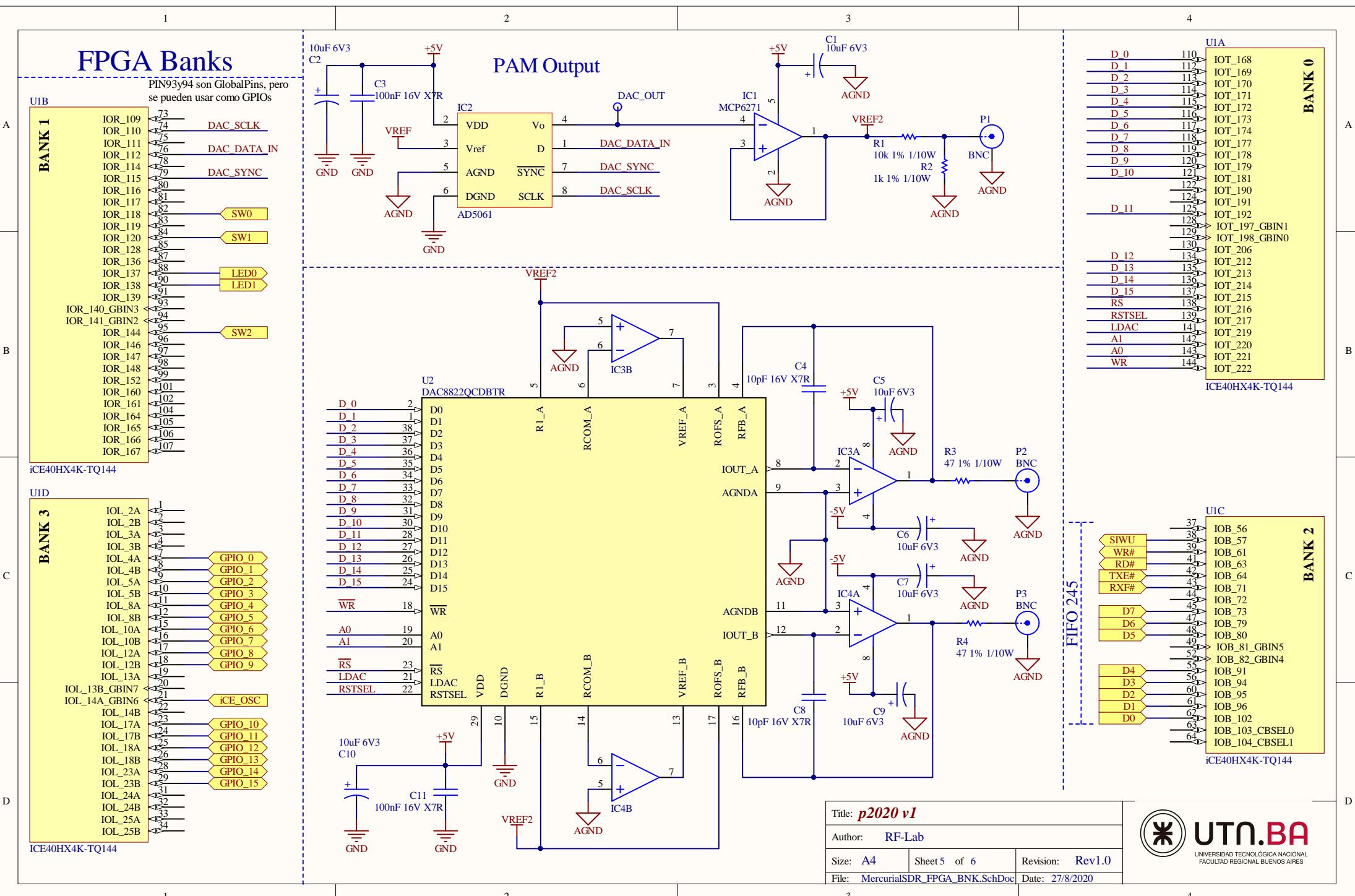
Remove JP2 only for programming ICE
Shunt JP2 for Flash programming or normal operation

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Author: RF-Lab

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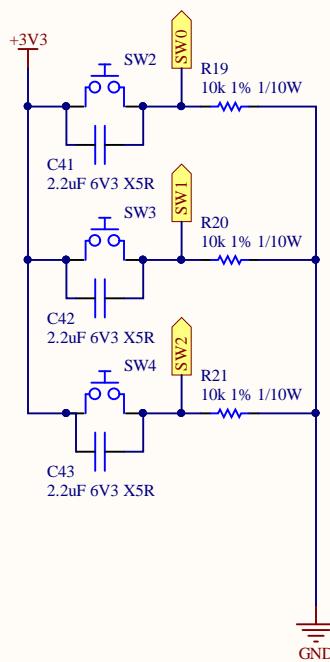
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Miscellaneous

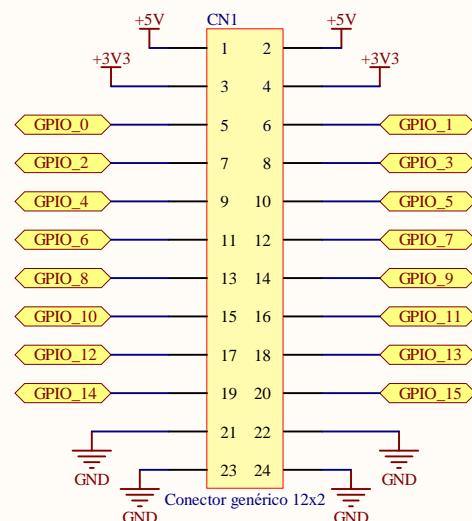
A

Buttons



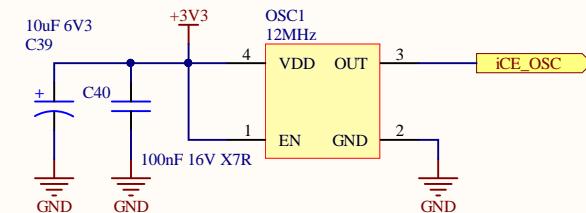
B

GPIO Ports



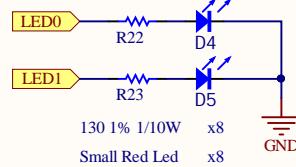
C

iCE Oscillator



D

Leds

Title: *p2020 v1*

Author: RF-Lab

Size: A4 | Sheet 6 of 6 | Revision: Rev1.0

File: MercurialSDR_MISC.SchDoc | Date: 27/8/2020

