# GigaDevice Semiconductor Inc.

Arm® Cortex®-Mx 32-bit MCU

应用笔记 AN040



# 目录

目录	
图索引	
表索引	
1. 简介	
2. IAP 程序	
2.1.1. Bootloader	6
2.2. 工程配置	
2.2.1. Bootloader	8
2.2.2. APP	10
2.3. 代码解读	11
3. 版本历史	13



# 图索引

图 2-1. Bootloader 工程配置	8
图 2-2. Bootloader 工程 map 文件	
图 2-3. Bootloader 工程中擦写 APP Flash 指令	10
图 2-4. Bootloader 工程中 APP 程序地址宏	10
图 2-4. APP 工程配置	.11



# 表索引

表 2-1. Bootloader 代码	. 6
表 2-2. APP 代码	. 7
表 3-1. 版本历史	13



### 1. 简介

IAP(在线应用编程)程序通过提前写入用于升级代码的 bootloader,可以完成 MCU APP 功能的升级,增强了代码的灵活性,在完成 APP 代码升级之后,程序需要从 Bootloader 代码跳转到 APP 代码运行, 本应用笔记基于 GD32F10x 系列,介绍如何实现程序从 Bootloader 代码跳转到 APP 代码。



### 2. IAP 程序

IAP 程序通常由两个部分组成: Bootloader 和 APP。Bootloader 和 APP 分别为两个工程程序, 存放在 Flash 的 Main Flash 区,即 0x08000000 开始的区域。

### 2.1. 程序结构

#### 2.1.1. Bootloader

Bootloader 代码结构如下表所示。

#### 表 2-1. Bootloader 代码

```
\brief
               main function
    \param[in] none
    \param[out] none
    \retval
               none
int main(void)
    /* init modules ... */
    /* if no need to update APP */
    if(.....){
        /* Check if valid stack address (RAM address) then jump to user application */
        if (0x200000000 == ((*(\_IO uint32_t*)USER_FLASH_BANK0_FIRST_PAGE_ADDRESS) &
0x2FFE0000)){
            /* disable all interrupts */
             nvic_irq_disable(EXTI0_IRQn);
            /* Jump to user application */
             JumpAddress = *(__IO uint32_t*) (USER_FLASH_BANK0_FIRST_PAGE_ADDRESS
+4);
             Jump_To_Application = (pFunction) JumpAddress;
            /* Initialize user application's Stack Pointer */
             __set_MSP(*(__IO uint32_t*) USER_FLASH_BANK0_FIRST_PAGE_ADDRESS);
             Jump_To_Application();
        } else {
            /* LED2 ON to indicate bad software (when not valid stack address) */
             gd_eval_led_on(LED2);
             /* do nothing */
             while(1){
```



```
}

/* Bootloader codes for update APP areas */
} else {

/* Bootloader realizing codes */

/* including commands of operating flash */

.....

while (1){

/* Bootloader realizing codes */

}

}
```

#### 2.1.2. APP

APP 代码结构如下表所示。

#### 表 2-2. APP 代码

```
\brief
                main function
    \param[in] none
    \param[out] none
    \retval
                none
int main(void)
  /* set the NVIC vector table base address to APP code area */
   nvic_vector_table_set(NVIC_VECTTAB_FLASH, APP_OFFSET);
  /* enable global interrupt, the same as __set_PRIMASK(0) */
   __enable_irq();
   /* init modules ... */
   . . . . . .
   while (1){
       /* APP realizing codes */
  }
```

### 2.2. 工程配置

要完成 APP 的升级,要事先将编写好的 Bootloader 代码下载到 MCU 0x08000000 地址开始的 Flash 中。并且要保证 APP 代码区域不与 Bootloader 代码区域有重叠。以 GD32F107VC



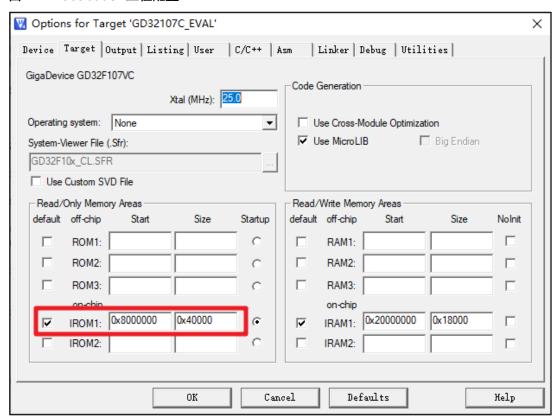
为例, 按以下操作进行。

#### 2.2.1. Bootloader

为了确保 APP 代码区域与 Bootloader 代码区域不重叠,Bootloader 工程配置需要按以下步骤进行:

1. 首先查询数据手册, flash 大小为 256KB, 工程配置如下图所示, 确认是从 0x08000000 开始。

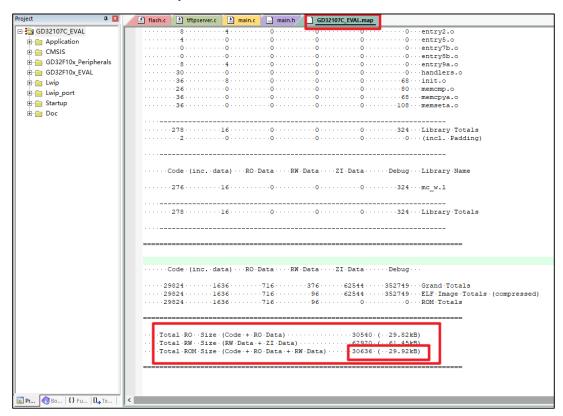
#### 图 2-1. Bootloader 工程配置



2. 查看 Bootloader 工程编译生成的 map 文件,确认代码大小,29.92KB 也就是 0x77AE 字节大小。



#### 图 2-2. Bootloader 工程 map 文件



3. 修改 Bootloader 代码中的擦写 APP Flash 区域相关指令,主要修改要擦写的 Flash 区域的起始地址: USER\_FLASH\_BANK0\_FIRST\_PAGE\_ADDRESS 宏所对应的地址,改为0x08010000,表示有 0x100000 字节用于 Bootloader 代码存储,大于 Bootloader 代码的0x77AE 字节大小。



#### 图 2-3. Bootloader 工程中擦写 APP Flash 指令

```
static int IAP tftp process write(struct udp pcb *upcb, const ip addr t *to, int to port)
210 🗏 🕻
         tftp_connection_args **args = NULL;
//* This function is called from a callback,
211
212
         * therefore interrupts are disabled,
213
        ...* therefore we can use regular malloc ...*/
..args = mem_malloc(sizeof.*args);
214
215
216
         - if (!args) {
217
             IAP_tftp_cleanup_wr(upcb, args);
218
            ···return 0;
219
220
221
      · · · · args->op · = · TFTP WRQ;
        args->to_ip.addr = to->addr;
223
        --args->to_port = to_port;
        ··/* the block # used as a positive response to a WRQ is _always_ 0!!! (see RFC1350) ··*/
224
225
         -args->block = 0;
226
      ···args->tot_bytes = 0;
227
        ··/*·set·callback·for·receives·on·this·UDP·PCB·(Protocol·Control·Block)·*/
228
229
        -udp_recv(upcb, IAP_wrq_recv_callback, args);
230
231
       ···total count =0;
232
       ···/*·init·flash·*/
233
234
         ·FLASH_If_Init();
235
236
          /*·erase·user·flash·area·*/
          FLASH_If_Erase(USER_FLASH_BANKO_FIRST_PAGE_ADDRESS);>
237
238
239
         Flash_Write_Address = USER_FLASH_BANKO_FIRST_PAGE_ADDRESS;
240
241
          IAP_tftp_send_ack_packet(upcb, to, to_port, args->block);
242
243
```

#### 图 2-4. Bootloader 工程中 APP 程序地址宏

```
flash.c tftpserver.c main.c GD32107C_EVAL.map
  11 */
  13 = #ifndef MAIN_H
14 #define MAIN_H
      #include . "gd32f10x.h"
  16
      #include "stdint.h"
#include "gd32f10x_enet_eval.h"
  18
  19
      //#define USE_DHCP · · · · · /* · enable · DHCP, · if · disabled · static · address · is · used · */
  21
      //#define USE_ENET_INTERRUPT
  23
      //#define TIMEOUT_CHECK_USE_LWIP
 25 #define USER_FLASH_BANKO_FIRST_PAGE_ADDRESS 0x080100000
                                                                          ·//user·Flash·address·start·from
                                                                            //user Flash address start from
      #define USER_FLASH_BANK1_LAST_PAGE_ADDRESS · 0x082FF000 · · · · · · ·
                                                                           ·//user·Flash·address·start·from·
      #define USER_FLASH_END_ADDRESS
  29
                                                      ·0x082FFFFF
```

#### 2.2.2. APP

APP 工程配置如下所示。工程 code 起始地址设置为 0x08010000,与 Bootloader 代码中要擦写的地址相同。



#### 图 2-5. APP 工程配置

☑ Options for Target 'GD32107C_EVAL'	×				
Device Target Output Listing User   C/C++   Asm   Linker   Debug   Utilities					
GigaDevice GD32F107VC					
Xtal (MHz): 25.0					
Operating system: None	Use Cross-Module Optimization				
System-Viewer File (.Sfr):	☐ Use MicroLIB ☐ Big Endian				
GD32F10x_CL.SFR					
Use Custom SVD File					
Read/Only Memory Areas	Read/Write Memory Areas				
default off-chip Start Size Startup	default off-chip Start Size NoInit				
ROM1:	□ RAM1: □ □				
□ ROM2: □ □	□ RAM2: □ □				
□ ROM3: □	RAM3:				
on-chin	on-chip				
✓ IROM1: 0x8010000 0x30000 ⓒ	✓ IRAM1: 0x20000000 0x18000 □				
☐ IROM2: C	□ IRAM2: □				
OK Cancel Defaults Help					

### 2.3. 代码解读

Bootloader 代码以及 APP 代码中比较特别的一段代码是 Bootloader 跳转到 APP 的相关代码,在 2.1.1 小节中给出的是适用于 Arm Cortex-M 内核的跳转指令,具体每行按下面来理解。

if  $(0x200000000 == ((*(__IO uint32_t*)USER_FLASH_BANK0_FIRST_PAGE_ADDRESS) & 0x2FFE0000))$ 

这里 USER\_FLASH\_BANKO\_FIRST\_PAGE\_ADDRESS 宏存储的是 APP 程序起始地址,而 APP 程序起始地址存储的是栈顶指针(查看启动文件向量表的前一个地址),如果下载了 APP 程序的话,则 APP 程序起始地址处必然是写入了栈顶指针,所以可以通过查看栈顶指针值是否位于 SRAM 地址范围来判断是否已经下载了 APP程序。SRAM 地址范围可以通过查看对应型号 MCU 的 datasheet 得知,例如本例是 96K,即 0x18000 字节,应当查看 SP 是否位于 0x20000000~0x20017FFF,可以检查 SP 的 bit 17-31,即与 0x2FFE0000 相与后判断值,但这个比较并不准确,可以直接采用准确的范围比较方式。如果判断结果为已经下载了 APP 程序,则进行后续的跳转动作。

nvic\_irq\_disable(EXTI0\_IRQn);

在跳转到 APP 程序之前需要关闭所有中断,这么做是为了避免 APP 程序运行出错或卡死。一个原因为在运行 Reset\_Handler 函数的\_\_main 时会初始化 APP 应用的 RAM 区数据,如果由于未关闭其他中断而来了一个中断,这个中断此时还是 Bootloader 程序的中断,可能恰好改变了 RAM 区的数据,那么在 APP 程序运行时就会出问题。另一个原因为在跳到 APP 程序后,由于我们跳转过程中只会对系统时钟进行重新配置,而不会影响到其他模块的寄存器,因此其他已配置的寄存器信息



将保持 Bootloader 时的配置,如果并未初始化所有模块,而在跳转之前又没有关闭所有中断,那么在 Bootloader 程序中运行的一些模块可能在 APP 程序中依然在运行,并自动触发中断,而 APP 程序中如果没有对相应中断服务函数的清标志处理,则 APP 程序可能会陷入中断死循环而无法正常运行,因此需要关闭所有中断。

JumpAddress = \*(\_IO uint32\_t\*) (USER\_FLASH\_BANK0\_FIRST\_PAGE\_ADDRESS + 4);

Jump\_To\_Application = (pFunction) JumpAddress;

USER\_FLASH\_BANKO\_FIRST\_PAGE\_ADDRESS + 4 地址处存储的是 Reset\_Handler 向量,该向量为 Reset\_Handler 处理函数的入口地址,由于已经将 pFunction 自定义为 void 类型的函数指针,所以下一句将 Jump\_To\_Application 指针指向 Reset\_Handler 函数的入口地址。

\_\_set\_MSP(\*(\_\_IO uint32\_t\*) USER\_FLASH\_BANK0\_FIRST\_PAGE\_ADDRESS);

执行 APP 程序的第一条指令,即将主堆栈指针设置为 APP 程序起始地址 USER\_FLASH\_BANKO\_FIRST\_PAGE\_ADDRESS,需要在真正运行 APP 程序之前就准备好 MSP,因为可能 Reset\_Handler 的第一条指令还没来得及执行,就发生了 NMI 或者其他 fault,此时就需要 MSP 来提供堆栈。

Jump\_To\_Application();

执行 Jump\_To\_Application 指针指向的函数,即 Reset\_Handler 函数,在 Reset\_Handler 函数中执行完\_\_main 函数后,将自动跳转到 main()函数,也就是 APP 主程序部分。



## 3. 版本历史

表 3-1. 版本历史

版本号.	说明	日期
1.0	首次发布	2021年11月30日



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