DIVINE: Analysis of C++ Programs

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22nd September 2017

DIVINE: Analysis of C++ Programs



- DIVINE is an analyser for C and C++ programs
- focus on language features
- built on top of the LLVM infrastructure
- tightly integrated compiler



waiting in journal review process:

■ Petr Ročkai, Vladimír Štill, Ivana Černá, Jiří Barnat:
DiVM: Model Checking with LLVM and Graph Memory



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freshly published:

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 From Model Checking to Runtime Verification and Back



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- Katarína Kejstová, Petr Ročkai, Jiří Barnat: From Model Checking to Runtime Verification and Back reworking:
 - Petr Ročkai, Jiří Barnat: A Simulator for LLVM Bitcode

Current Status: New Features I



DIVINE Operating System (DiOS)

- modularized for more flexibility and configurability
- support for processes (fork)

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Symbolic Data

- symbolization of data structures
- multiple data domains in the same program
- refactoring and cleaning

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Symbolic Data

- symbolization of data structures
- multiple data domains in the same program
- refactoring and cleaning

- resurrected and refactored implementation
- work on efficiency

Current Status: New Features II



Simulator

■ multiple usability improvements

Current Status: New Features II



Simulator

multiple usability improvements

Algorithms

- more robust counterexample format
- divine run can output stream of executed instructions

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Core

■ improved state space reduction



■ DiOS: A Lightweight Approach to Verifying POSIX-Based Programs



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- A Semi-Dynamic State Space Reduction for C/C++ Programs



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- Symbolic Computation via Program Transformations



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- Verification of Concurrent C++ Programs with Realistic Memory Models



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- but syscall ordering has to match
- in future we want to allow reordering of independent syscalls
- also possibility to replace replies of some syscalls with symbolic/nondeterministic values



What is relaxed memory model?

describes how memory operations propagate between processors



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- effects of cache, out-of-order execution
- depends on architecture
 - Intel x86: store buffering
 - ARM/POWER: almost arbitrary reordering of loads and stores
- significantly increases theoretical complexity of verification



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- thesis proposal submitted



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 - opportunity for static analysis, heuristics



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 - opportunity for static analysis, heuristics
- plans to support multiple memory models
- completeness of analysis

Symbolic and Abstract Verification: Motivation



program's behaviour can depend on input values

Symbolic and Abstract Verification: Motivation



- program's behaviour can depend on input values
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Symbolic and Abstract Verification: Motivation



- program's behaviour can depend on input values
- need for verification under arbitrary inputs
- cannot be handled explicitly: too many combinations



main idea: transform the program to encode symbolic/abstracted data

Henrich's master's thesis, later PhD



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- framework for using different abstractions



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- framework for using different abstractions
- master's thesis in December
 - finalizing the initial implementation
 - testing and benchmarking
 - support for functions, data structures
 - but no dynamic memory and aliased pointers



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- framework for using different abstractions
- master's thesis in December
 - finalizing the initial implementation
 - testing and benchmarking
 - support for functions, data structures
 - but no dynamic memory and aliased pointers
- planned PhD work
 - full abstraction of programs with memory
 - symbolic memory
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