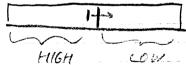
BRF	1 BRANG	TH FORWARD)	7467	26	ADDE	0.	*** **** ****
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BRNCH	THERE	+ LDH E + DC	RO, RO ALZ (THEZ	C - 18\$ 1	7		The second secon
LCON	(LOAD C	ONSTANT)	R1, 25				
		+ LOH1 + DC	R1, R0, 2 H'25'				
HADD	R 2 , R	1	Ra	22			;*
ADD	R 2 R1		1234	5677 562C			
HTL			1256	56 AC			
4005 1 LTH			0068	562C 62 AC		د د د د در دده	
for wh	, Kalimer G	ena after.	(Ra wil	Cle alon	September !)	
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LAD 21	, THERE		L	40 R1,	THER	, (M - 10 V -
						10'	

ADD R1, R2		
R1 1234		
R2 AB, C, D		-
R1 3501		
To the property of the state of		
ADDS 1 R1, R2	,	
21 .		
1234 RZ 1234 ABCD	A STATE OF THE STA	
1 237 7 800		
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	ANALOGO CONTRACTOR CON	
R1 00 DF		
		<u>:</u>
HTL LTH		
HTL R1 R2		
	FOR ADDING 16 BIT'S	
	102 MODING, 46 WIT 3	
LTH RA RZ		
LDHI RA, R2, 2	R1 R2	
LDHI D R1 RZ	1234 5678	
Cont.	ABCD	
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TWIF D. X 71	R1 = AB1B
EMIT R1, X'7A'	R1 = AB7A
JEQ R1, RZ	
EMIT R1, X'3A'	
-> LDHI R3,	
EMIT R2, 12	,
JHAM RA, RZ	JUMP 21 R2 -> FF ., FF
	JUMP ADOR ESS NORMAILY
	FOR EXAMPLE
R1 = 1002	1000 EAIT R1, 12 1002 JHAM 81, RZ
R2 = 1004	1002 JHAM 87, RE
HIFTR	



HIGH ORDER BYTE NOT AFFECTED LOW ORDER BIT OF MUHORDER BYTE IS SHIFTED TO THE HIBM CROER BIT OF LOW ORDER BYTE

