CN<sub>1</sub> CPU Card Connector /SHO /SI 0 /SL0 /SH0 4 /SL1 /SL1 /SH1 /SH1 5 6 /SH<sub>2</sub> /SL2 /SI 2 /SH2 /INT0 /IAKO /INTO J/IAK0 10 /INT1 /IAK1 /INT1 /IAK1 /IAK2 /INT2 /INT2 J/IAK2 13 14 /RSIR /RSIR /KBIR /KBIR 15 16 /IOIR /IOIR E 17 18 **GND DGND DGND GND** 19 20 +5V +5V +5V $l_{+5V}$ 21 22 /RESET /RESET **CPUCK CPUCK** 24 /LDMA /BGACK /LDMA /BGACK 25 26 /BRL /BR /BG /BG 27 28 BD0 **BD0** BD<sub>1</sub> BD1 29 30 BD<sub>2</sub> BD3 BD2 BD3 31 32 BD4 BD5 BD4 BD5 34 BD6 BD<sub>6</sub> BD7 BD7 35 36 BD8 BD8 BD9 BD9 38 BD11 BD10L **BD10** BD11 39 40 BD12 **BD12 BD13 BD13** 41 **BD15** BD14 **BD14** BD15 43 44 **GND DGND DGND** GND 45 +5V +5V+5V +5V47 48 A1 **A1 A2** A<sub>2</sub> 49 50 A3L **A3 A4** A4 51 52 A5[ **A5** 1A6 **A6** 53 54 A7[ **A7 A8 A8** 55 56 **A9**[ **A9** A10 A10 57 58 lA12 A11 A11 A12 59 /VMA /VPA 1/VPA/VMA 61 62 +12V+12V/DTACK /DTACK 64 /AS /AS **READ** READ 65 66 /LDS /LDS /UDS /UDS 67 68 /CSYNC /INITIO /CSYNC /INITIO 69 70 VA10B VA10B VA9B VA9B 71 R1 R2 R1 R2 73 74 MD0L M<sub>D</sub>0 MD1 MD1 76 75 MD2 MD<sub>2</sub> MD3 MD3 78 MD5 MD4 MD4 MD5 79 80 MD6 MD<sub>6</sub> MD7 MD7 81 82 MD8 MD8 MD9 MD9 84 83 MD10 **MD11** MD10 JMD11 85 86 MD12 **MD12 MD13** MD13 87 88 **MD15** MD14 **MD14** MD15 89 90 GND **DGND DGND GND** 91 +5V +5V+5VJ+5V 93 94 RA1 RA<sub>2</sub> RA1 RA2 95 96 RA3 RA3 RA4 JRA4 97 98 RA5 RA5 RA<sub>6</sub> lRA6 99 100 RA7 RA7 RA8 RA8 101 102 /RSTSW[ /RSTSW A16 A16 103 104 A17 A17 A18 \_\_A18 105 106 **DOTCK** A19 A19 DOTCK 107 108 MREAD **MREAD** /CAS J/CAS 109 110 /RAS /RASL A20 A20 111 112 /HSYNC /HDER /HSYNC /HDER 113 114 /VSYNC /SFER VSYNCL /SFER 115 116 VID /NMI VIDL /NMI 117 118 GND **GND DGND DGND** 

LISA\_60\_PIN\_EDGE\_MK5

+5V

120

1+5V

119

+5V

+5V