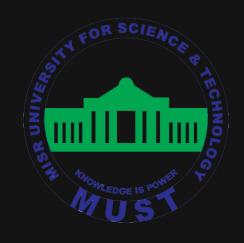
MISR UNIVERSITY FOR SCIENCE AND TECHNOLOGY COLLEGE OF ENGINEERING MECHATRONICS DEPARTMENT



MTE 405 SENSORS AND MEASUREMENTS

LAB 1 - SPRING 2020

Goals Of The Lab

Introduction to Sensors and Signal Conditioning with Virtual Prototyping







Introduction to temperature sensors

Lab 1

Preliminary Flow Of The Course

Expected Goals Reached

Simulation and Datasheet Reading Signal Conditioning Improving Measurements Arduino Analog Reading Data Display Live embedded system measurements Arduino Analog Reading Circuit Communication (I2C) Serial Peripheral Interface (SPI) Serial Communication	Measuring Physical Parameters	Data Acquisition	Going Smart	Signal Logging (Project)
	Datasheet Reading Signal Conditioning Improving	Reading Data Display Live embedded system	Circuit Communication (I2C) Serial Peripheral Interface (SPI) Serial	based multi

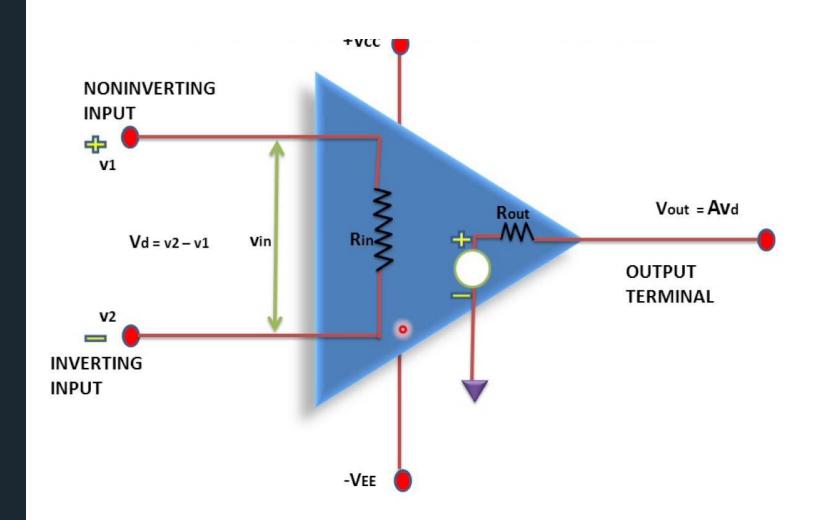
Signal Conditioning

Review on operational amplifiers

Commonly Used Configurations

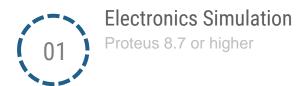
Amplifiers most frequently used:

- Inverting Amplifier.
- Non-Inverting Amplifier.
- Differential
- Amplification of AC signal with single supply.



Software For Simulation

Needed for all labs



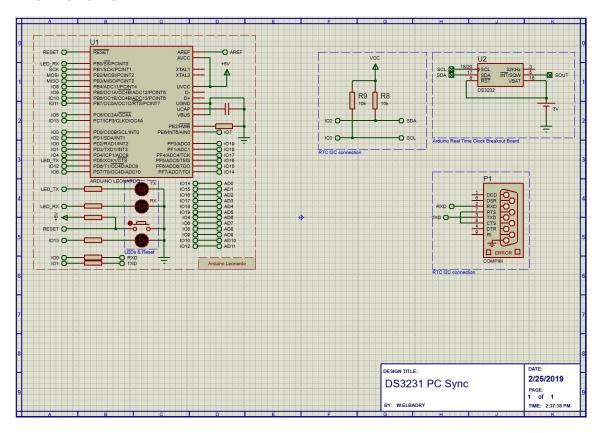


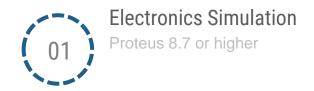


Student must install software in advance before attending any lab

Software For Simulation

Needed for all labs





Version 8.9 SP1 is highly recommended (not mandatory)

Operational Amplifier

- Amplification.
- Attenuation.
- Buffering.
- Filtering (active filters).

Where:

 V_i ... Input voltage V_o ... Output voltage G ... Amplifier G ain f_c ... Cutoff frequency F and F ... filter parameters

$$V_{o} = V_{i} * G, G > 1$$

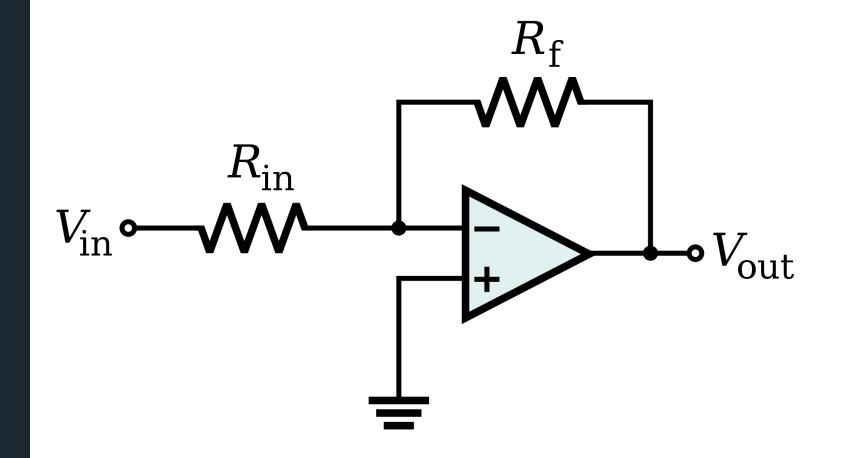
 $V_{o} = V_{i} * G, G < 1$
 $V_{o} = V_{i} * G, G = 1$
 $V_{o} = V_{i} * G, f_{c} = \frac{1}{j\omega RC}$

Lab 1 Signal Conditioning

Exercise 1

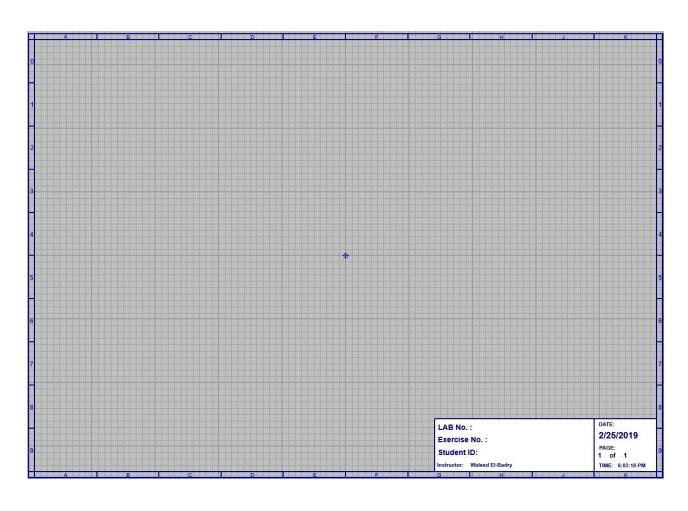
Inverting Amplifier

- 1. Amplification.
- 2. Attenuation.
- 3. Buffering



Lab 1

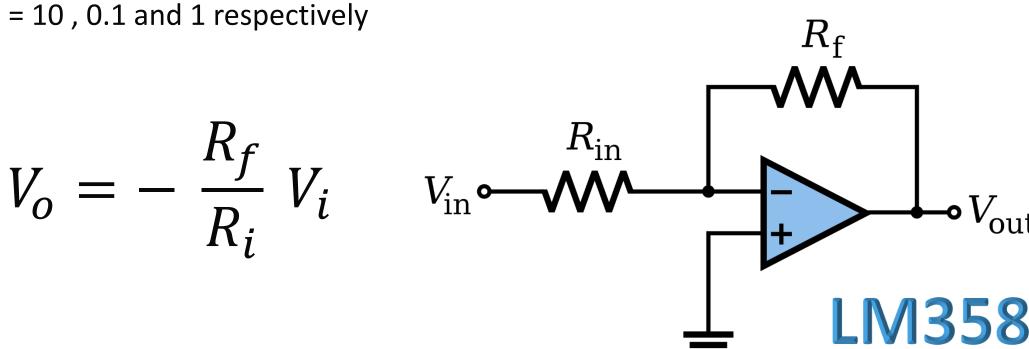
Proteus Course Template



MTE 405 Template (available on course repository)

Exercise 1 – Inverting Amplifier

Using Proteus ISIS schematic capture, apply inverting amplifier circuits with G



Exercise 1 – Inverting Amplifier













LM158-N, LM258-N, LM2904-N, LM358-N SNOSBT3I – JANUARY 2000 – REVISED DECEMBER 2014

LMx58-N Low-Power, Dual-Operational Amplifiers

1 Features

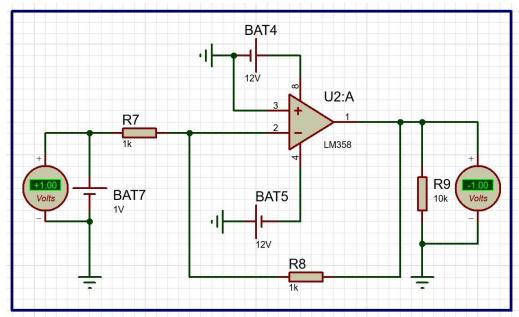
- Available in 8-Bump DSBGA Chip-Sized Package, (See AN-1112, SNVA009)
- · Internally Frequency Compensated for Unity Gain
- Large DC Voltage Gain: 100 dB
- Wide Bandwidth (Unity Gain): 1 MHz (Temperature Compensated)
- Wide Power Supply Range:
 - Single Supply: 3V to 32V
 - Or Dual Supplies: ±1.5V to ±16V
- Very Low Supply Current Drain (500)
 - uA)—Essentially Independent of Ownly Voltage

3 Description

The LM158 series consists of two independent, high gain, internally frequency compensated operational amplifiers which were designed specifically to operate from a single power supply over a wide range of voltages. Operation from split power supplies is also possible and the low power supply current drain is independent of the magnitude of the power supply voltage.

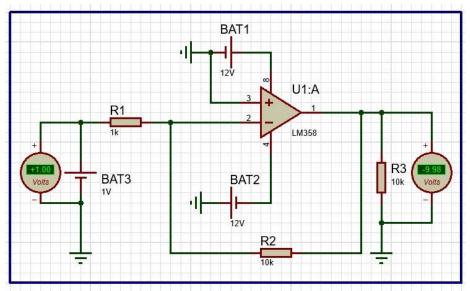
Application areas include transducer amplifiers, do gain blocks and all the conventional op-amp circuits which now can be more easily implemented in single power supply systems. For example, the LM158 series can be directly operated off of the standard

Exercise 1 – Inverting Amplifier

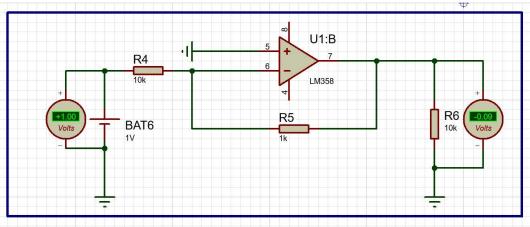


Inverting Amplifier G = 1

- Why there is a loss in output voltage?
- Does the output voltage vary with load?
- What is the constraint of using inverting configuration?



Inverting Amplifier G = 10



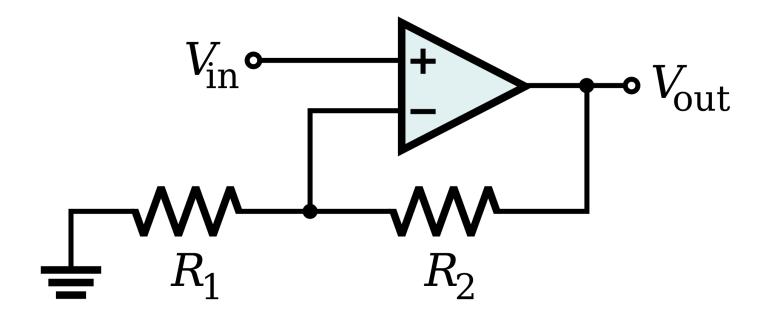
Inverting Amplifier G = 0.1

Lab 1 Signal Conditioning

Exercise 2

Non-Inverting Amplifier

- 1. Amplification.
- 2. Attenuation.
- 3. Buffering



Exercise 2 – Non-Inverting Amplifier

Using Proteus ISIS schematic capture, apply non-inverting amplifier circuits with G = 10 and G = 1 respectively

$$V_o = \left(1 + \frac{R_2}{R_1}\right) V_i$$

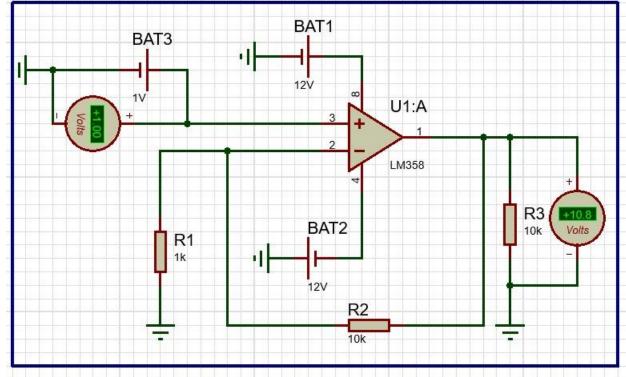
$$= \frac{V_{\text{in}}}{R_1} V_{\text{out}}$$

$$= \frac{V_{\text{in}}}{R_2} V_{\text{out}}$$

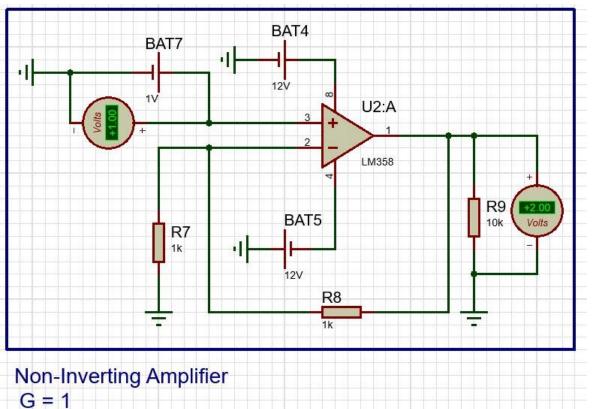
$$= \frac{V_{\text{in}}}{R_2} V_{\text{out}}$$

Lab 1

Exercise 2 – Non-Inverting Amplifier







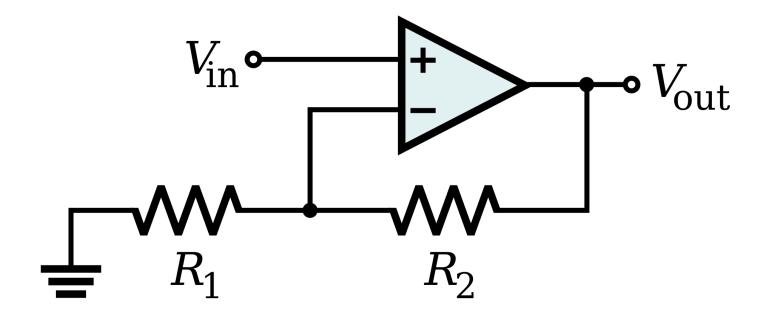
- Is it better than non-inverting?
- Do you notice any disadvantage?

Lab 1 Signal Conditioning

Exercise 3

Differential Amplifier

- 1. Difference to Single Ended
- 2. Attenuation.
- 3. Amplification
- 4. Buffering

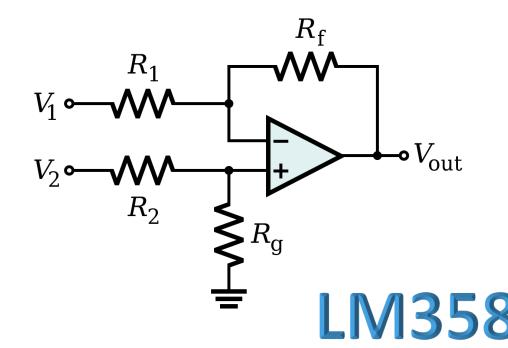


Exercise 3 – Differential Amplifier

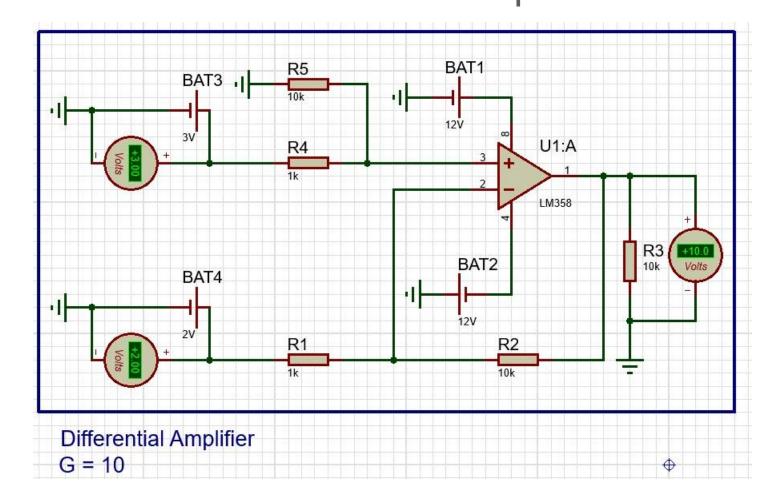
Using Proteus ISIS schematic capture, apply differential amplifier circuits with

$$G = 10$$

$$V_o = \frac{R_f}{R_1} (V_2 - V_1)$$
 $V_1 - W_1$
 $V_2 - W_1$



Exercise 3 – Differential Amplifier



- Did you get the concept of differential to single ended?
- We will need this configuration when using Wheatstone bridge ?

END OF LAB 1