Research Report

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review of the issues to solve/What I needed to do

- Needed to know how existing programs perform the memory/dependency analysis
- Needed to know its weakness
- Needed to figure out where the memory trace could step in and help
- Others
  - Plos ONE (cardiac)
  - Lulesh (using INRIA PPCG)
  - PNNL benchmark

Progress on PoCC Memory Trace

- Thought that: the memory trace could at least be used to convince (or prove to) the compiler certain dependences (from static analysis) are false, given the trace collected dynamically.

- Still No substancial progress achieved
  - because got distracted from preparing for cardiac revised manuscript. Will submit it soon so that I would be focused before next report.

The plan
(remain the same as previous one)

- Getting to know how existing programs perform the memory/dependency analysis
- Getting to know its weakness
- Think where the memory trace should step in and help