Research Report

Monday 12/09/2013

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review of the issues

- Make NLR work with triangle loop boundaries
- Integrate NLR to PoCC (adding one more source code to the Collection)
- Addressing IMPACT 2014 final version issues
- Aim to get a paper (on what topic?) out in a CONFERENCE by March 1st, 2014.
- Energy Tuning Project
  - Lulesh benchmark using PPCG
  - PNNL benchmark

Progress & Problems

- Addressed IMPACT'14 reviewers' comments

  - Generated stencil program energy/time correlation

  - Explained why maxfuse required more power for covariance benchmark on SandyBridge and brdr2d on Xeon Phi: more memory access (fused statement) and parallelization caused higher power usage, parallelization reduced execution time

- Others

  - did not move forward

The plan

- Make NLR work with triangle loop boundaries
- Integrate NLR to PoCC (adding one more source code to the Collection)
- Aim for a CONFERENCE (PACT'14?) by March 1st, 2014.
- Energy Tuning Project
  - Lulesh benchmark using PPCG
  - PNNL benchmark