# General Description

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 5P49V5901B686NLGI. Please refer to the device datasheet for further information about the device.

### General Configuration

| Parameter | Value | Units |
|---|---|---|
| Device I²C Address | Primary 0xD4 | |
| XTAL Load Capacitor | 0 | pF |
| SD/OE Pin Function | Output Enable | |
| SD/OE Polarity | Negative | |

### Frequency Overview

| Parameter | Configuration 0 | Configuration 1 | Configuration 2 | Configuration 3 | Units |
|---|---|---|---|---|---|
| Input | 25 | 25 | | | MHz |
| Output 0 | 25 | 25 | | | MHz |
| Output 1 | 100 | 50 | | | MHz |
| Output 2 | 100 | 50 | | | MHz |
| Output 3 | 50 | 50 | | | MHz |
| Output 4 | 50 | 50 | | | MHz |

### Input Configuration

| Parameter | Input when CLKSEL = LOW or OPEN | Input when CLKSEL = HIGH |
|---|---|---|
| Configuration 0 | Crystal input (pin 4 XIN/REF) | No active input (differential clock input is disabled) |
| Configuration 1 | Single-ended clock on XIN crystal input (crystal overdrive) | No active input (differential clock input is disabled) |

### Configuration 0 Parameters: SEL[1:0] = 00

| Parameter | Output 0 | Output 1 | Output 2 | Output 3 | Output 4 | Units |
|---|---|---|---|---|---|---|
| Input Frequency | 25 | 25 | 25 | 25 | 25 | MHz |
| Default Output Status | On | On | Off | On | On | |
| VDDO Voltage | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | V |
| Output Type | LVCMOS | HCSL | HCSL | LVPECL | LVPECL | |
| Frequency | 25 | 100 | 100 | 50 | 50 | MHz |
| Spread Spectrum | | Off | Off | Off | Off | |
| Spread Spectrum Modulation | | | | | | % |
| Slew Rate | 1.0x nominal | | | | | |
| Phase Shift | | 0 | 0 | 0 | 0 | Degrees |

## Configuration 1 Parameters: SEL[1:0] = 01

| Parameter | Output 0 | Output 1 | Output 2 | Output 3 | Output 4 | Units |
|---|---|---|---|---|---|---|
| Input Frequency | 25 | 25 | 25 | 25 | 25 | MHz |
| Default Output Status | On | On | On | On | On | |
| VDDO Voltage | 3.3 | 3.3 | 3.3 | 3.3 | 3.3 | V |
| Output Type | LVCMOS | LVPECL | LVPECL | LVPECL | LVPECL | |
| Frequency | 25 | 50 | 50 | 50 | 50 | MHz |
| Spread Spectrum | | Off | Off | Off | Off | |
| Spread Spectrum Modulation | | | | | | % |
| Slew Rate | 1.0x nominal | | | | | |
| Phase Shift | | 0 | 0 | 0 | 0 | Degrees |

## 5P49V5901B686NLGI Ordering Information

| Part/Order Number | Marking | Package | Shipping Packaging | Temperature |
|---|---|---|---|---|
| 5P49V5901B686NLGI | 5901A<br>686<br>YWW**$ | "Lead-Free" 24-pin VFQFPN | Tray | -40° to +85°C |
| 5P49V5901B686NLGI8 | 5901A<br>686<br>YWW**$ | "Lead-Free" 24-pin VFQFPN | Tape and Reel | -40° to +85°C |

Marking notes:
1. Line 1 is the truncated part number.
2. Line 2 is the dash code.
3. "YWW" is the last digit of the year and work week that the part was assembled.
4. "**" denotes the lot number.
5. "$" denotes the mark code.

**Corporate Headquarters**
6024 Silver Creek Valley Road
San Jose, CA 95138 USA

**Sales**
1-800-345-7015 or 408-284-8200
Fax: 408-284-2775
www.IDT.com

**Tech Support**
email: clocks@idt.com