



General Description

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 5P49V5901B686NLGI. Please refer to the device datasheet for further information about the device.

General Configuration

Parameter	Value	Units
Device I ² C Address	Primary 0xD4	
XTAL Load Capacitor	0	pF
SD/OE Pin Function	Output Enable	
SD/OE Polarity	Negative	

Frequency Overview

Parameter	Configuration 0	Configuration 1	Configuration 2	Configuration 3	Units
Input	25	25			MHz
Output 0	25	25			MHz
Output 1	100	50			MHz
Output 2	100	50			MHz
Output 3	50	50			MHz
Output 4	50	50			MHz

Input Configuration

Parameter	Input when CLKSEL = LOW or OPEN	Input when CLKSEL = HIGH
Configuration 0	Crystal input (pin 4 XIN/REF)	No active input (differential clock input is disabled)
Configuration 1	Single-ended clock on XIN crystal input (crystal overdrive)	No active input (differential clock input is disabled)

Configuration 0 Parameters: SEL[1:0] = 00

Parameter	Output 0	Output 1	Output 2	Output 3	Output 4	Units
Input Frequency	25	25	25	25	25	MHz
Default Output Status	On	On	Off	On	On	
VDDO Voltage	3.3	3.3	3.3	3.3	3.3	V
Output Type	LVC MOS	HCSL	HCSL	LVPECL	LVPECL	
Frequency	25	100	100	50	50	MHz
Spread Spectrum		Off	Off	Off	Off	
Spread Spectrum Modulation						%
Slew Rate	1.0x nominal					
Phase Shift		0	0	0	0	Degrees

Configuration 1 Parameters: SEL[1:0] = 01

Parameter	Output 0	Output 1	Output 2	Output 3	Output 4	Units
Input Frequency	25	25	25	25	25	MHz
Default Output Status	On	On	On	On	On	
VDDO Voltage	3.3	3.3	3.3	3.3	3.3	V
Output Type	LVC MOS	LVPECL	LVPECL	LVPECL	LVPECL	
Frequency	25	50	50	50	50	MHz
Spread Spectrum		Off	Off	Off	Off	
Spread Spectrum Modulation						%
Slew Rate	1.0x nominal					
Phase Shift		0	0	0	0	Degrees

5P49V5901B686NLGI Ordering Information

Part/Order Number	Marking	Package	Shipping Packaging	Temperature
5P49V5901B686NLGI	5901A 686 YWW**\$	"Lead-Free" 24-pin VFQFPN	Tray	-40° to +85°C
5P49V5901B686NLGI8	5901A 686 YWW**\$	"Lead-Free" 24-pin VFQFPN	Tape and Reel	-40° to +85°C

Marking notes:

1. Line 1 is the truncated part number.
2. Line 2 is the dash code.
3. "YWW" is the last digit of the year and work week that the part was assembled.
4. "***" denotes the lot number.
5. "\$" denotes the mark code.



Corporate Headquarters

6024 Silver Creek Valley Road
San Jose, CA 95138 USA

Sales

1-800-345-7015 or 408-284-8200
Fax: 408-284-2775
www.IDT.com

Tech Support

email: clocks@idt.com

DISCLAIMER Integrated Device Technology, Inc. (IDT) and its subsidiaries reserve the right to modify the products and/or specifications described herein at any time and at IDT's sole discretion. All information in this document, including descriptions of product features and performance, is subject to change without notice. Performance specifications and the operating parameters of the described products are determined in the independent state and are not guaranteed to perform the same way when installed in customer products. The information contained herein is provided without representation or warranty of any kind, whether express or implied, including, but not limited to, the suitability of IDT's products for any particular purpose, an implied warranty of merchantability, or non-infringement of the intellectual property rights of others. This document is presented only as a guide and does not convey any license under intellectual property rights of IDT or any third parties. IDT's products are not intended for use in applications involving extreme environmental conditions or in life support systems or similar devices where the failure or malfunction of an IDT product can be reasonably expected to significantly affect the health or safety of users. Anyone using an IDT product in such a manner does so at their own risk, absent an express, written agreement by IDT. Integrated Device Technology, IDT and the IDT logo are registered trademarks of IDT. Product specification subject to change without notice. Other trademarks and service marks used herein, including protected names, logos and designs, are the property of IDT or their respective third party owners. Copyright ©2014 Integrated Device Technology, Inc.. All rights reserved.