General Description

This document details the custom configuration that is programmed into the one-time-programmable (OTP) memory of the 5P49V5901B802NLGI. Please refer to the device datasheet for further information about the device.

General Configuration

Parameter	Value	Units
Device I²C Address	Primary 0xD4	
Crystal Load Capacitance	8	pF
SD/OE Pin Function	Output Enable	
SD/OE Polarity	Positive	

Frequency Overview

Parameter	Configuration 0	Configuration 1	Configuration 2	Configuration 3	Units
Input	25	25	25	25	MHz
Output 0	25	25	25	25	MHz
Output 1	100	100	100	100	MHz
Output 2	100	100	100	100	MHz
Output 3	100	100	100	100	MHz
Output 4	100	100	100	100	MHz

Input Configuration

Parameter	Input when CLKSEL = LOW or OPEN	Input when CLKSEL = HIGH
Configuration 0	Crystal input (pin 4 XIN/REF)	No active input (differential clock input is disabled)
Configuration 1	Crystal input (pin 4 XIN/REF)	No active input (differential clock input is disabled)
Configuration 2	Crystal input (pin 4 XIN/REF)	No active input (differential clock input is disabled)
Configuration 3	Crystal input (pin 4 XIN/REF)	No active input (differential clock input is disabled)

Configuration 0 Parameters: SEL[1:0] = 00

Parameter	Output 0	Output 1	Output 2	Output 3	Output 4	Units
Input Frequency	25	25	25	25	25	MHz
Default Output Status	Off	Off	Off	Off	Off	
VDDO Voltage	3.3 V	1.8 V	3.3 V	1.8 V	3.3 V	V
Output Type	LVCMOS	LVCMOS1, 1 output on OUTx	LVPECL	LVDS	HCSL	
Frequency	25	100	100	100	100	MHz
Spread Spectrum		Off	Off	Off	Off	
Spread Spectrum Modulation						%
Slew Rate	1.0x nominal	1.0x nominal				
Phase Shift		0	0	0	0	Degrees

Configuration 1 Parameters: SEL[1:0] = 01

Parameter	Output 0	Output 1	Output 2	Output 3	Output 4	Units
Input Frequency	25	25	25	25	25	MHz
Default Output Status	Off	On	On	On	On	
VDDO Voltage	3.3 V	1.8 V	3.3 V	1.8 V	3.3 V	V
Output Type	LVCMOS	LVCMOS1, 1 output on OUTx	LVPECL	LVDS	HCSL	
Frequency	25	100	100	100	100	MHz
Spread Spectrum		Down	Down	Down	Down	
Spread Spectrum Modulation		-0.5%	-0.5%	-0.5%	-0.5%	%
Slew Rate	1.0x nominal	1.0x nominal				
Phase Shift		0	0	0	0	Degrees

Configuration 2 Parameters: SEL[1:0] = 10

Parameter	Output 0	Output 1	Output 2	Output 3	Output 4	Units
Input Frequency	25	25	25	25	25	MHz
Default Output Status	Off	On	On	On	On	
VDDO Voltage	3.3 V	1.8 V	3.3 V	1.8 V	3.3 V	V
Output Type	LVCMOS	LVCMOS1, 1 output on OUTx	LVPECL	LVDS	HCSL	
Frequency	25	100	100	100	100	MHz
Spread Spectrum		Down	Down	Down	Down	
Spread Spectrum Modulation		-0.25%	-0.25%	-0.25%	-0.25%	%
Slew Rate	1.0x nominal	1.0x nominal				
Phase Shift		0	0	0	0	Degrees

Configuration 3 Parameters: SEL[1:0] = 11

Parameter	Output 0	Output 1	Output 2	Output 3	Output 4	Units
Input Frequency	25	25	25	25	25	MHz
Default Output Status	Off	On	On	On	On	
VDDO Voltage	3.3 V	1.8 V	3.3 V	1.8 V	3.3 V	V
Output Type	LVCMOS	LVCMOS1, 1 output on OUTx	LVPECL	LVDS	HCSL	
Frequency	25	100	100	100	100	MHz
Spread Spectrum		Off	Off	Off	Off	
Spread Spectrum Modulation						%
Slew Rate	1.0x nominal	1.0x nominal				
Phase Shift		0	0	0	0	Degrees

5P49V5901B802NLGI Ordering Information

Part/Order Number	Marking	Package	Shipping Packaging	Temperature
5P49V5901B802NLGI	5901B 802 YWW**$	"Lead-Free" 24-pin VFQFPN	Tray	-40° to +85°C
5P49V5901B802NLGI8	5901B 802 YWW**$	"Lead-Free" 24-pin VFQFPN	Tape and Reel	-40° to +85°C

Marking notes:
1. Line 1 is the truncated part number.
2. Line 2 is the dash code.
3. "YWW" is the last digit of the year and work week that the part was assembled.
4. "**" denotes the lot number.
5. "$" denotes the mark code.

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