Electronics lab
Assignment1: Digital data

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Multisim task

Draw the digital circuit and graph of simple boolean expression for the following logic gates *A NAND B*, and *A NOR B*,

Show the simulation results (including input and output) using (a) digital clock, and (b) digital word generator. Use the logic analyzer to view the results in Grapher window. Your result should show all the logic levels for different inputs. For example, two input logic will have four different combinations and hence four different outputs need to be formed in a single graph.

Submit the assignment by naming the PDF file according to your roll no. For example IMT2016XXX.pdf. Again lab patners can submit the assignment together. Upload the assignment to lab-assignment2 channel in LMS by 3rd November, 2016 12 Noon.