$$D_2^\delta$$

| D_1 | D_2 | D_3 | D_4 |

$$\Omega$$

Processor	Data	Redundancy
U_1	D_1^δ	D_2^δ
U_2	D_2^δ	D_1^δ
U_3	D_3^δ	D_4^δ
U_4	D_4^δ	D_3^δ