

The diagram illustrates the pin connections for the MAX7456 / AT2456 module. The module is represented by a central yellow box with pins numbered 1 through 29. The connections are as follows:


- VOUT (Pin 26):** Connected to a 47uF capacitor, which is then connected to a 75 ohm resistor and the VIDEO_OUT pin.
- SAG (Pin 6):** Connected to a 47uF capacitor, which is then connected to a 75 ohm resistor and the VIDEO_OUT pin.
- VIN (Pin 22):** Connected to a 100nF capacitor, which is then connected to a 75 ohm resistor and the VIDEO_IN pin.
- XTAL1_OSD (Pin 9) and XTAL2_OSD (Pin 10):** Connected to a 27Mhz crystal oscillator circuit.
- Power and Ground Pins:**
 - EP (Pin 29): Connected to 5V.
 - AVDD (Pin 21): Connected to 5V.
 - DVDD (Pin 3): Connected to 5V.
 - PVDD (Pin 24): Connected to 5V.
 - AGND (Pin 20): Connected to GND.
 - DGND (Pin 4): Connected to GND.
 - PGND (Pin 23): Connected to GND.
- Other Pins:**
 - CS (Pin 8): Connected to 5V.
 - RESET (Pin 19): Connected to 5V.
 - CLKIN (Pin 5): Connected to 5V.
 - SAG (Pin 25): Connected to 5V.
 - SCK (Pin 10): Connected to 5V.
 - MOSI (Pin 9): Connected to 5V.
 - VIN (Pin 22): Connected to 5V.
 - HSYNC (Pin 18): Connected to 5V.
 - VSYNC (Pin 17): Connected to 5V.
 - CLKOUT (Pin 7): Connected to 5V.
 - LOS (Pin 12): Connected to 5V.
 - SDOUT (Pin 11): Connected to 5V.
 - VOUT (Pin 26): Connected to 5V.
 - XTAL2_OSD (Pin 10): Connected to 5V.

5V to 3.3V

The diagram shows the PWR_FLAG XC6206P332MR voltage detector circuit. The device is represented by a yellow box with pins labeled VI, VO, GND, and PWR_FLAG. A 5V input is connected to the VI pin through a 1uF capacitor. The VO pin is connected to a 3.3V output through a 1uF capacitor. The GND pin is connected to a common ground. The PWR_FLAG pin is connected to a common ground through a 1k resistor.

JUMPER

power from servo



5V 1 2 5V-BEC

Pin configuration diagram for the MPU6050 sensor module. The diagram shows the MPU6050 chip with its pins connected to various components. Power pins (VLOGIC, VDD, GND) are connected to 3.3V and GND with decoupling capacitors (10nF, 100nF). I2C pins (SDA, SCL) are connected to 3.3V and GND with 4.7K pull-up resistors. Other pins (INT, AUX_DA, AUX_CL, FSYNC, CLKIN, CPOUT, REGOUT) are connected to GND or left unconnected. A 2.2nF capacitor is connected to AUX_CL, and 100nF capacitors are connected to CPOUT and REGOUT.

PINS

OSD

VIDEO_IN	1
GND	2
VIDEO_OUT	3
GND	4
5V	5

COMM

THROTTLE	1
5V	2
GND	3
SDA	4
SCL	5

PPM

GND	1
5V-BEC	2
PPM	3

ALE

GND	1
5V-BEC	2
ALERON	3

GPS

GPS_RX	1
GPS_TX	2
GND	3
5V	4

FTDI

DTR	1
TX	2
RX	3
GND	4
5V	5

ELE

GND	1
5V-BEC	2
ELEVATOR	3

THR

GND	1
5V-BEC	2
THROTTLE	3