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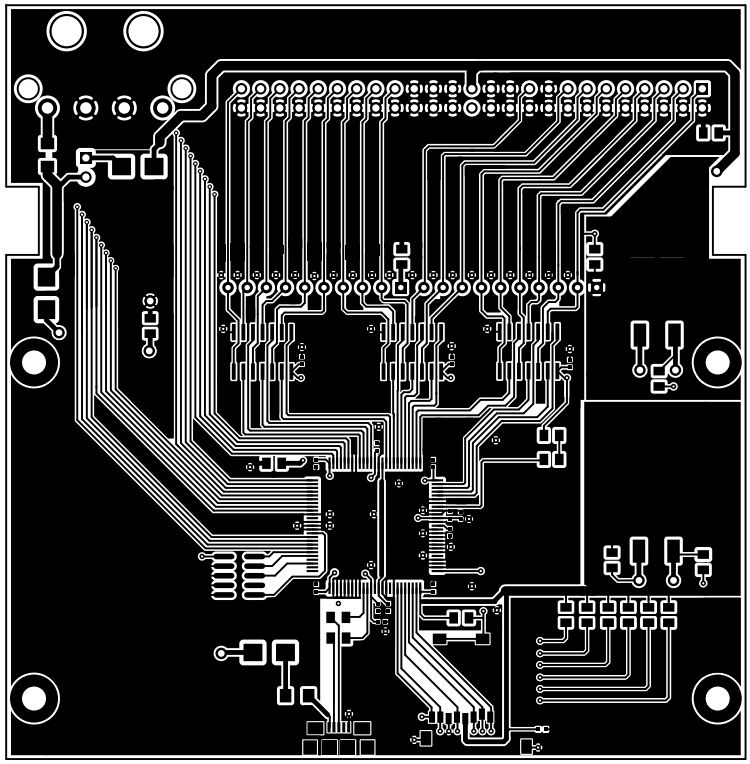
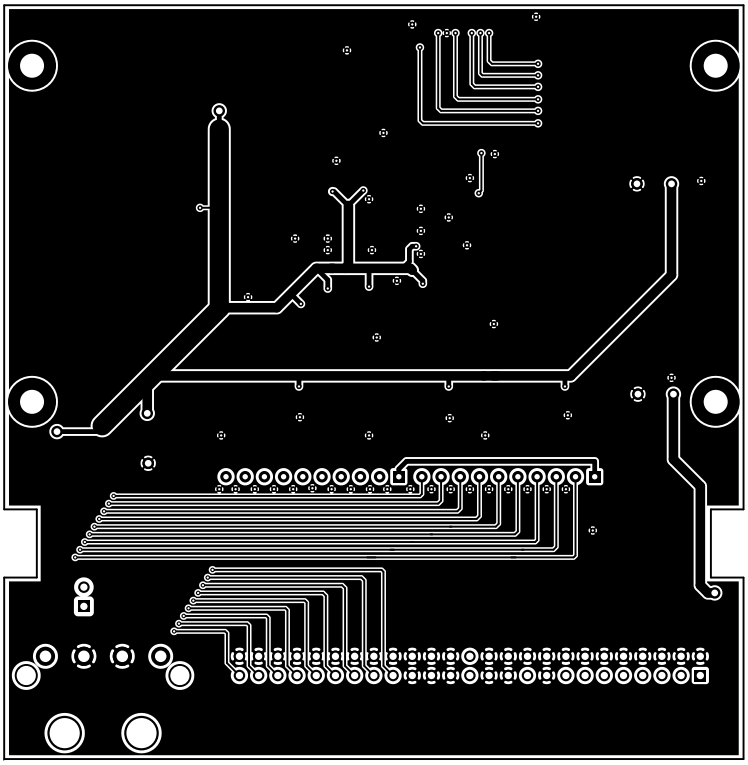


top, scale = 1:1.000
scsi2sd.pcb



bottom (mirrored), scale = 1:1.000
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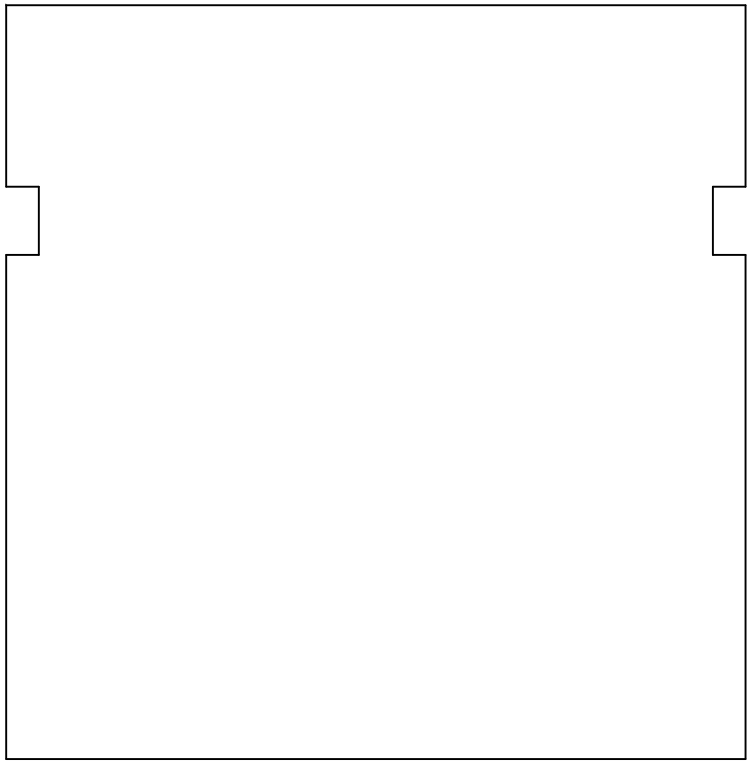

outline, scale = 1:1.000
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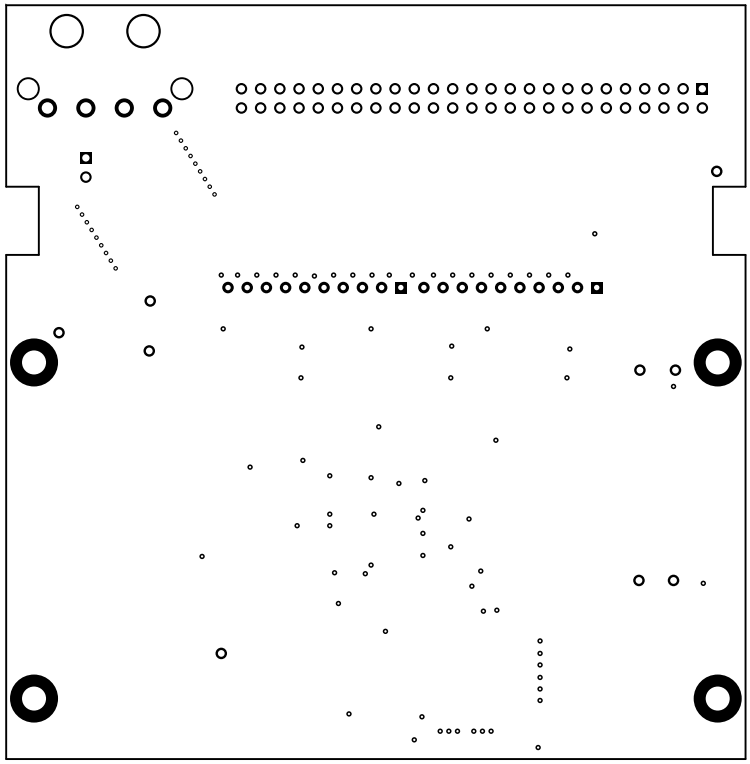

group5, scale = 1:1.000
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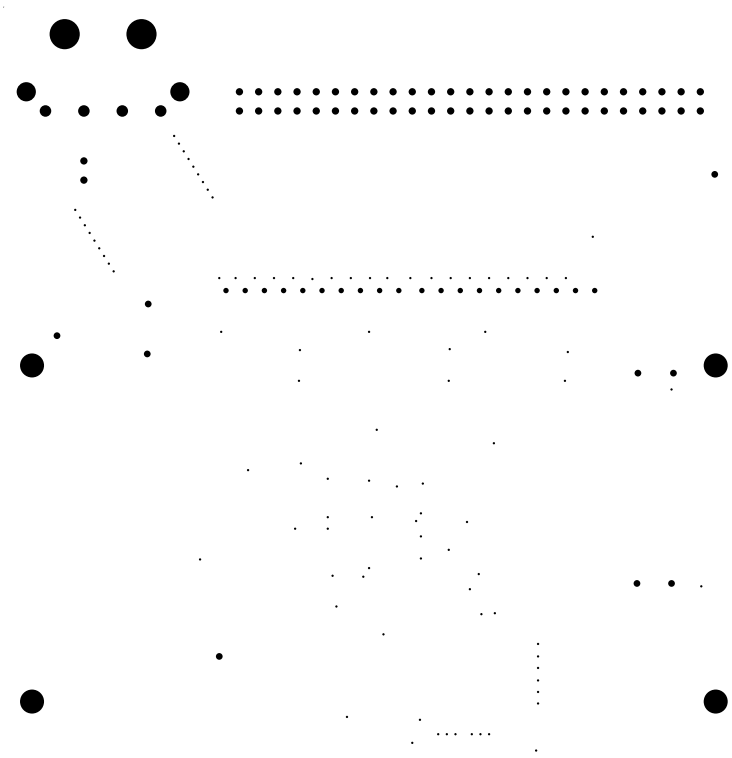
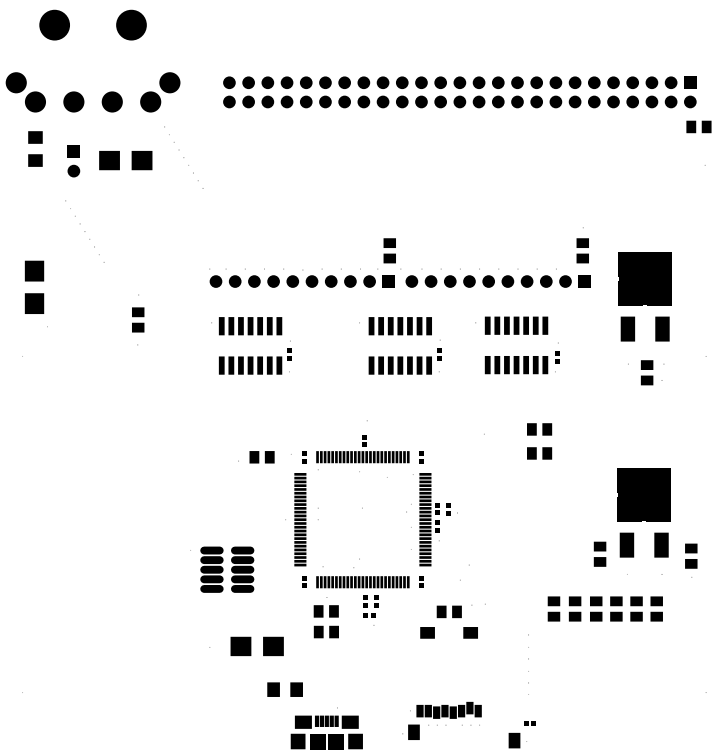
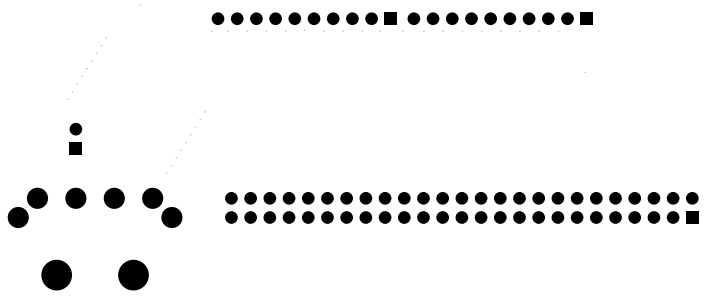


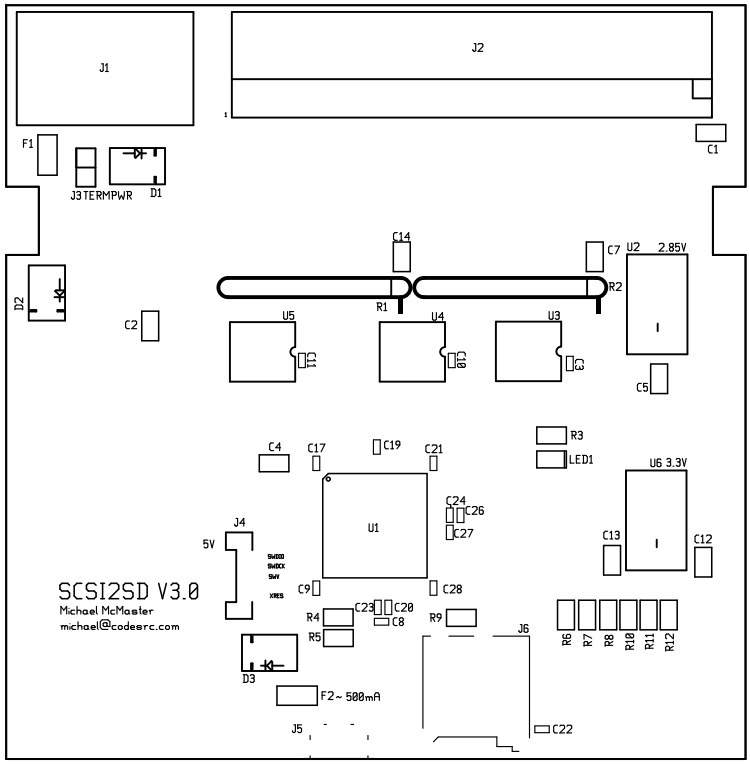
plated-drill, scale = 1:1.000
scsi2sd.pcb



topmask, scale = 1:1.000
scsi2sd.pcb



bottommask (mirrored), scale = 1:1.000
scsi2sd.pcb



topsilk, scale = 1:1.000
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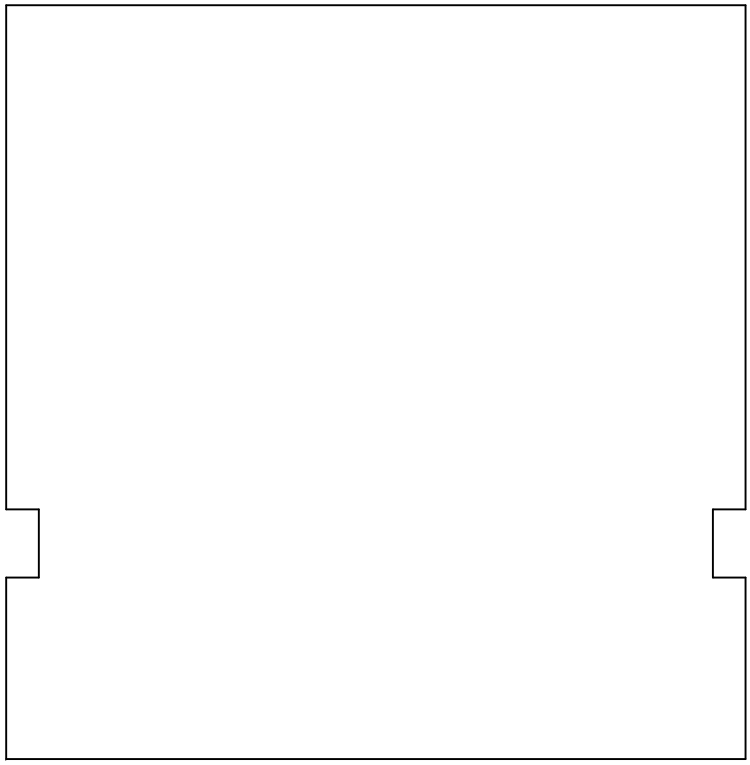
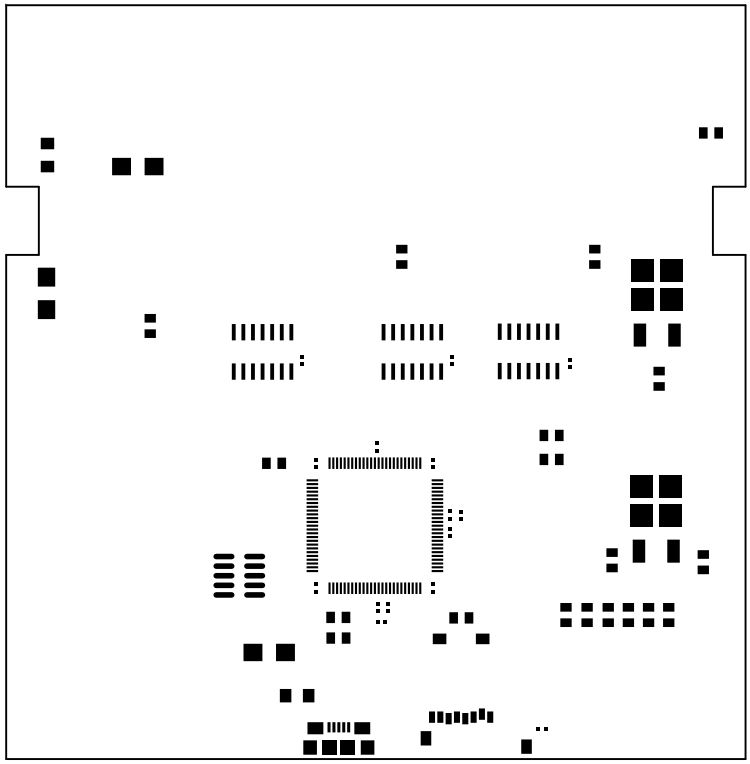
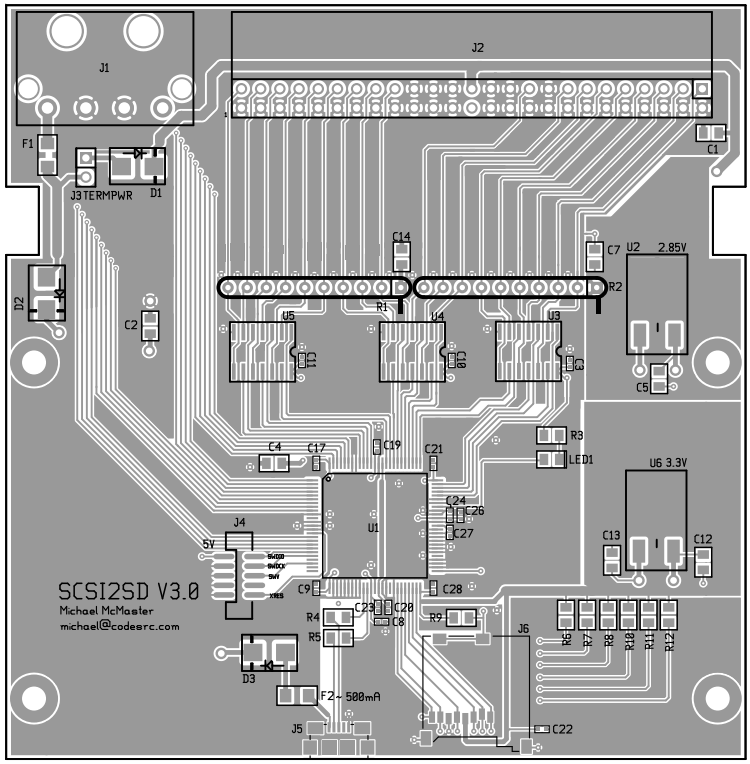


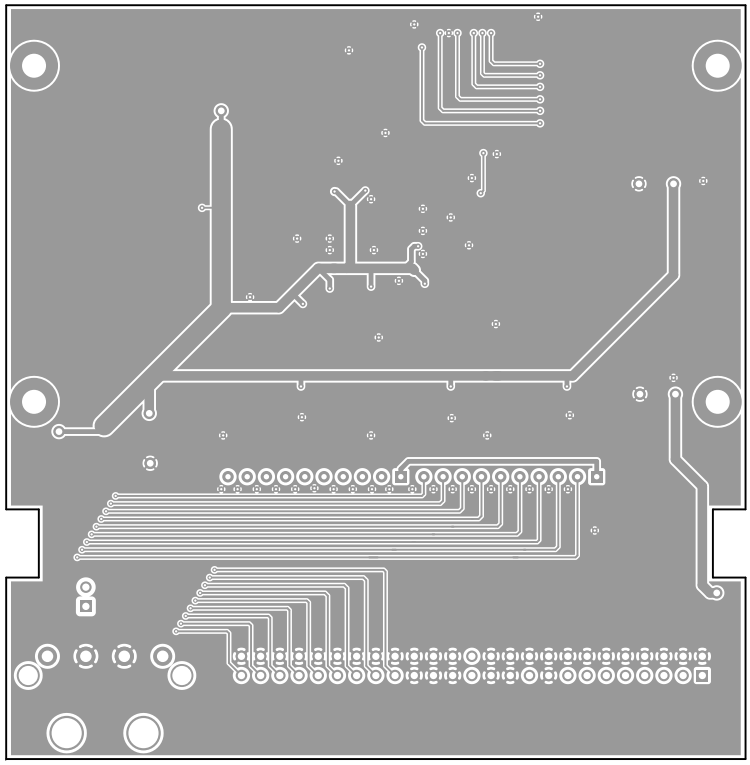
bottomsilk (mirrored), scale = 1:1.000
scsi2sd.pcb



toppaste, scale = 1:1.000
scsi2sd.pcb



topassembly, scale = 1:1.000
scsi2sd.pcb

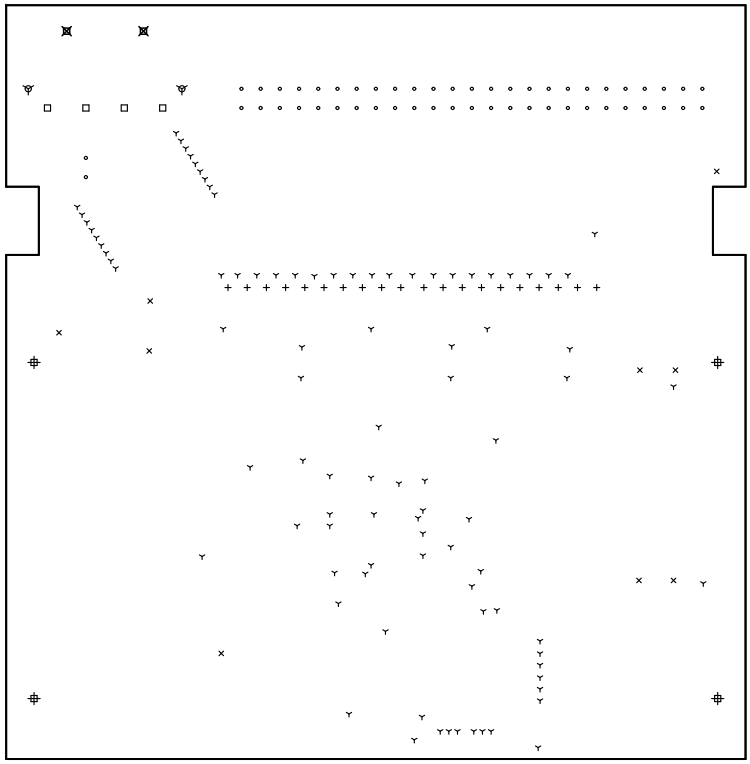


bottomassembly (mirrored), scale = 1:1.000
scsi2sd.pcb

There are 8 different drill sizes used in this layout, 186 holes total

Symbol	Diam. (Inch)	Count	Plated?
⋎	0.012	93	YES
+	0.028	20	YES
×	0.035	9	YES
∘	0.038	52	YES
□	0.060	4	YES
⋎	0.100	2	YES
⌖	0.125	4	YES
⊠	0.157	2	YES

Title: (unknown) - Fabrication Drawing
Author: Michael McMaster
Date: Wed 09 Oct 2013 11:01:07 GMT UTC



Board outline is the centerline of this path

fab, scale = 1:1.000
scsi2sd.pcb