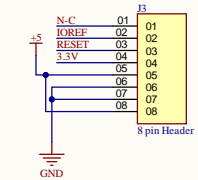
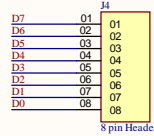
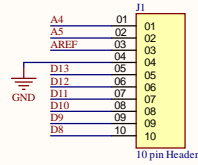
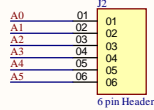
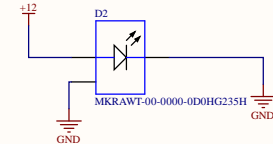
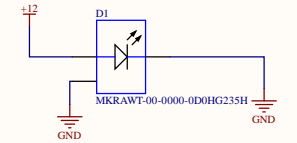
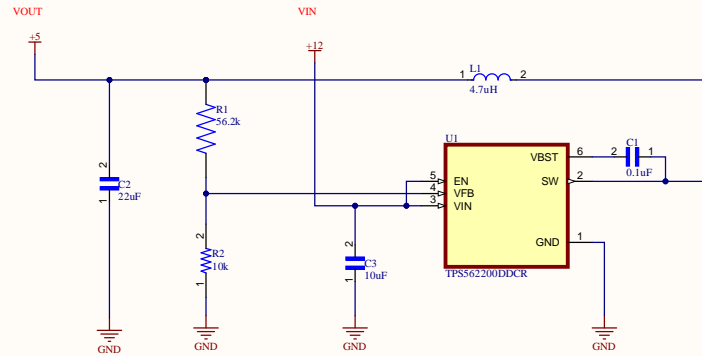


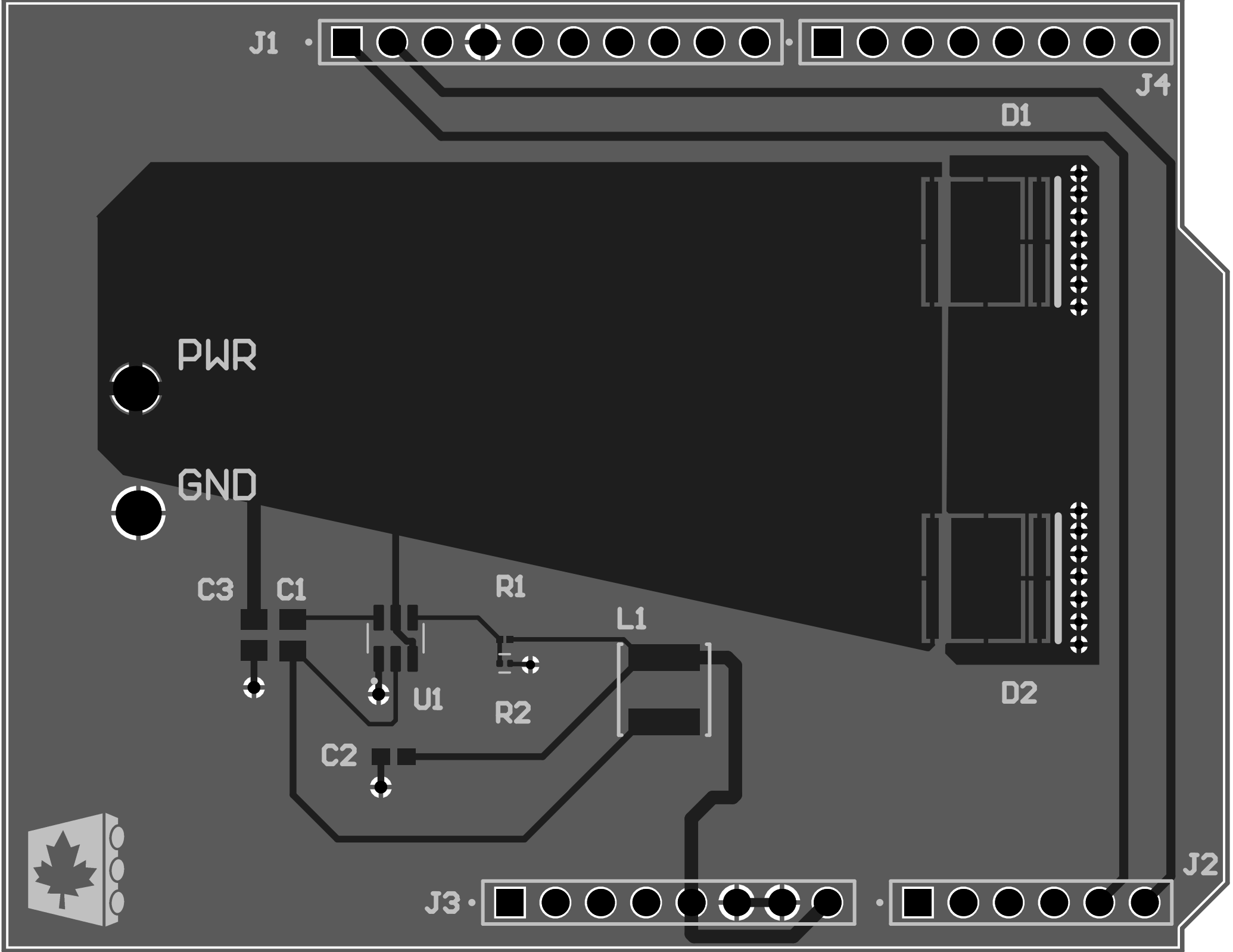
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REVISION	DESCRIPTION	DATE	APPROVED



NetLabel U1

APPROVALS	DATE	PROJECT	Altium
ENG: Eric		PROJECT REVISION: Not in version control	DESIGN ITEM: Not in version control
DSN: Eric		TITLE: Power Board	
CHK: Mooooose			
BOM:			
ASSY DWG:			
FAB DWG:			
PCB DWG:			
SCALE:		FILE NAME: Powerboard Rev01.SchDoc	SHEET 1 OF 1



Board Stack Report