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Author: Brandon Arnold

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Sheet: /

File: ra9704.kicad_sch

Title: RA9704, System Diagram

Size: B Date: 2024-12-31

KiCad E.D.A. 8.0.7

Rev: 004

Id: 1/15

A

A

B

B

Parts side

Solder side

C

C

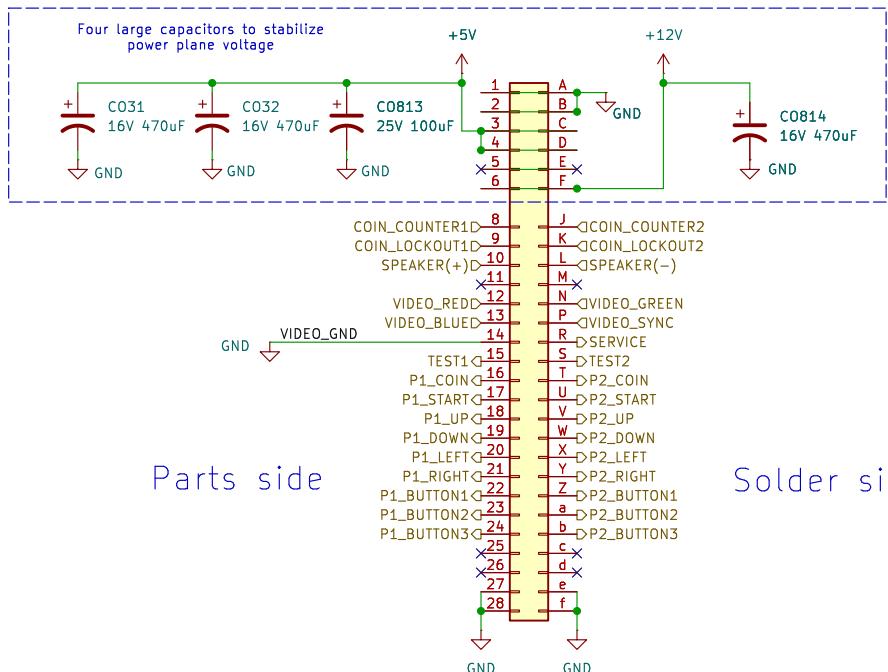
D

D

E

E

JAMMA



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Sheet: /JAMMA/

File: jamma.kicad_sch

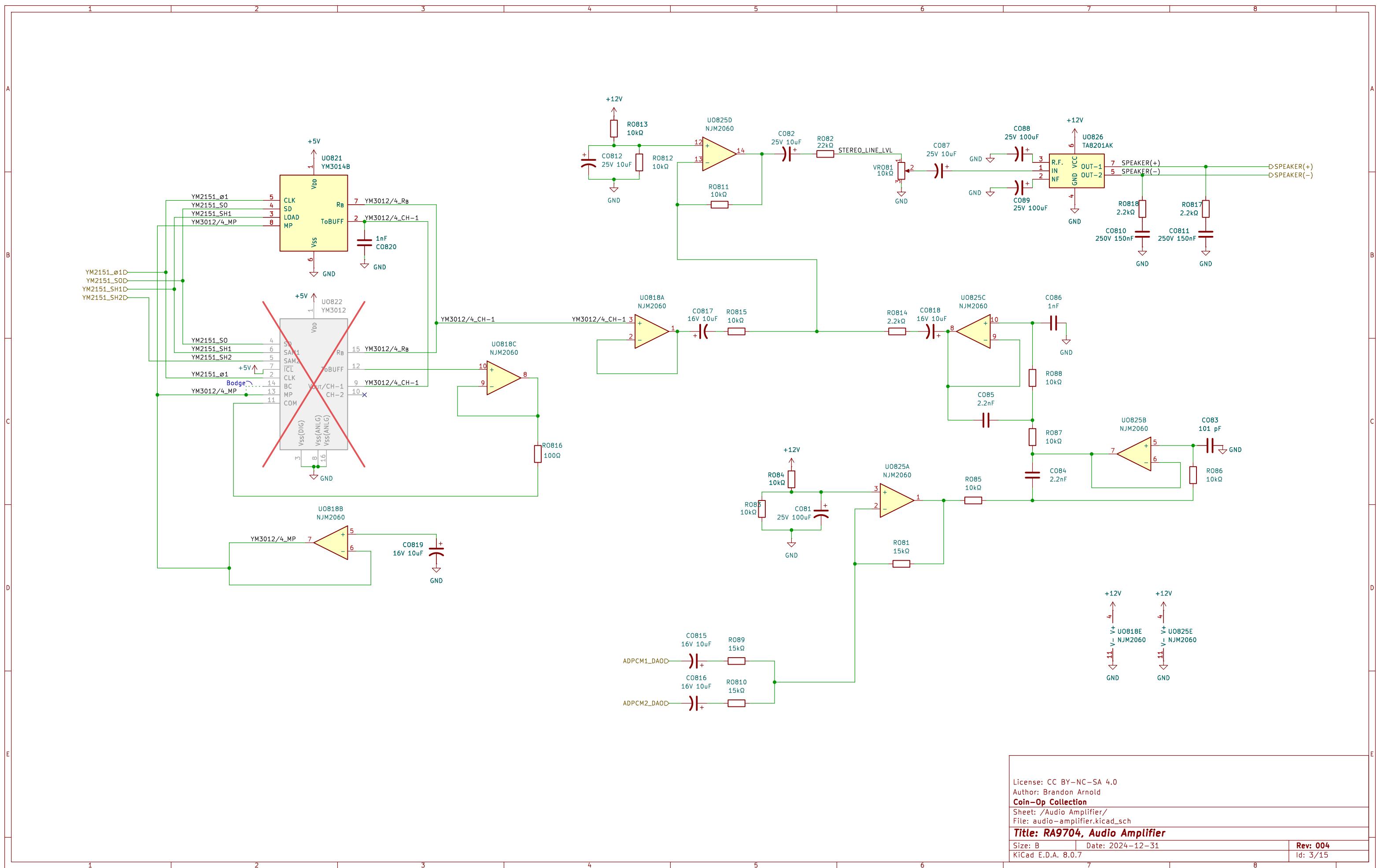
Title: RA9704, JAMMA

Size: A Date: 2024-12-31

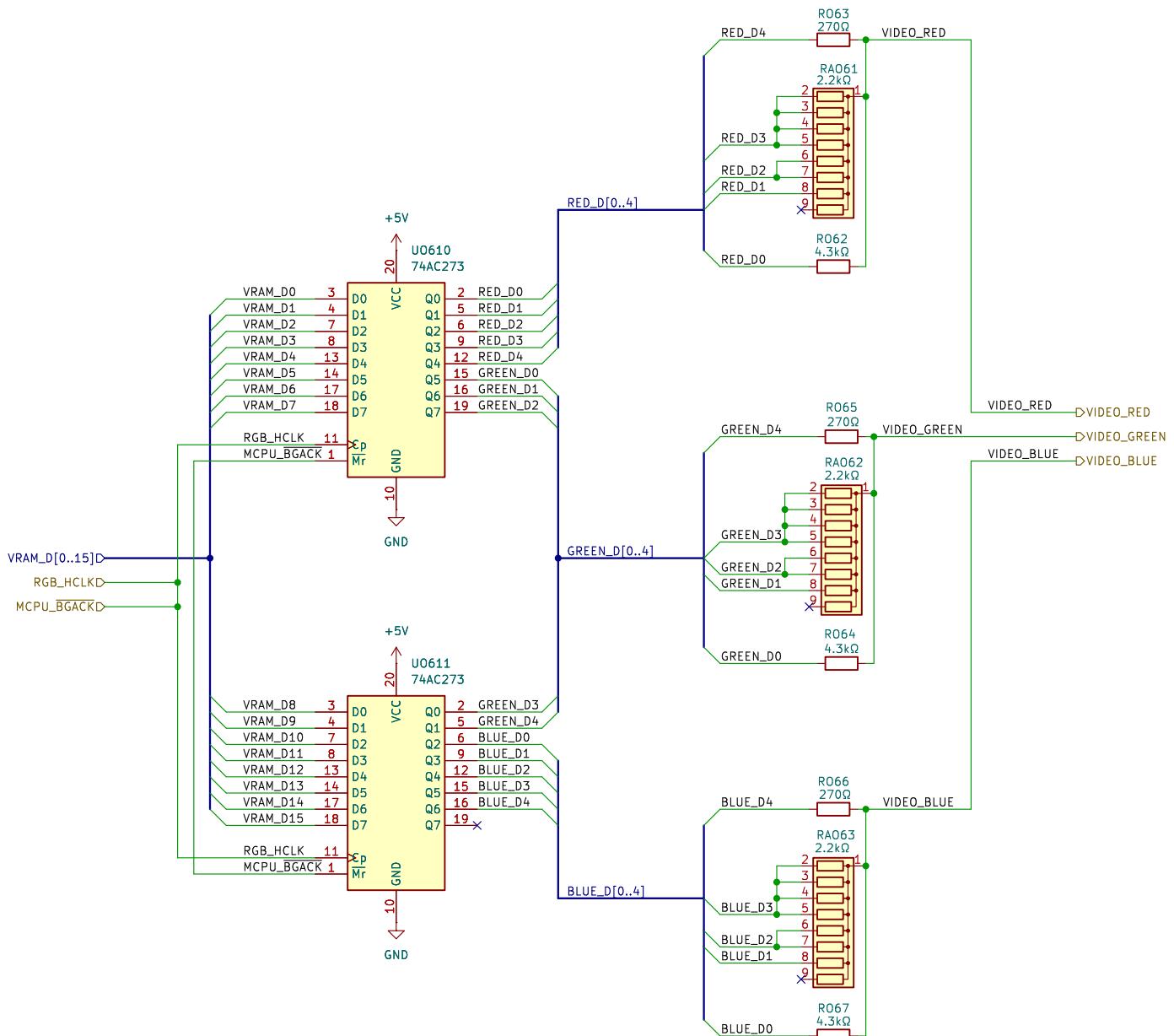
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Rev: 004

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Voltage-mode binary-weighted resistor DAC
see B. B. Smith, "Coding by Feedback Methods,"
Proceedings of the I. R. E., Vol. 41



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Sheet: /RGB DAC/

File: rgb-dac.kicad_sch

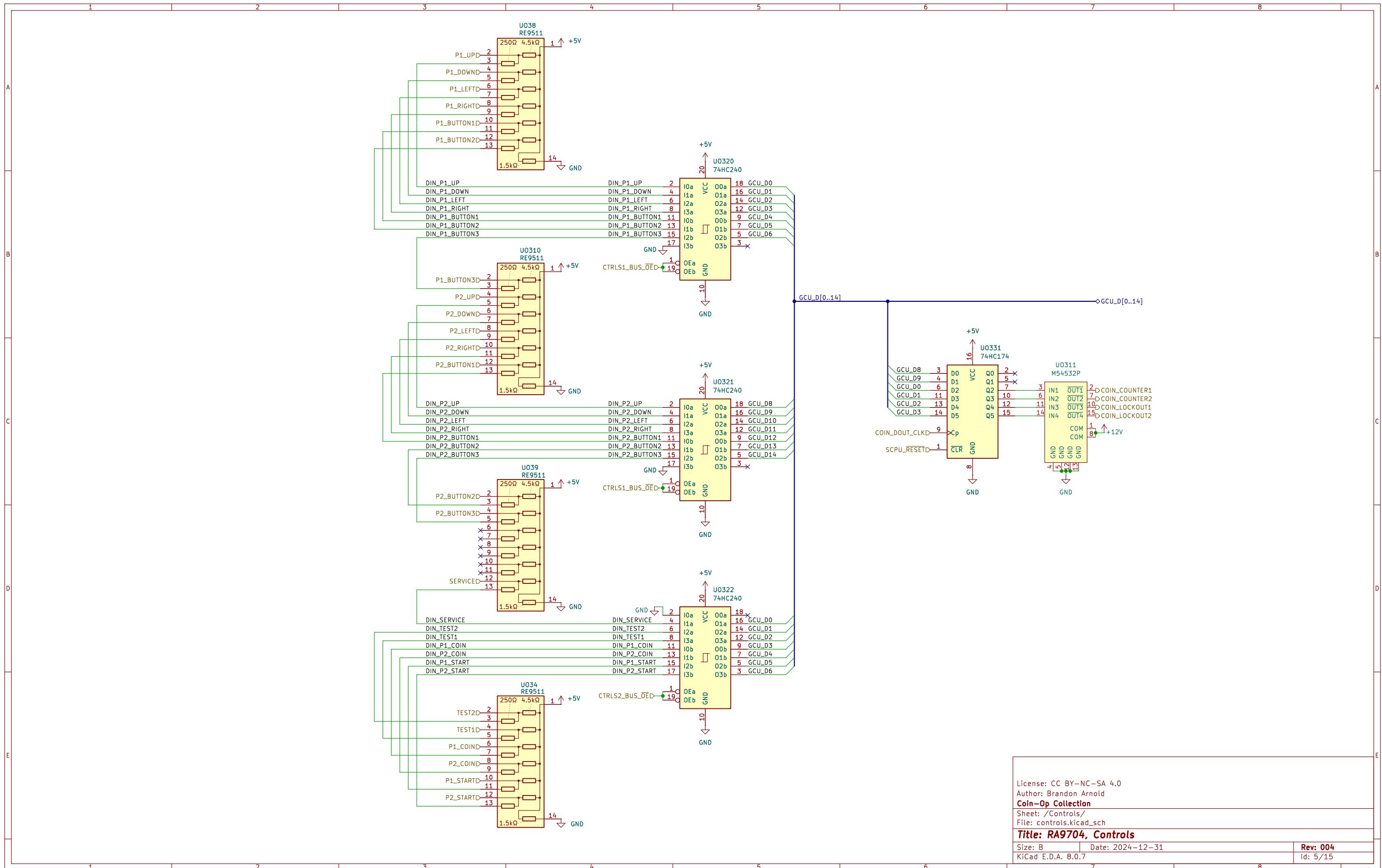
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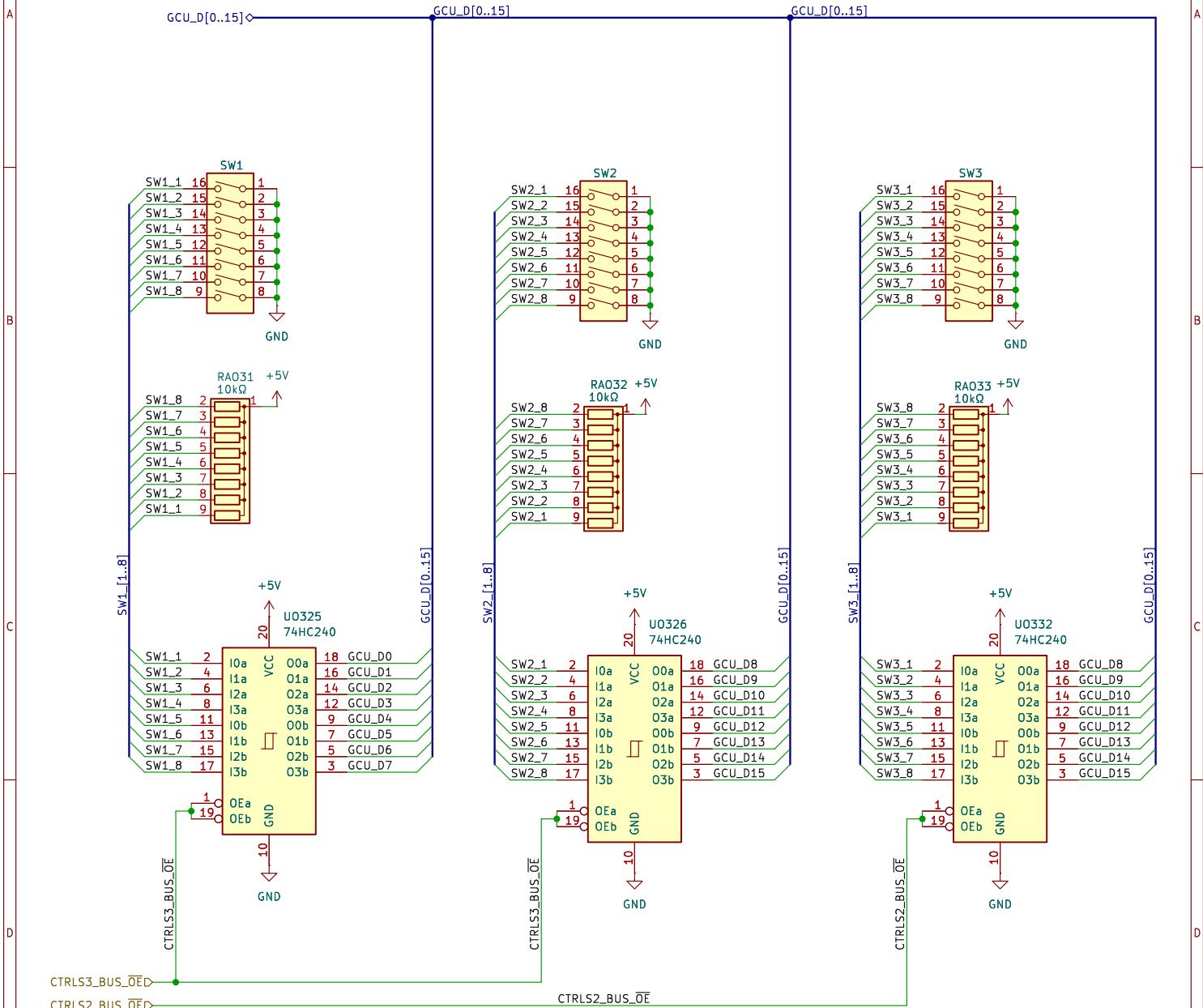
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Sheet: /DIP_Switches/

File: dip-switches.kicad_sch

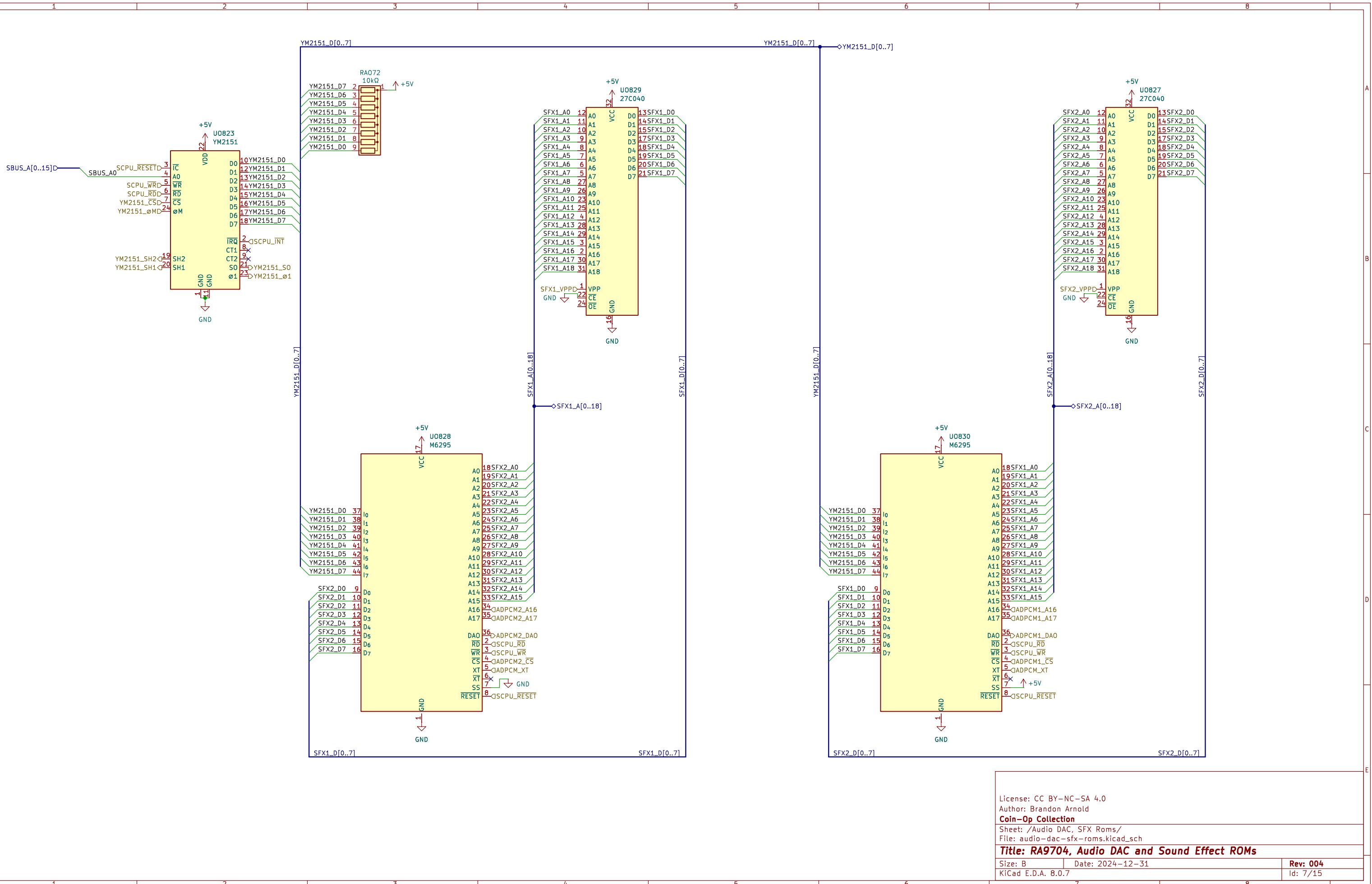
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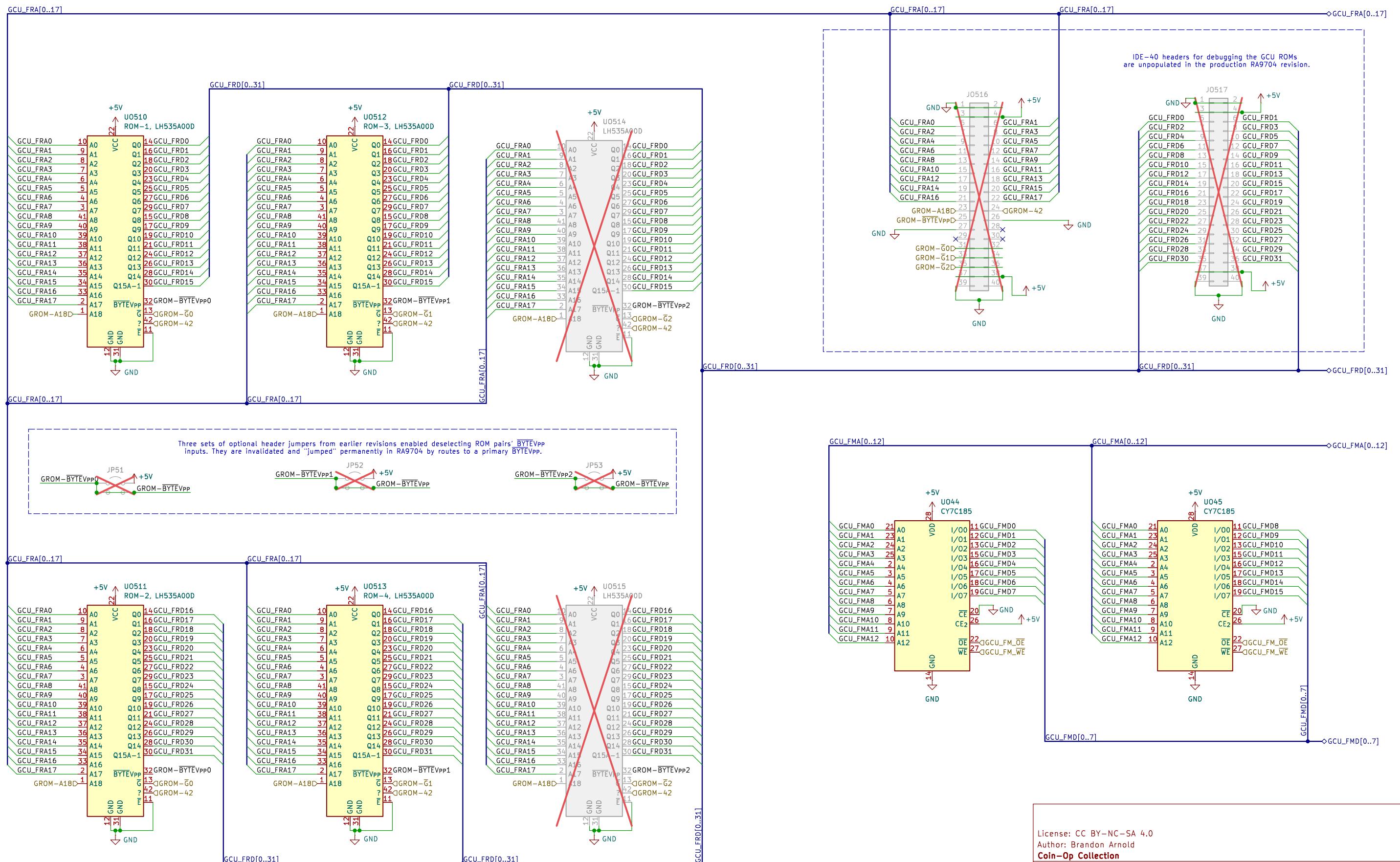
Size: A Date: 2024-12-31

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meet: /GCU_F ROM and RAM

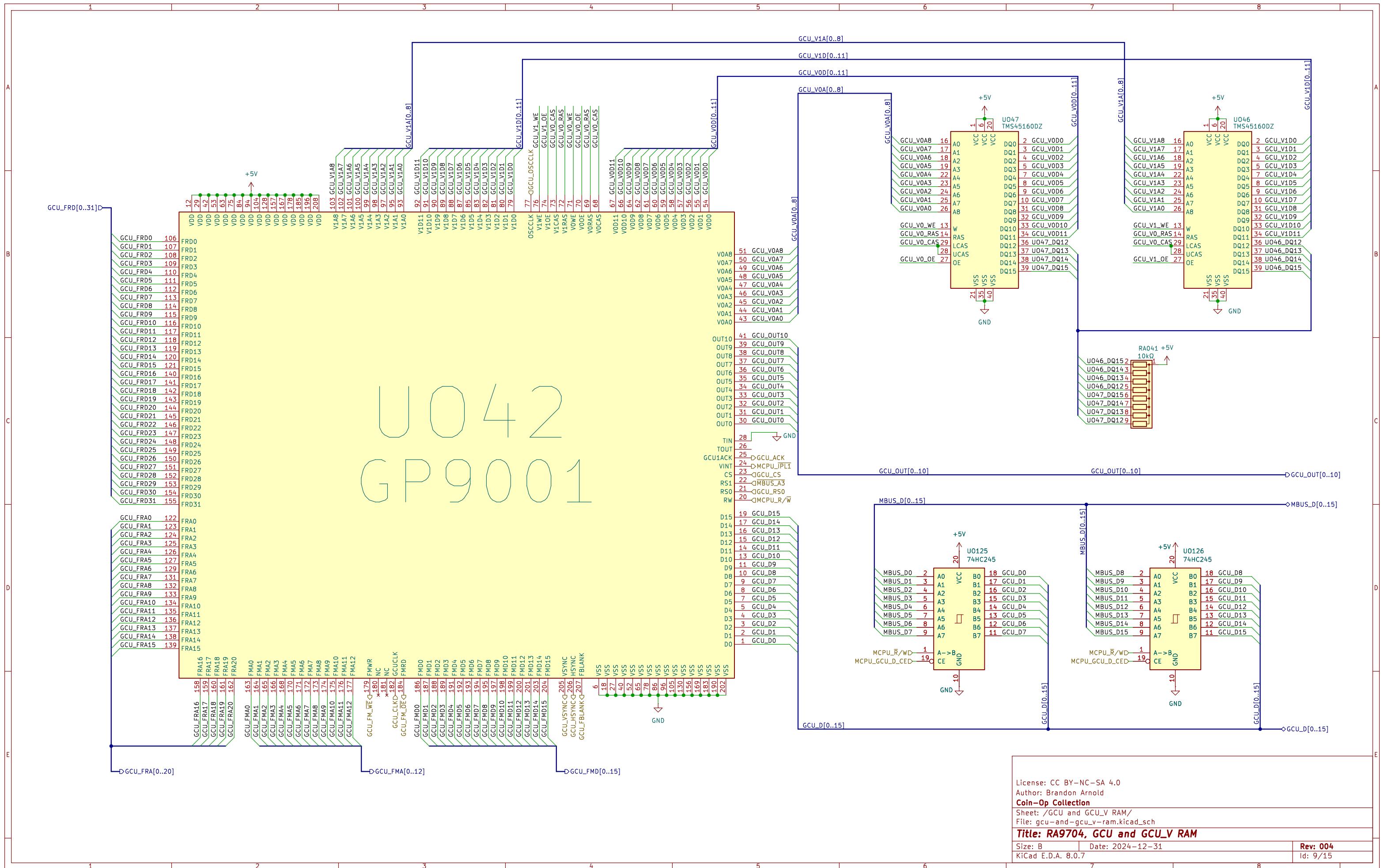
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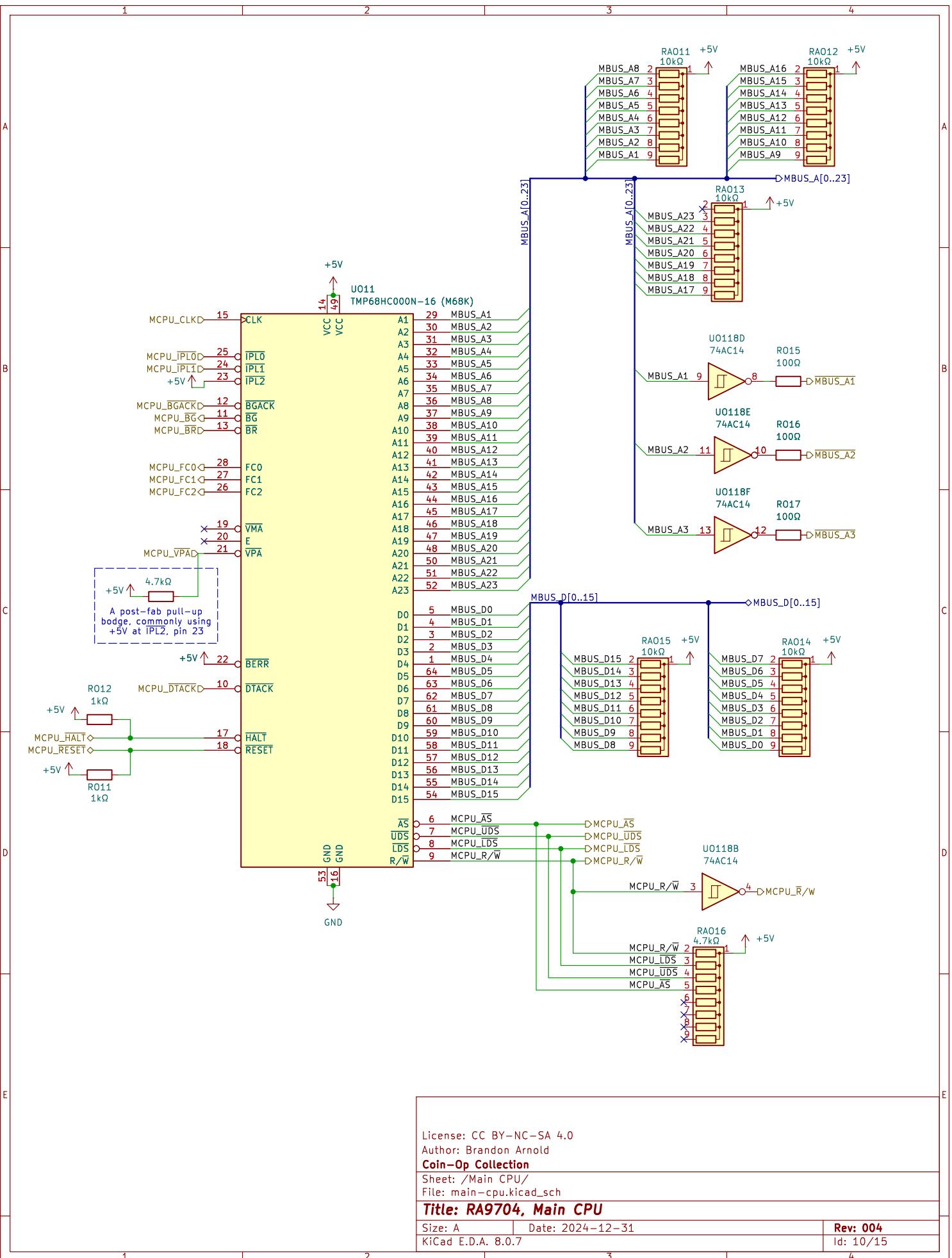
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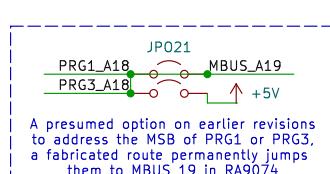
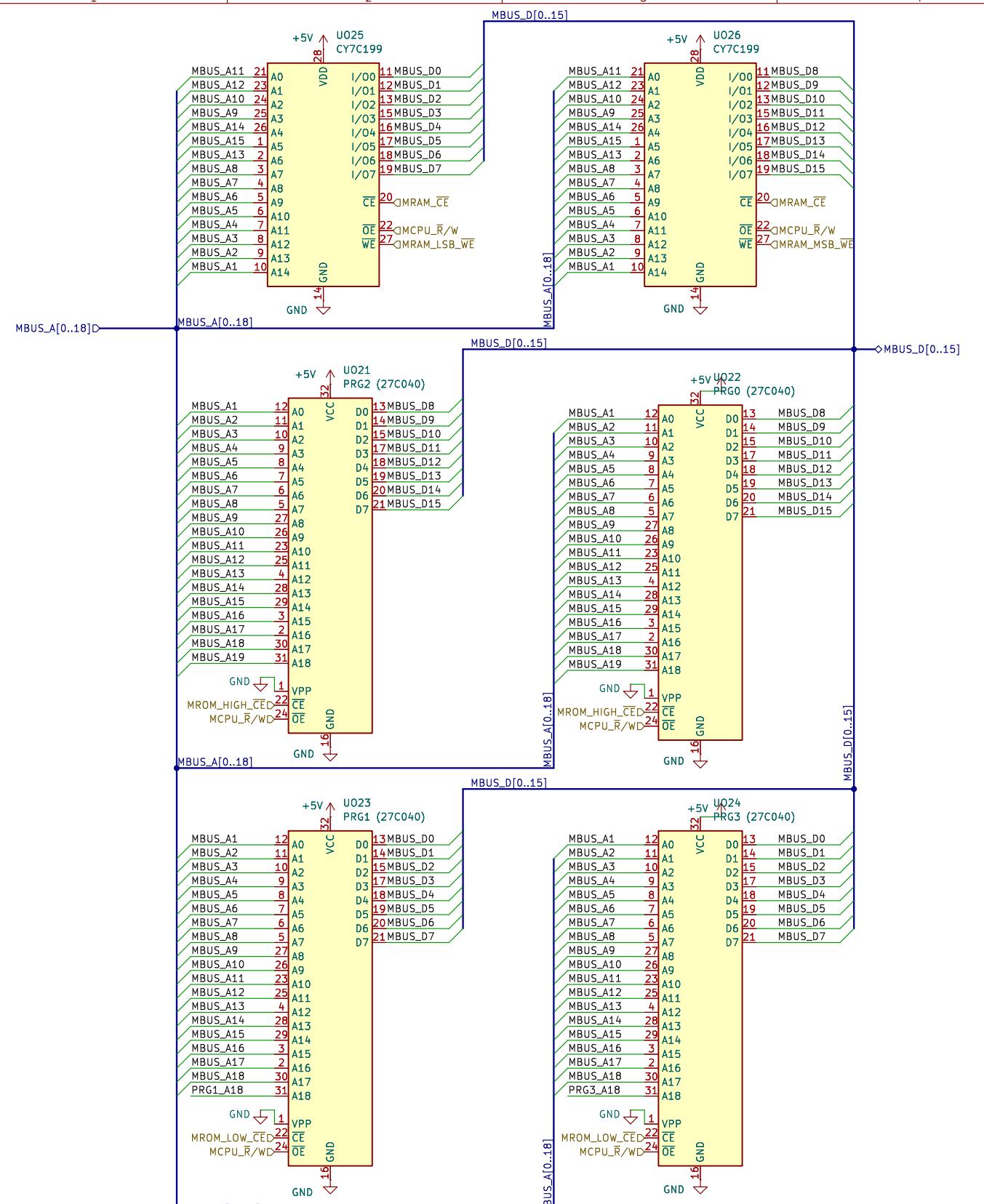
Date: 2024-12-31

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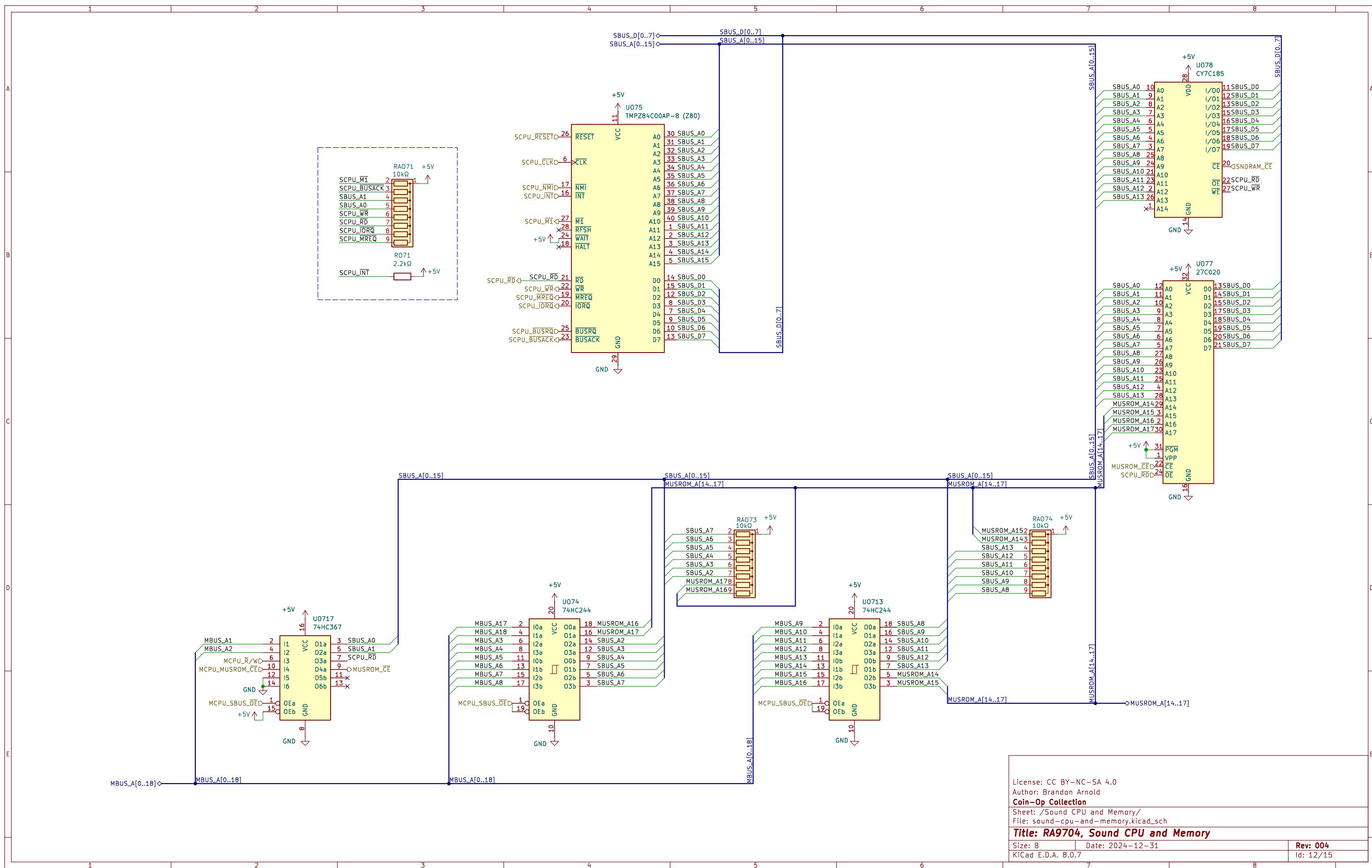
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File: main-ram-and-rom.kicad_sch

Title: RA9704, Main RAM and ROM

Size: A Date: 2024-12-31
KiCad E.D.A. 8.0.7

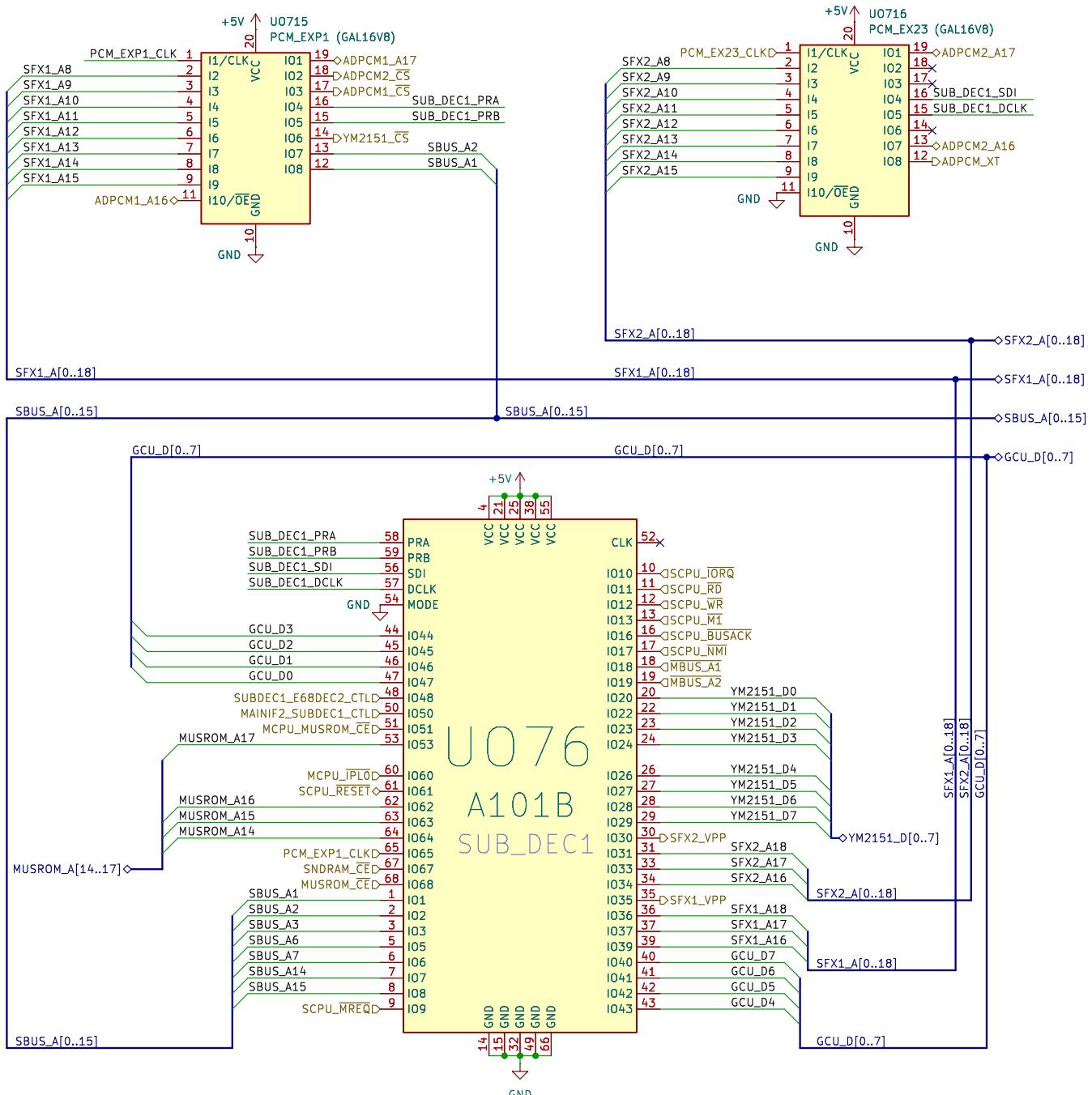
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A

A



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Sheet: /Sound IC and Bus Mgmt/

File: sound-ic-bus-mgmt.kicad_sch

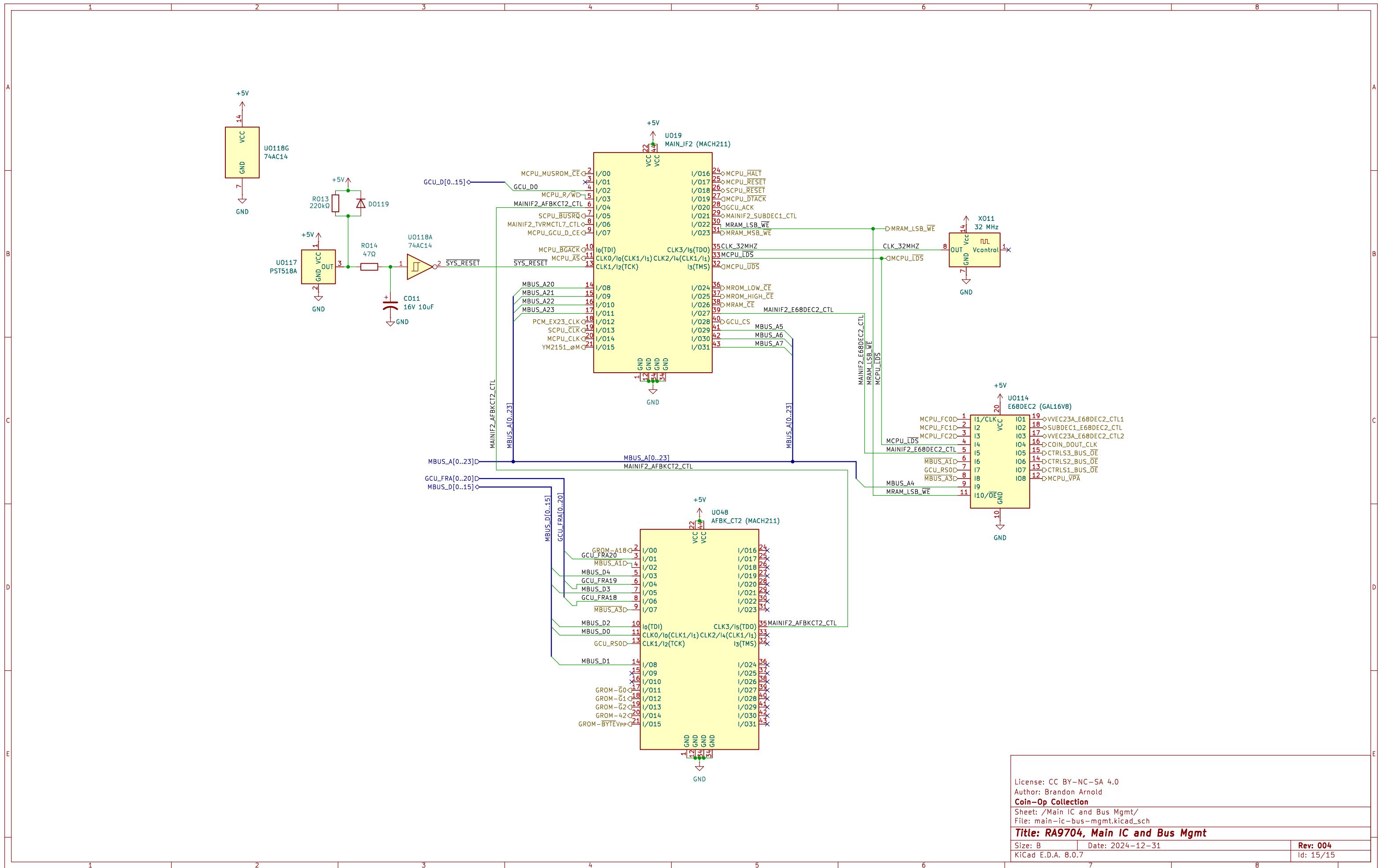
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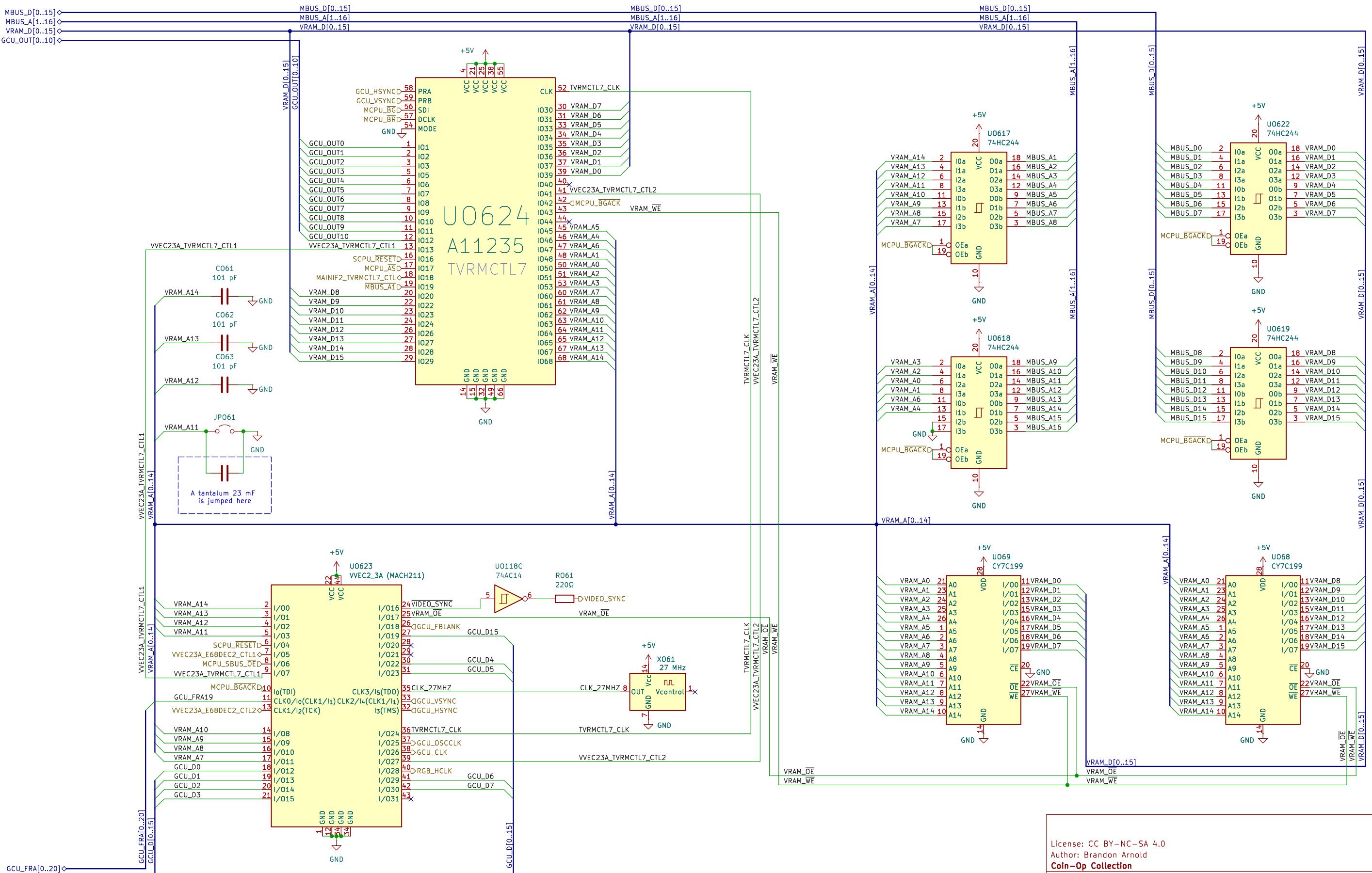
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Sheet: /Video RAM and Bus Mgmt/
File: video-ram-and-bus-mgmt.kicad_sch

Title: RA9704, Video RAM and Bus Mgmt

Size: B Date: 2024-12-31

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Rev: 004

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