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Author: Brandon Arnold

Coin-Op Collection

Sheet: /

File: ra9704.kicad_sch

Title: RA9704, System Diagram

Size: B Date: 2024-12-30

KiCad E.D.A. 8.0.6

Rev: 003

Id: 1/15

A

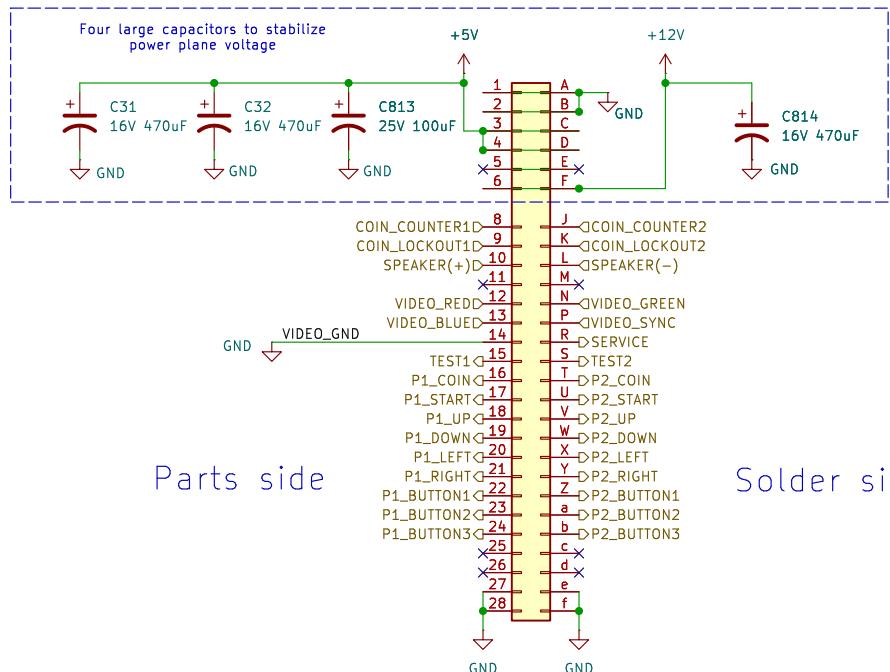
A

B

B

Parts side

Solder side



D

D

E

E

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Sheet: /JAMMA/

File: jamma.kicad_sch

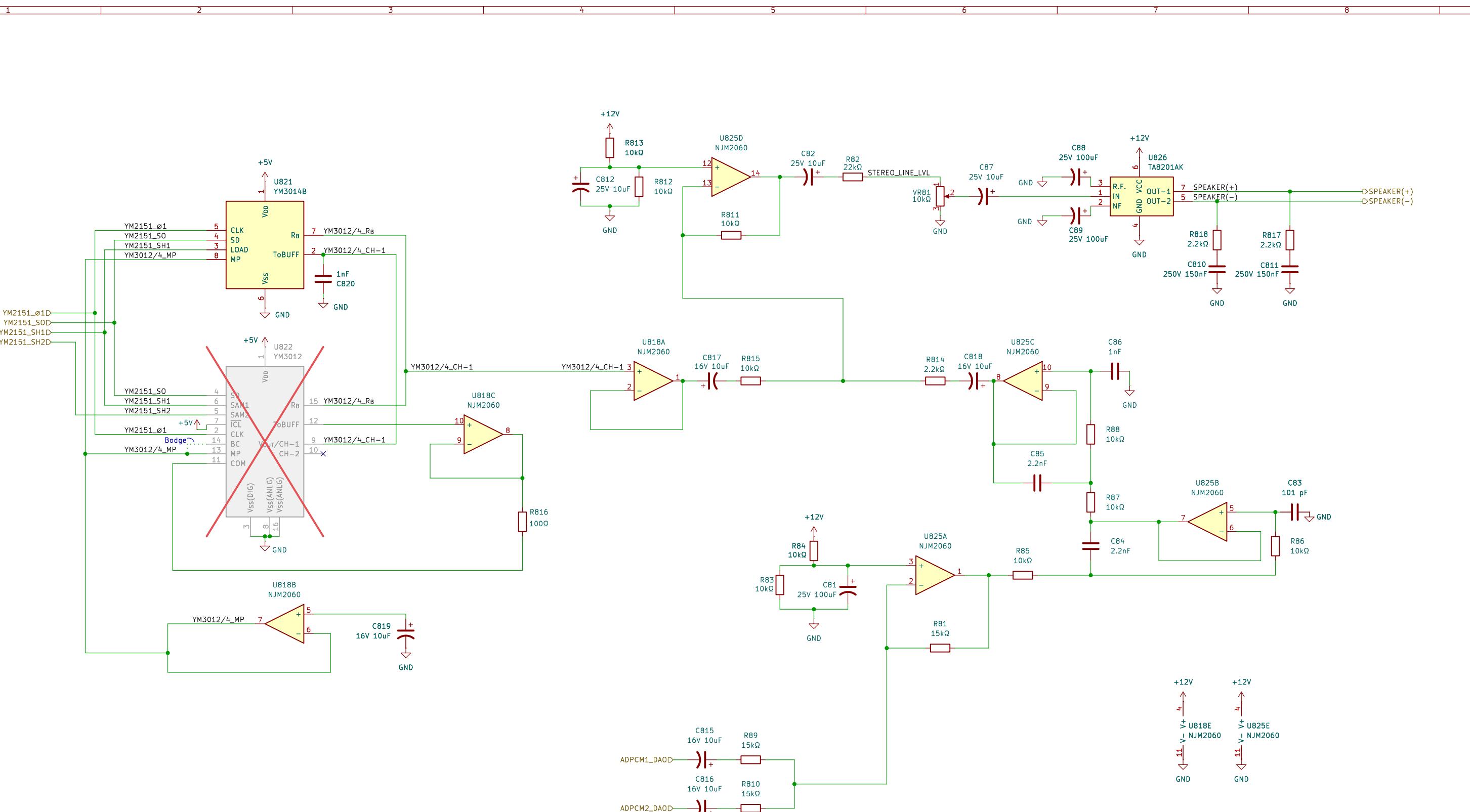
Title: RA9704, JAMMA

Size: A Date: 2024-12-30

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Sheet: /Audio Amplifier/

File: audio-amplifier.kicad_sch

Title: RA9704, Audio Amplifier

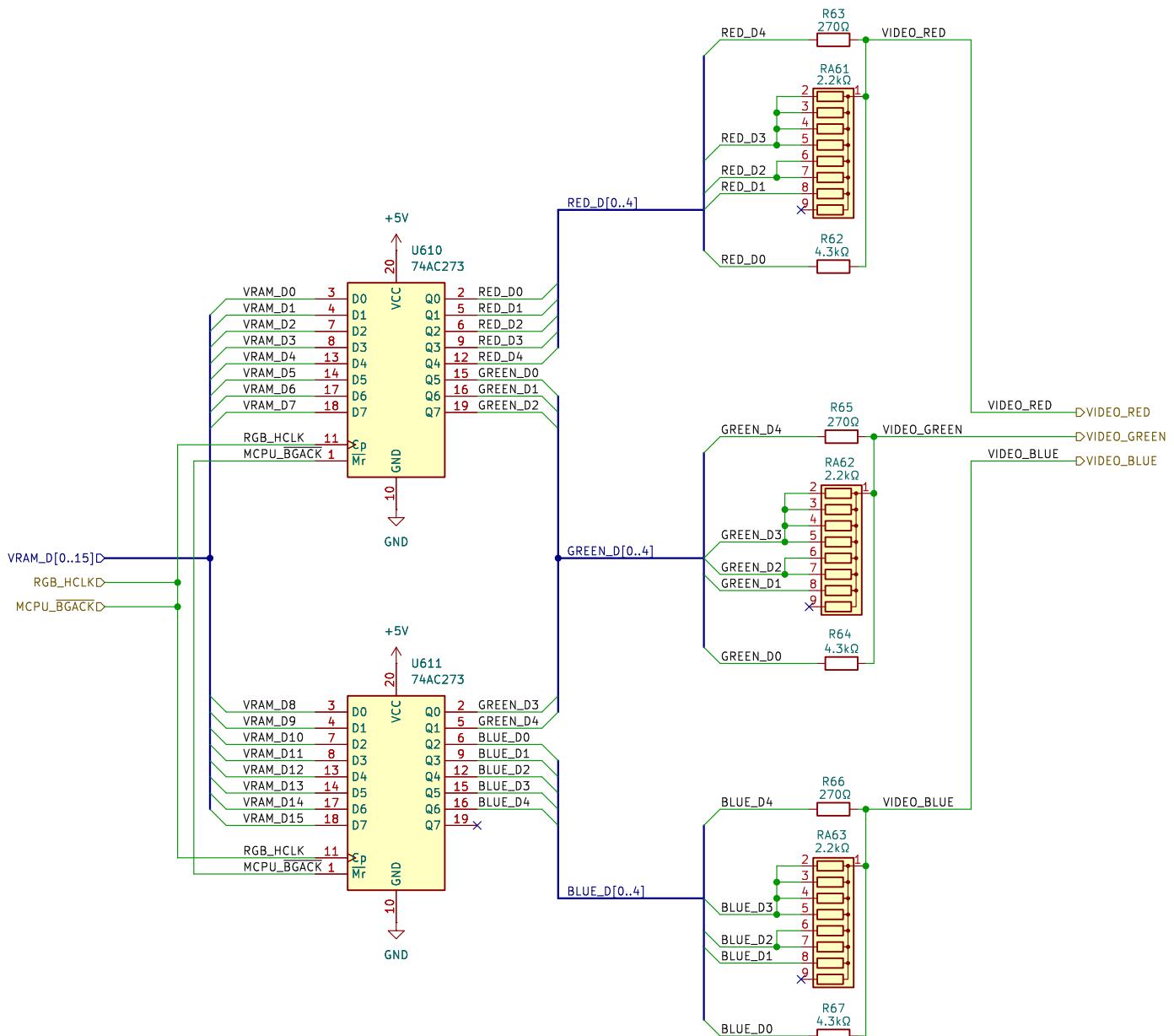
Size: B Date: 2024-12-30

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Voltage-mode binary-weighted resistor DAC
see B. B. Smith, "Coding by Feedback Methods,"
Proceedings of the I. R. E., Vol. 41



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Sheet: /RGB DAC/

File: rgb-dac.kicad_sch

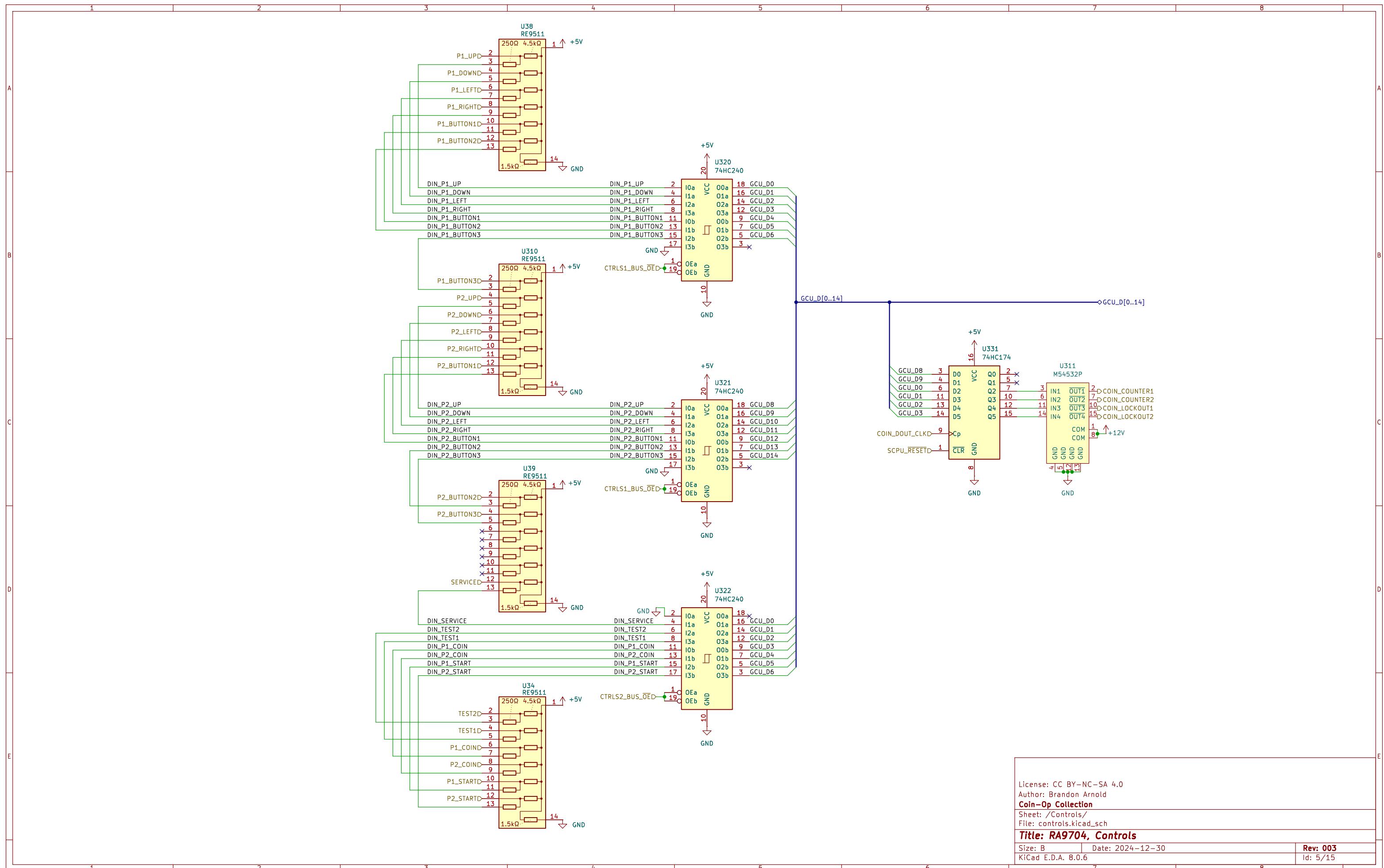
Title: RA9704, RGB DAC

Size: A Date: 2024-12-30

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Sheet: /Controls/

File: controls.kicad_sch

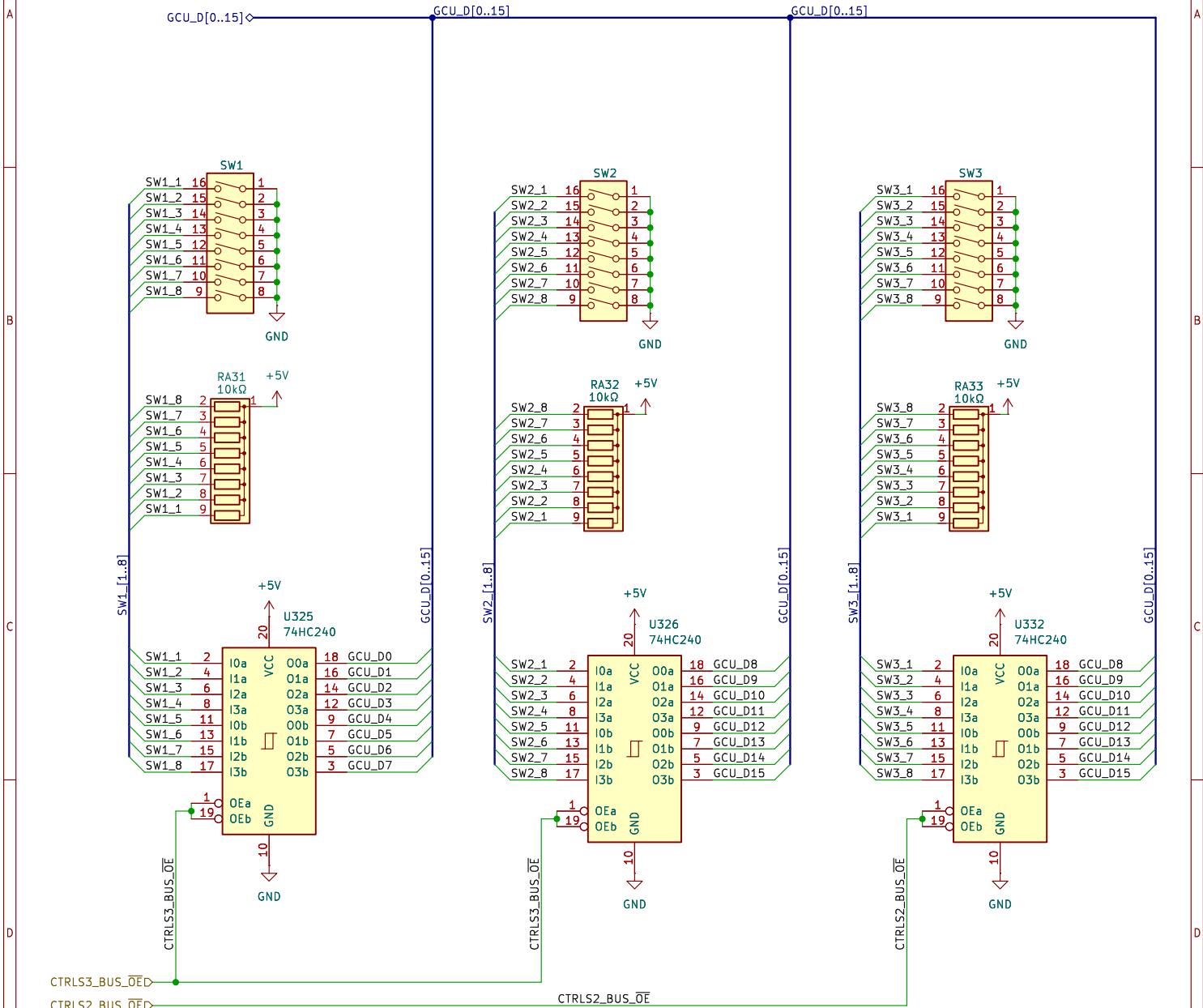
Title: RA9704, Controls

Size: B Date: 2024-12-30

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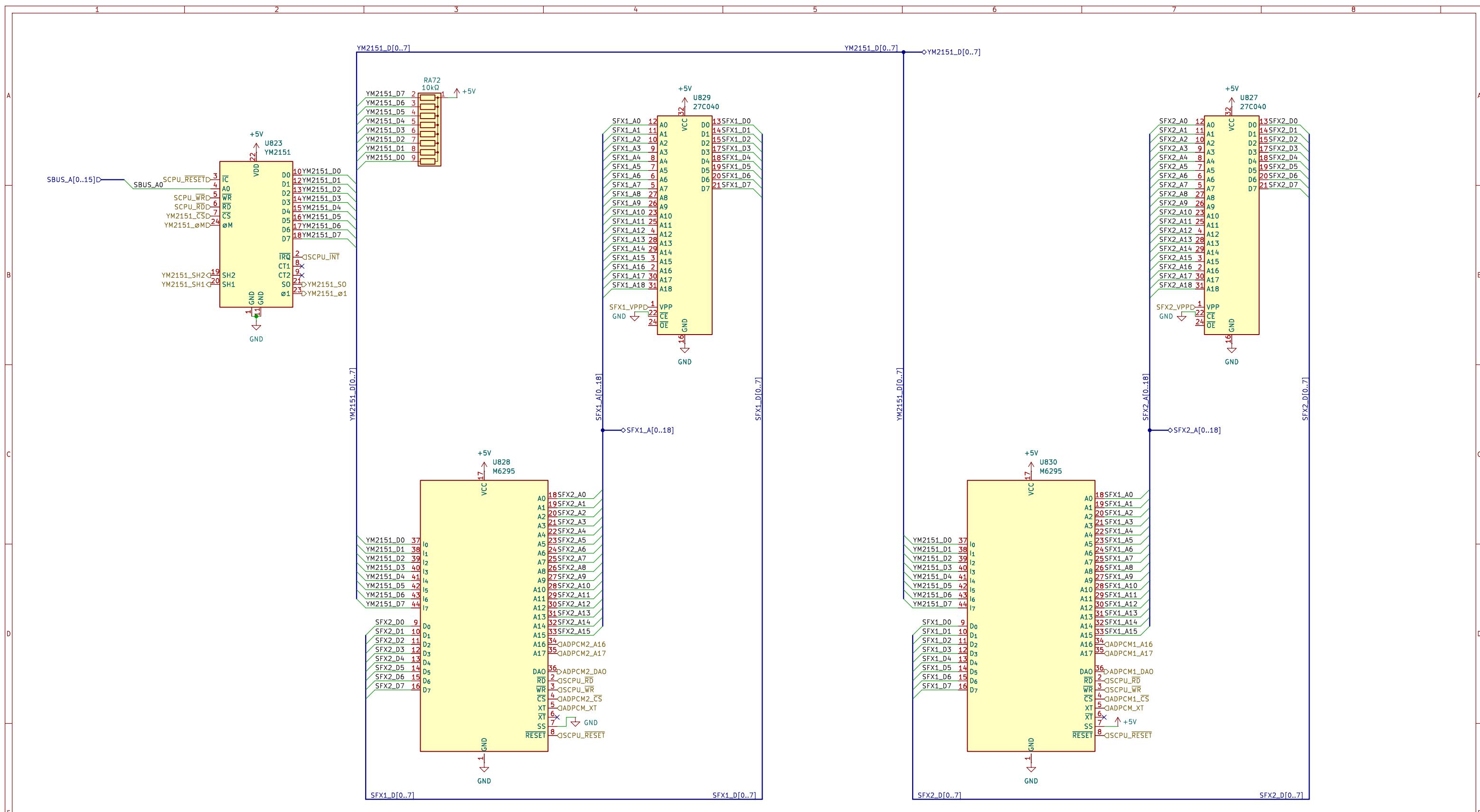
Coin-Op CollectionSheet: /DIP_Switches/
File: dip-switches.kicad_sch**Title: RA9704, DIP Switches**

Size: A Date: 2024-12-30

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Id: 6/15



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Sheet: /Audio DAC, SFX Roms/

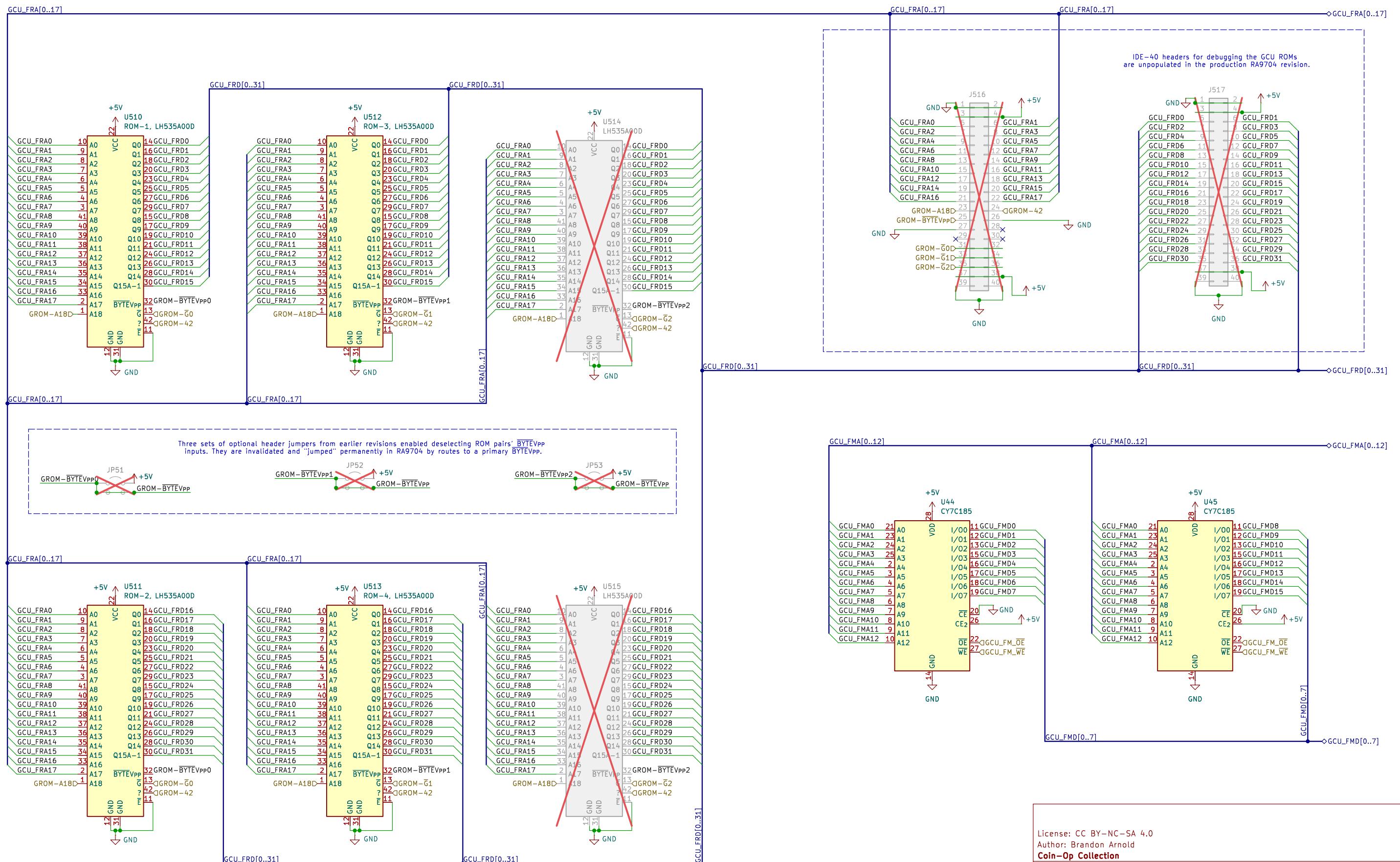
File: audio-dac-sfx-roms.kicad_sch

Title: RA9704, Audio DAC and Sound Effect ROMs

Size: B Date: 2024-12-30

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7 8



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meet: /GCU_F ROM and RAM

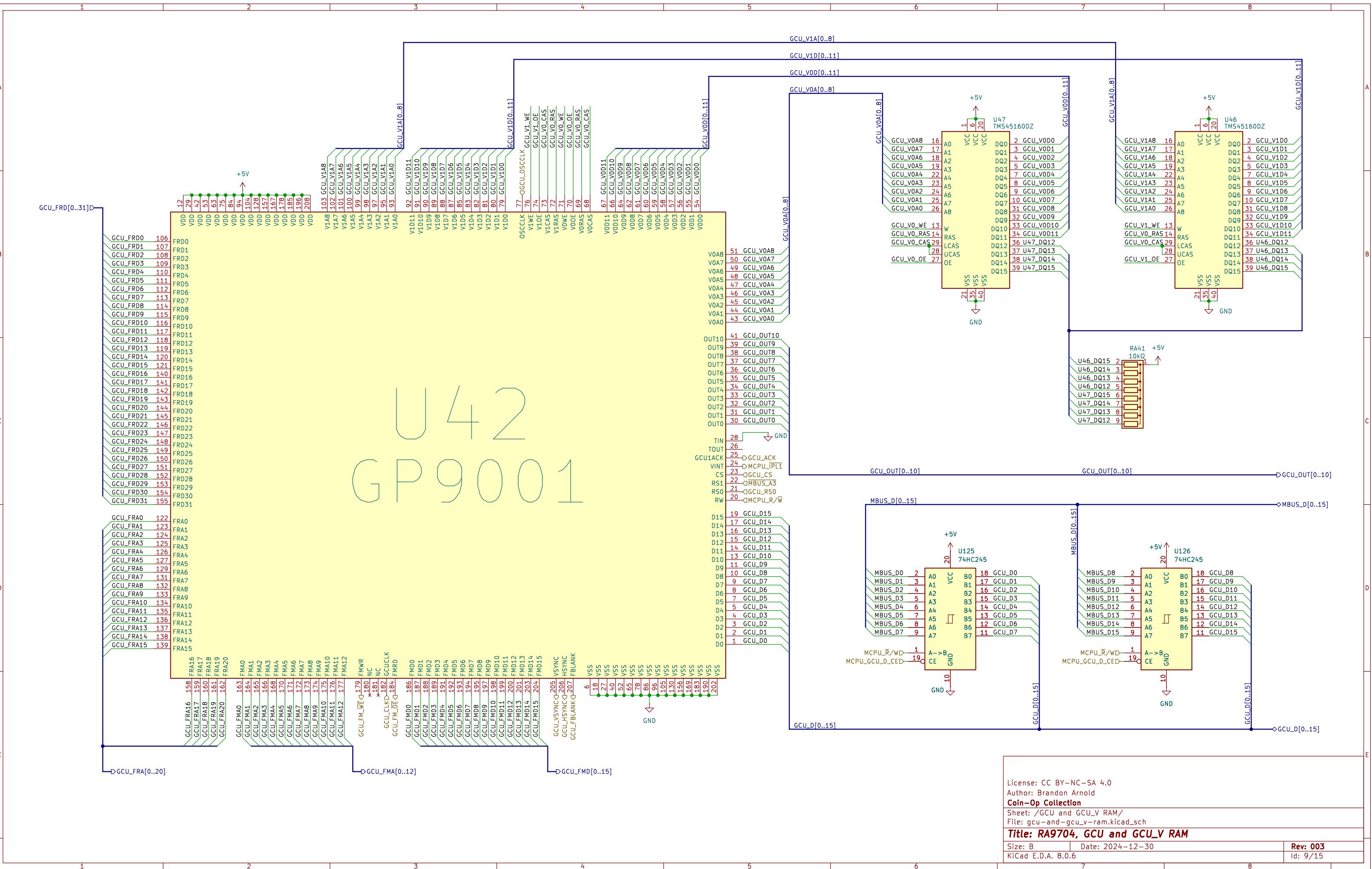
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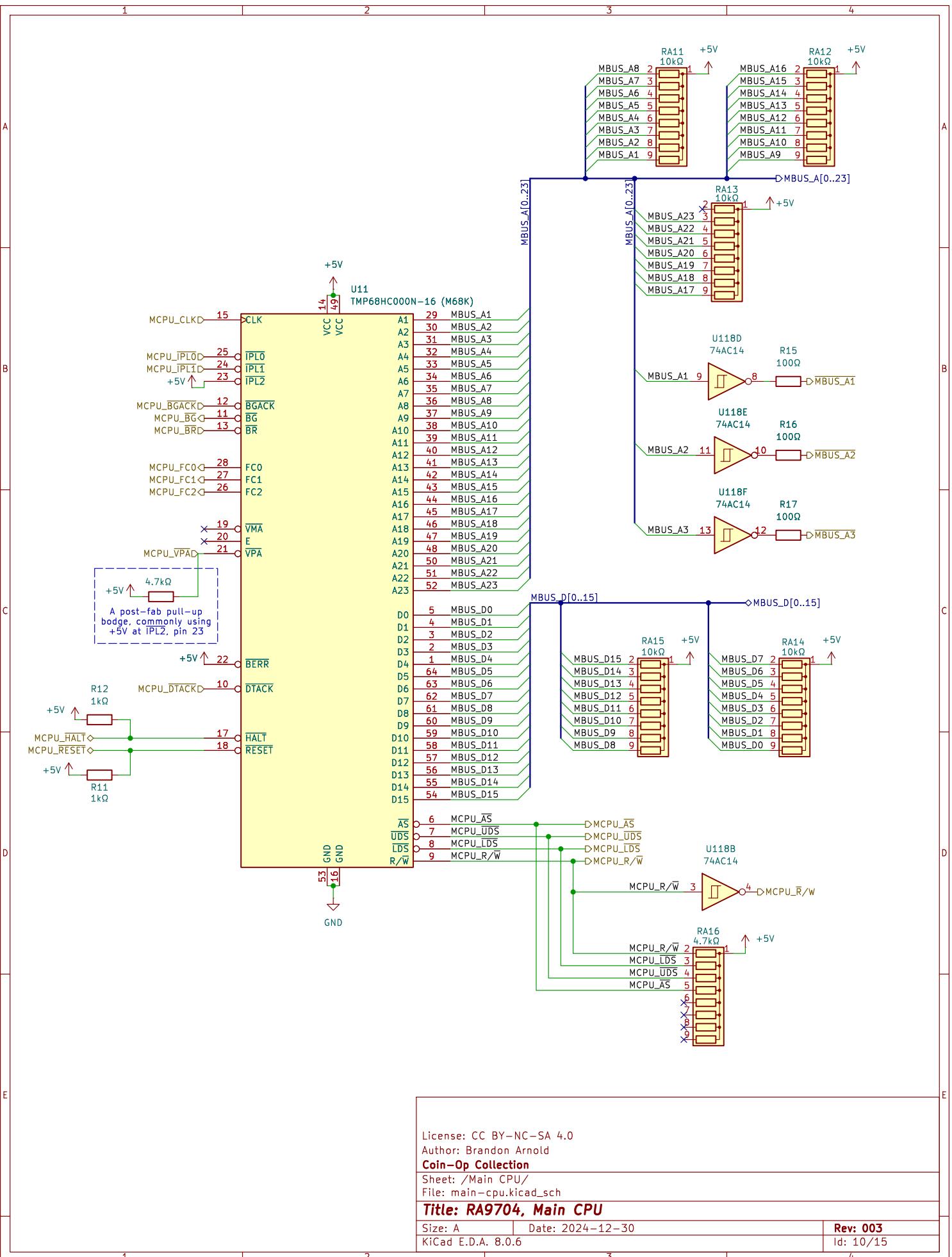
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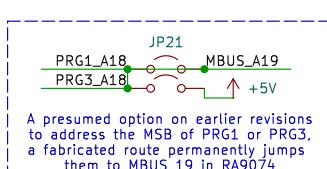
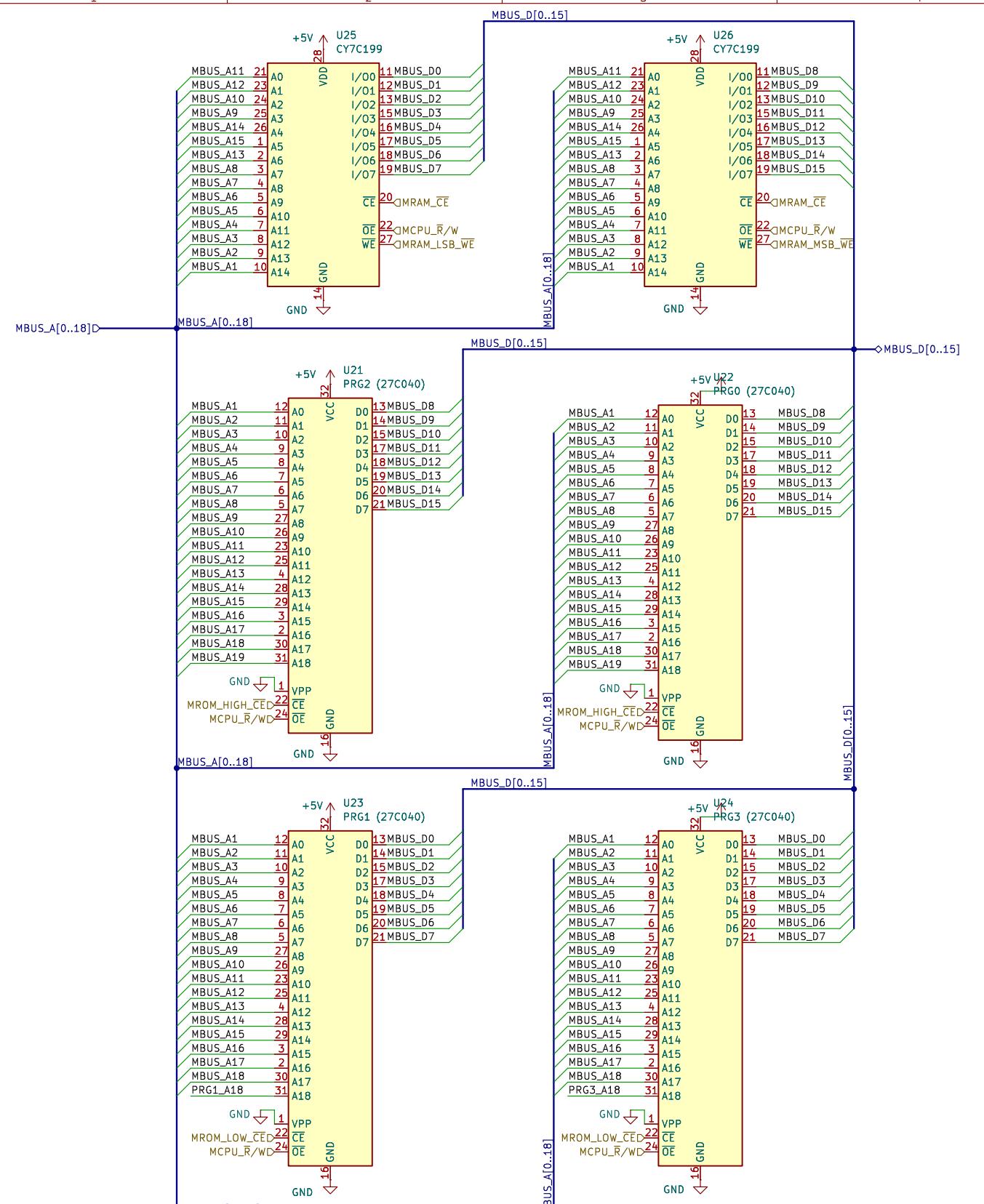
ze: B Date: 2024-12-30

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A presumed option on earlier revisions to address the MSB of PRG1 or PRG3, a fabricated route permanently jumps them to MBUS_19 in RA9074

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Sheet: /Main RAM and ROM/
File: main-ram-and-rom.kicad_sch

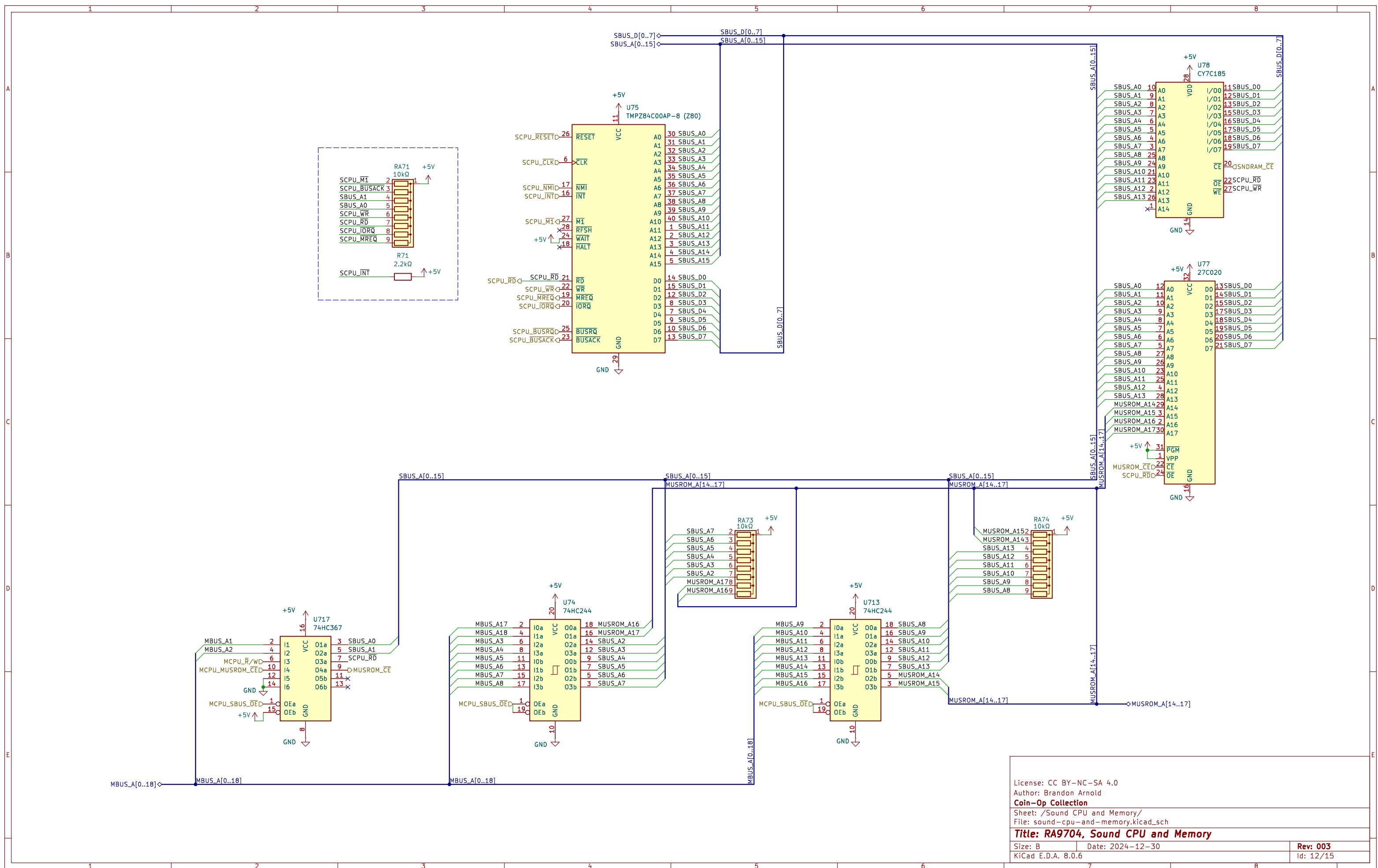
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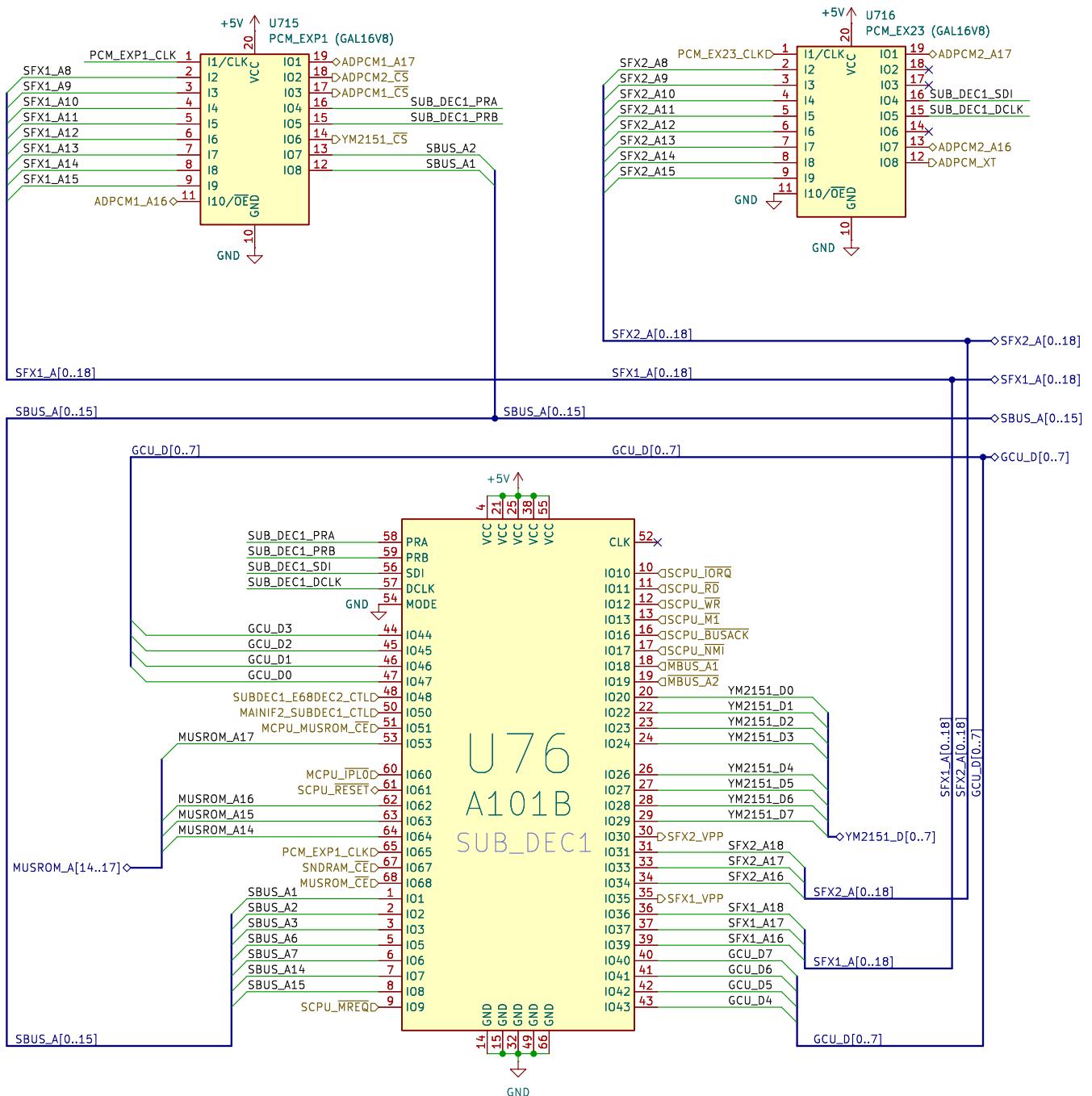
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Sheet: /Sound IC and Bus Mgmt/
File: sound-ic-bus-mgmt.kicad_sch

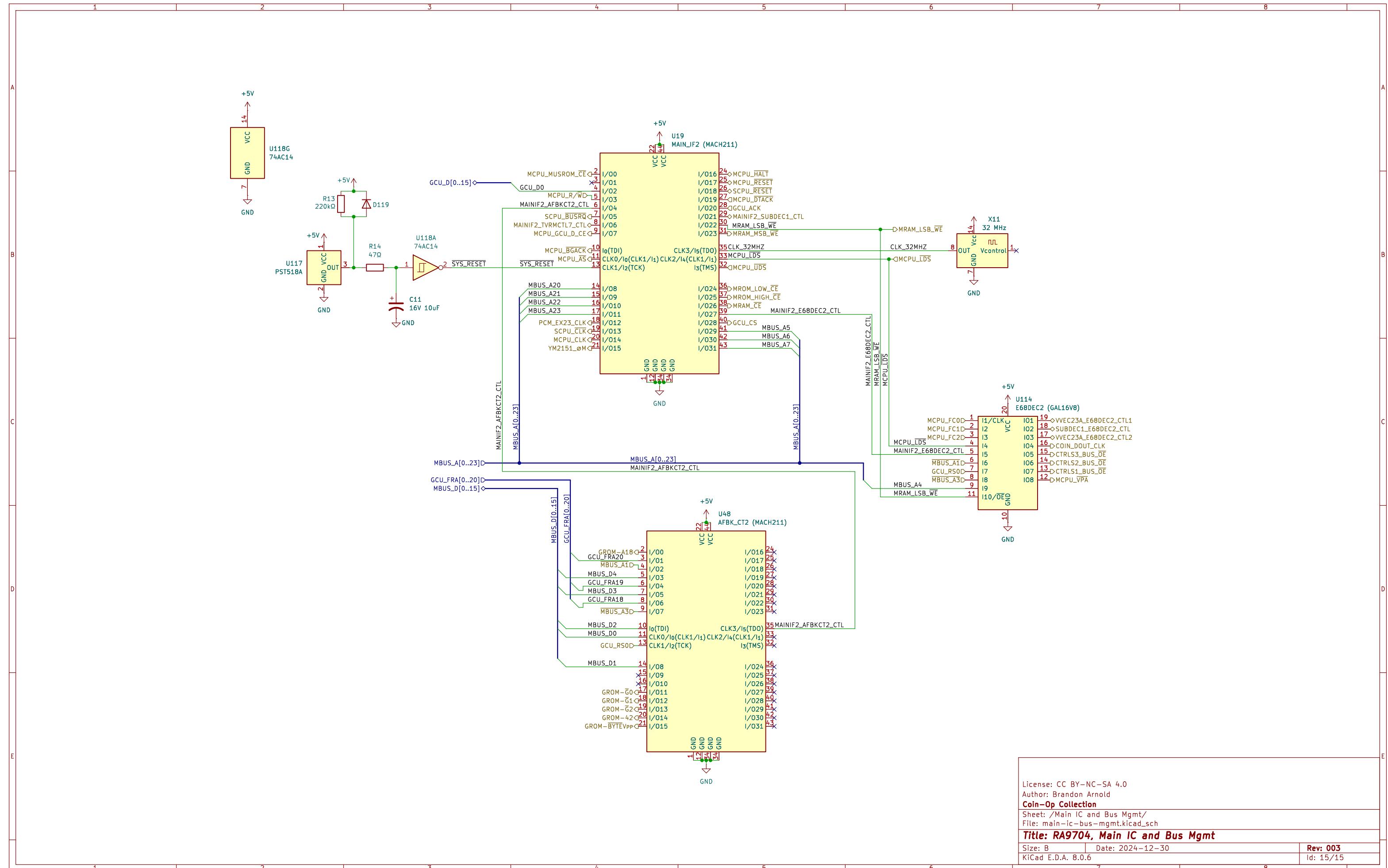
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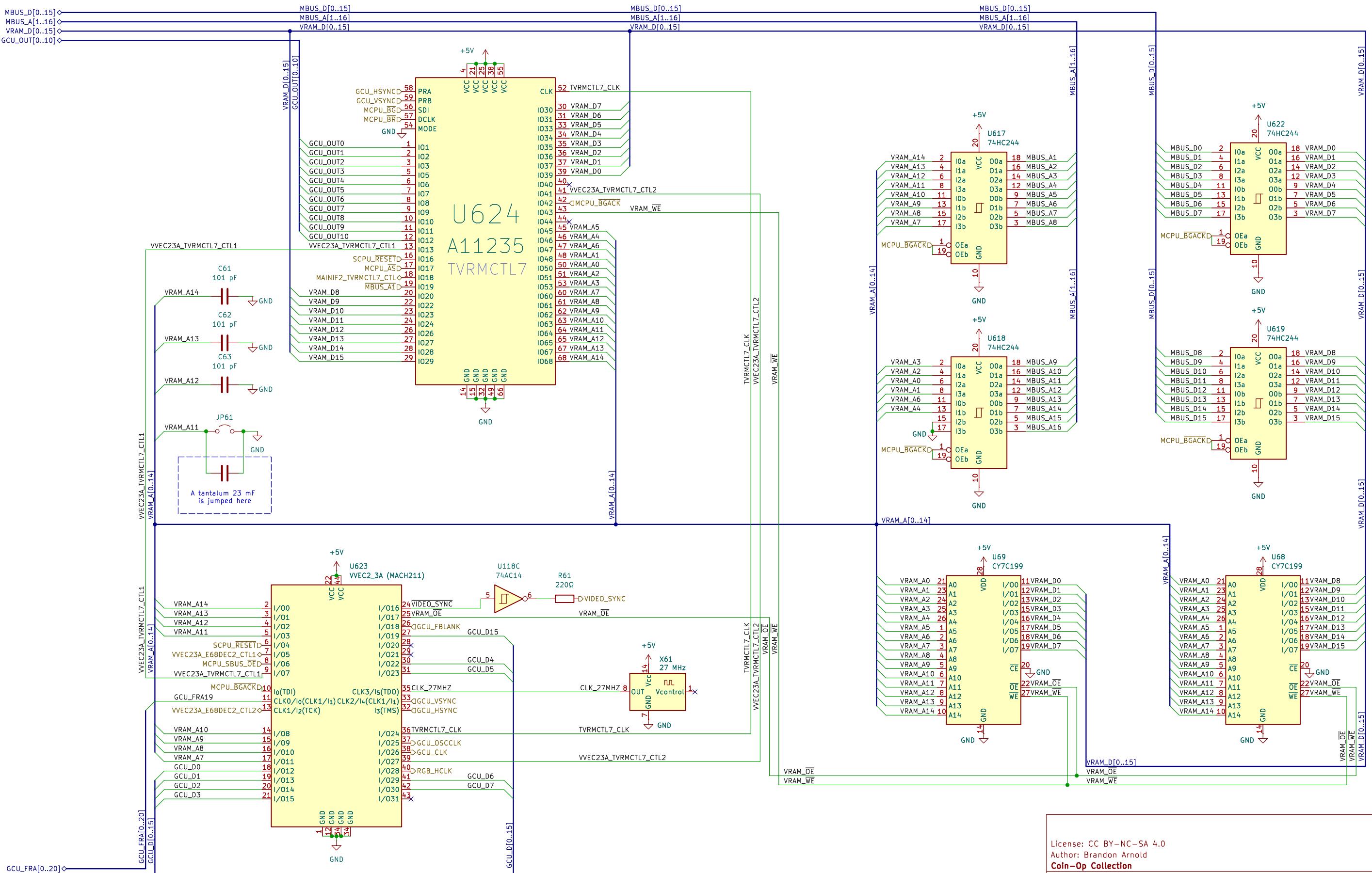
Size: A Date: 2024-12-30

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Sheet: /Video RAM and Bus Mgmt/
File: video-ram-and-bus-mgmt.kicad_sch

Title: RA9704, Video RAM and Bus Mgmt

Size: B Date: 2024-12-30

KiCad E.D.A. 8.0.6

Rev: 003

Id: 16/15