ID	Task Name	Duration	Start	Finish	Pred
1	**A Block of Code**	**211 days**	**Fri 11/14/14**	**Fri 6/12/15**	
2	Capstones Assigned	0 days	Fri 11/14/14	Fri 11/14/14	
3	**Complete Rev 1**	**125 days**	**Wed 11/26/14**	**Mon 3/30/15**	
4	**Generate Concepts**	**54 days**	**Wed 11/26/14**	**Sun 1/18/15**	2
5	Meet with Advisor	1 day	Wed 11/26/14	Wed 11/26/14	2
6	Document Ideas for 5 Design Areas	18 days	Tue 12/23/14	Fri 1/9/15	5
7	Meet with Sponsors	1 day	Fri 1/9/15	Fri 1/9/15	
8	Decide on Design Direction	9 days	Sat 1/10/15	Sun 1/18/15	7
9	**Propose Project**	**21 days**	**Mon 1/19/15**	**Mon 2/9/15**	8
10	Create Proposal Draft	5 days	Mon 1/19/15	Fri 1/23/15	
11	Create Project Schedule Dra	5 days	Mon 1/19/15	Fri 1/23/15	
12	Review Proposal with team	4 days	Sat 1/24/15	Tue 1/27/15	10
13	Review Project Schedule with team	4 days	Sat 1/24/15	Tue 1/27/15	11
14	Revise Proposal	3 days	Wed 1/28/15	Fri 1/30/15	12
15	Revise Project Schedule	3 days	Wed 1/28/15	Fri 1/30/15	13
16	Present Drafts to Advisor for Feedback	0 days	Fri 1/30/15	Fri 1/30/15	14,1
17	Revise Drafts for Submission	4 days	Sat 1/31/15	Tue 2/3/15	16
18	Submit Docs to Sponsors	0 days	Tue 2/3/15	Tue 2/3/15	17
19	Feedback from Sponsors	0 days	Mon 2/9/15	Mon 2/9/15	18
20	**Prototype 5 Design Areas**	**28 days**	**Sat 1/31/15**	**Fri 2/27/15**	
21	**Prototype Power**	**28 days**	**Sat 1/31/15**	**Fri 2/27/15**	
22	Research Methods	8 days	Sat 1/31/15	Sat 2/7/15	16
23	Research Best Practices	8 days	Sun 2/8/15	Sun 2/15/15	22
24	Order Parts	0 days	Sat 2/7/15	Sat 2/7/15	22
25	Receive Parts	0 days	Sun 2/15/15	Sun 2/15/15	23,2
26	Assemble Proof of Concep	6 days	Mon 2/16/15	Sat 2/21/15	25
27	Test Proof of Concept	6 days	Sun 2/22/15	Fri 2/27/15	26
28	**Prototype Communication**	**28 days**	**Sat 1/31/15**	**Fri 2/27/15**	
29	Research Methods	8 days	Sat 1/31/15	Sat 2/7/15	16

ID	Task Name	Duration	Start	Finish	Pred
30	Research Best Practices	8 days	Sun 2/8/15	Sun 2/15/15	29
31	Order Parts	0 days	Sat 2/7/15	Sat 2/7/15	29
32	Receive Parts	0 days	Sun 2/15/15	Sun 2/15/15	30,3
33	Assemble Proof of Conce	6 days	Mon 2/16/15	Sat 2/21/15	32
34	Test Proof of Concept	6 days	Sun 2/22/15	Fri 2/27/15	33
35	**Prototype Processing**	**28 days**	**Sat 1/31/15**	**Fri 2/27/15**	
36	Research Methods	8 days	Sat 1/31/15	Sat 2/7/15	16
37	Research Best Practices	8 days	Sun 2/8/15	Sun 2/15/15	36
38	Order Parts	0 days	Sat 2/7/15	Sat 2/7/15	36
39	Receive Parts	0 days	Sun 2/15/15	Sun 2/15/15	37,3
40	Assemble Proof of Conce	6 days	Mon 2/16/15	Sat 2/21/15	39
41	Test Proof of Concept	6 days	Sun 2/22/15	Fri 2/27/15	40
42	**Prototype Form Factor**	**28 days**	**Sat 1/31/15**	**Fri 2/27/15**	
43	Research Methods	8 days	Sat 1/31/15	Sat 2/7/15	16
44	Research Best Practices	8 days	Sun 2/8/15	Sun 2/15/15	43
45	Order Parts	0 days	Sat 2/7/15	Sat 2/7/15	43
46	Receive Parts	0 days	Sun 2/15/15	Sun 2/15/15	44,4
47	Assemble Proof of Conce	6 days	Mon 2/16/15	Sat 2/21/15	46
48	Test Proof of Concept	6 days	Sun 2/22/15	Fri 2/27/15	47
49	**Prototype UI/IO**	**28 days**	**Sat 1/31/15**	**Fri 2/27/15**	
50	Research Methods	8 days	Sat 1/31/15	Sat 2/7/15	16
51	Research Best Practices	8 days	Sun 2/8/15	Sun 2/15/15	50
52	Order Parts	0 days	Sat 2/7/15	Sat 2/7/15	50
53	Receive Parts	0 days	Sun 2/15/15	Sun 2/15/15	51,5
54	Assemble Proof of Conce	6 days	Mon 2/16/15	Sat 2/21/15	53
55	Test Proof of Concept	6 days	Sun 2/22/15	Fri 2/27/15	54
56	Integrate Modular Prototypes for Full System	8 days	Sat 2/28/15	Sat 3/7/15	20,2
57	Test Full System	5 days	Sun 3/8/15	Thu 3/12/15	56
58	Record Video of Prototyped System Demonstration	1 day	Fri 3/13/15	Fri 3/13/15	57

Gantt chart legend (timeline): Feb 8, '15 | Feb 15, '15 | Feb 22, '15 | Mar 1, '15 | Mar 8, '15 | Mar 1

Resource labels: Daniel,Greg,Jacob (task 56); Daniel,G (task 57); Greg (task 58)

ID	Task Name	Duration	Start	Finish	Pred
59	Present Demonstration to Advisor and Sponsors for	0 days	Fri 3/13/15	Fri 3/13/15	58
60	[Break for Finals]	9 days	Sat 3/14/15	Sun 3/22/15	59
61	Revise Designs based on Feedb	4 days	Mon 3/23/15	Thu 3/26/15	60
62	Test Revisions	4 days	Fri 3/27/15	Mon 3/30/15	61
63	Rev 1 Complete	0 days	Mon 3/30/15	Mon 3/30/15	62
64	**Complete Rev 2**	**69 days**	**Tue 3/31/15**	**Sun 6/7/15**	**63**
65	Brainstorm Additional Features	5 days	Tue 3/31/15	Sat 4/4/15	63
66	Prototype Additional Features	6 days	Sun 4/5/15	Fri 4/10/15	65
67	**Complete Board Designs**	**37 days**	**Sat 4/11/15**	**Sun 5/17/15**	
68	**Complete ERC Error-Free Schematics**	**16 days**	**Sat 4/11/15**	**Sun 4/26/15**	
69	Research and Select Part	4 days	Sat 4/11/15	Tue 4/14/15	66
70	Design Schematics	4 days	Wed 4/15/15	Sat 4/18/15	69
71	Review Schematics Internal to Group	4 days	Sun 4/19/15	Wed 4/22/15	70
72	Send Schematics to Third Party for Review	4 days	Sun 4/19/15	Wed 4/22/15	70
73	Revise Schematic	4 days	Thu 4/23/15	Sun 4/26/15	71,7
74	**Complete DRC Error-Free La**	**12 days**	**Mon 4/27/15**	**Fri 5/8/15**	
75	Layout Boards	4 days	Mon 4/27/15	Thu 4/30/15	68
76	Review Layouts Internally to Group	4 days	Fri 5/1/15	Mon 5/4/15	75
77	Send Layouts to Third Party for Review	4 days	Fri 5/1/15	Mon 5/4/15	75
78	Revise Layouts	4 days	Tue 5/5/15	Fri 5/8/15	76,7
79	Print Small Run of Boards in PSU LID	2 days	Sat 5/9/15	Sun 5/10/15	78
80	Assemble Small-Run Boards	2 days	Mon 5/11/15	Tue 5/12/15	79
81	Test Small-Run Boards	2 days	Wed 5/13/15	Thu 5/14/15	80
82	Print and Assemble Remaining Boards	3 days	Fri 5/15/15	Sun 5/17/15	81
83	**Complete Code**	**37 days**	**Sat 4/11/15**	**Sun 5/17/15**	**66**
84	Code Block Identification	6 days	Sat 4/11/15	Thu 4/16/15	

ID	Task Name	Duration	Start	Finish	Pred
85	Code Program Execution	6 days	Fri 4/17/15	Wed 4/22/15	84
86	Code Error Detection	6 days	Thu 4/23/15	Tue 4/28/15	85
87	Code Block Communication	6 days	Wed 4/29/15	Mon 5/4/15	86
88	Test Full Code System	7 days	Tue 5/5/15	Mon 5/11/15	87
89	Revise System Components	6 days	Tue 5/12/15	Sun 5/17/15	88
90	**Complete Enclosures**	**9 days**	**Sat 5/9/15**	**Sun 5/17/15**	
91	Design Enclosures	2 days	Sat 5/9/15	Sun 5/10/15	66,7
92	Print and Assemble Small-Run of Enclosures	2 days	Mon 5/11/15	Tue 5/12/15	91
93	Revise Enclosure Design	2 days	Wed 5/13/15	Thu 5/14/15	92
94	Print and Assemble Remaining Enclosures	3 days	Fri 5/15/15	Sun 5/17/15	93
95	Consider Specialized Output Block Designs	4 days	Wed 5/13/15	Sat 5/16/15	92
96	Integrate Modular Prototypes for Full System	7 days	Mon 5/18/15	Sun 5/24/15	67,8
97	Test Full System	5 days	Mon 5/25/15	Fri 5/29/15	96
98	Record Video of Prototyped System Demonstration	1 day	Sat 5/30/15	Sat 5/30/15	97
99	Present Demonstration to Advisor and Sponsors for	0 days	Sat 5/30/15	Sat 5/30/15	98
100	Revise Designs based on Tests and Feedback	4 days	Sun 5/31/15	Wed 6/3/15	98
101	Test Revisions	4 days	Thu 6/4/15	Sun 6/7/15	100
102	Rev 2 Complete	0 days	Sun 6/7/15	Sun 6/7/15	101
103	**Create Presentation**	**14 days**	**Sat 5/30/15**	**Fri 6/12/15**	
104	Design Poster	4 days	Sat 5/30/15	Tue 6/2/15	97
105	Send Poster off for Print	1 day	Wed 6/3/15	Wed 6/3/15	104
106	Create Presentation Slides	4 days	Sat 5/30/15	Tue 6/2/15	97
107	Write "Curriculum"	4 days	Sun 5/31/15	Wed 6/3/15	98
108	Present	5 days	Mon 6/8/15	Fri 6/12/15	102