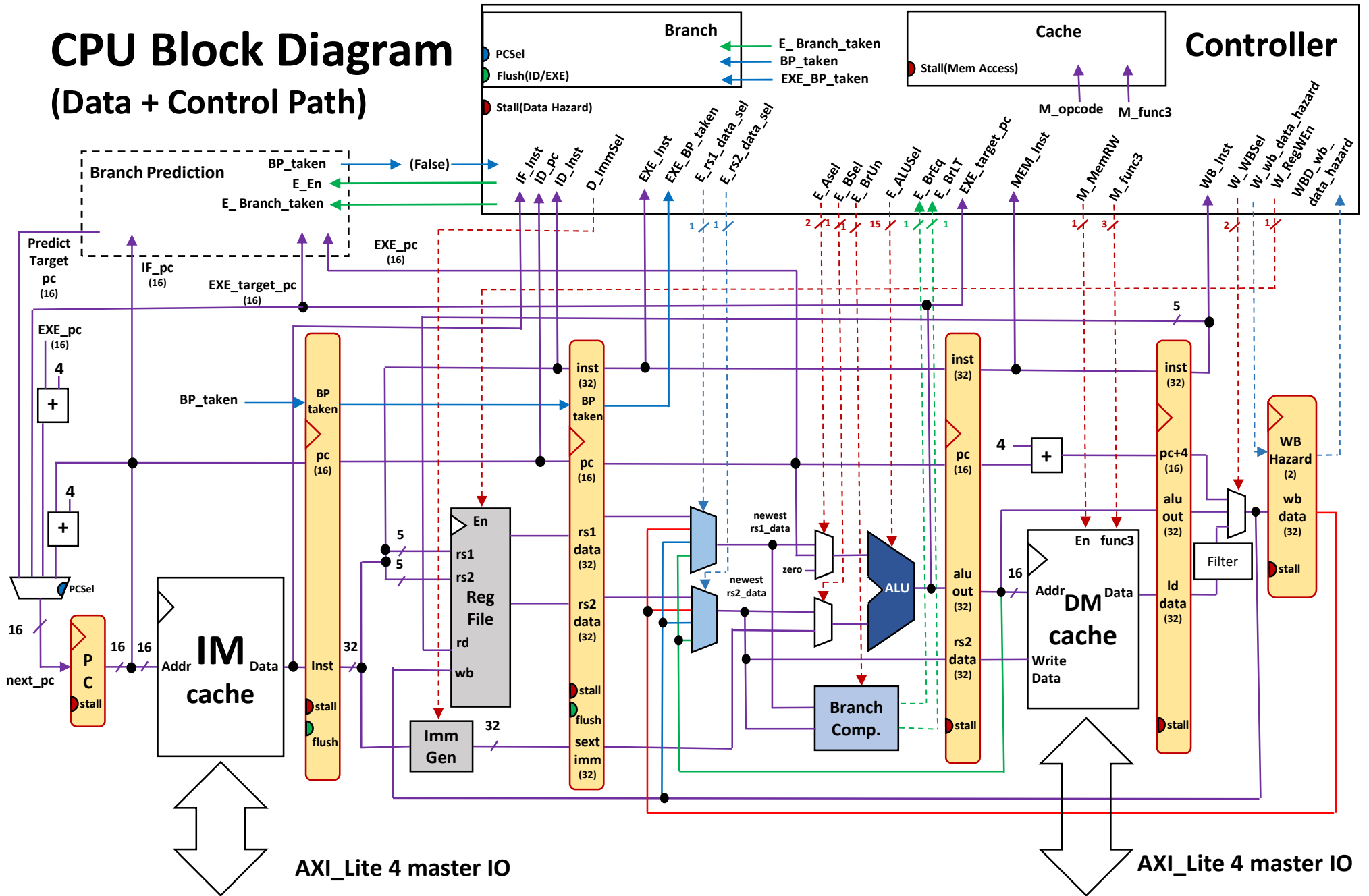
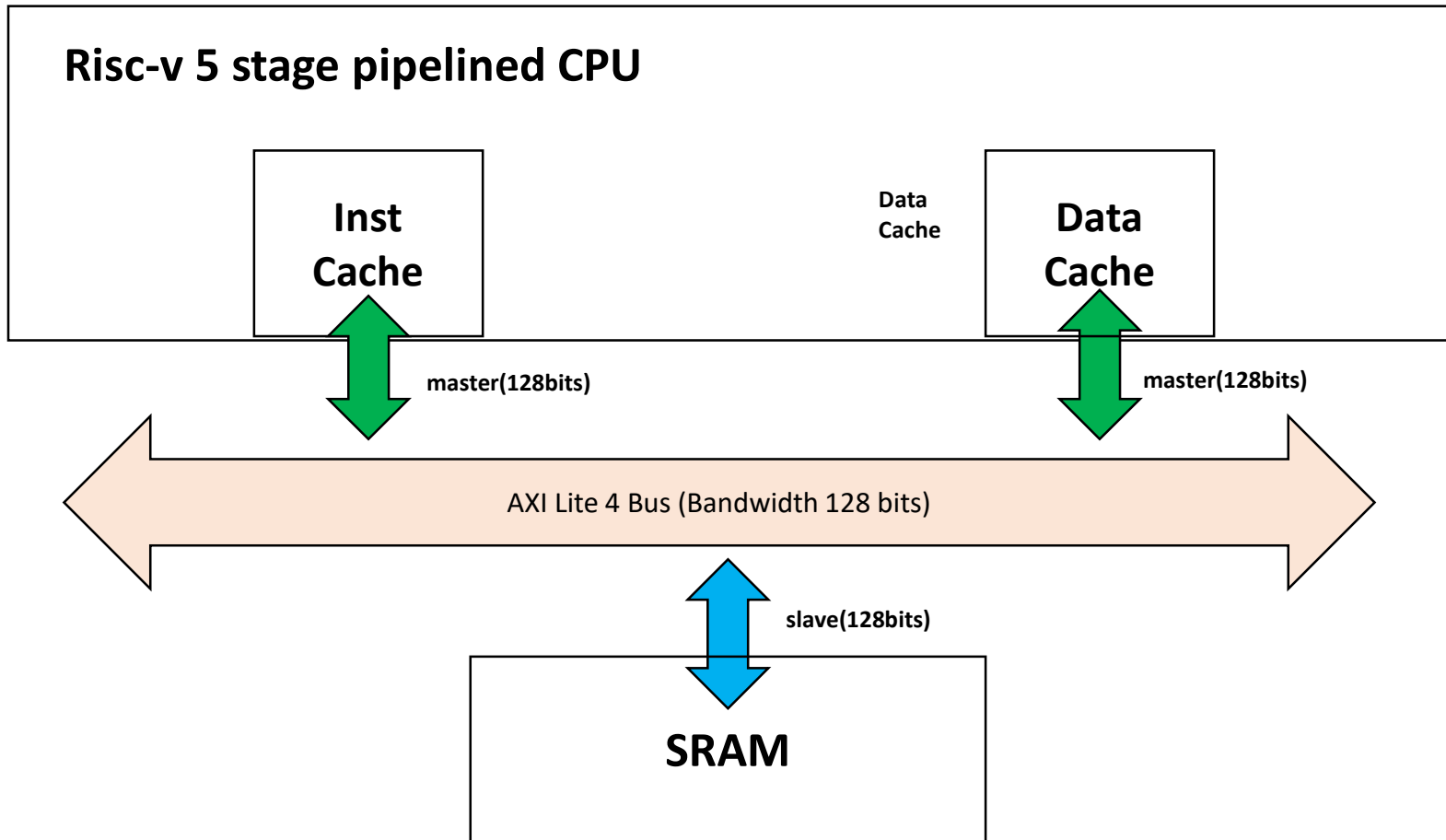
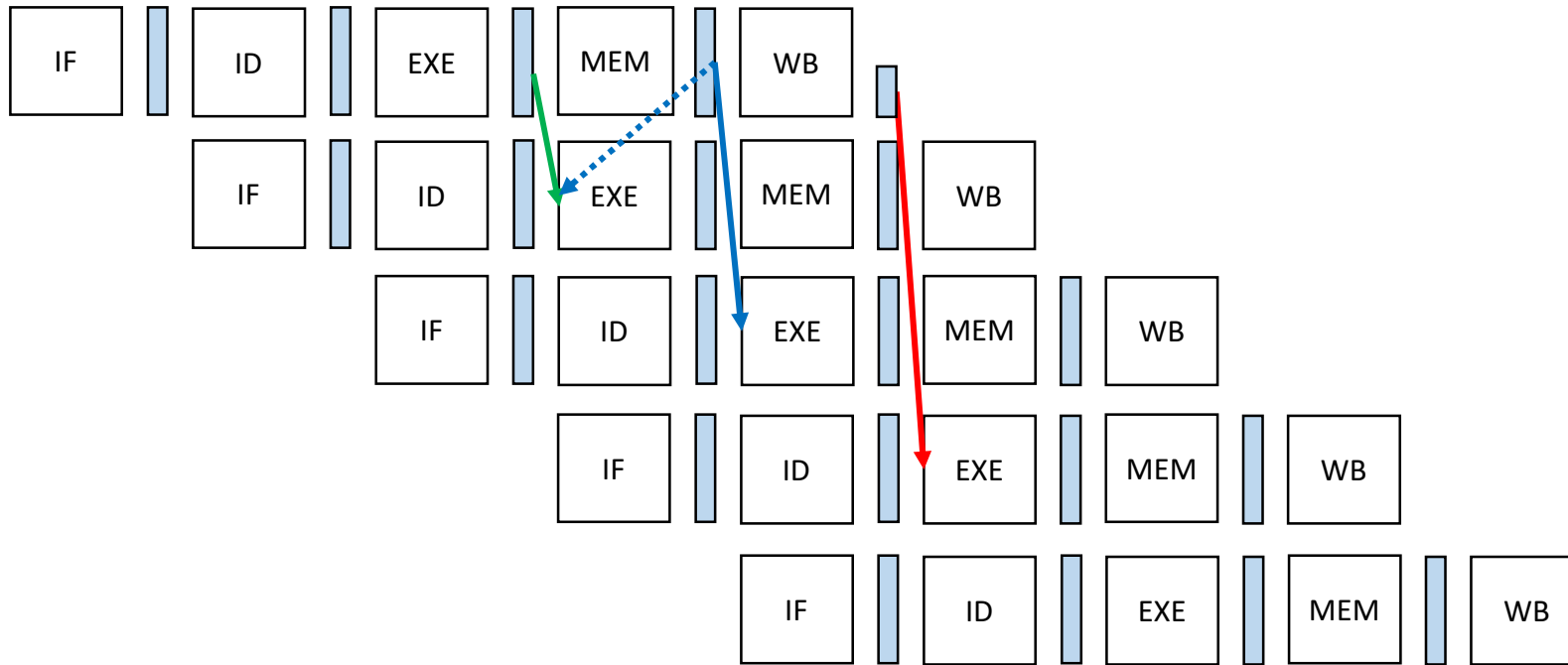


CPU Block Diagram (Data + Control Path)







(No MEM Access)
 $\text{MEM_rd} == \text{EXE_rs}$
 (opcode \neq LOAD)

(Has MEM Access)
 $\text{WB_rd} == \text{EXE_rs}$
 (Stall if Need)

(REG R/W Problem)
 $(\text{Previous})\text{WB_rd} == (\text{now})\text{EXE_rs}$
 $\rightarrow (\text{Previous})\text{WB_rd} == (\text{Previous})\text{ID_rs}$

