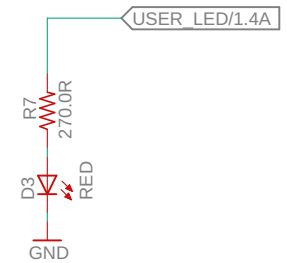
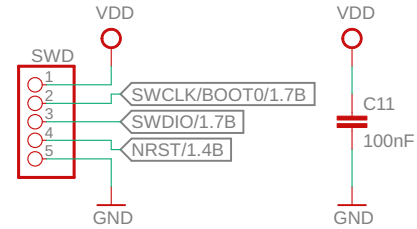
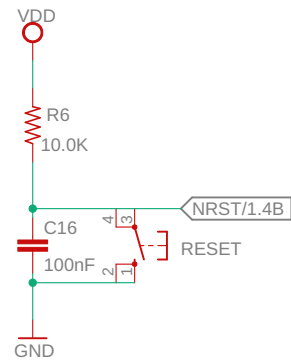
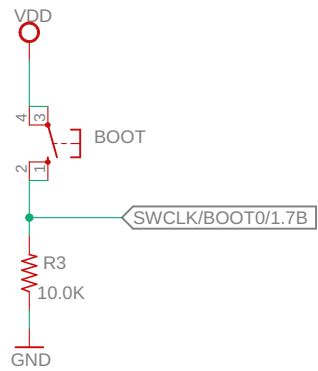
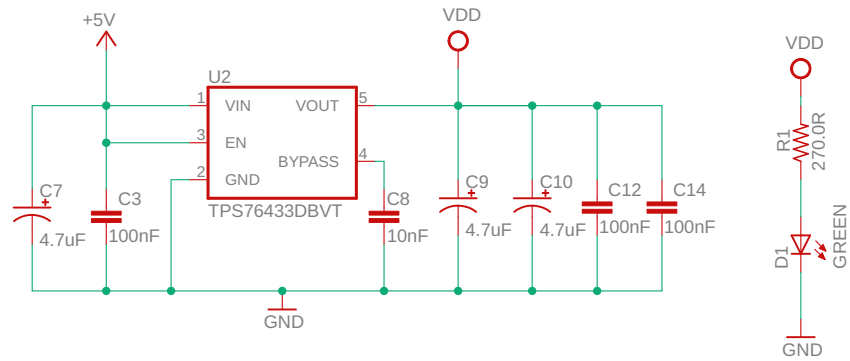
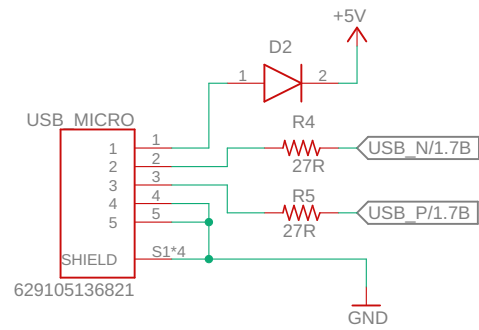


The image displays four circuit diagrams illustrating different coupling methods:

- Vref Coupling:** A circuit with a positive terminal (VREF) and a negative terminal (VSSA). A capacitor C1 with a value of 100nF is connected between them.
- Vbat Coupling:** A circuit with a positive terminal (VBAT) and a negative terminal (VSSA). A capacitor C2 with a value of 1.00uF is connected between them.
- VDDA Coupling:** A circuit with a positive terminal (VDDA) and a negative terminal (VSSA). It features two capacitors: C5 (100nF) connected directly between VDDA and VSSA, and C4 (4.7uF) connected in parallel with C5.
- Feedback Loop:** A circuit showing a feedback path from VDD to VDDA. It consists of a resistor R2 with a value of 0.0R connected between the two terminals.



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Description	Revision 1
STM32G0B1CEU6N DevKit	Size Letter
Author Ibrahim Zaeiter	Sheet 2 of 2