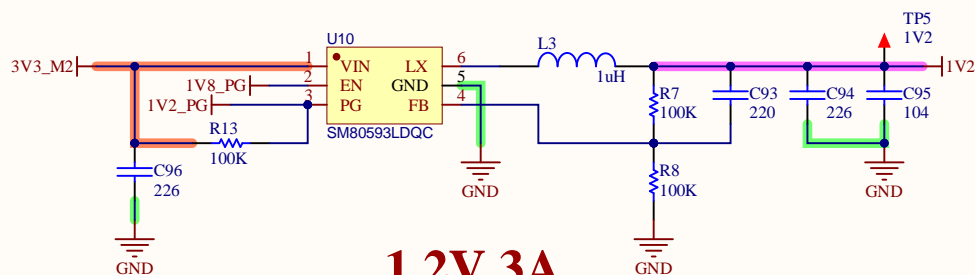
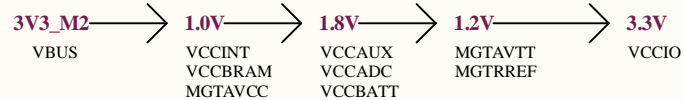
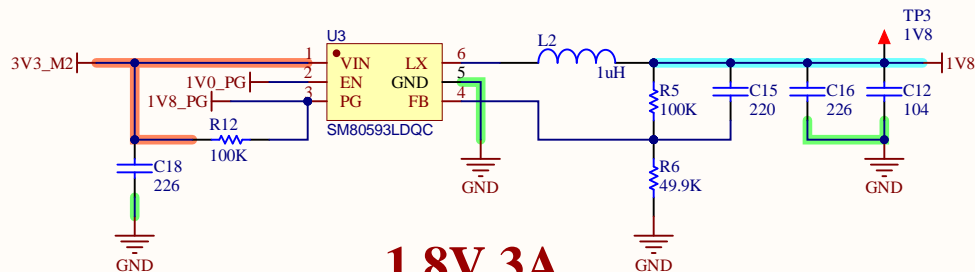
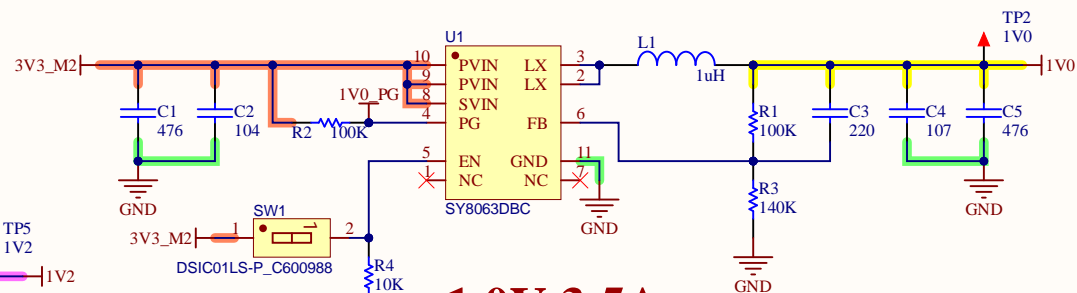
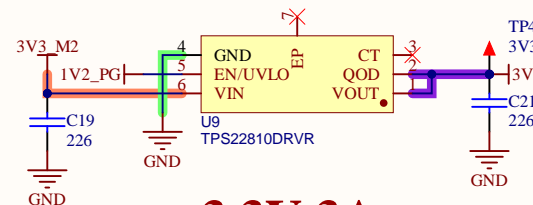
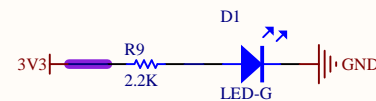
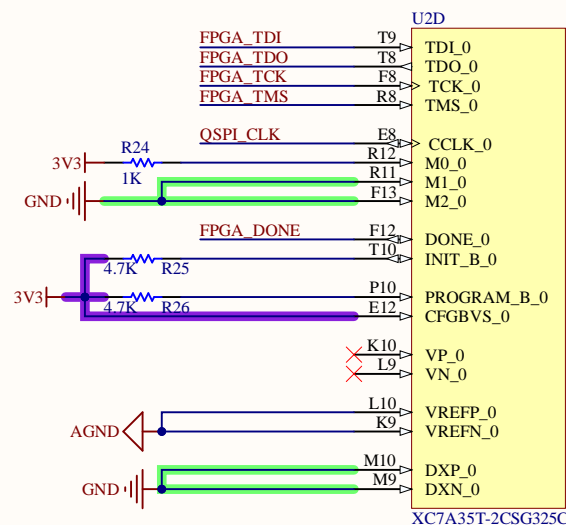
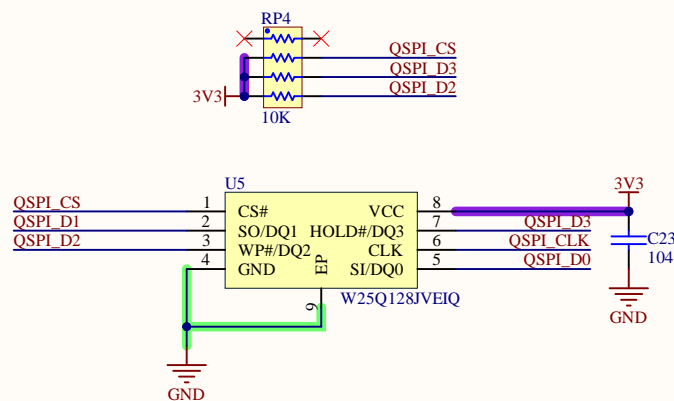
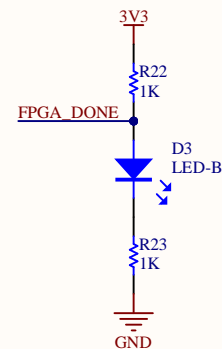
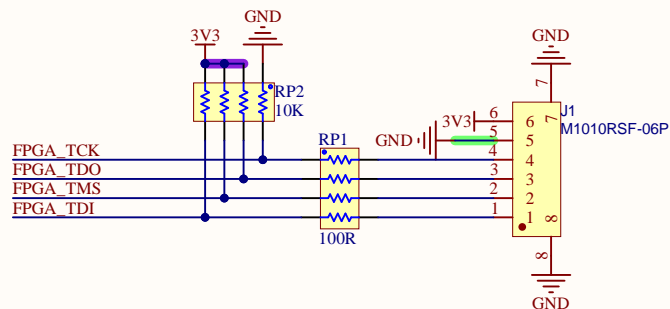
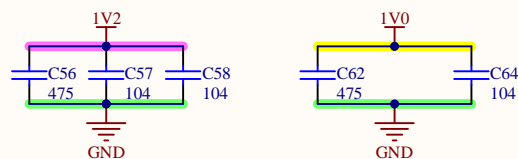
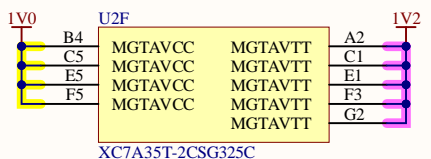
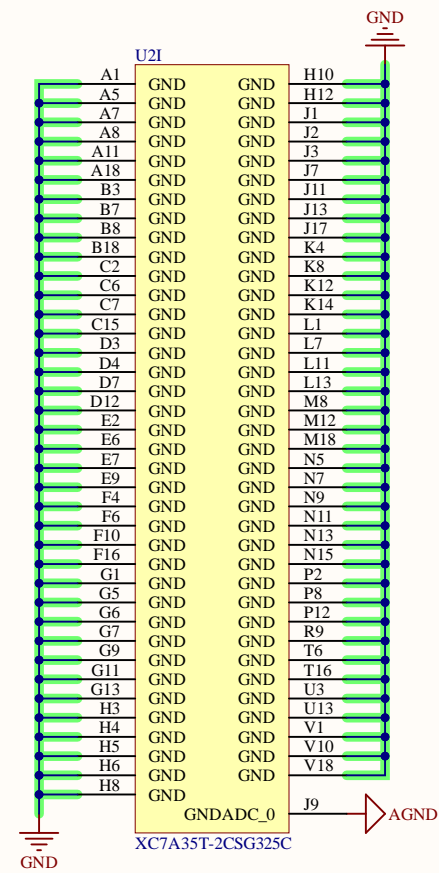
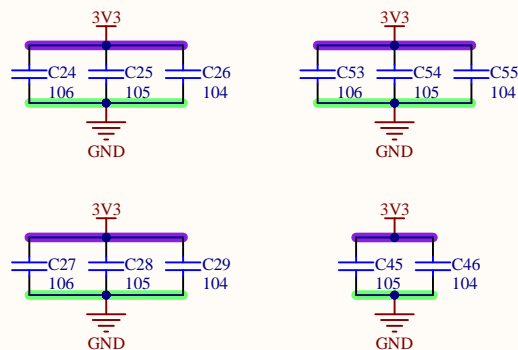
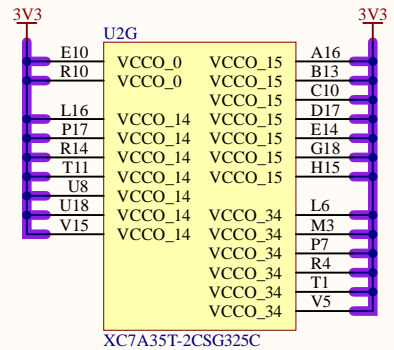
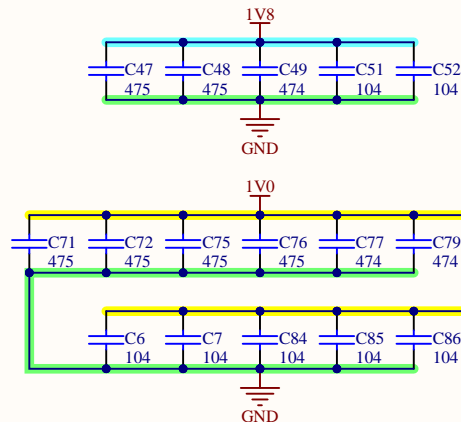
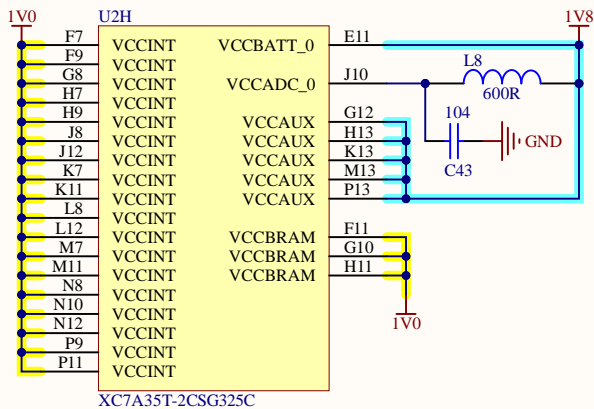


**POWER ON SEQUENCE****1.2V 3A****1.8V 3A****1.0V 3.5A****3.3V 3A****POWER GOOD**

Title		
Size	Number	Revision
A4		
Date:	12/15/2023	Sheet of
File:	E:\AD_Projects\...\01 POWER.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	12/15/2023	Sheet of
File:	E:\AD_Projects\...\02 FPGA_CFG.SchDoc	Drawn By:



Title		
Size	Number	Revision
A4		
Date:	12/15/2023	Sheet of
File:	E:\AD_Projects\...\03 FPGA_POWER.Sch	Drawn By:

U2A

<b>BANK 14</b>	IO_0_14	L14	
	IO_L1P_T0_D00_MOSI_14	K16	QSPI_D0
	IO_L1N_T0_D01_DIN_14	L17	QSPI_D1
	IO_L2P_T0_D02_14	J15	QSPI_D2
	IO_L2N_T0_D03_14	J16	QSPI_D3
	IO_L3P_T0_DQS_PUDC_B_14	J18	FT_D21
	IO_L3N_T0_DQS_EMCCLK_14	K18	FT_D22
		K17	FT_D23
	IO_L4P_T0_D04_14	L18	FT_D24
	IO_L4N_T0_D05_14	J14	
	IO_L5P_T0_D06_14	K15	
	IO_L5N_T0_D07_14	L15	QSPI_CS
	IO_L6P_T0_FCS_B_14	M15	
	IO_L6N_T0_D08_VREF_14	M16	FT_BE2
	IO_L7P_T1_D09_14	M17	FT_D25
	IO_L7N_T1_D10_14		
	IO_L8P_T1_D11_14	M14	
	IO_L8N_T1_D12_14	N14	
	IO_L9P_T1_DQS_14	N16	
	IO_L9N_T1_DQS_D13_14	N17	FT_D27
	IO_L10P_T1_D14_14	N18	FT_D26
	IO_L10N_T1_D15_14	P18	FT_D28
	IO_L11P_T1_SRCC_14	P15	FT_TXE
	IO_L11N_T1_SRCC_14	P16	FT_BE1
	IO_L12P_T1_MRCC_14	P14	
	IO_L12N_T1_MRCC_14	R15	
	IO_L13P_T2_MRCC_14	T14	
	IO_L13N_T2_MRCC_14	T15	FT_RXF
	IO_L14P_T2_SRCC_14	R16	FT_SIWU
	IO_L14N_T2_SRCC_14	R17	FT_BE3
	IO_L15P_T2_DQS_RDWR_B_14	R18	FT_D29
	IO_L15N_T2_DQS_DOUT_CSO_B_14	T18	FT_D30
	IO_L16P_T2_CSI_B_14	T17	FT_D31
	IO_L16N_T2_A15_D31_14	U17	FT_BE0
	IO_L17P_T2_A14_D30_14	U15	FT_RD
	IO_L17N_T2_A13_D29_14	U16	FT_WR
	IO_L18P_T2_A12_D28_14	V16	FT_OE
	IO_L18N_T2_A11_D27_14	V17	FT_RST
	IO_L19P_T3_A10_D26_14	R13	
	IO_L19N_T3_A09_D25_VREF_14	T13	
	IO_L20P_T3_A08_D24_14	U14	FT_GPIO0
	IO_L20N_T3_A07_D23_14	V14	FT_GPIO1
	IO_L21P_T3_DQS_14	V12	
	IO_L21N_T3_DQS_A06_D22_14	V13	
	IO_L22P_T3_A05_D21_14	T12	
	IO_L22N_T3_A04_D20_14	U12	
	IO_L23P_T3_A03_D19_14	U11	
	IO_L23N_T3_A02_D18_14	V11	
	IO_L24P_T3_A01_D17_14	U9	
	IO_L24N_T3_A00_D16_14	U9	
	IO_25_14	U10	

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U2B

<b>BANK 15</b>	IO_0_15	D10	
	IO_L1P_T0_AD0P_15	D8	
	IO_L1N_T0_AD0N_15	C8	
	IO_L2P_T0_AD8P_15	D9	
	IO_L2N_T0_AD8N_15	C9	
	IO_L3P_T0_DQS_AD1P_15	B9	PERST
	IO_L3N_T0_DQS_AD1N_15	A9	CLKREQ
	IO_L4P_T0_15	C11	
	IO_L4N_T0_15	B11	
	IO_L5P_T0_AD9P_15	B10	PEWAKE
	IO_L5N_T0_AD9N_15	A10	
	IO_L6P_T0_15	D11	
	IO_L6N_T0_VREF_15	C12	
	IO_L7P_T1_AD2P_15	B12	
	IO_L7N_T1_AD2N_15	A12	
	IO_L8P_T1_AD10P_15	A13	FT_D0
	IO_L8N_T1_AD10N_15	A14	FT_D1
	IO_L9P_T1_DQS_AD3P_15	C14	
	IO_L9N_T1_DQS_AD3N_15	B15	FT_D4
	IO_L10P_T1_AD11P_15	B14	FT_D2
	IO_L10N_T1_AD11N_15	A15	FT_D3
	IO_L11P_T1_SRCC_15	D13	FT_D13
	IO_L11N_T1_SRCC_15	C13	FT_D12
	IO_L12P_T1_MRCC_15	E13	
	IO_L12N_T1_MRCC_15	D14	
	IO_L13P_T2_MRCC_15	E15	FT_D15
	IO_L13N_T2_MRCC_15	D15	FT_D14
	IO_L14P_T2_SRCC_15	E16	FT_CLK
	IO_L14N_T2_SRCC_15	D16	
	IO_L15P_T2_DQS_15	B16	FT_D5
	IO_L15N_T2_DQS_ADV_B_15	A17	FT_D6
	IO_L16P_T2_A28_15	C16	
	IO_L16N_T2_A27_15	B17	FT_D7
	IO_L17P_T2_A26_15	E17	FT_D11
	IO_L17N_T2_A25_15	D18	FT_D9
	IO_L18P_T2_A24_15	C17	
	IO_L18N_T2_A23_15	C18	FT_D8
	IO_L19P_T3_A22_15	G17	FT_D18
	IO_L19N_T3_A21_VREF_15	F18	FT_D16
	IO_L20P_T3_A20_15	H16	
	IO_L20N_T3_A19_15	G16	
	IO_L21P_T3_DQS_15	G15	
	IO_L21N_T3_DQS_A18_15	F15	
	IO_L22P_T3_A17_15	G14	
	IO_L22N_T3_A16_15	F14	
	IO_L23P_T3_FOE_B_15	H17	FT_D20
	IO_L23N_T3_FWE_B_15	H18	FT_D19
	IO_L24P_T3_RS1_15	F17	FT_D17
	IO_L24N_T3_RS0_15	E18	FT_D10
	IO_25_15	H14	

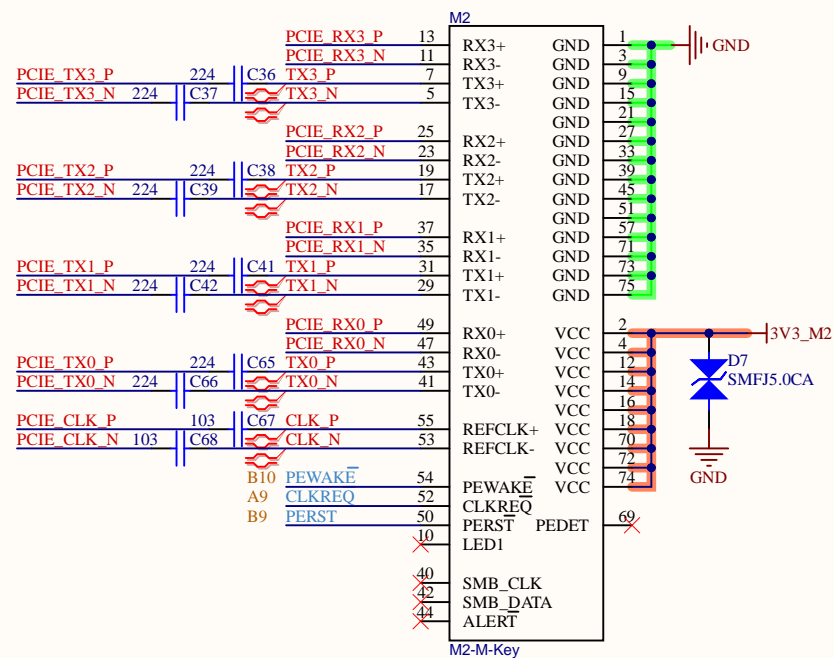
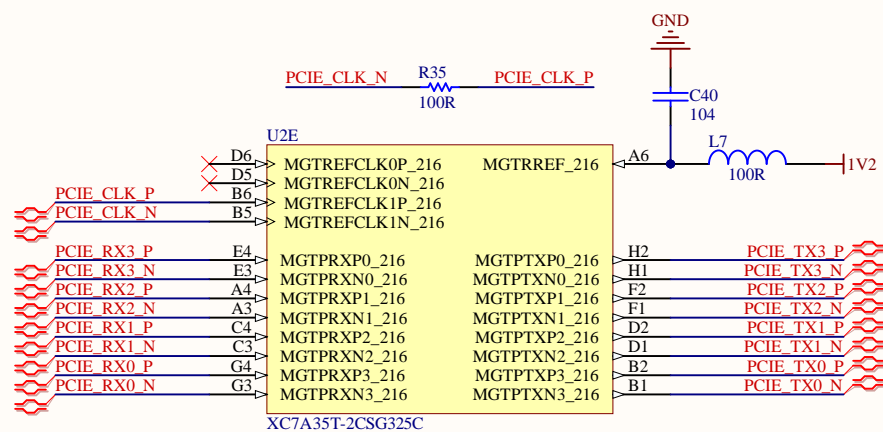
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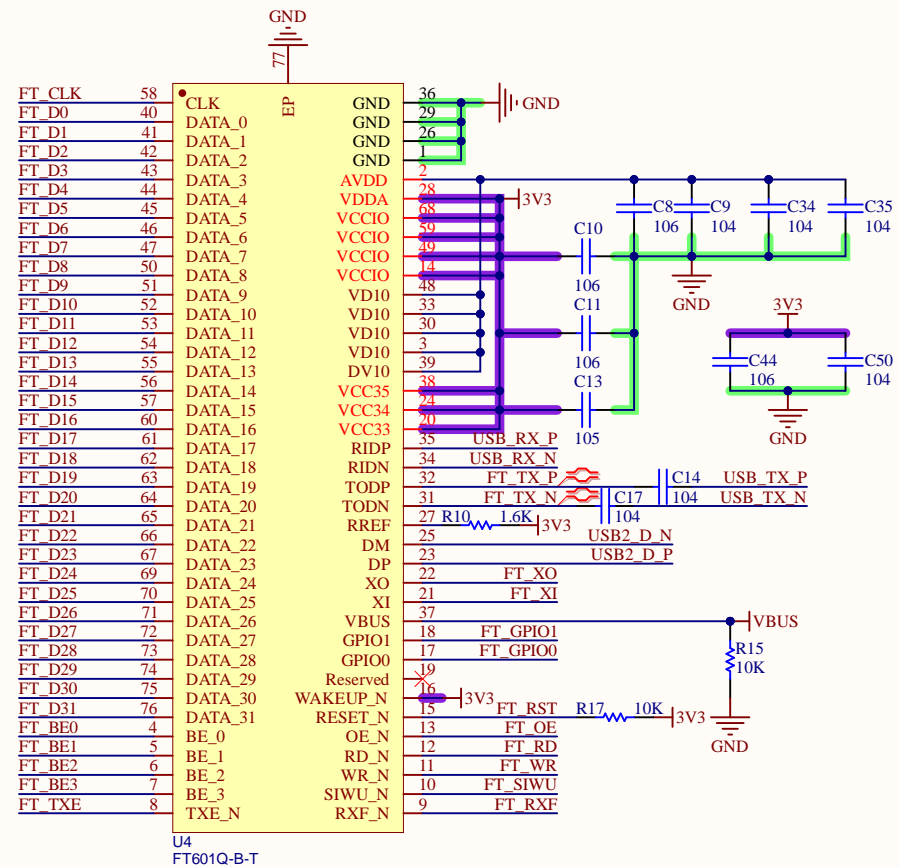
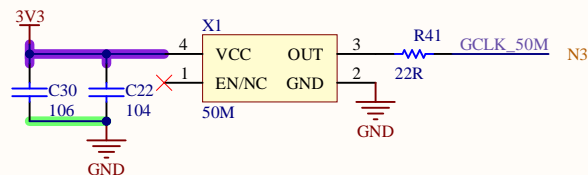
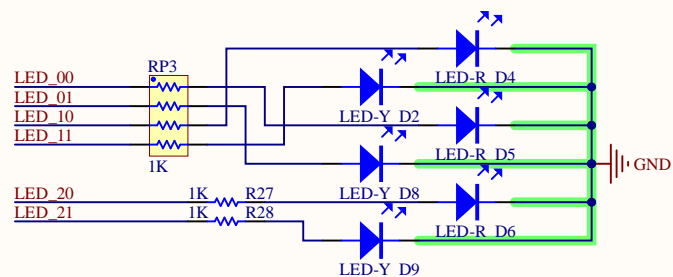
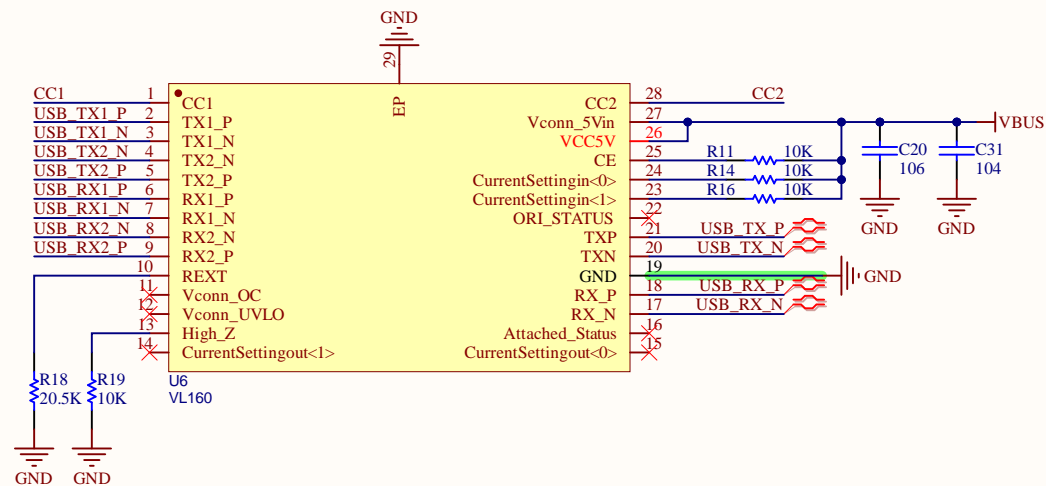
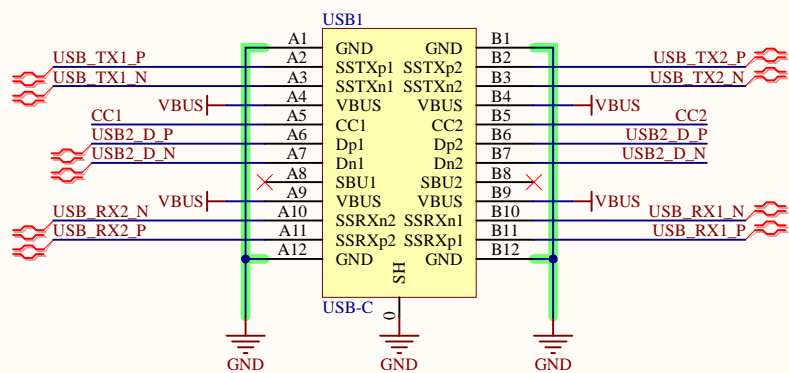
<b>BANK 34</b>	IO_0_34	J6	
	IO_L1P_T0_34	K6	
	IO_L1N_T0_34	K5	
	IO_L2P_T0_34	J5	
	IO_L2N_T0_34	K4	
	IO_L3P_T0_DQS_34	K1	
	IO_L3N_T0_DQS_34	K3	
	IO_L4P_T0_34	L2	
	IO_L4N_T0_34	L4	
	IO_L5P_T0_34	L3	
	IO_L5N_T0_34	L5	
	IO_L6P_T0_34	M5	
	IO_L6N_T0_VREF_34	M2	
	IO_L7P_T1_34	M1	
	IO_L7N_T1_34	M6	
	IO_L8P_T1_34	N6	
	IO_L8N_T1_34	N1	
	IO_L9P_T1_DQS_34	P1	LED_11
	IO_L9N_T1_DQS_34	M4	
	IO_L10P_T1_34	N4	
	IO_L10N_T1_34	N3	GCLK_50M
	IO_L11P_T1_SRCC_34	N2	
	IO_L11N_T1_SRCC_34	P4	
	IO_L12P_T1_MRCC_34	P3	
	IO_L12N_T1_MRCC_34	R2	
	IO_L13P_T2_MRCC_34	R1	LED_10
	IO_L13N_T2_MRCC_34	R3	
	IO_L14P_T2_SRCC_34	T2	
	IO_L14N_T2_SRCC_34	U2	LED_20
	IO_L15P_T2_DQS_34	U1	LED_01
	IO_L15N_T2_DQS_34	V3	LED_21
	IO_L16P_T2_34	V2	LED_00
	IO_L16N_T2_34	P4	
	IO_L17P_T2_34	T3	
	IO_L17N_T2_34	U4	
	IO_L18P_T2_34	V4	
	IO_L18N_T2_34	P6	
	IO_L19P_T3_34	P5	
	IO_L19N_T3_VREF_34	U6	
	IO_L20P_T3_34	U5	
	IO_L20N_T3_34	R5	
	IO_L21P_T3_DQS_34	T5	
	IO_L21N_T3_DQS_34	R7	
	IO_L22P_T3_34	T7	
	IO_L22N_T3_34	U7	
	IO_L23P_T3_34	V6	
	IO_L23N_T3_34	V8	
	IO_L24P_T3_34	V7	
	IO_L24N_T3_34	R6	
	IO_25_34		

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Title		
Size	Number	Revision
A4		
Date:	12/15/2023	Sheet of
File:	E:\AD_Projects\104 FPGA_BANK14_15\BANK14_SchDoc	



Title		
Size	Number	Revision
A4		
Date:	12/15/2023	Sheet of
File:	E:\AD_Projects\...\05 M2_M_KEY.SchDocDrawn By:	



Title		
Size A4	Number	Revision
Date: 12/15/2023	Sheet of	
File: E:\AD_Projects\...\06 USB3_LED_OSC.Sch	Drawn By:	