Data-flow Analysis

Idea

 Data-flow analysis derives information about the dynamic behavior of a program by only examining the static code

Example

- How many registers do we need for the program on the right?
- Easy bound: the number of variables used (3)
- Better answer is found by considering the **dynamic** requirements of the program

```
1    a := 0

2 L1: b := a + 1

3    c := c + b

4    a := b * 2

5    if a < 9 goto L1

6    return c
```

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2

Liveness Analysis

Definition

- A variable is **live** at a particular point in the program if its value at that point will be used in the future (**dead**, otherwise).
 - :. To compute liveness at a given point, we need to look into the future

Motivation: Register Allocation

- A program contains an unbounded number of variables
- Must execute on a machine with a bounded number of registers
- Two variables can use the same register if they are never in use at the same time (*i.e,* never simultaneously live).
- :. Register allocation uses liveness information

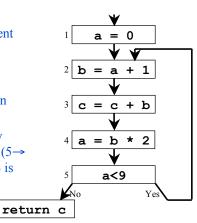
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Liveness by Example

What is the live range of b?

- Variable **b** is read in statement 4, so **b** is live on the $(3 \rightarrow 4)$ edge
- Since statement 3 does not assign into b, b is also live on the (2→3) edge
- Statement 2 assigns b, so any value of b on the (1→2) and (5→2) edges are not needed, so b is dead along these edges



b's live range is $(2\rightarrow 3\rightarrow 4)$

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5

Liveness by Example (cont)

Live range of a

- **a** is live from $(1\rightarrow 2)$ and again from $(4\rightarrow 5\rightarrow 2)$
- **a** is dead from $(2\rightarrow 3\rightarrow 4)$

Live range of b

- **b** is live from $(2 \rightarrow 3 \rightarrow 4)$

Live range of c

- c is live from (entry $\rightarrow 1\rightarrow 2\rightarrow 3\rightarrow 4\rightarrow 5\rightarrow 2, 5\rightarrow 6$)

6 return c

1 $\mathbf{a} = \mathbf{0}$ 2 $\mathbf{b} = \mathbf{a} + \mathbf{1}$ 3 $\mathbf{c} = \mathbf{c} + \mathbf{b}$ 4 $\mathbf{a} = \mathbf{b} * \mathbf{2}$ No Yes

Variables **a** and **b** are never simultaneously live, so they can share a register

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Control Flow Graphs (CFGs)

Definition

 A CFG is a graph whose nodes represent program statements and whose directed edges represent control flow

Example

```
1     a := 0
2 L1: b := a + 1
3     c := c + b
4     a := b * 2
5     if a < 9 goto L1
6     return c</pre>
```

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return c

7

a = b *

a<9

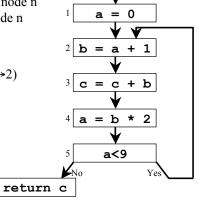
Terminology

Flow Graph Terms

- A CFG node has out-edges that lead to successor nodes and in-edges that come from predecessor nodes
- pred[n] is the set of all predecessors of node n
 succ[n] is the set of all successors of node n

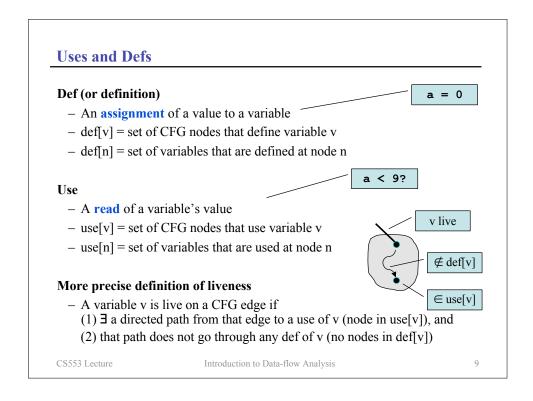
Examples

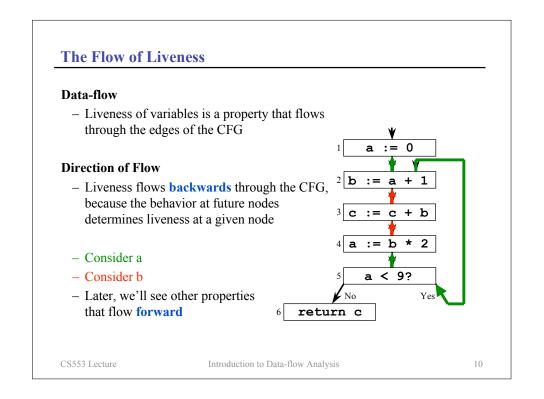
- Out-edges of node 5: $(5\rightarrow 6)$ and $(5\rightarrow 2)$
- $-\operatorname{succ}[5] = \{2,6\}$
- $\text{ pred}[5] = \{4\}$
- $\text{ pred}[2] = \{1,5\}$

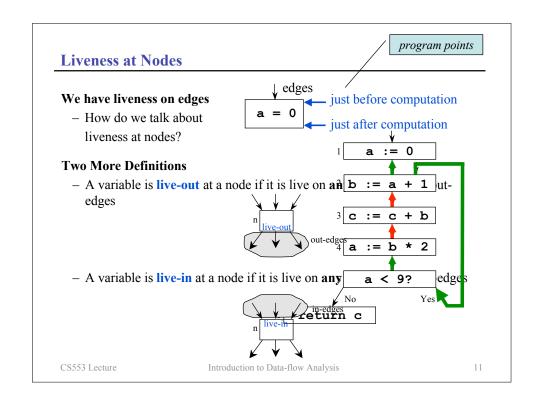


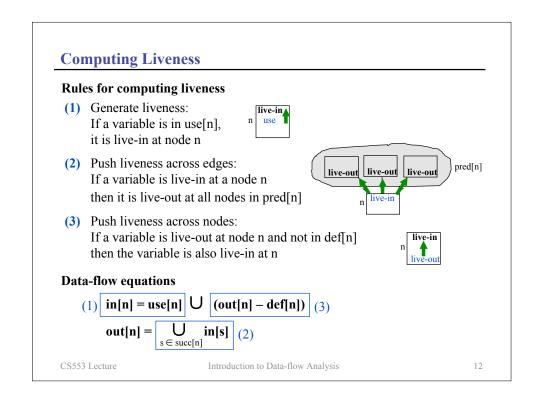
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Solving the Data-flow Equations

Algorithm

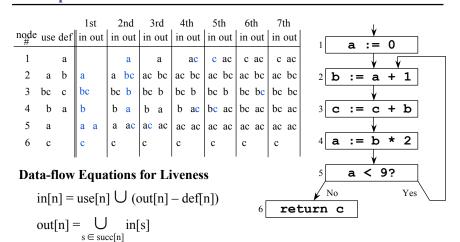
This is iterative data-flow analysis (for liveness analysis)

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13

Example



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