Note: empty entries in the table indicate state+event pairs that will not happen and do not need to be supported by the cache controller

Note: the term "state" means the cache controller should transition to the state provided by the coherence message; the cache controller can assume the state provided is the correct next state

Note: For the cache controller, implementing a protocol is simply implementing the subset of rows from MOESIF that are required, M and I are required at a minimum, F and O require S to be present

MOESIF - Cache Controller / LCE

	Core Action		CCE Action								
State	Load	Store	Invalidate	Set Tag + Data	Set State + Wakeup	Set State	Set State + Writeback	Writeback	Transfer	Set State + Transfer	Set State + Transfer + Writeback
Invalid (I)	Read Req	Write Req		do load or store / state							
Shared (S)	hit	Write Req	InvAck / I		store / state (M)	/ state					
Exclusive (E)	hit	hit / M					Null WB / state	Null WB / E		Set Tag + Data / state	Set Tag + Data, Null WB / state
Modified (M)	hit	hit					WB / state	WB / M		Set Tag + Data / state	Set Tag + Data, WB / state
Owned (O)	hit	Write Req			store / state (M)		WB / state	WB / O	Set Tag + Data / O	Set Tag + Data / state	
Forward (F)	hit	Write Req			store / state (M)	/ state			Set Tag + Data / F	Set Tag + Data / state	
			Invalidate sharer	Data from Dir	Upgrade	Downgrade	Downgrade	Update Mem	Data from Owner	Data from Owner	Data from Owner
			on Write Req	Data from Owner		Replacement Inv	Replacement Inv		on Read Req	on Write Req	on Read Req

MI - Cache Controller / LCE

	Core Action		Coherence Message						
State	Load	Store	Invalidate	Set Tag + Data	Set State + Wakeup	Set State + Writeback	Writeback	Set State +	Set State + Transfer + Writeback
Invalid (I)	Read Reg	Write Req		do load or store					
Modified (M)	hit	hit				WB / state	WB / M	Set Tag + Data / state	Set Tag + Data, WB / state

MSI - Cache Controller / LCE

	Core Action		Coherence Mes	Coherence Message							
State	Load	Store	Invalidate	Set Tag + Data	Set State + Wakeup		Set State + Writeback	Writeback		Set State +	Set State + Transfer + Writeback
Invalid (I)	Read Req	Write Req		do load or store / state							
Shared (S)	hit	Write Req	InvAck / I		store / state (M)	/ state					
Modified (M)	hit	hit					WB / state	WB / M		Set Tag + Data / state	Set Tag + Data, WB / state

MESI - Cache Controller / LCE

	Core Action		Coherence Mess	oherence Message							
State	Load	Store	Invalidate	Set Tag + Data	Set State + Wakeup	Set State	Set State + Writeback	Writeback	Transfer	Set State + Transfer	Set State + Transfer + Writeback
Invalid (I)	Read Req	Write Req		Do load or store/ State							
Shared (S)	Hit	Write Req	InvAck / I		Do store / M	/ State					
Exclusive (E)	Hit	Hit / M					Null WB / State	Null WB / E		Set Tag + Data / State	Set Tag + Data, Null WB / State
Modified (M)	Hit	Hit					WB / State	WB / M		Set Tag + Data / State	Set Tag + Data, WB / State

Data from
Directory or
Invalidate

Data from
Directory or
Directory or
Upgrade
Downgrade
Downgra

MESIF - Cache Controller / LCE

	Core Action		CCE Action								
State	Load	Store	Invalidate	Set Tag + Data	Set State + Wakeup		Set State + Writeback	Writeback		Set State +	Set State + Transfer + Writeback
				do load or store							
Invalid (I)	Read Req	Write Req		/ state							
Shared (S)	hit	Write Req	InvAck / I		store / state (M)	/ state					
Exclusive (E)	hit	hit / M					Null WB / state	Null WB / E			Set Tag + Data, Null WB / state
Modified (M)	hit	hit					WB / state	WB / M			Set Tag + Data, WB / state
Forward (F)	hit	Write Req			store / state (M)	/ state			Set Tag + Data / F	Set Tag + Data / state	

MOSI - Cache Controller / LCE

	Core Action		CCE Action								
State	Load	Store	Invalidate	Set Tag + Data	Set State + Wakeup	Set State	Set State + Writeback	Writeback	Transfer	Set State + Transfer	Set State + Transfer + Writeback
Invalid (I)	Read Req	Write Req		do load or store / state							
Shared (S)	hit	Write Req	InvAck / I		store / state (M)	/ state					
Modified (M)	hit	hit					WB / state	WB / M			Set Tag + Data, WB / state
Owned (O)	hit	Write Req			store / state (M)		WB / state	WB / O	Set Tag + Data / O	Set Tag + Data / state	

MOSIF - Cache Controller / LCE

	Core Action		CCE Action								
State	Load	Store	Invalidate	Set Tag + Data	Set State + Wakeup		Set State + Writeback	Writeback	Transfer	Set State + Transfer	Set State + Transfer + Writeback
				do load or store							
Invalid (I)	Read Req	Write Req		/ state							
Shared (S)	hit	Write Req	InvAck / I		store / state (M)	/ state					
Modified (M)	hit	hit					WB / state	WB/M		Set Tag + Data / state	Set Tag + Data, WB / state
Owned (O)	hit	Write Req			store / state (M)		WB / state	WB / O	Set Tag + Data / O	Set Tag + Data / state	
Forward (F)	hit	Write Req			store / state (M)	/ state			Set Tag + Data / F	Set Tag + Data / state	

MOESI - Cache Controller / LCE

	Core Action		CCE Action								
State	Load	Store	Invalidate	Set Tag + Data	Set State + Wakeup		Set State + Writeback	Writeback	Transfer	Set State +	Set State + Transfer + Writeback
Invalid (I)	Read Req	Write Req		do load or store / state							
Shared (S)	hit	Write Req	InvAck / I		store / state (M)	/ state					
Exclusive (E)	hit	hit / M					Null WB / state	Null WB / E		Set Tag + Data / state	Set Tag + Data, Null WB / state
Modified (M)	hit	hit					WB / state	WB / M		Set Tag + Data / state	Set Tag + Data, WB / state
Owned (O)	hit	Write Req			store / state (M)		WB / state	WB / O	Set Tag + Data / O	Set Tag + Data / state	