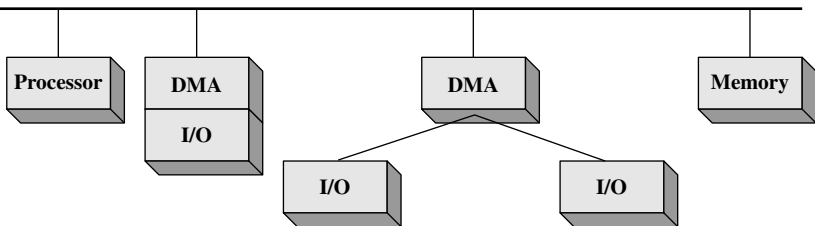
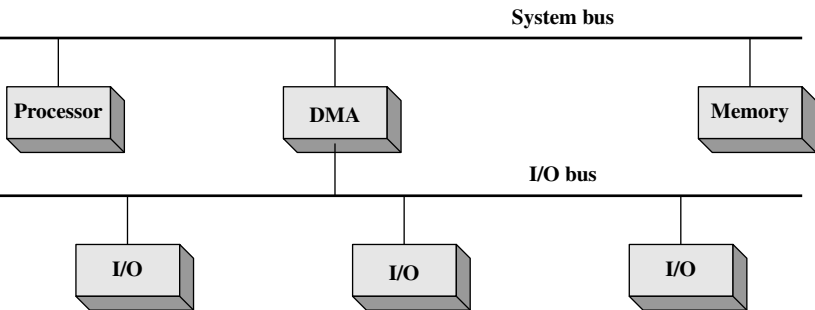


(a) Single-bus, detached DMA



(b) Single-bus, integrated DMA-I/O



(c) I/O bus

**Figure 7.13** Alternative DMA Configurations