-	
*	Pagino Man-Continuous Memori
	Pagine Mon-Contiguous Memory Allocation
	price and ment and beautiful and the first of the first
	To programa the "I am at Button of for
	To overcome the issue of External fragmen
	Ways Ways Thom - contiquous meriory auscolo
	1-400 A Of doing this is laging and
	We use Non-Contiguous memory allocations two hours doing this is "Paging" and
	Control of the Party of the Control
	Paging In Break the memory and
	pooplers 1170 fixed-Sized blocks
THE PROPERTY OF THE PARTY OF TH	
	royscial Merosory -> (RAM) -> divide into fram
	Logical Memory (Proces) - ) divide into Pagel
The state of the s	of mathematical of mathematical of the mathema
The state of the s	Physical Memory -> (RAM) -> divide into fram Logical Memory (pooces) -> divide into Pagel "Page Size = frame Size"
00 AC	Now pages can stored anywhere in the
	framel of RAM downer on him to
	the state of the s
	example 1- Page/Frame Size 1/8/
1977	Example 1- Pagel Frame Size 1 kBilled into A process of 4 kBoo will be divided into 4 pages 12 por proper PS.
The second	THE PAGE MENT POR PIPE PZ DRZO TEND MANDE
001,6	remframes: P30LFAII F10/1/21(3)
	over an be allocated al
	original standing the
	$PI \rightarrow PA$
	P2 > F10
	P3 >> F12

	Page No.: Date:
	Even though RAM Spaces are scattered, the process rum fines because OS knows Where each page is store Via Page toubl
	the process run fines because Os knows
as a	Where each page is store Via Page toubl
	The second secon
	Page Table! :-
0	Et is a type of Data Structure maintain
100	by OS.
2	It maple Page Number -> Frame Number
Mater	and and the second of the seco
•	Address Translation/:- 1001 - hold
Acij	Each logical address generated by CPU is
- 8 . e	divided into
- 12	(1) Page number (p) - Index into page take.
	(E) Page offset (d) ) Exact location inside
197	the page, mache (2)
	Physical address => Frame Address + Offsot.
403	As no but and a to transact doct
	I I ssue in Paging tead and somothibe
	Each memory access needs to:-
	(1) Read the page table mount answer
	Diner accell the memory
	Solution !- Shirt har en unit !.
1.001.6	1
	TLB (Translation boxaside Buffer) 9
	Small fast cache hardware, stories the recent page -> frame roappings.
41.4.1.	Joanne comprises
	Page counting anabbigation TUBLATUBLICH
	it directly gets the frame number.
	John Marie Control of the Control of
<b>E</b>	If not (TIB miss). it checks Page Table and
	If not (TUB miss), it checks Page Table and updates TLB for next time.