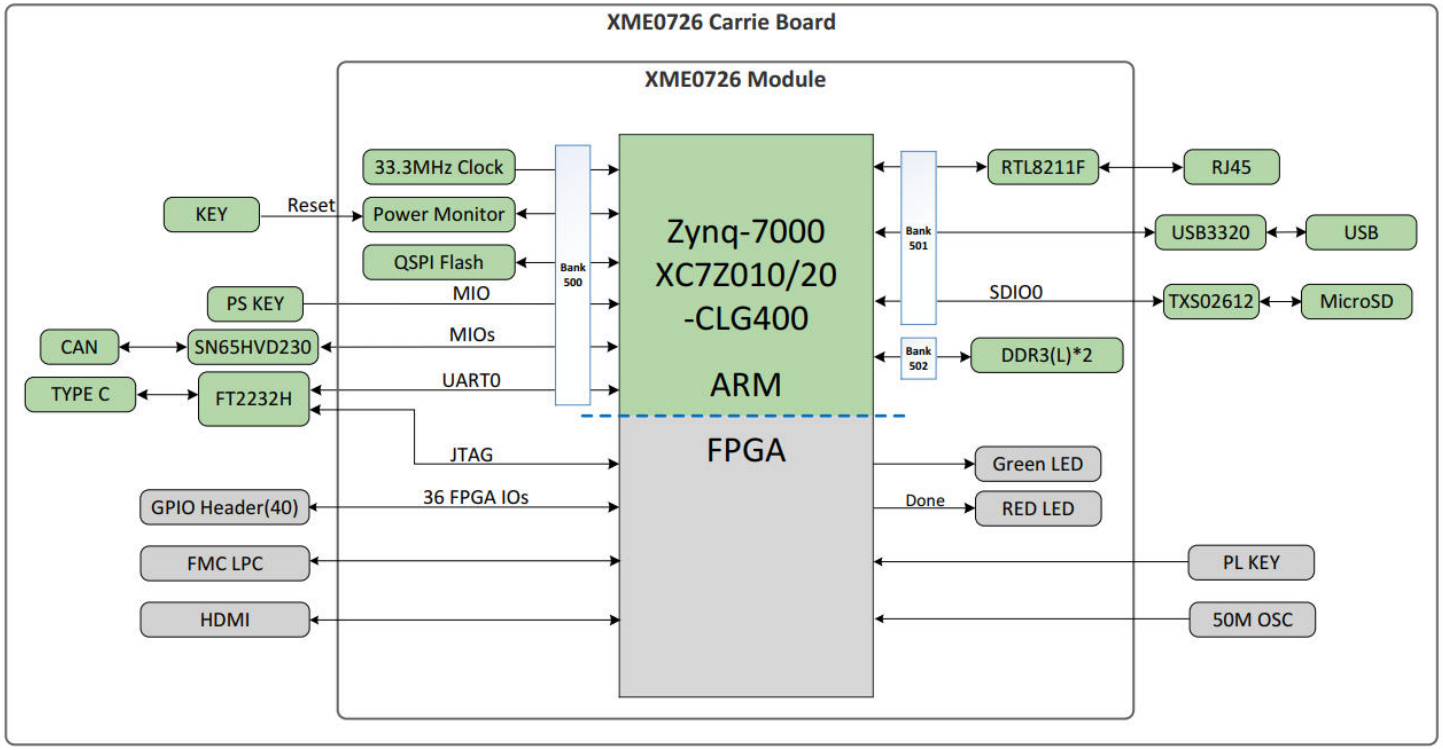
XME0726

REV	DATE	PAGES	DESCRIPTION
1.0	15/09/2023	All	Rev 1.0 Release



PAGE	DESCRIPTION
1	Title, Notes, Block Diagram, Revision History
2	POWER
3	FPGA_POWER
4	FPGA_CONFIG
5	FPGA_IO_PL
6	FPGA_IO_PS
7	FPGA_DDR
8	DDR3
9	eMMC
10	GPHY
11	CONNECTOR
12	
13	
14	
15	
16	
17	
18	
19	
20	
21	
22	
23	
24	
25	
26	
27	
28	
29	

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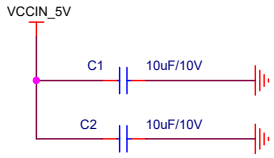
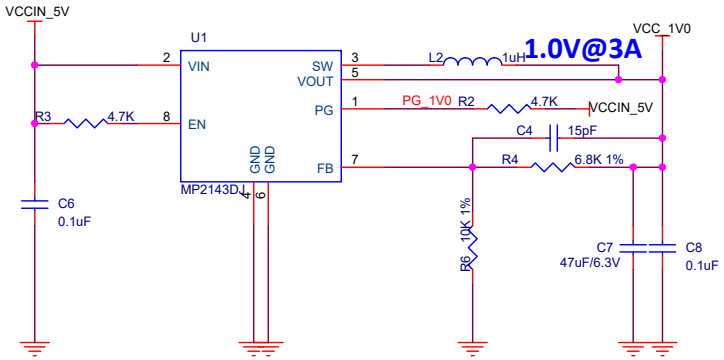
Title: XME0726

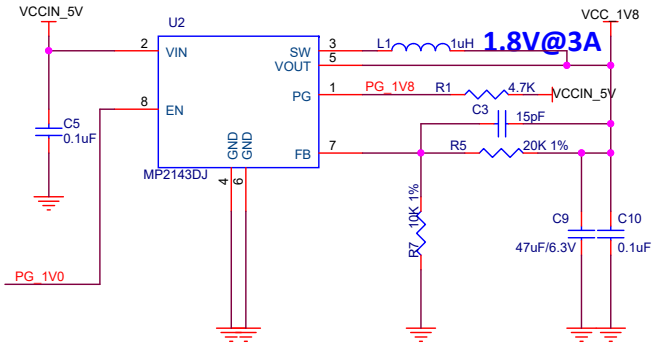
Size: B | Document Number: Block Diagram | Rev: 1.0

Date: Monday, December 25, 2023 | Sheet 1 of 10

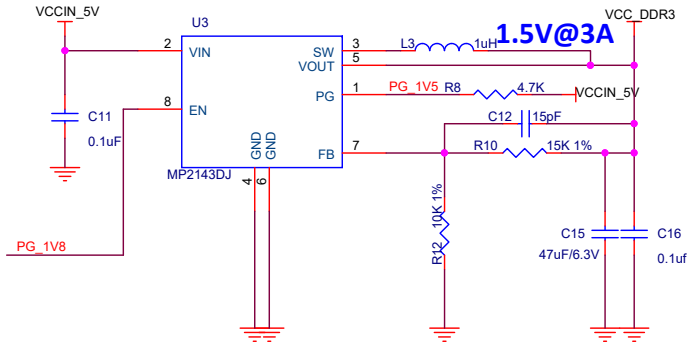




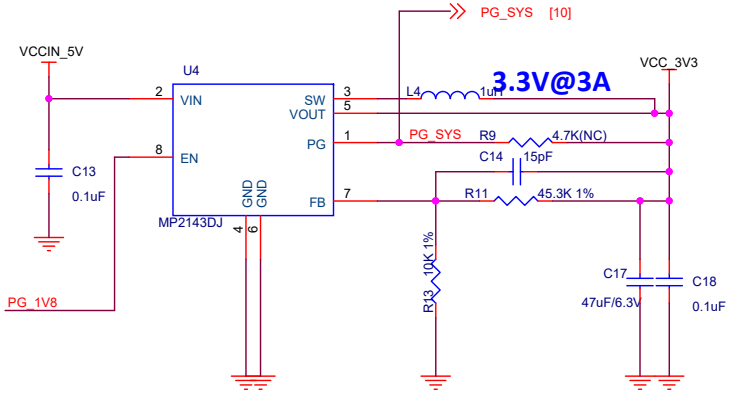
Vref = 0.6V VOUT = 1.0V



Vref = 0.6V VOUT = 1.8V



Vref = 0.6V VOUT = 1.5V



Vref = 0.6V VOUT = 3.3V

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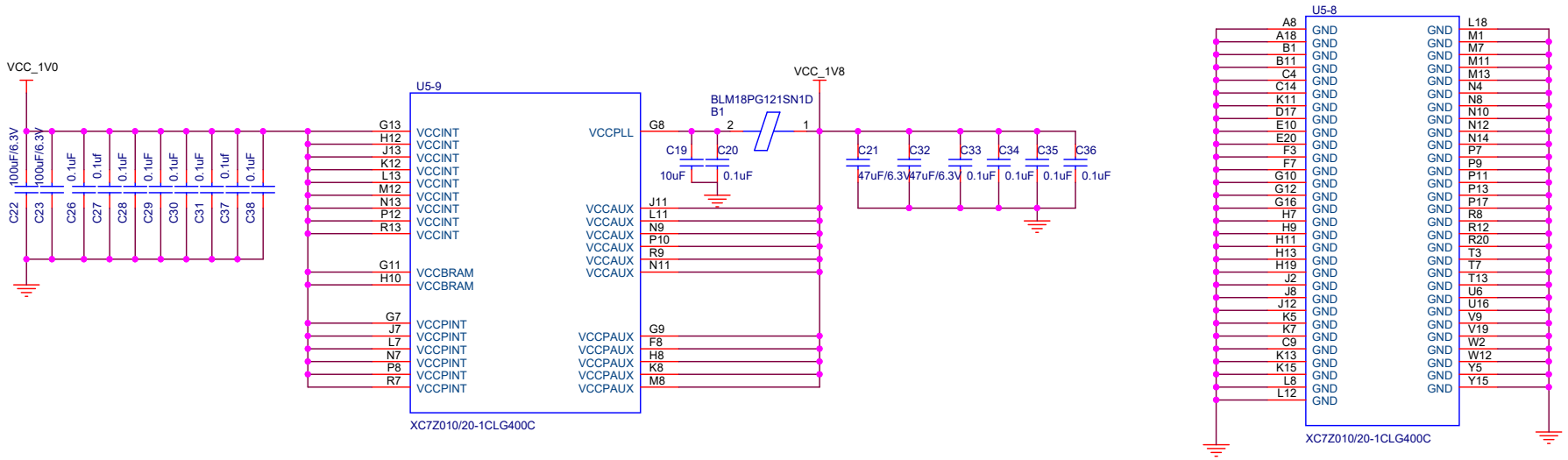
Title: XME0726		
Size: B	Document Number: Power	Rev: 1.0
Date: Monday, December 25, 2023	Sheet 2 of 10	



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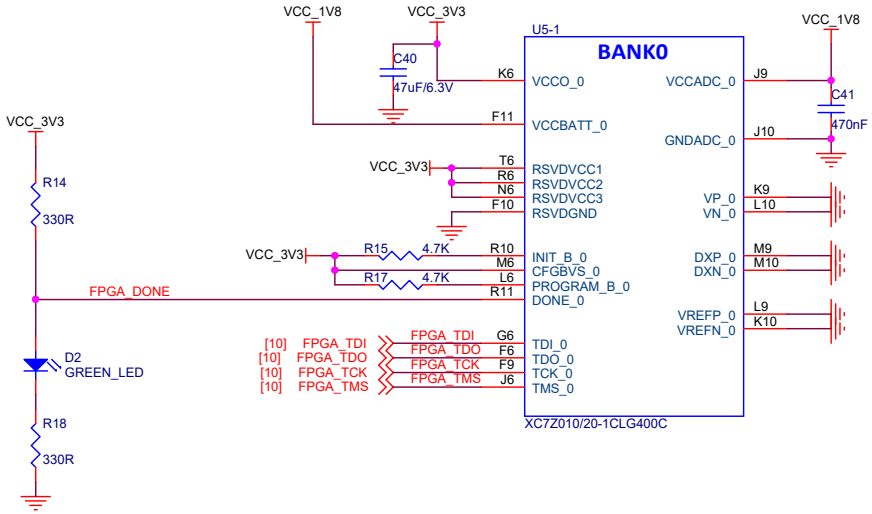
Title

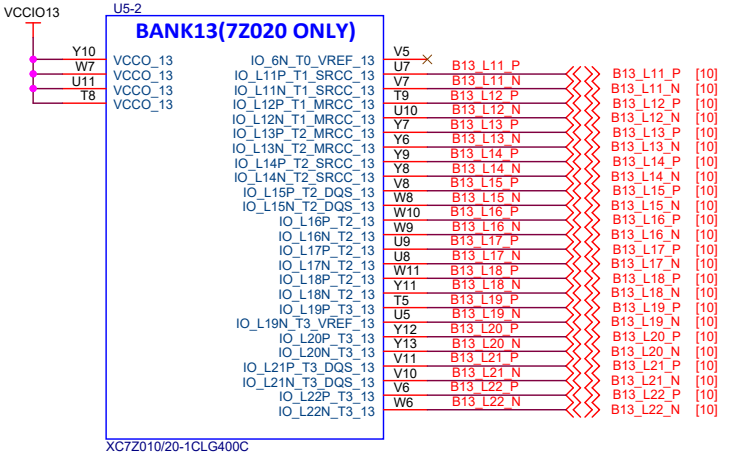
XME0726

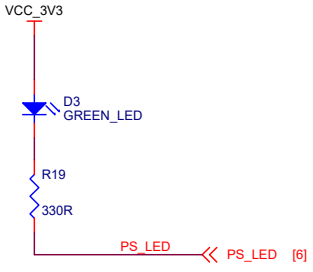
Size	Document Number	Rev
B	FPGA Power	1.0

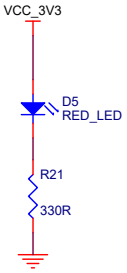
Date: Monday, December 25, 2023 Sheet 3 of 10

ZYNQ_CONFIG









Four LEDs:
1. **Power LED: Red**
2. **DONE LED: Green**
3. **PS LED: Green**

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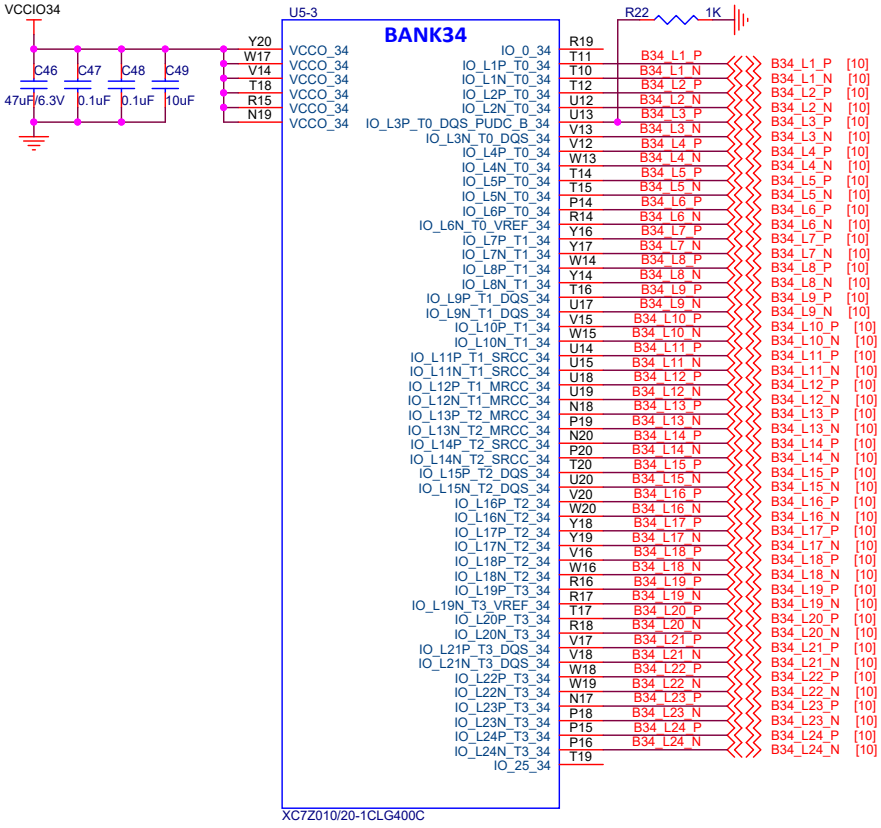
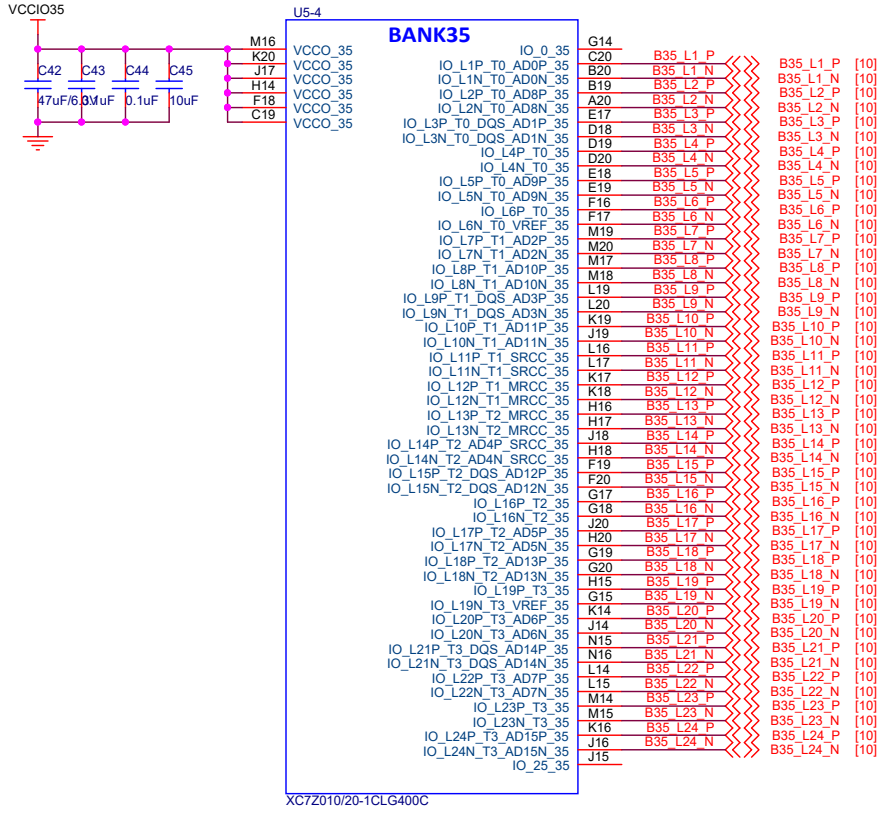
Title: XME0726		
Size: B	Document Number: BANK0&13	Rev: 1.0
Date: Monday, December 25, 2023	Sheet 4 of 10	

FPGA_PL





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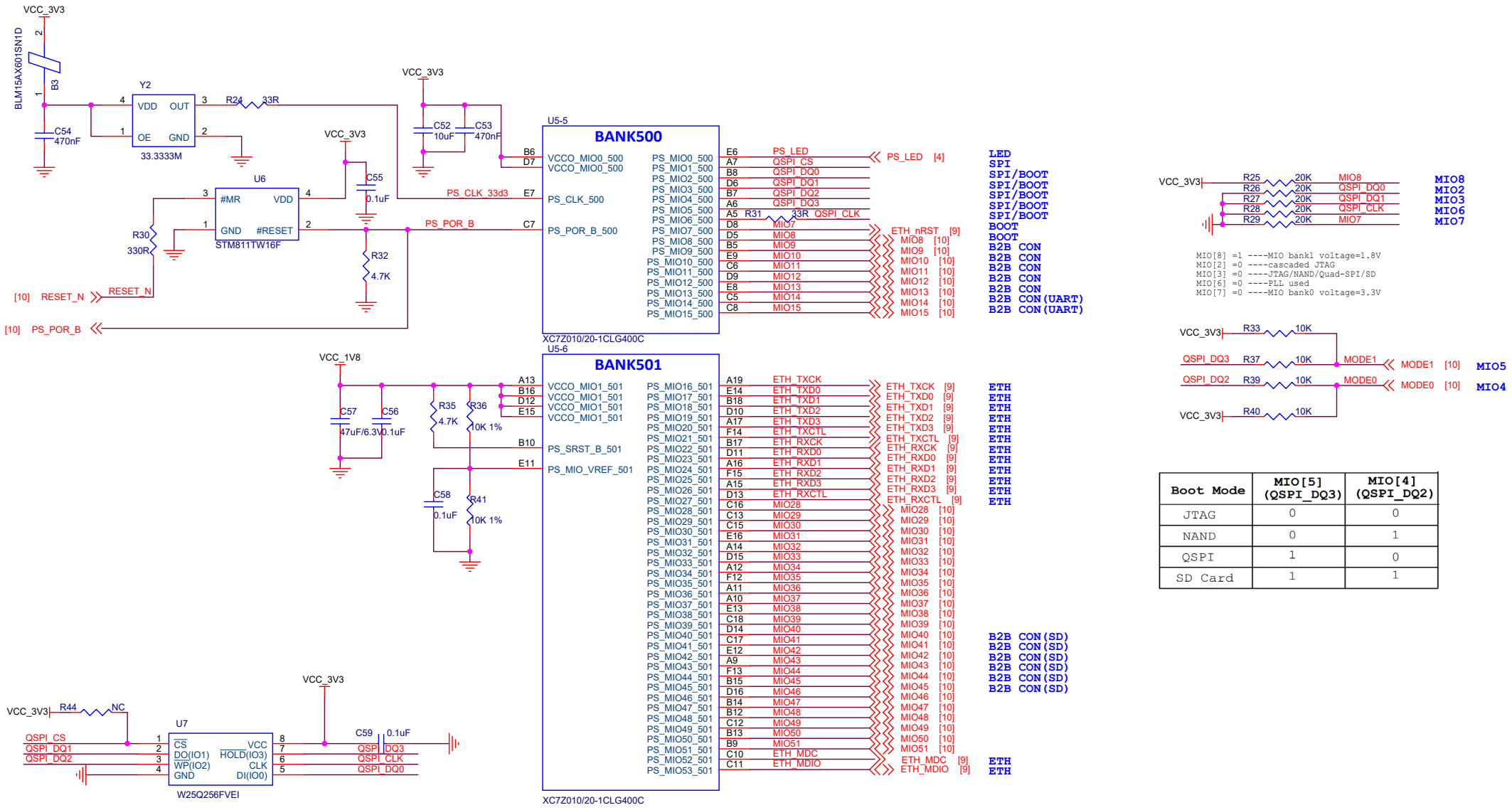
Title: XME0726

Size	Document Number	Rev
Custom	BANK34&35	1.0

Date: Monday, December 25, 2023 Sheet 5 of 10



Boot Mode	MIO[5] (QSPI_DQ3)	MIO[4] (QSPI_DQ2)
JTAG	0	0
NAND	0	1
QSPI	1	0
SD Card	1	1

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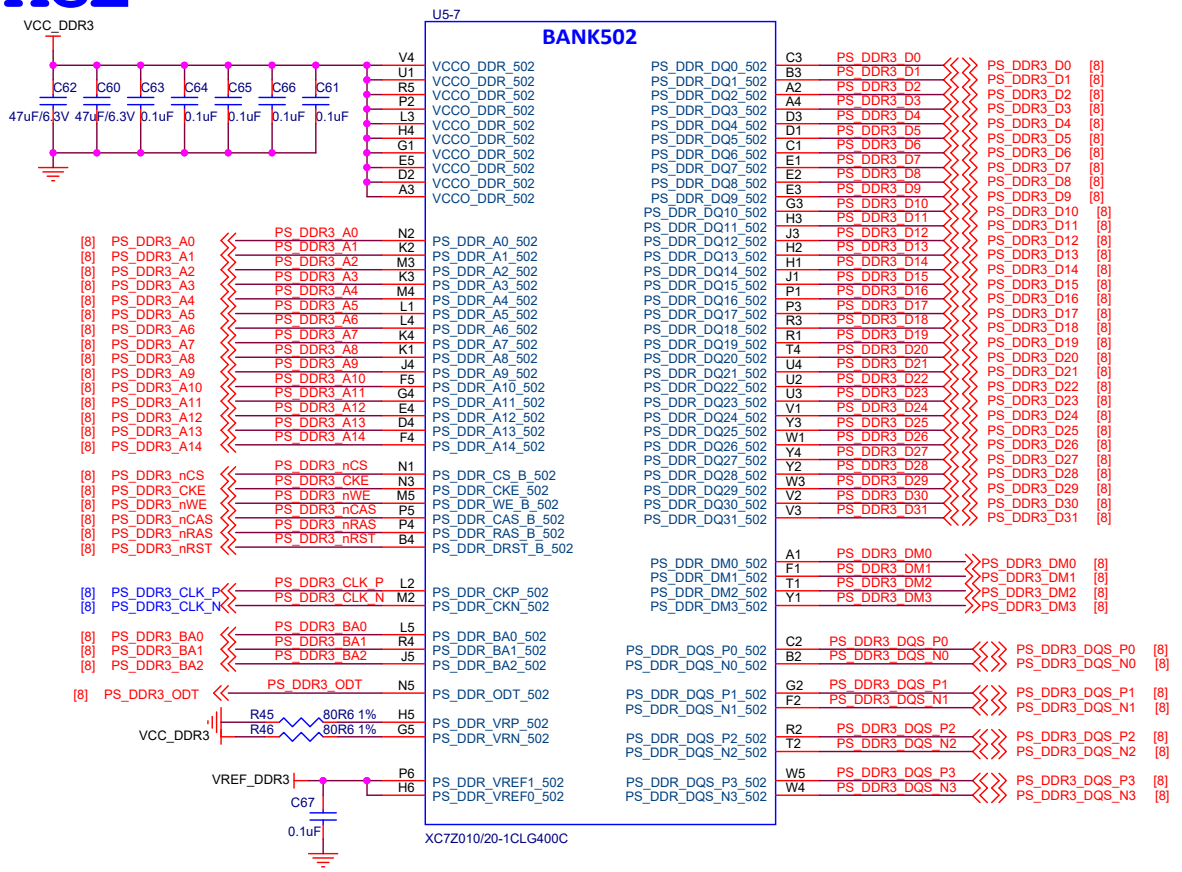
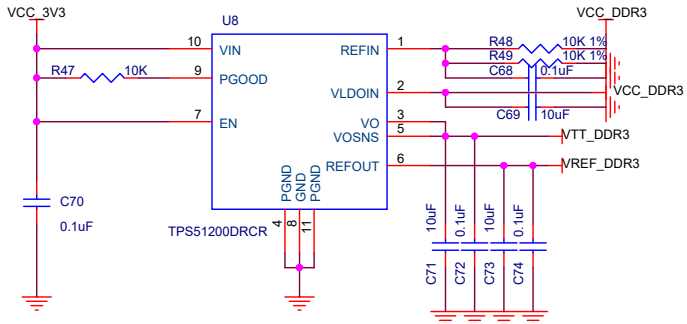
Title: XME0726

Size	Document Number	Rev
B	BANK500&501	1.0

Date: Monday, December 25, 2023 Sheet 6 of 10

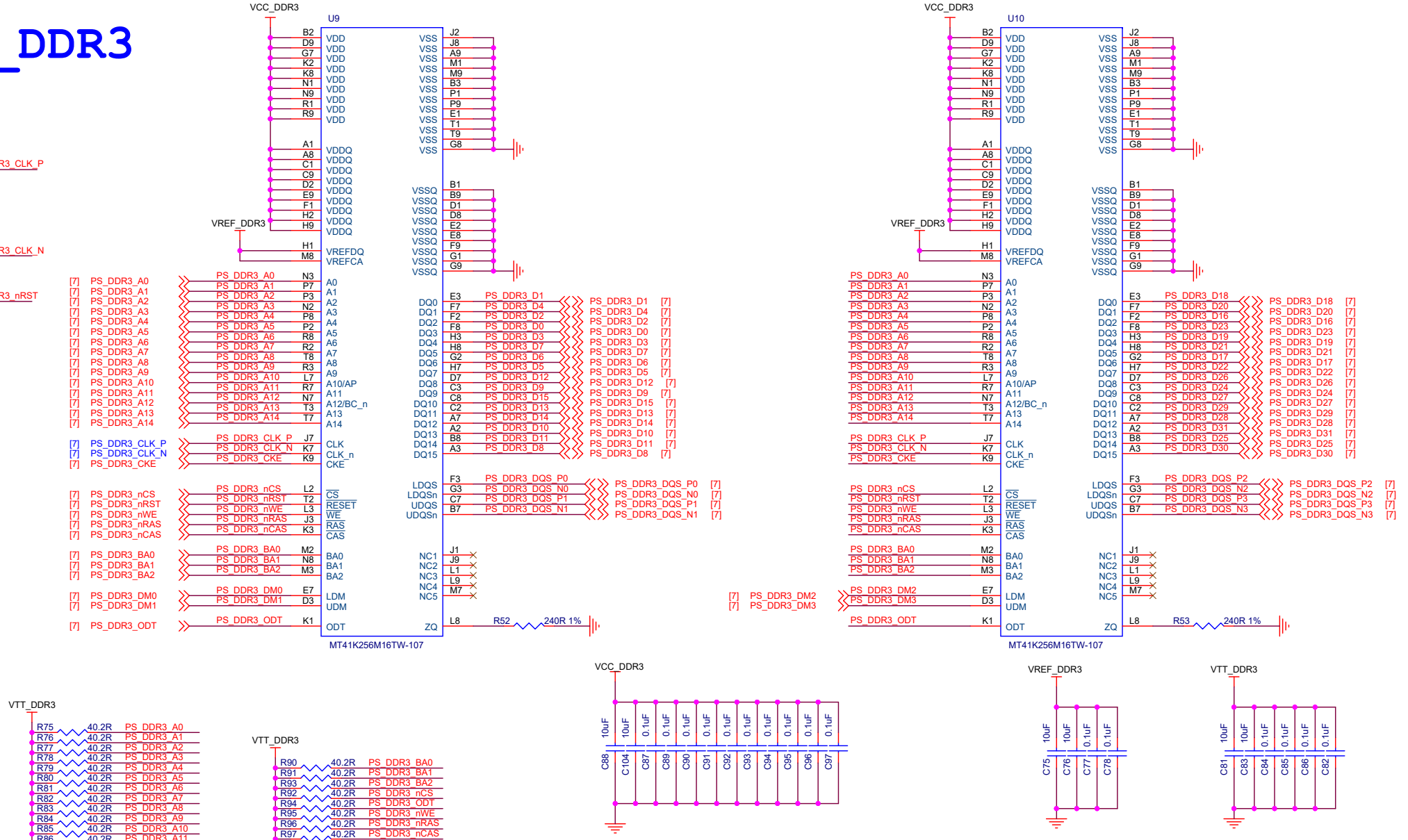
DDR_INTERFACE



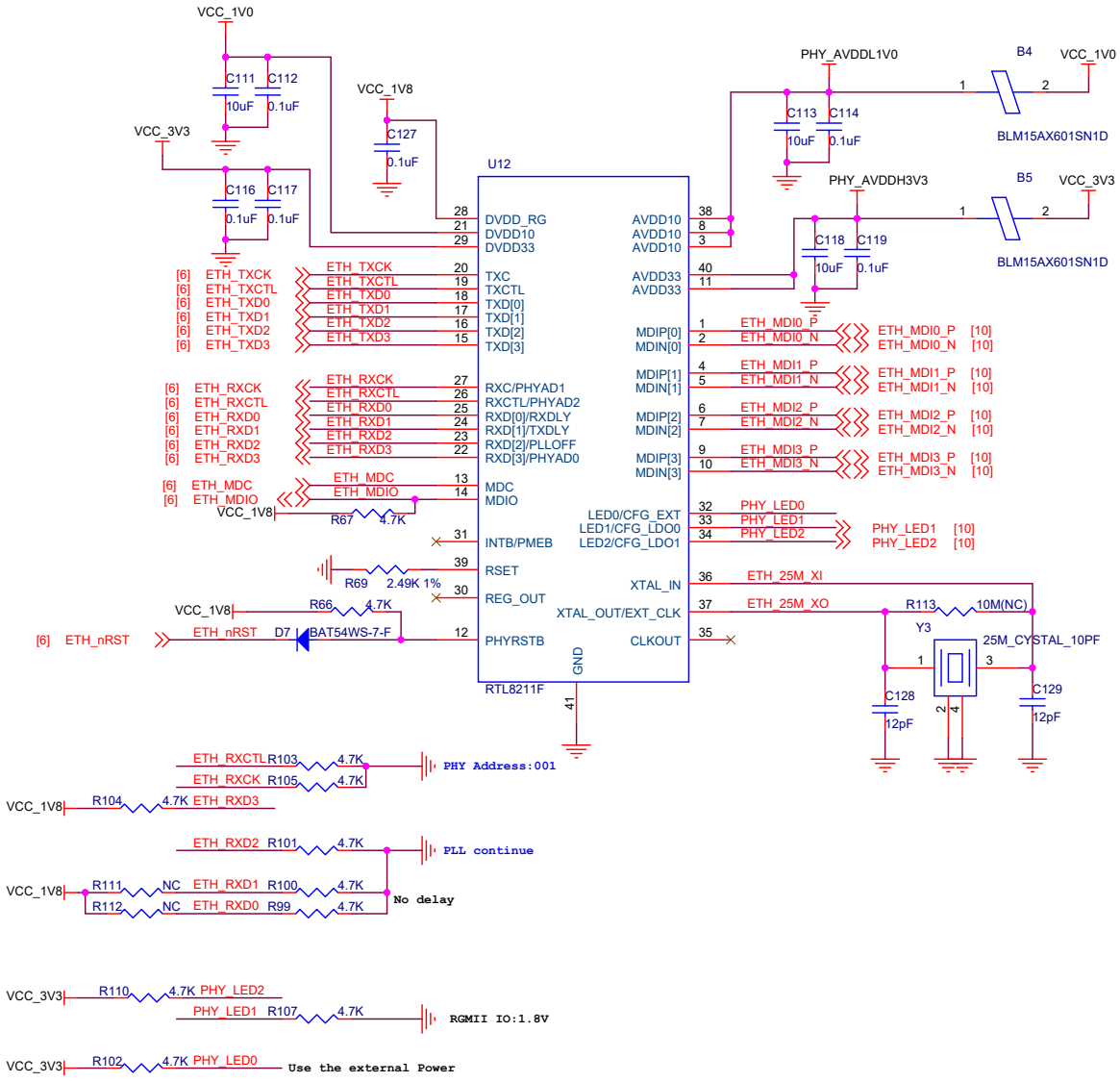


MicroPhase	MicroPhase Inc. www.microphase.cn		
Title	XME0726		
Size B	Document Number BANK502 DDR		Rev 1.0
Date: Monday, December 25, 2023	Sheet 7 of 10		

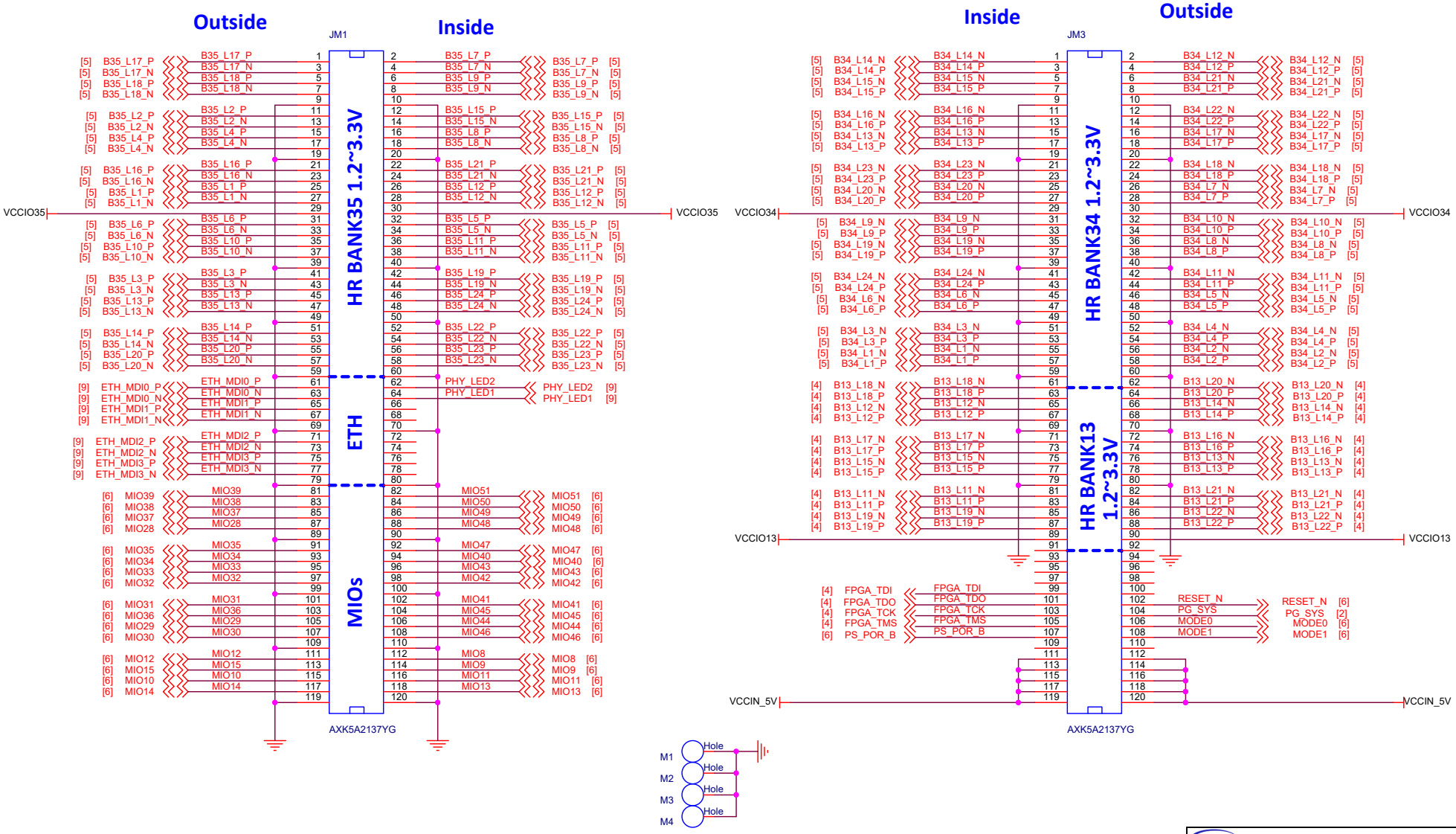
PS_DDR3



MicroPhase	MicroPhase Inc. www.microphase.cn		
Title	XME0726		
Size B	Document Number DDR3		Rev 1.0
Date: Monday, December 25, 2023	Sheet 8 of 10		



MicroPhase	MicroPhase Inc. www.microphase.cn	
Title	XME0726	
Size B	Document Number ETH	Rev 1.0
Date: Monday, December 25, 2023	Sheet 9 of 10	



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Title: XME0726

Size	Document Number	Rev
B	Connector	1.0

Date: Monday, December 25, 2023 Sheet 10 of 10