XME7100

REV	DATE	PAGES	DESCRIPTION
1.0	15/03/2020	All	Rev 1.0 Release

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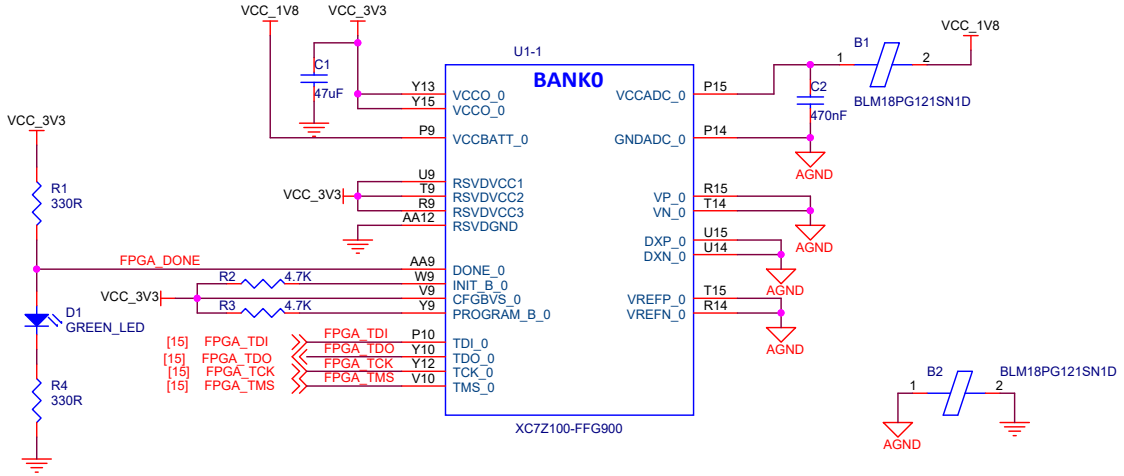

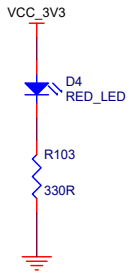

PWR LED

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Title: XME7100		
Size: B	Document Number: ZYNQ BANK0	Rev: 1.1
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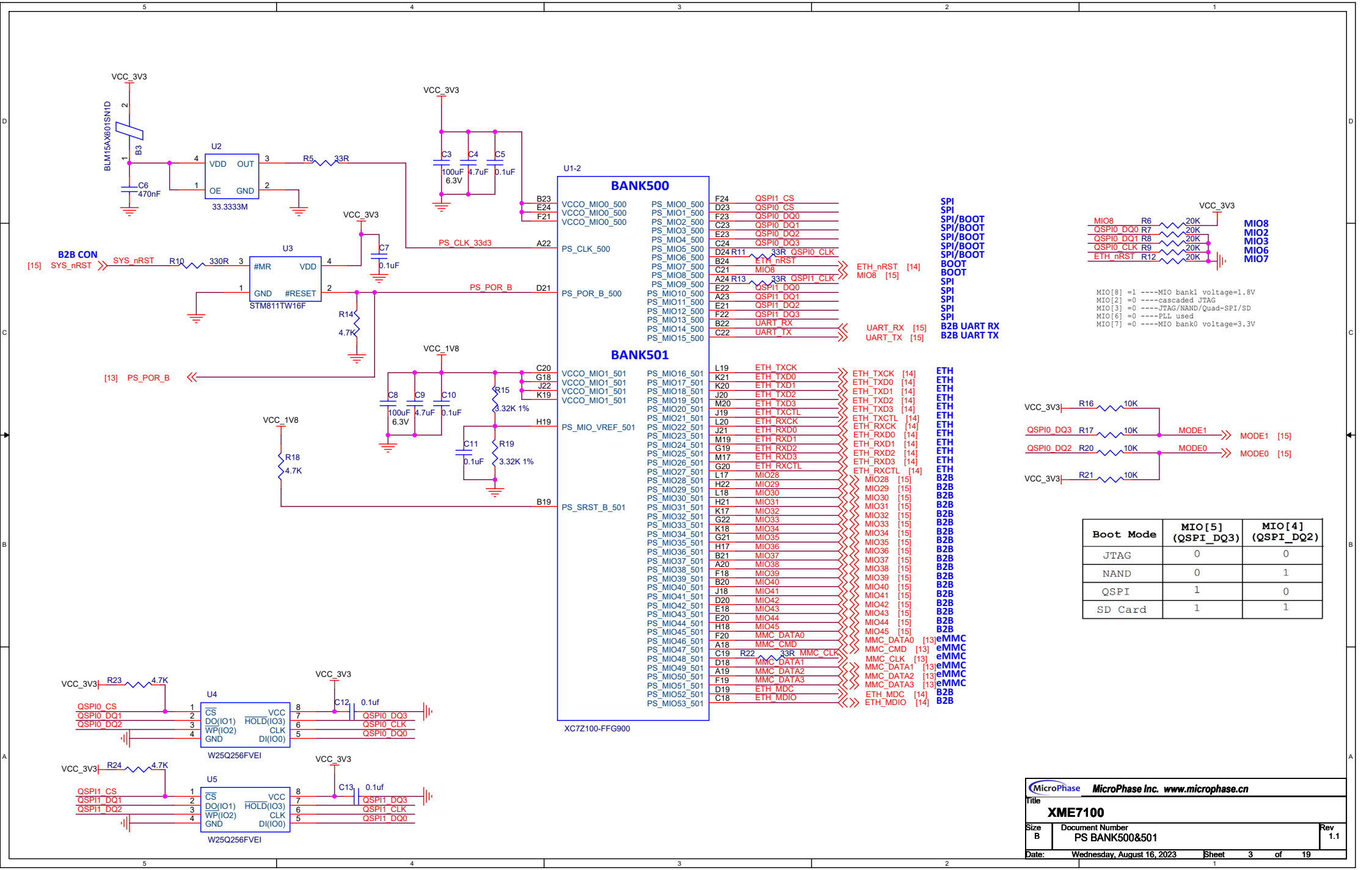
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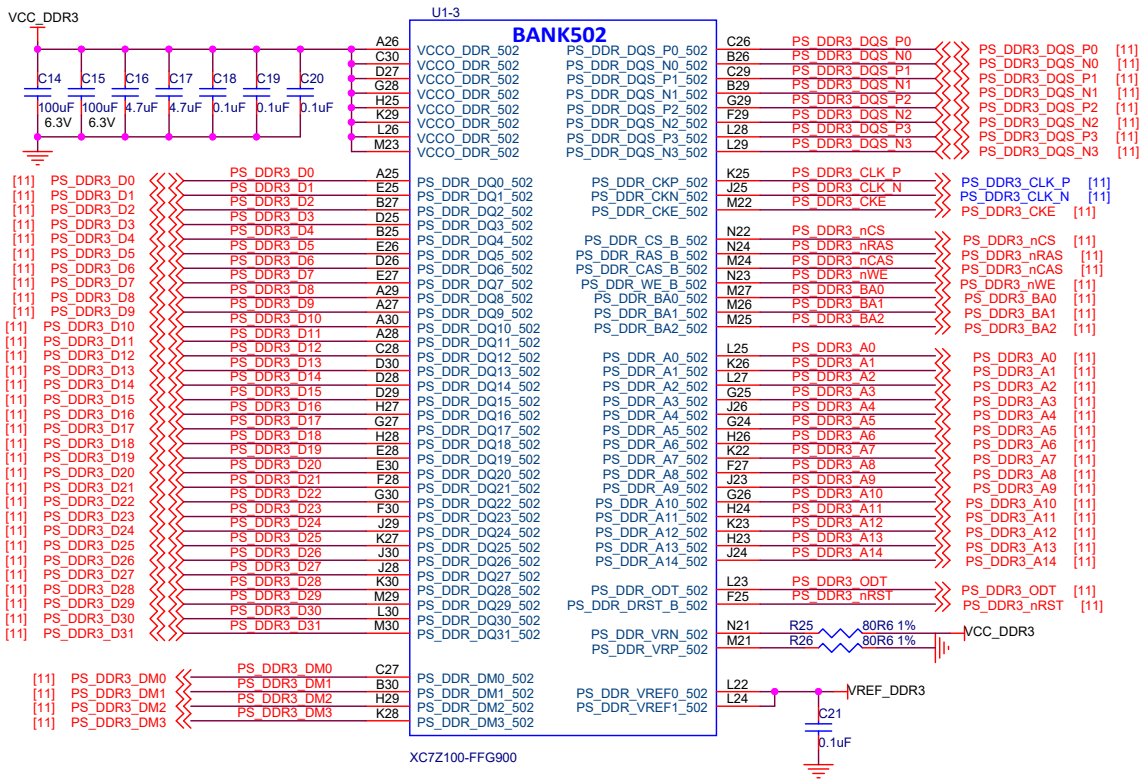
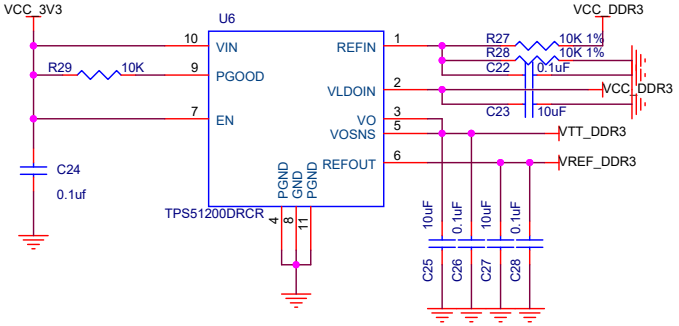
BLM15AX601SN1D
B3
U2
VDD
OUT
OE
GND
33.3333M
C6
470nF
R5
33R
C3
C4
C5
100uF
6.3V
4.7uF
0.1uF
U1-2
BANK500
VCCO_MIO0_500
VCCO_MIO0_500
VCCO_MIO0_500
B23
E24
F21
PS_CLK_33d3
A22
PS_CLK_500
PS_POR_B
D21
PS_POR_B_500
B2B CON
[15] SYS_nRST
SYS_nRST
R10
330R
U3
#MR
VDD
GND
#RESET
STM811TW16F
C7
0.1uF
R14
4.7K
[13] PS_POR_B
VCC_1V8
C8
C9
C10
100uF
6.3V
4.7uF
0.1uF
R15
3.32K 1%
C11
0.1uF
R19
3.32K 1%
R18
4.7K
C20
G18
J22
K19
H19
PS_MIO_VREF_501
B19
PS_SRST_B_501
BANK501
XC7Z100-FFG900
F24 QSPI1_CS
D23 QSPI0_CS
F23 QSPI0_DQ0
C23 QSPI0_DQ1
E23 QSPI0_DQ2
C24 QSPI0_DQ3
D24 R11 33R QSPI0_CLK
B24 ETH_nRST
C21 MIO8
A24 R13 33R QSPI1_CLK
E22 QSPI1_DQ0
A23 QSPI1_DQ1
E21 QSPI1_DQ2
F22 QSPI1_DQ3
B22 UART_RX
C22 UART_TX
ETH_nRST [14]
MIO8 [15]
UART_RX [15]
UART_TX [15]
SPI
SPI
SPI/BOOT
SPI/BOOT
SPI/BOOT
SPI/BOOT
SPI/BOOT
BOOT
BOOT
SPI
SPI
SPI
SPI
SPI
B2B UART RX
B2B UART TX
L19 ETH_TXCK
K21 ETH_TXD0
K20 ETH_TXD1
J20 ETH_TXD2
M20 ETH_TXD3
J19 ETH_TXCTL
L20 ETH_RXCK
J21 ETH_RXD0
M19 ETH_RXD1
G19 ETH_RXD2
M17 ETH_RXD3
G20 ETH_RXCTL
ETH_TXCK [14]
ETH_TXD0 [14]
ETH_TXD1 [14]
ETH_TXD2 [14]
ETH_TXD3 [14]
ETH_TXCTL [14]
ETH_RXCK [14]
ETH_RXD0 [14]
ETH_RXD1 [14]
ETH_RXD2 [14]
ETH_RXD3 [14]
ETH_RXCTL [14]
ETH
ETH
ETH
ETH
ETH
ETH
ETH
ETH
ETH
ETH
ETH
ETH
L17 MIO28
H22 MIO29
L18 MIO30
H21 MIO31
K17 MIO32
G22 MIO33
K18 MIO34
G21 MIO35
H17 MIO36
B21 MIO37
A20 MIO38
F18 MIO39
B20 MIO40
J18 MIO41
D20 MIO42
E18 MIO43
E20 MIO44
H18 MIO45
F20 MMC_DATA0
A18 MMC_CMD
C19 R22 33R MMC_CLK
D18 MMC_DATA1
A19 MMC_DATA2
F19 MMC_DATA3
D19 ETH_MDC
C18 ETH_MDIO
MIO28 [15]
MIO29 [15]
MIO30 [15]
MIO31 [15]
MIO32 [15]
MIO33 [15]
MIO34 [15]
MIO35 [15]
MIO36 [15]
MIO37 [15]
MIO38 [15]
MIO39 [15]
MIO40 [15]
MIO41 [15]
MIO42 [15]
MIO43 [15]
MIO44 [15]
MIO45 [15]
MMC_DATA0 [13]
MMC_CMD [13]
MMC_CLK [13]
MMC_DATA1 [13]
MMC_DATA2 [13]
MMC_DATA3 [13]
ETH_MDC [14]
ETH_MDIO [14]
B2B
B2B
B2B





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Size	Document Number	Rev
B	PS BANK502	1.1

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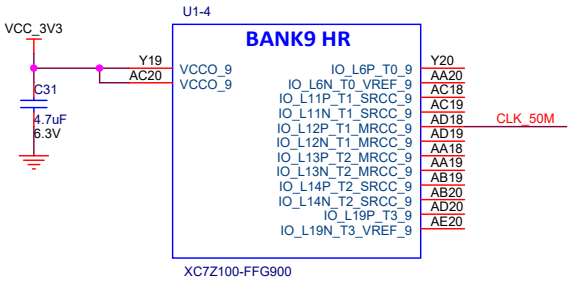

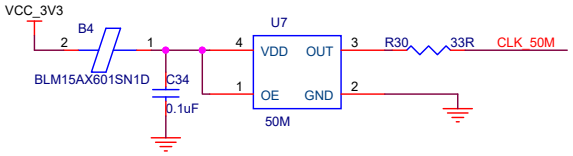

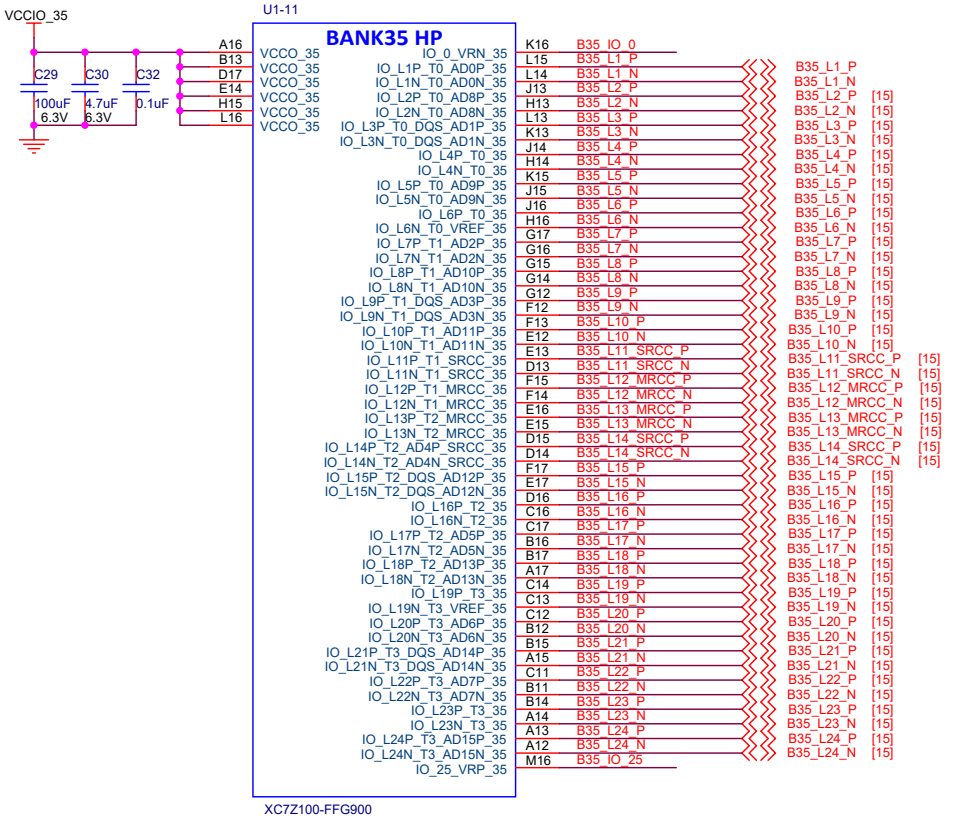

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Title: XME7100

Size	Document Number	Rev
B	PL BANK9&35	1.1

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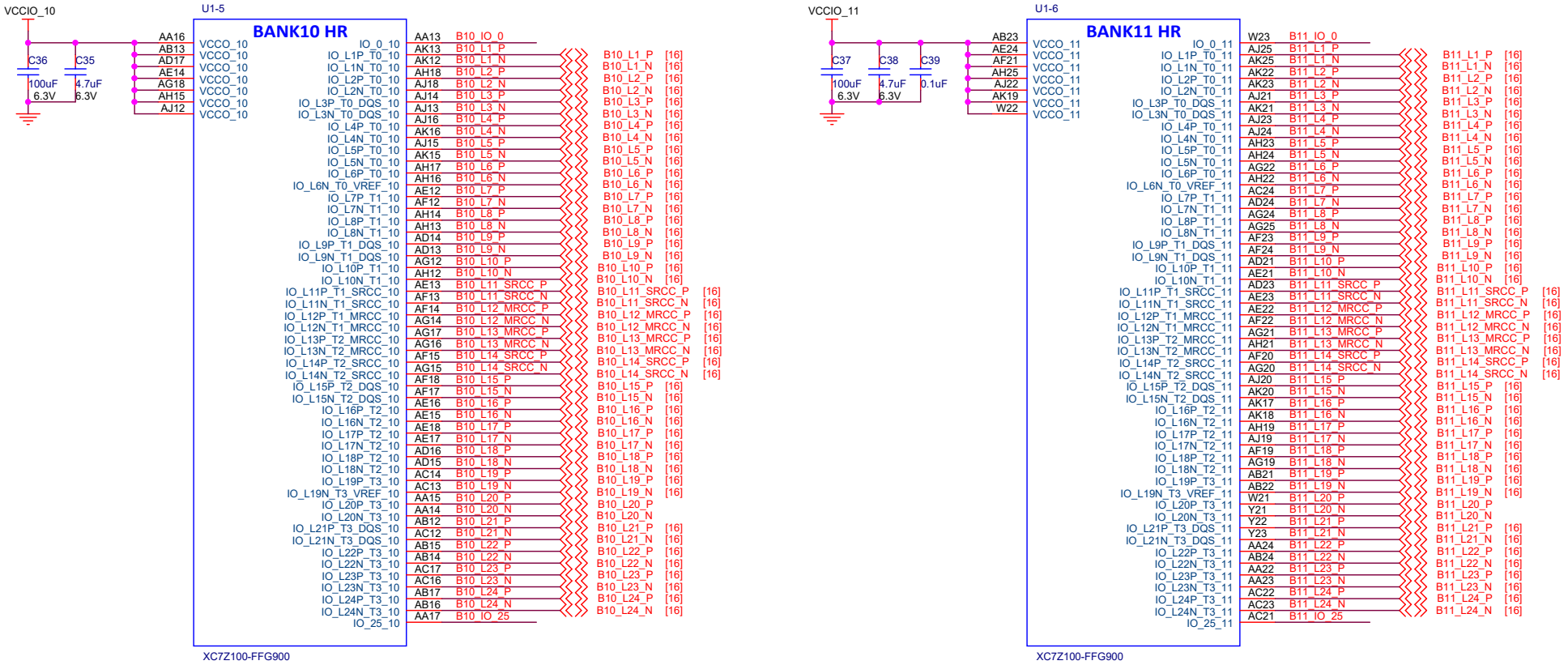

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Title: XME7100

Size	Document Number	Rev
B	PL BANK10&11	1.1

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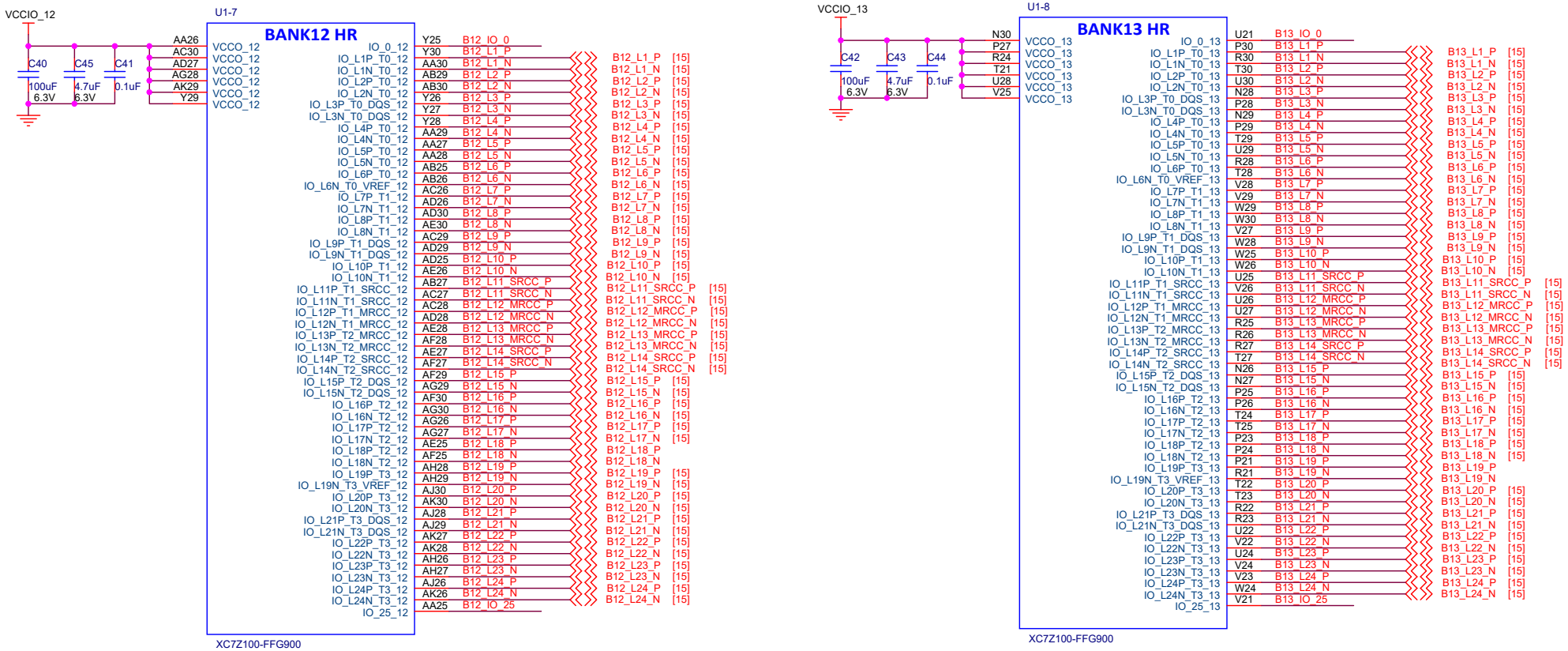

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Title: XME7100

Size	Document Number	Rev
B	PL BANK12&13	1.1

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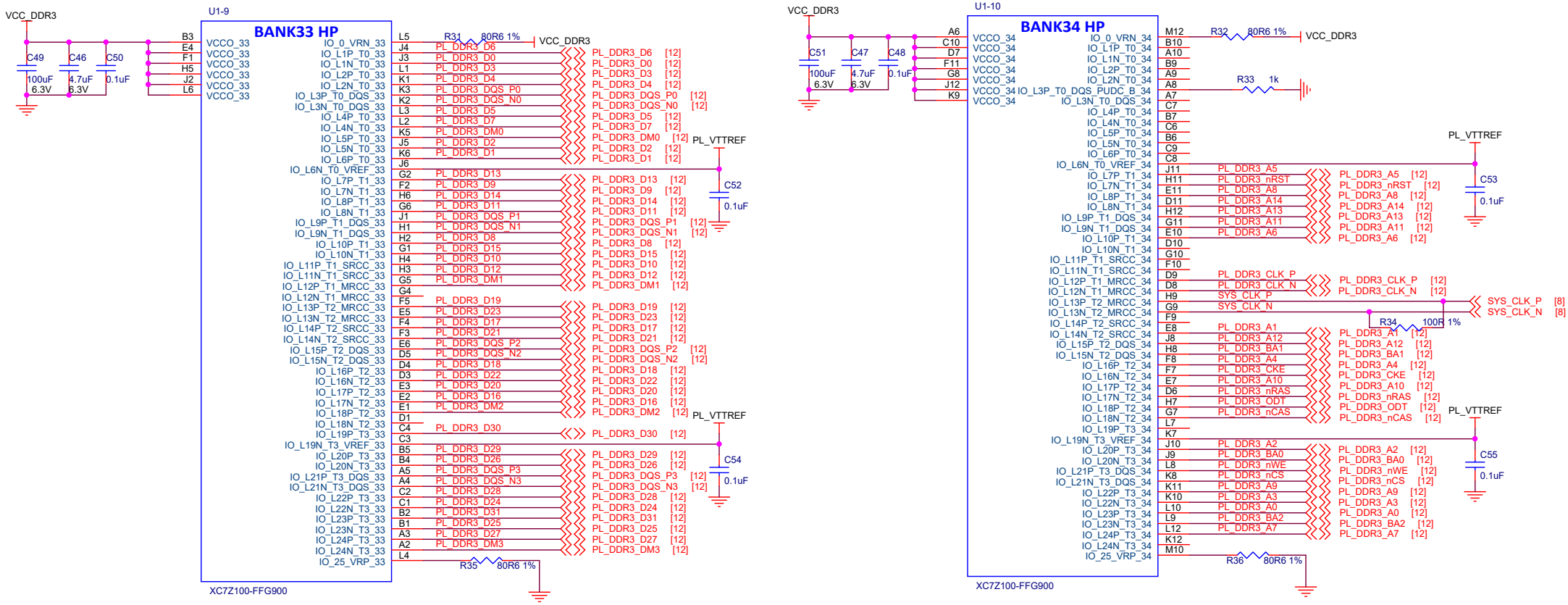

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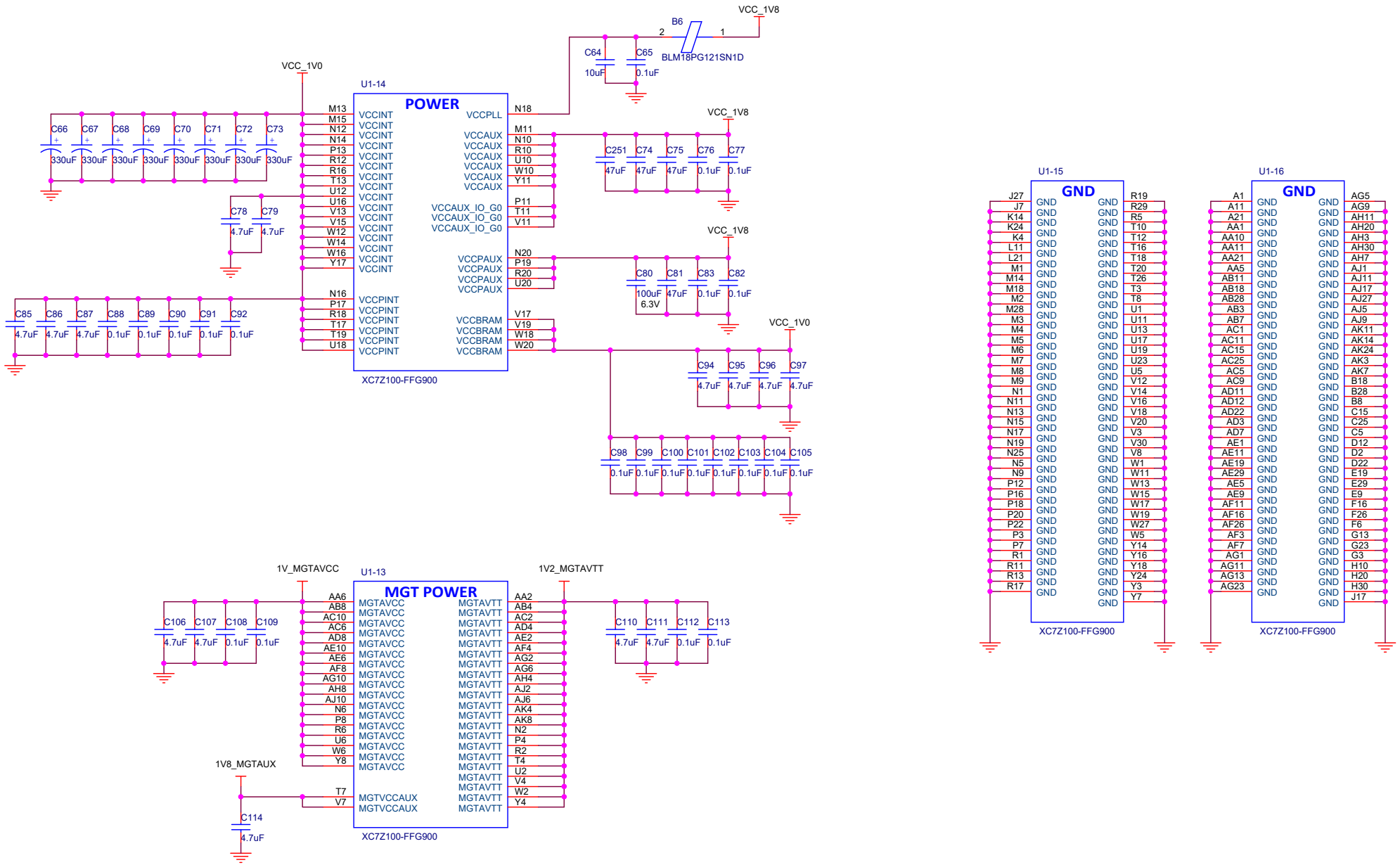

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Title	XME7100		
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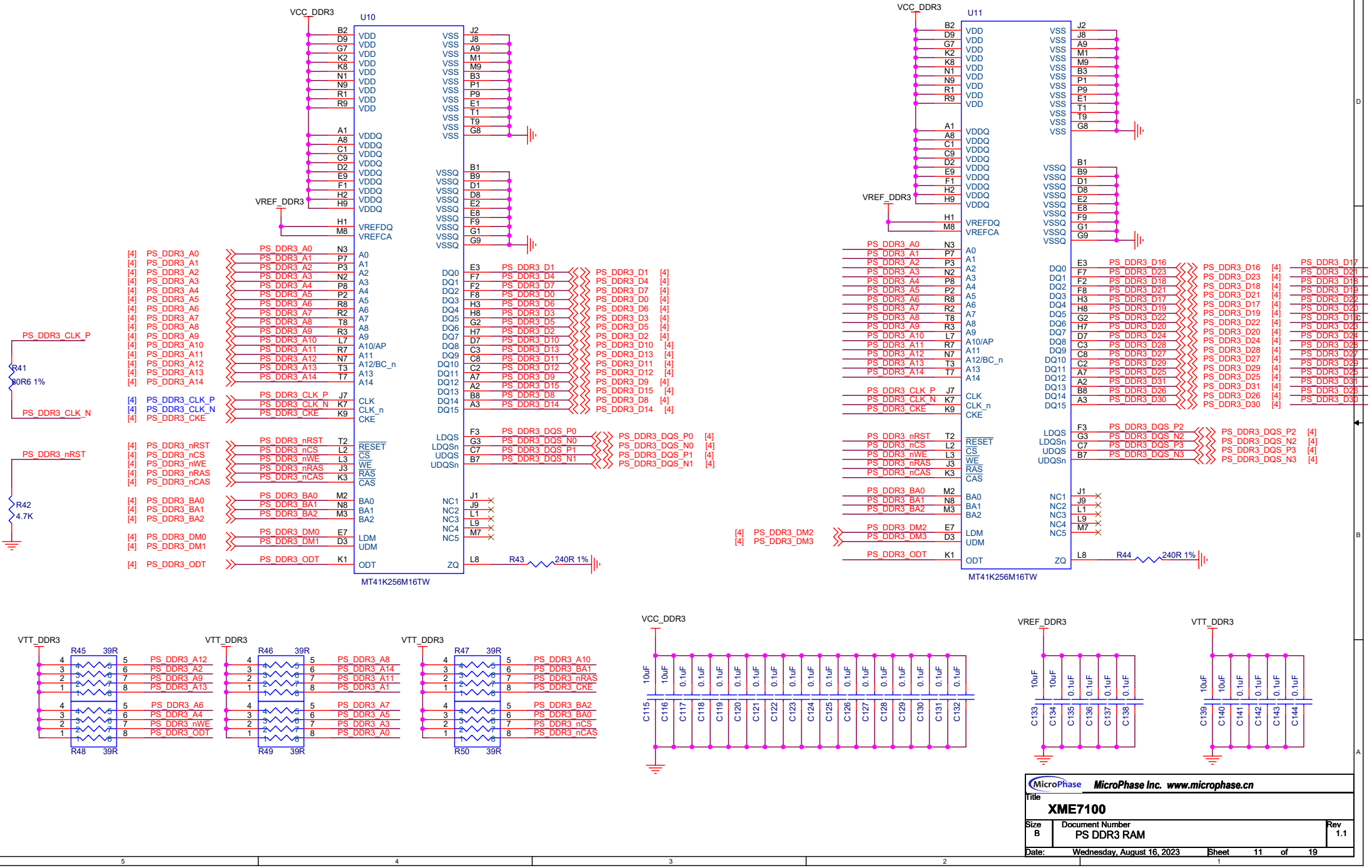
VCC_DDR3
U10
VREF_DDR3
MT41K256M16TW
PS_DDR3_CLK_P
PS_DDR3_CLK_N
R41
80R6 1%
PS_DDR3_nRST
R42
4.7K
VTT_DDR3
R45 39R
R46 39R
R47 39R
R48 39R
R49 39R
R50 39R
U11
MT41K256M16TW
R43 240R 1%
R44 240R 1%
MicroPhase
MicroPhase Inc. www.microphase.cn
Title
XME7100
Size B
Document Number
PS DDR3 RAM
Rev 1.1
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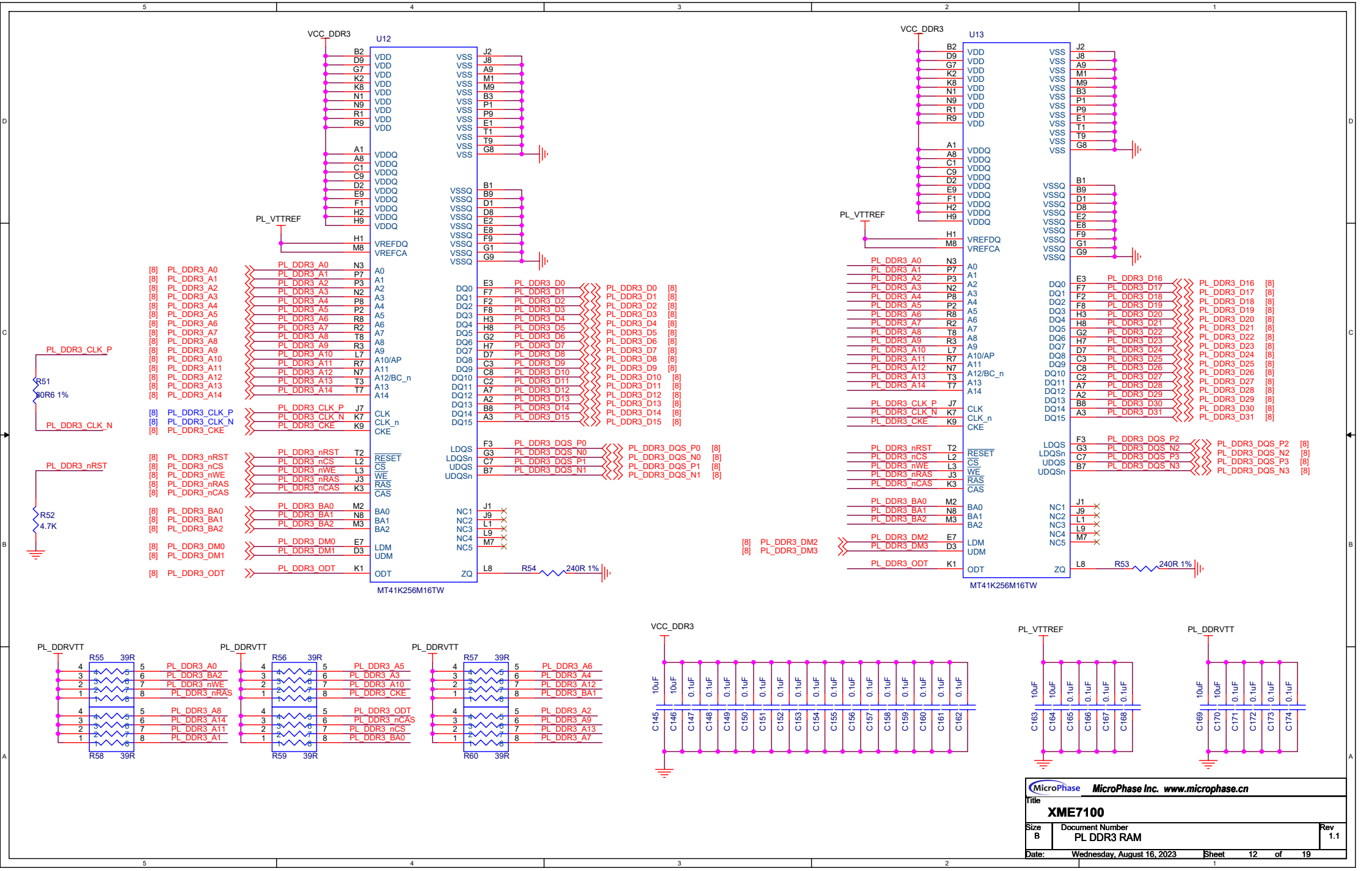
VCC_DDR3
U12
MT41K256M16TW
PL_VTTREF
PL_DDR3_CLK_P
PL_DDR3_CLK_N
PL_DDR3_nRST
R51
80R6 1%
R52
4.7K
R54
240R 1%
U13
MT41K256M16TW
R53
240R 1%
PL_DDRVTT
R55 39R
R56 39R
R57 39R
R58 39R
R59 39R
R60 39R
C145 C146 C147 C148 C149 C150 C151 C152 C153 C154 C155 C156 C157 C158 C159 C160 C161 C162
C163 C164 C165 C166 C167 C168
C169 C170 C171 C172 C173 C174
10uF
0.1uF
MicroPhase
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Title
XME7100
Size B
Document Number
PL DDR3 RAM
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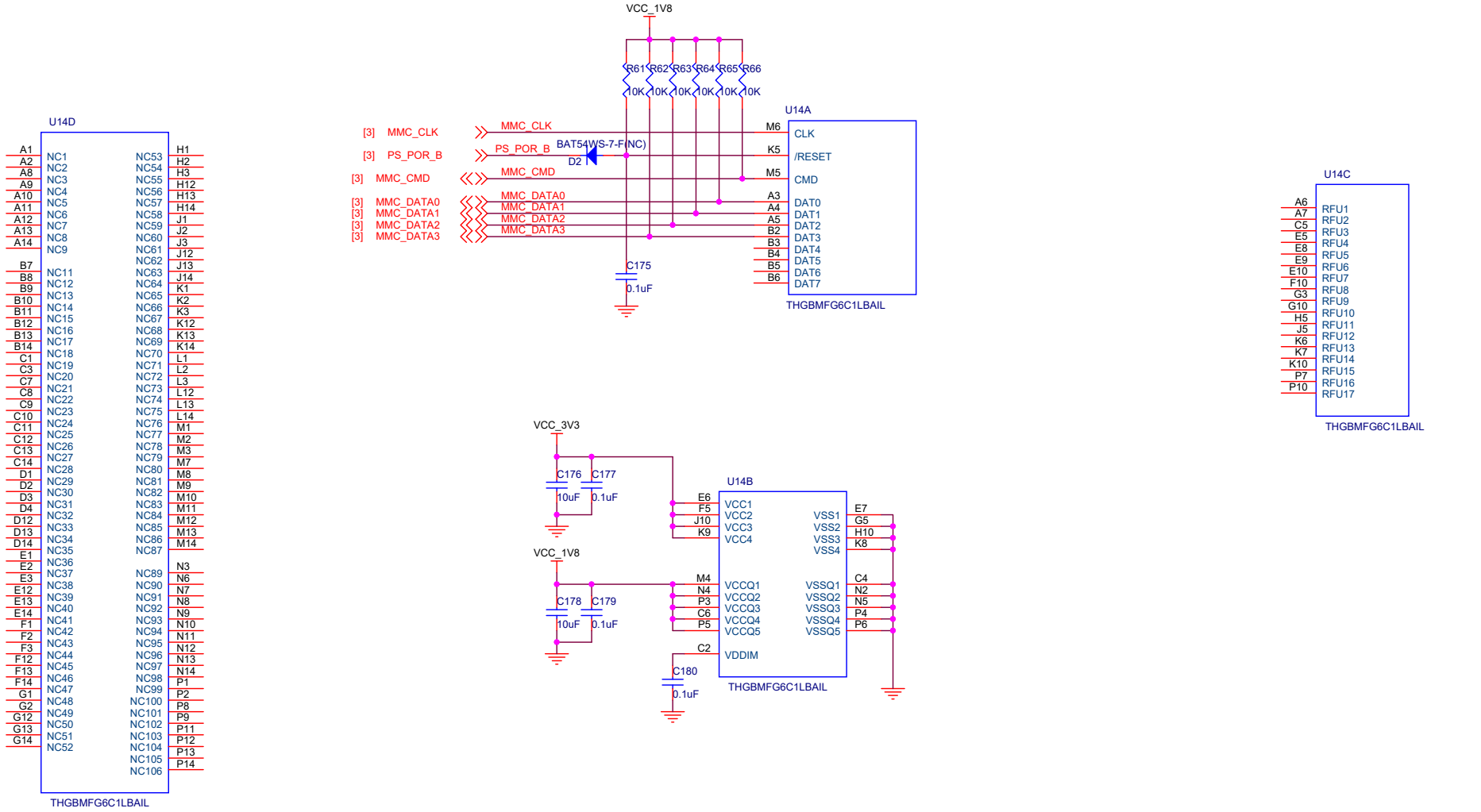

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Title: XME7100

Size	Document Number	Rev
B	eMMC	1.1

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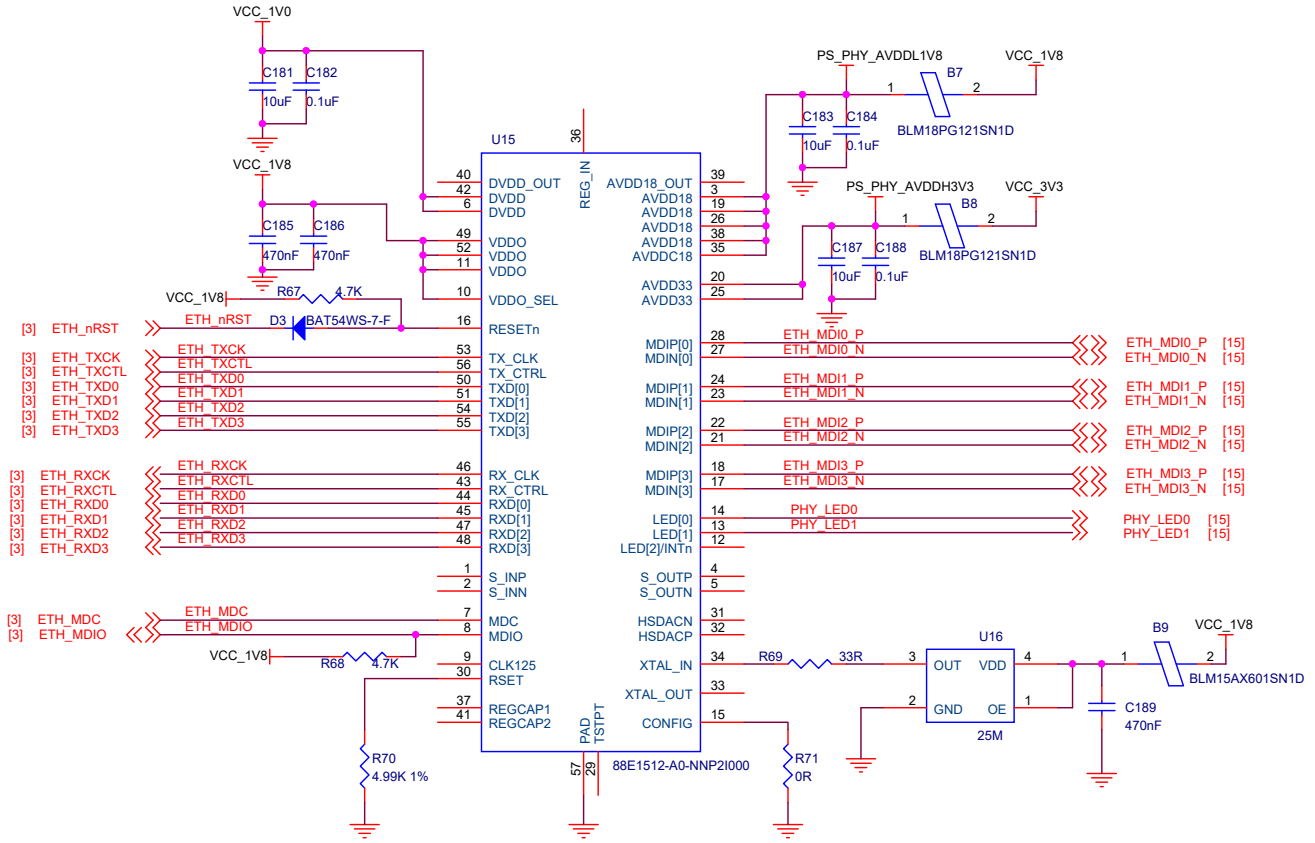

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Title: XME7100

Size	Document Number	Rev
B	PS ETH	1.1

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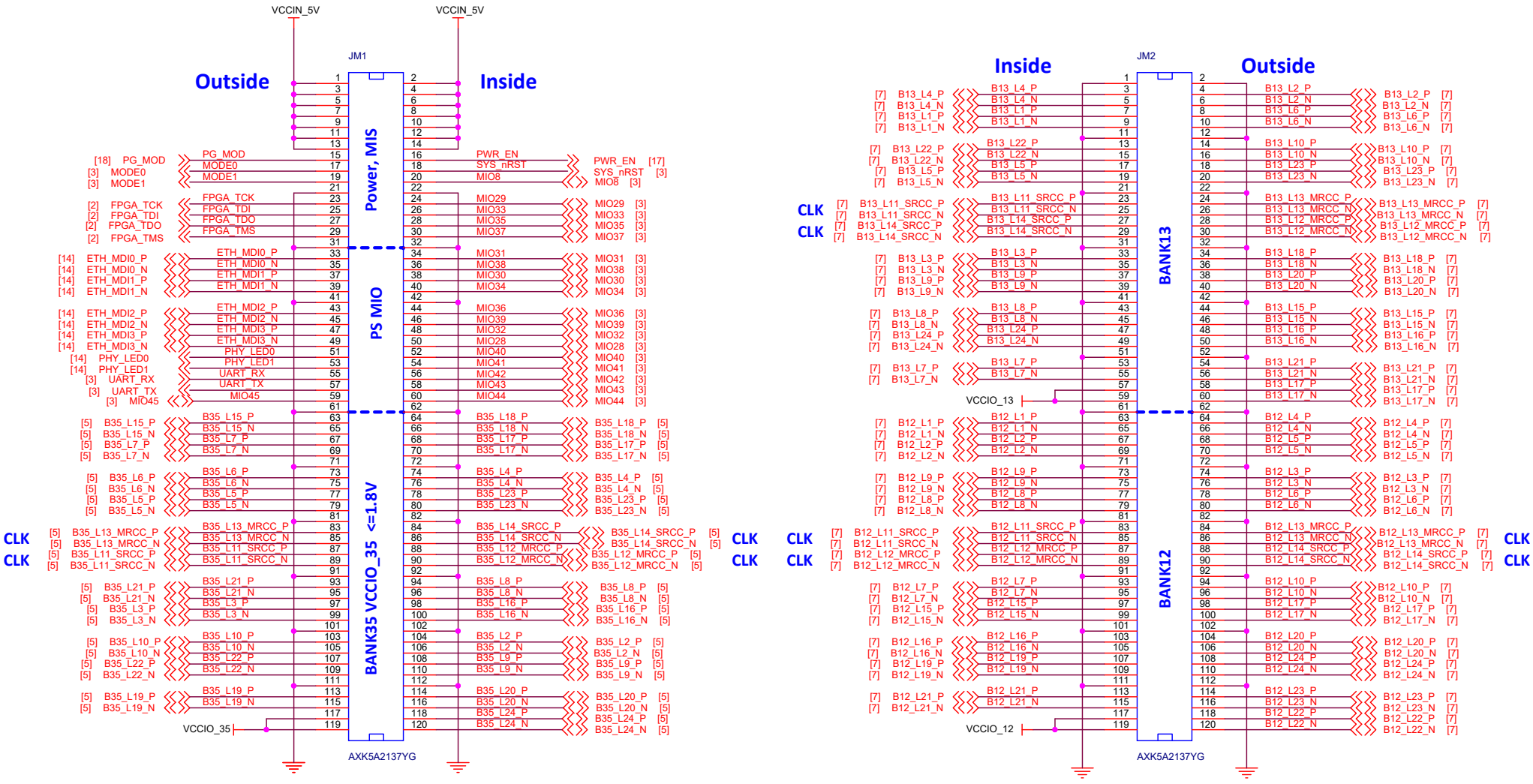

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Title: XME7100

Size	Document Number	Rev
B	B2B_JM1&2	1.1

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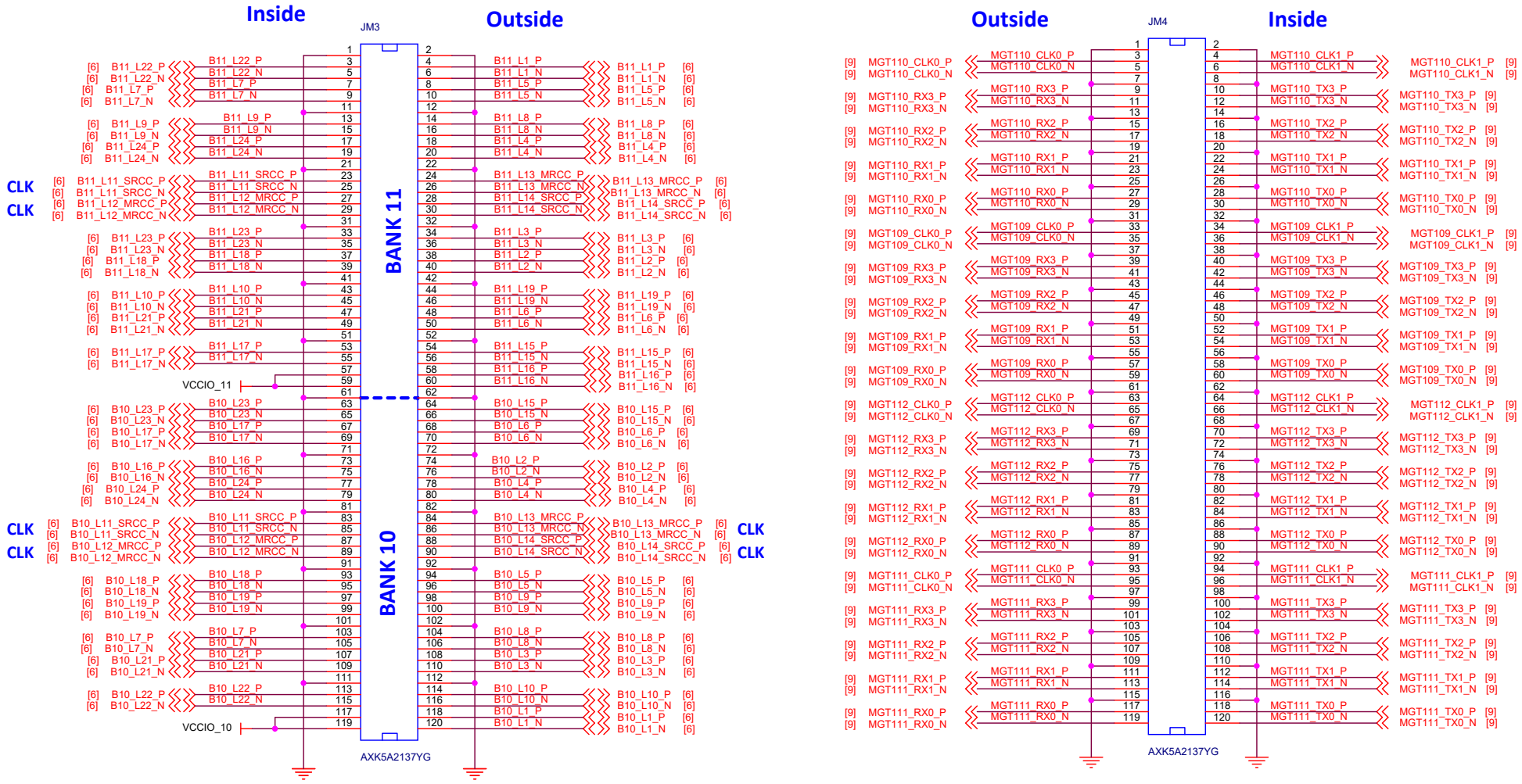

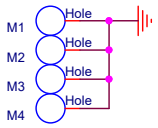

Mount.Hole 3.2mm

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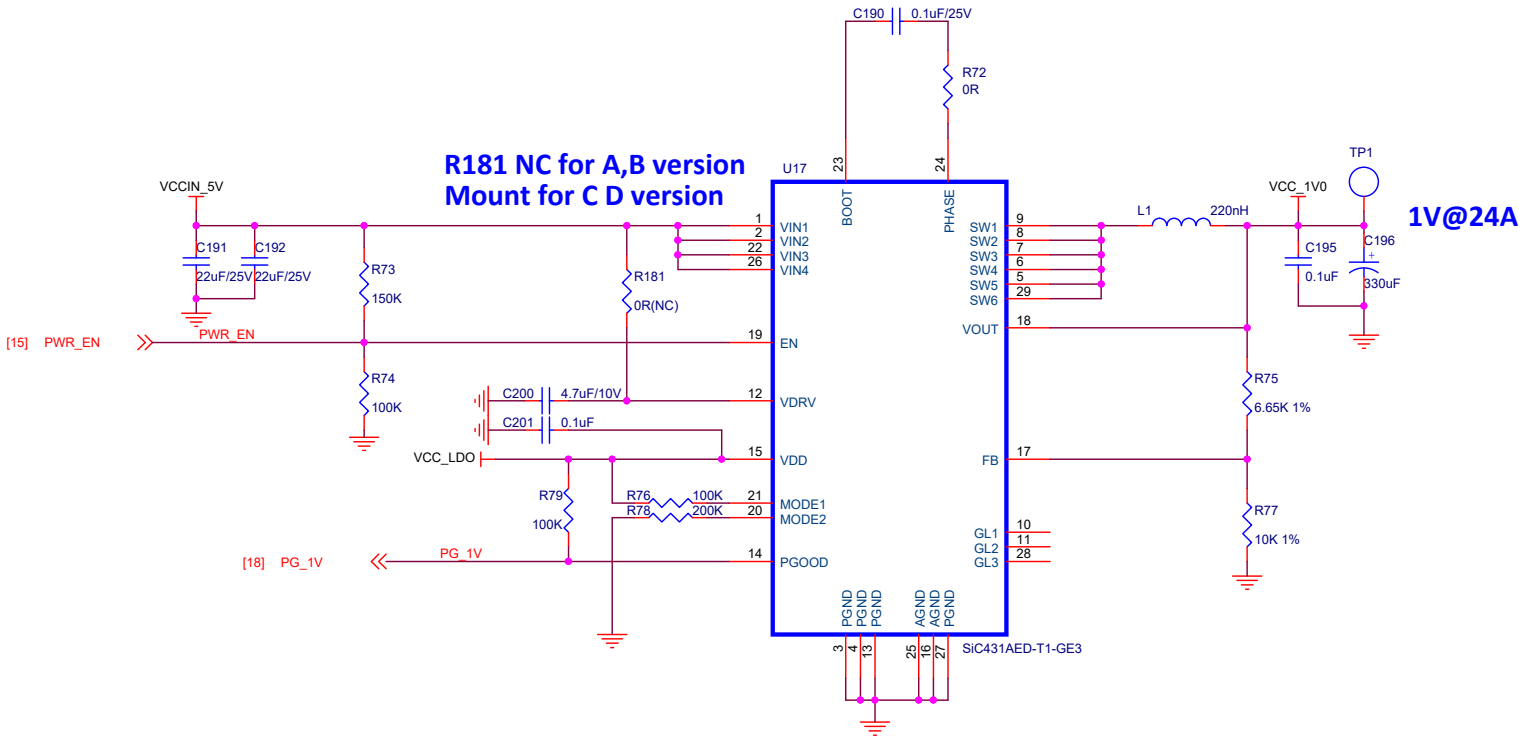

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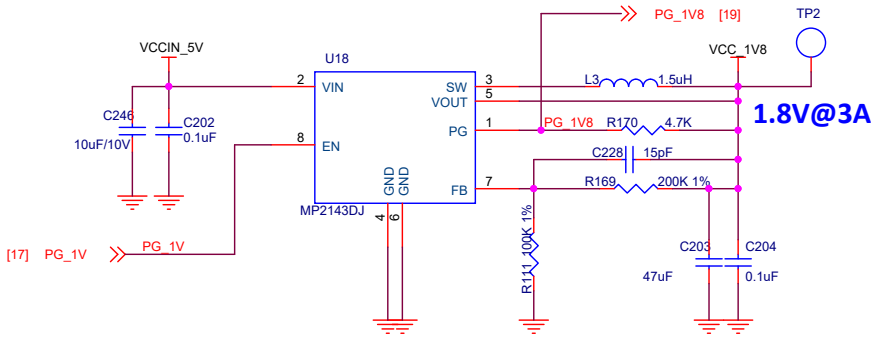
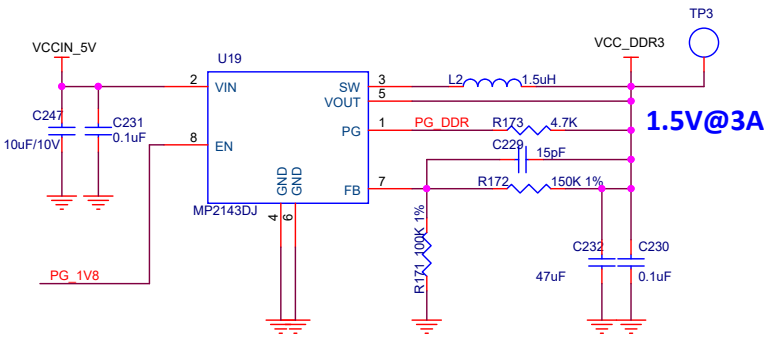
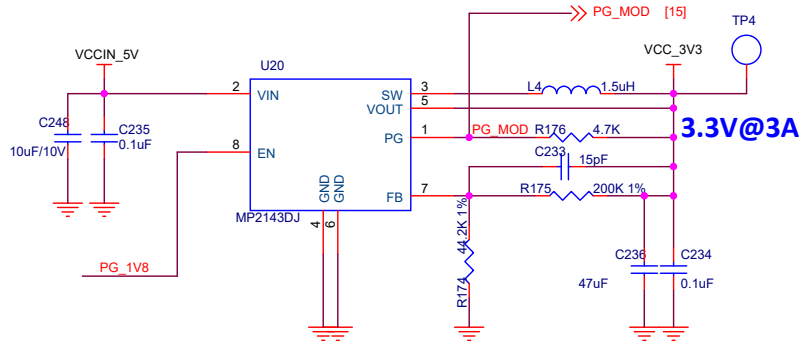


Vref = 0.6V VOUT = 1.8V



Vref = 0.6V VOUT = 1.5V



Vref = 0.6V VOUT = 3.3V

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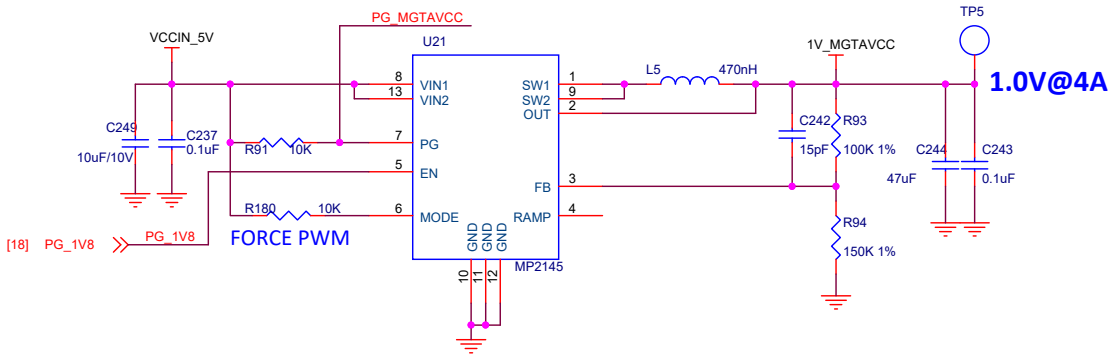
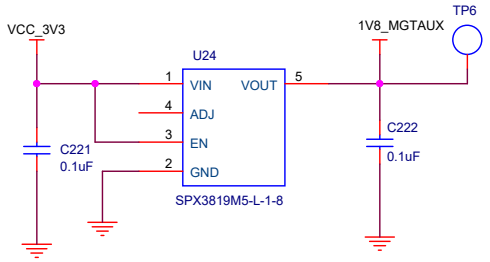
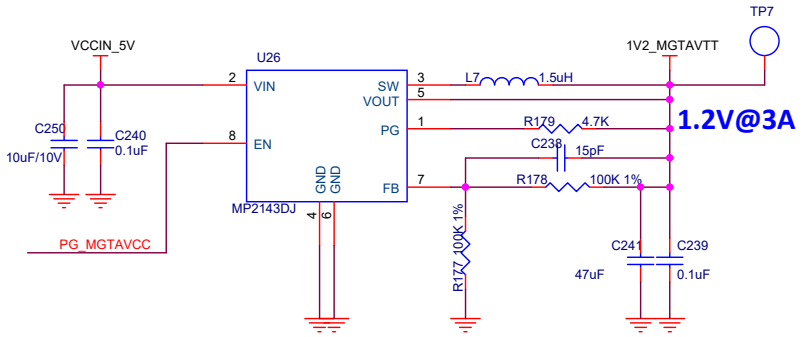


Vref = 0.6V VOUT = 1.0V

Place it on toplay





Vref = 0.6V VOUT = 1.2V

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