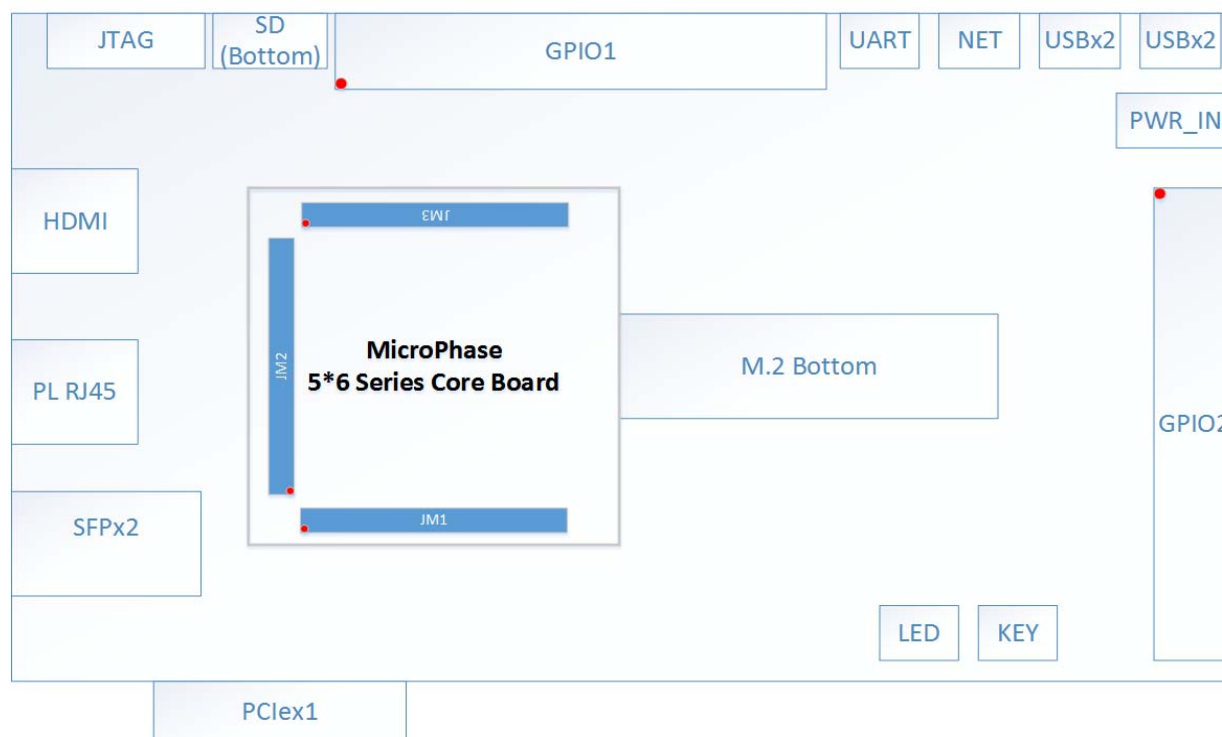


PE100 Block Diagram

REV	DATE	PAGES	DESCRIPTION
1.0	8/30/2018	All	Rev 1.0 Release
1.1	11/30/2018	All	Rev 1.1 Release

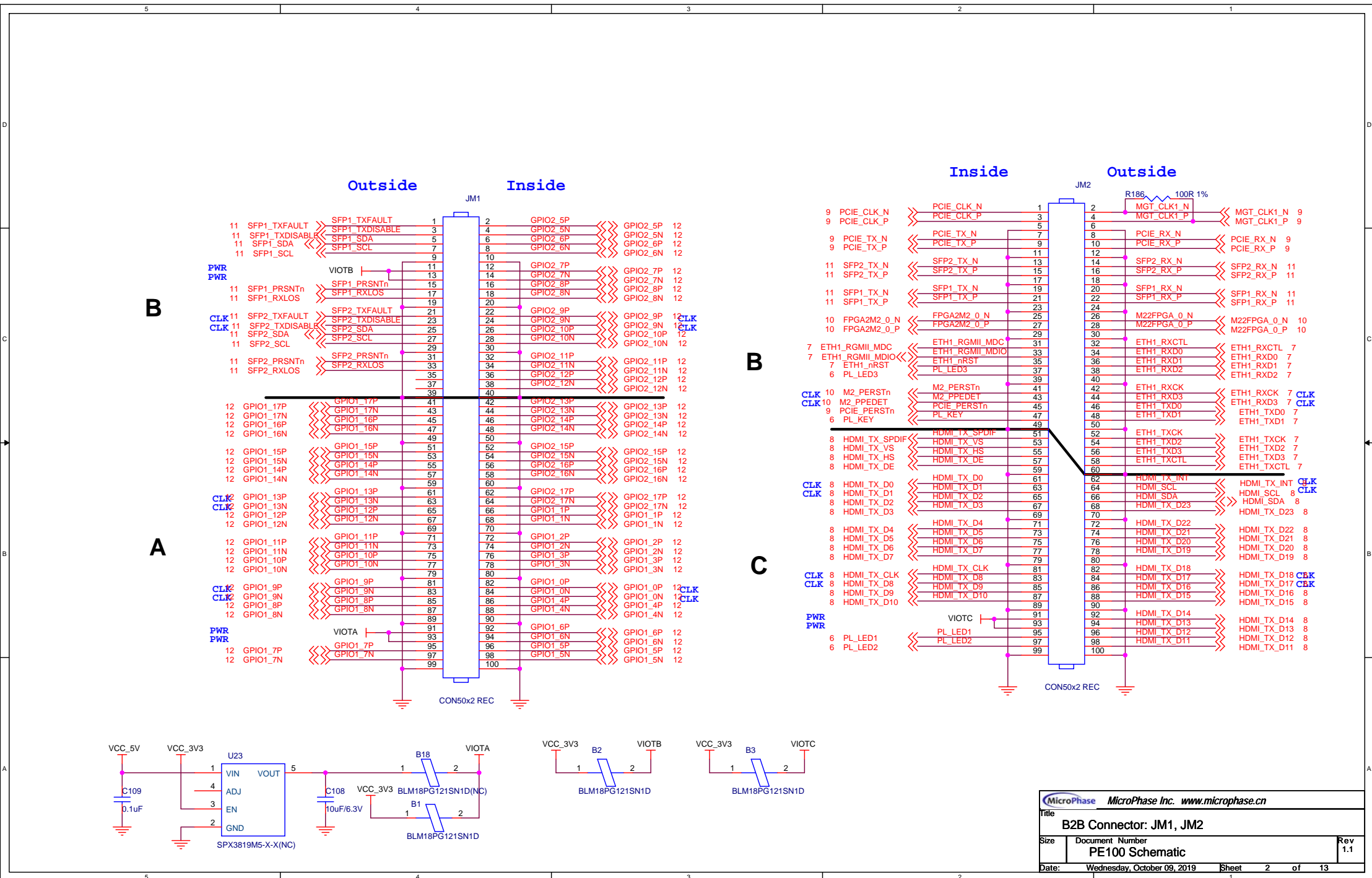


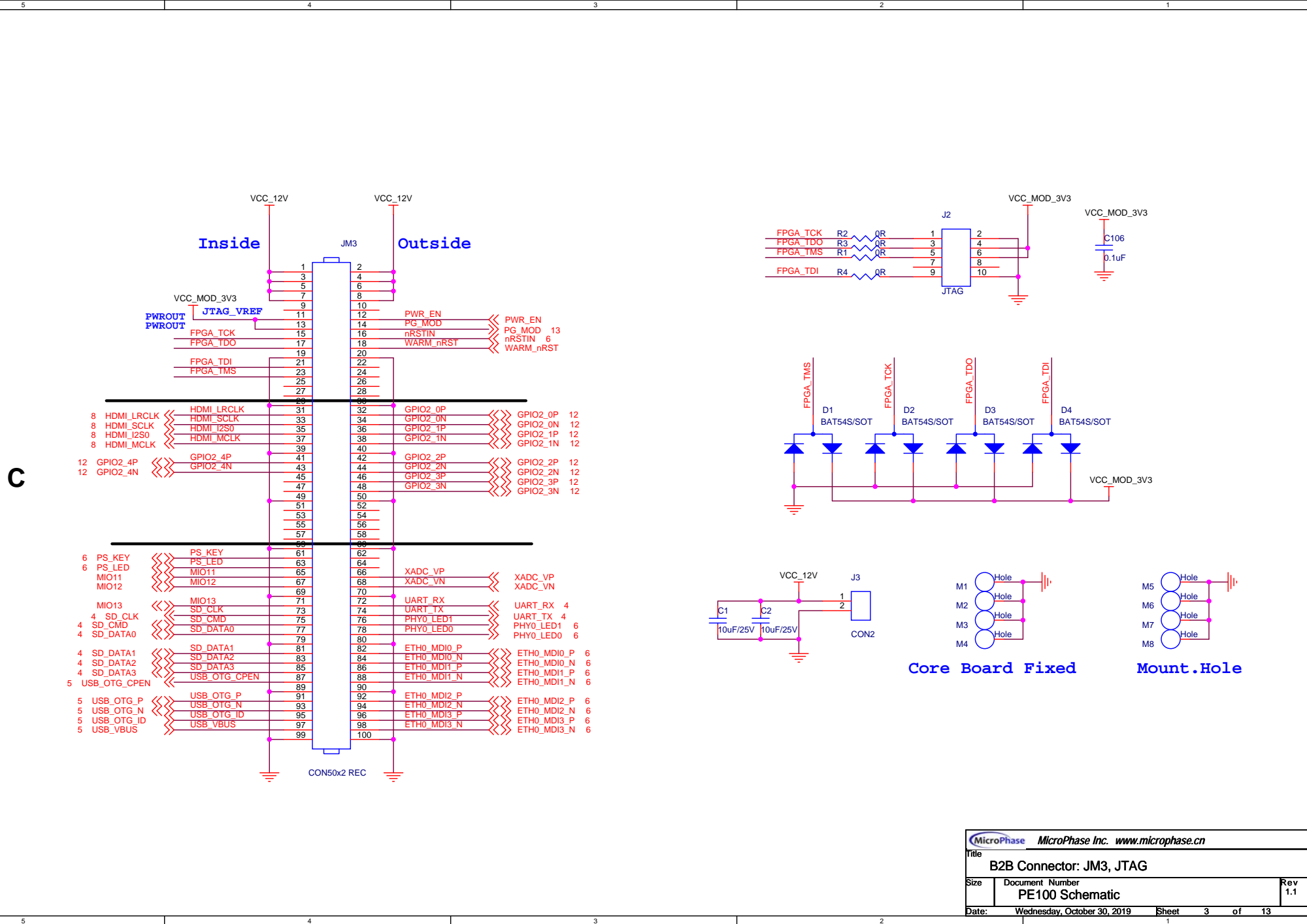
PAGE	DESCRIPTION
1	Title, Notes, Block Diagram, Revision History
2	B2B Connector: JM1, JM2
3	B2B Connector: JM3, JTAG
4	USB-UART, SD
5	USB Host(Only For ZYNQ)
6	ETH0, KEY, LED
7	ETH1
8	HDMI TX
9	PCIe, GTP Clock
10	M.2 Connector
11	SFP
12	GPIO Connector
13	Power
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Title Block Diagram			
Size	Document Number	Rev	
	PE100 Schematic	1.1	
Date:	Wednesday, October 09, 2019	Sheet	1 of 13





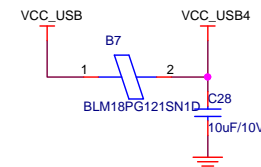
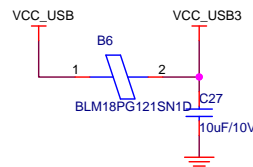
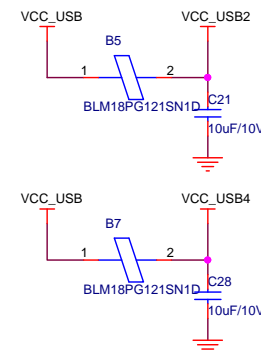
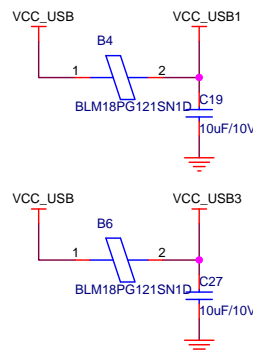
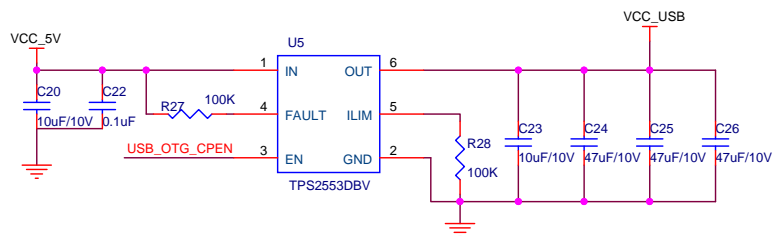
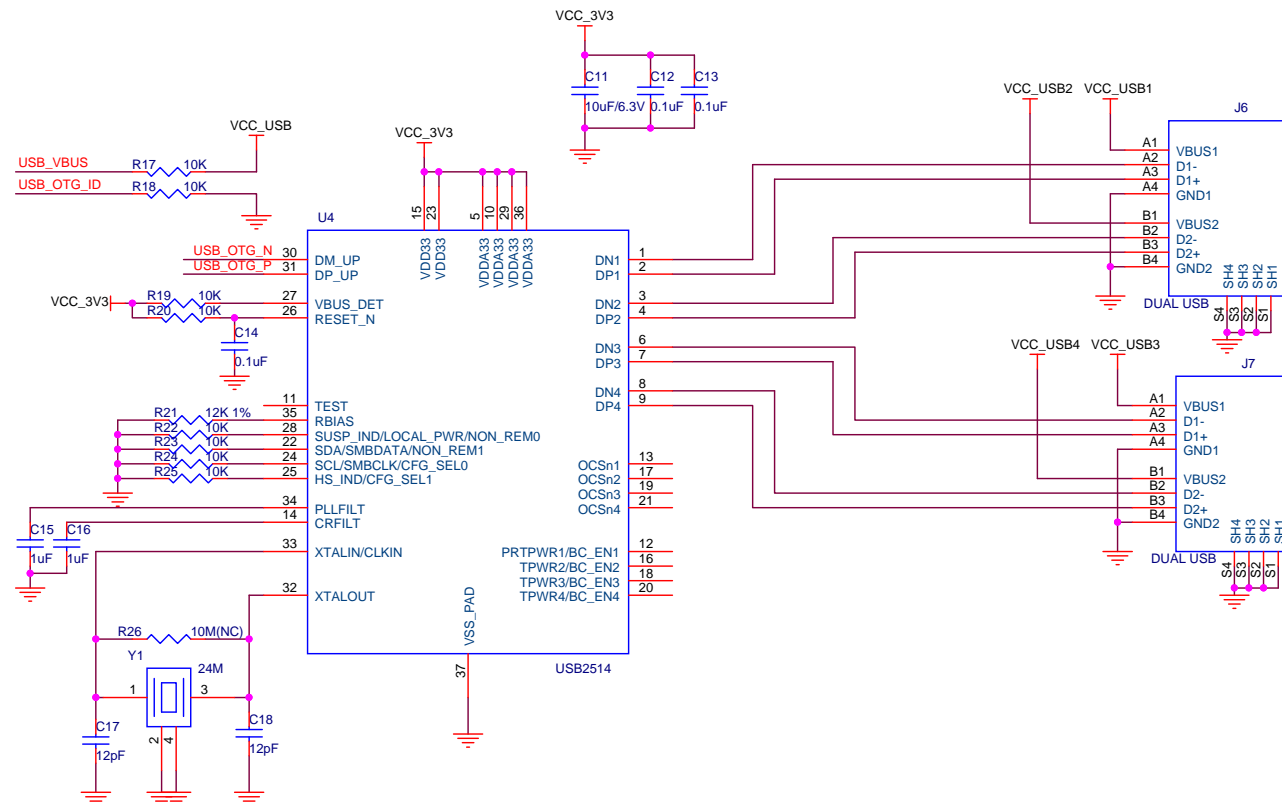
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3 USB_OTG_N <<< USB_OTG_N
 3 USB_OTG_P <<< USB_OTG_P
 3 USB_OTG_ID <<< USB_OTG_ID
 3 USB_OTG_COPEN <<< USB_OTG_COPEN
 3 USB_VBUS <<< USB_VBUS



RJ45

3 ETH0_MDIO_P <<> ETH0_MDIO_P
 3 ETH0_MDIO_N <<> ETH0_MDIO_N
 3 ETH0_MD1_P <<> ETH0_MD1_P
 3 ETH0_MD1_N <<> ETH0_MD1_N
 3 ETH0_MD2_P <<> ETH0_MD2_P
 3 ETH0_MD2_N <<> ETH0_MD2_N
 3 ETH0_MD3_P <<> ETH0_MD3_P
 3 ETH0_MD3_N <<> ETH0_MD3_N
 3 PHY0_LED0 <<> PHY0_LED0
 3 PHY0_LED1 <<> PHY0_LED1

