Chapter 5 -
Process Design
Kit (PDK) -
QUESTIONS

Course authors
(Git file)

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Recap questions for Chapter 5

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- What is a PDK?
- Define the border between development (design) and production of a microchip.
- Where is the (actual) border of open- to closed-source in this?
- Name as many parts of the IHP open-source PDK as you can (round robin)
- What are layers?
- What is a layer stack?
- Can you name some layers and their purpose?
- Name some of the important file formats from the PDK. What are they used for? (round robin).
- Describe the content of the design rules document.
- Which tool does the DRC?
- Which tool does the LVS?