Chapter 02 - Terminology - TRAINING - Advanced

Course authors (Git file)

1. Preparation for chapter 03 + 04 Bonus Training

Another option for building your own design can be started.

Task: Learn about LFSR

- Read the Wikipedia about Linear Feedback Shift Registers.
- https://en.wikipedia.org/wiki/Linear-feedback_shift_register

Task: Design your own LSFR

- Try drawing a small LSFR
- 4 to 8 bits length maximum
- 1 or 2 feedback lines with XOR

Task: Simulate the LSFR with a Bit table

- Simulate your LSFR with a Bit table
- How should the table look?
- Clock, Register content, Feedback Bits, Input, Output