

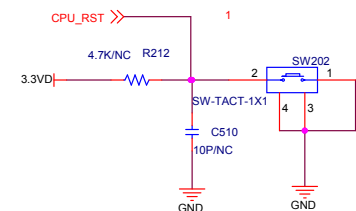
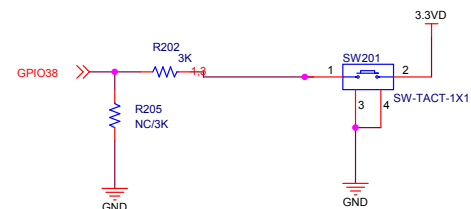
5

4

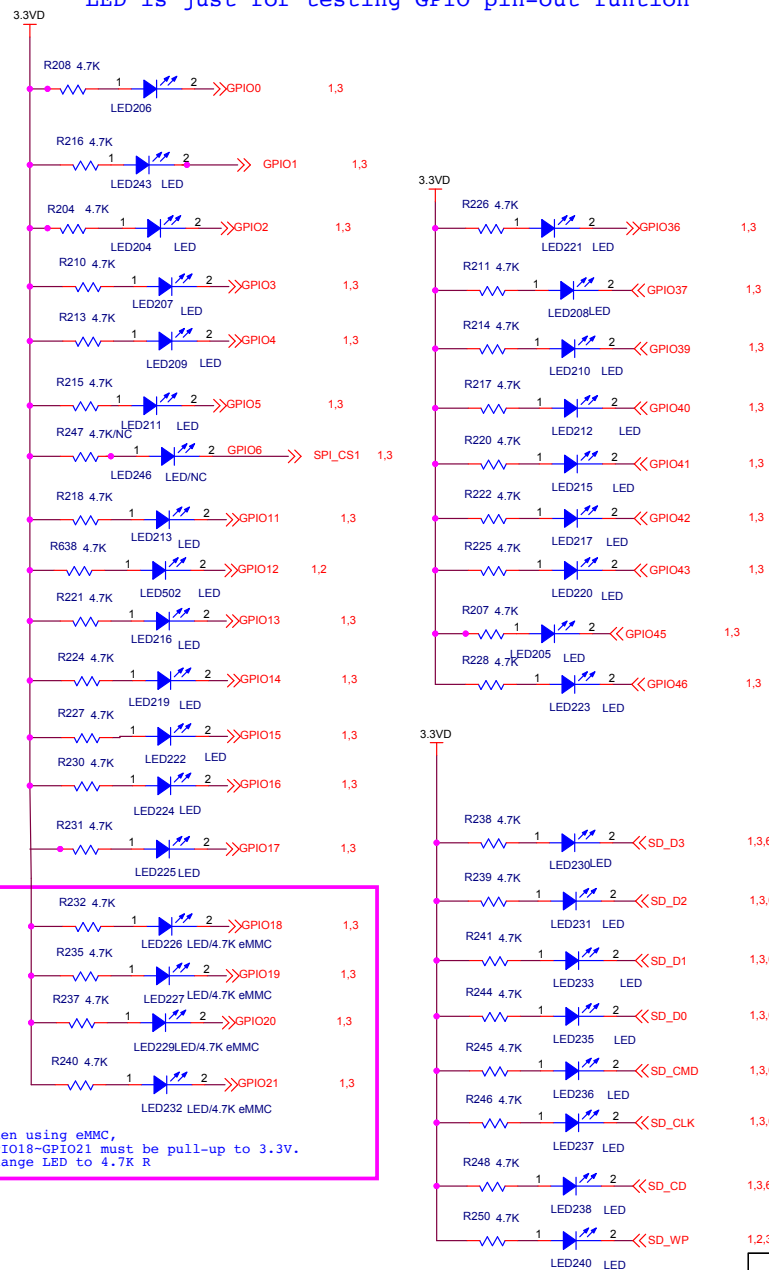
3

2

1



LED is just for testing GPIO pin-out funtion

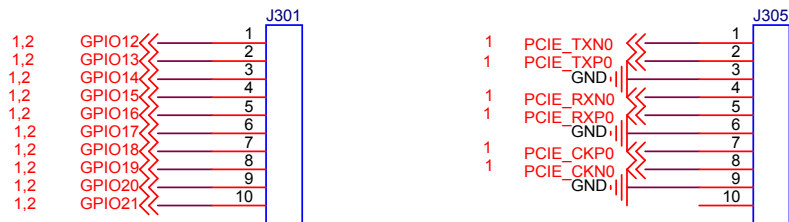


Title			<Title>
Size	Document Number		Rev
A3	<Doc>		<RevCode>
Date:	Saturday, August 26, 2017	Sheet	2 of 7

GPIO Dual-Line-DIP-header for MP Test and debug

Be Used for twice

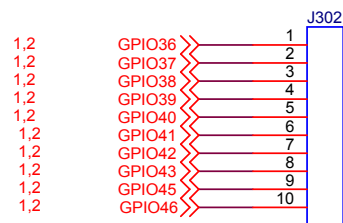
PCIE



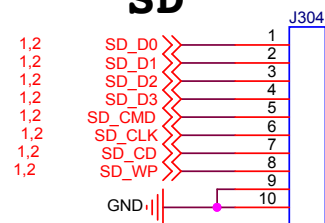
SPI



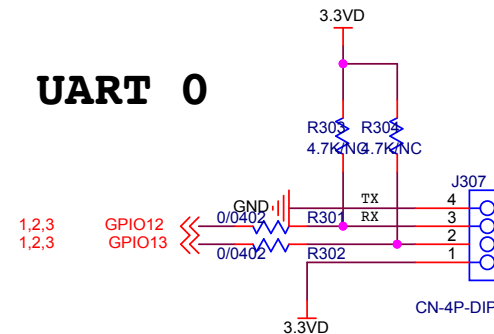
GPIO



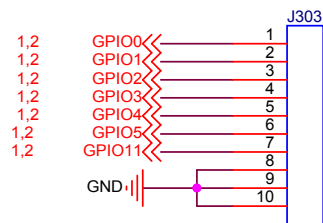
SD

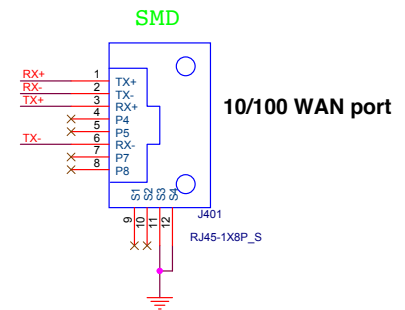
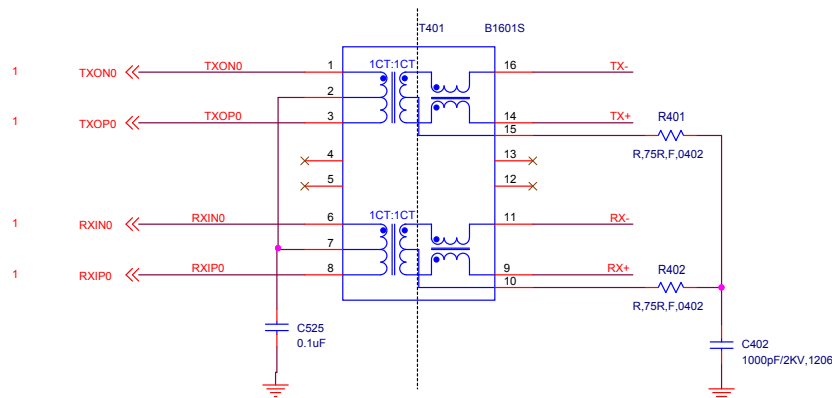


UART 0

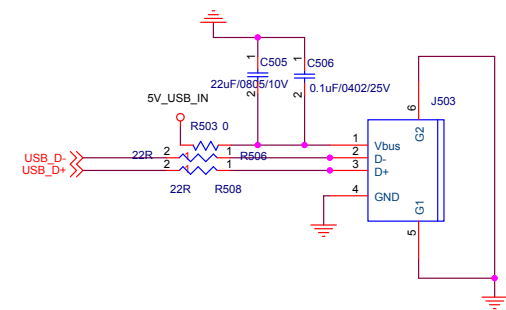


GPIO

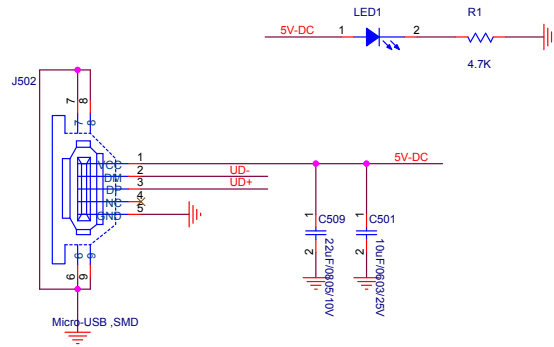
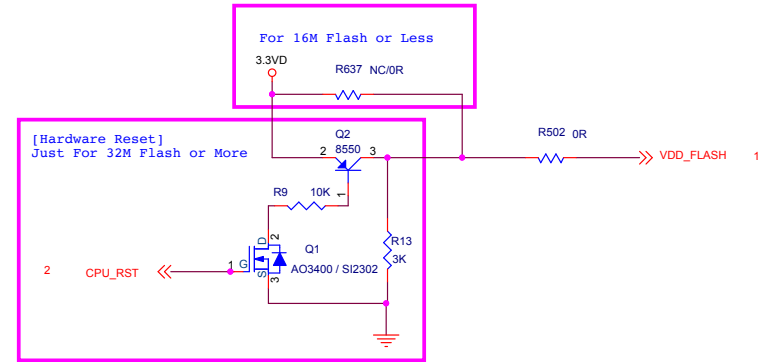




5V USB A TYPE OUT



Title			<Title>
Size	Document Number		Rev
A3	<Doc>		<RevCode>
Date:	Saturday, August 26, 2017	Sheet	4 of 7

[illegible]

PC_RXD 25 RXD VDD 6 C524 2 0.1uF/0402/25V

5V_USB_IN 8 VBUS REGIN 7 C523 2 0.1uF/0402/25V

PC_TXD 26 TXD 26 C5V_USB_IN

GND 29 GND 29

D+ 4 UD+ 4

D- 5 UD- 5

RST 9 RST 9 4.7K R629 C5V_USB_IN

DTR 28 DTR 28

RTS 24 RTS 24

GPIO13 1 GPIO13 1

GPIO12 2 GPIO12 2

GND 3 GND 3

CP2102

USB to serial circuit



