

The diagram shows the LM4040AIM3-3.0+T voltage reference circuit. The VCC pin (pin 1) is connected to a 1k resistor (R18) which is connected to the VCC supply. The GND pin (pin 2) is connected to ground. The output pin (pin 3) is labeled 'N.C.' (Not Connected). The output voltage is labeled Vref.

The schematic diagram illustrates a DC-DC converter circuit. The input is a PSU connected to a series of capacitors (C9, C13, C2, C3) and resistors (R16, R3) to ground. The input to the MAX5026EUT+T (U3) is connected to the output of the filter network. The output of the converter is connected to a diode (D1) and an inductor (L1) in series, followed by a capacitor (C10) to ground. The output voltage is labeled VCC. The feedback network consists of resistors R4, R9, and R13 connected to the FB pin of U3.

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