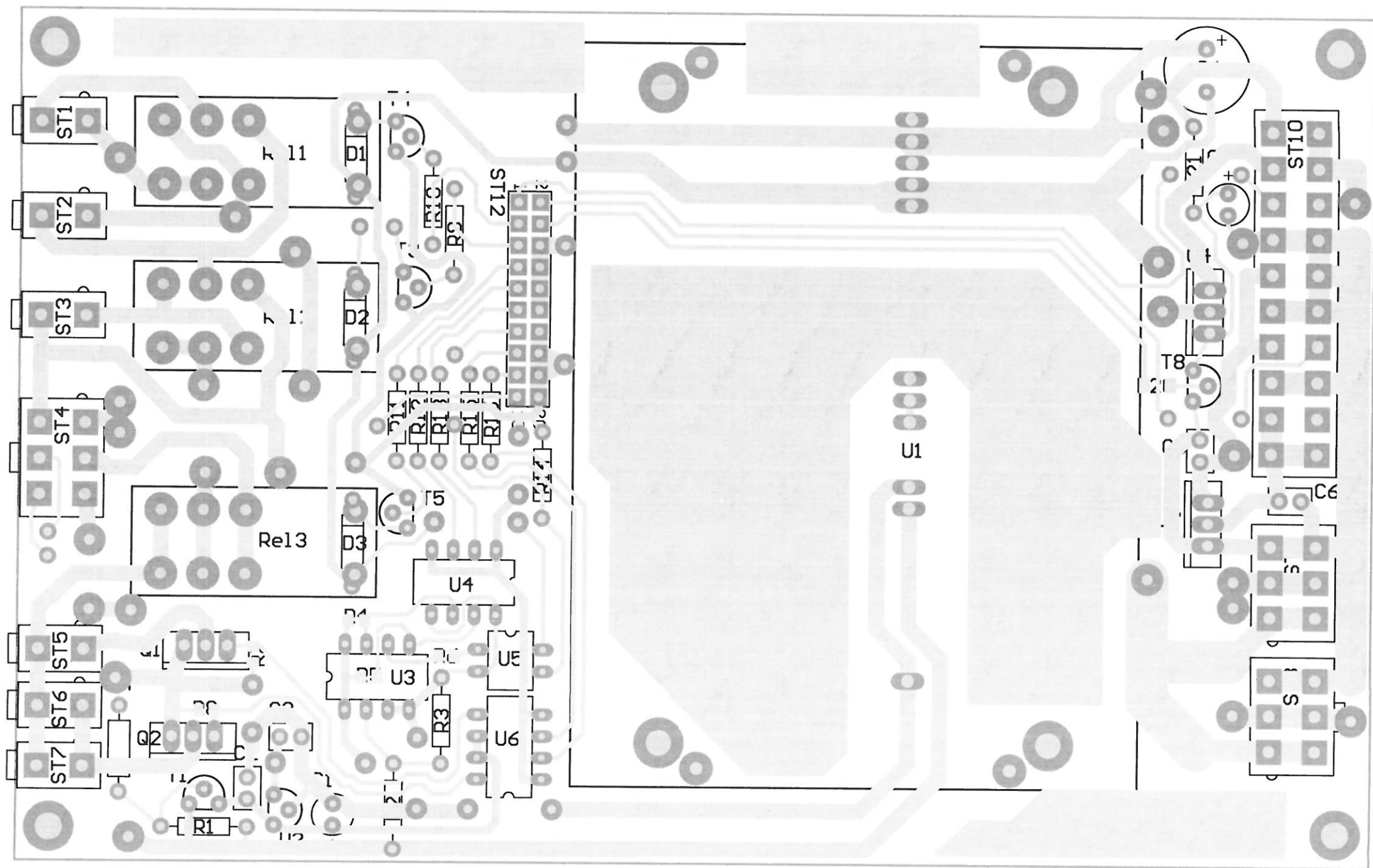
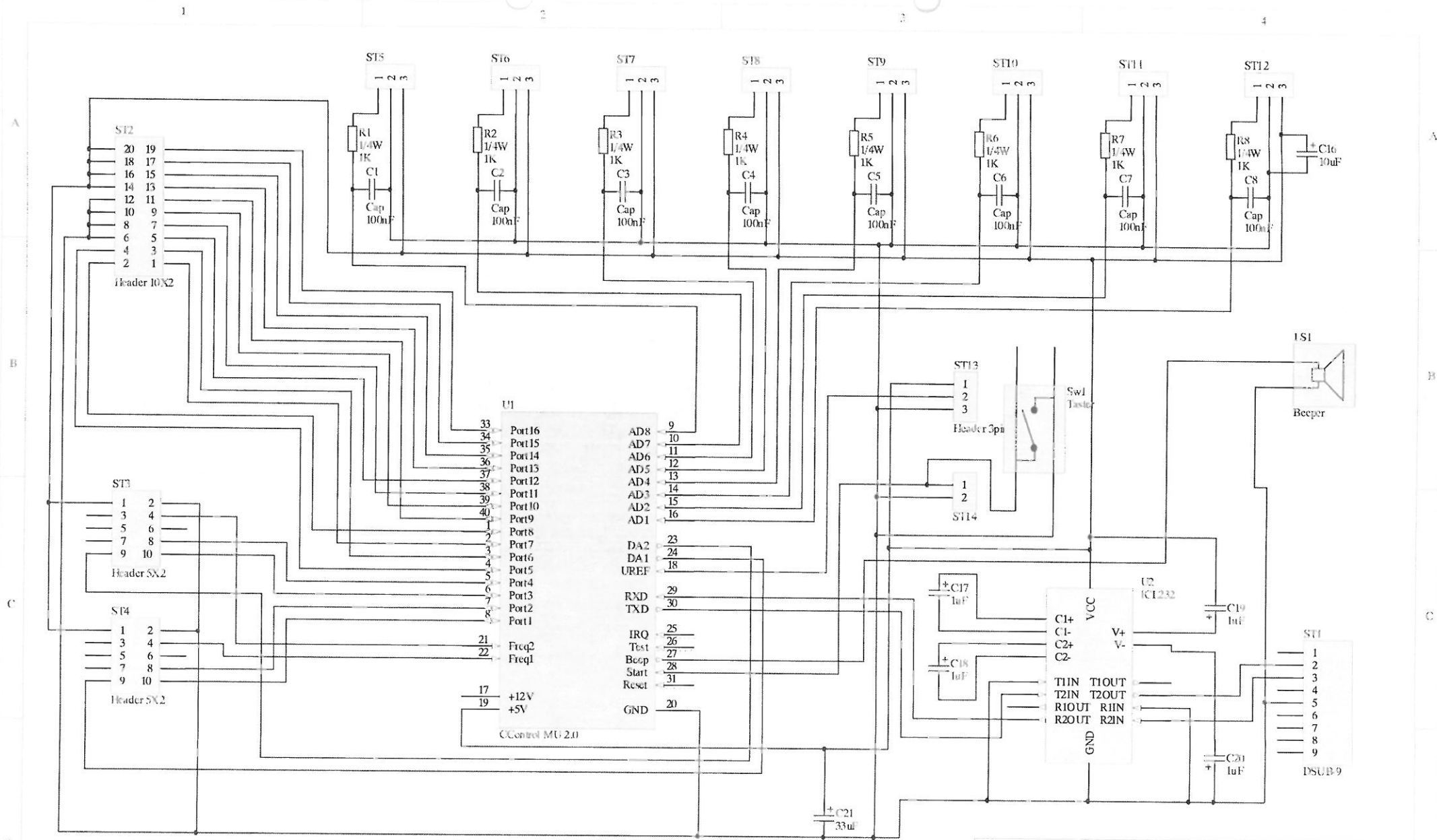
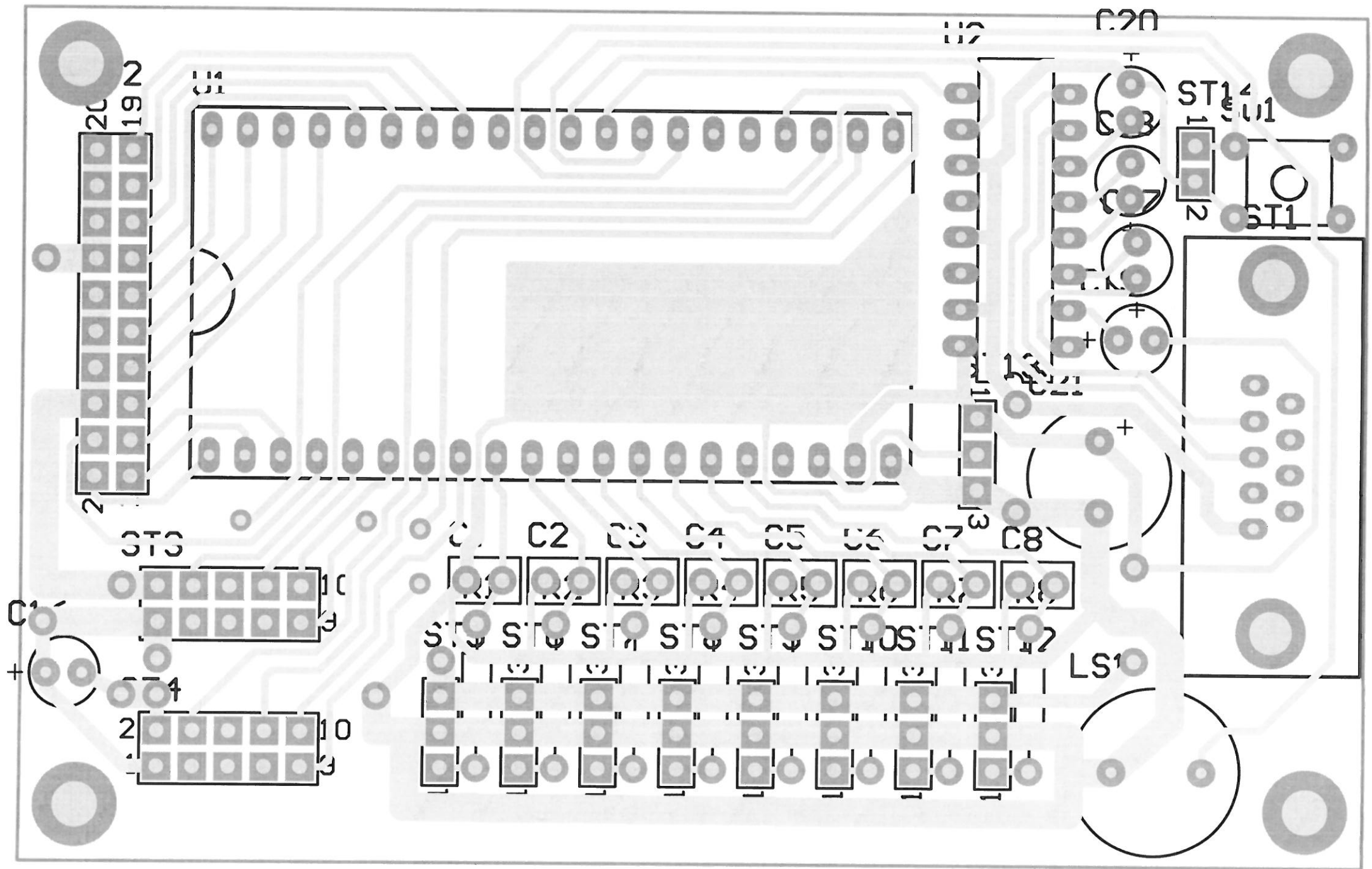


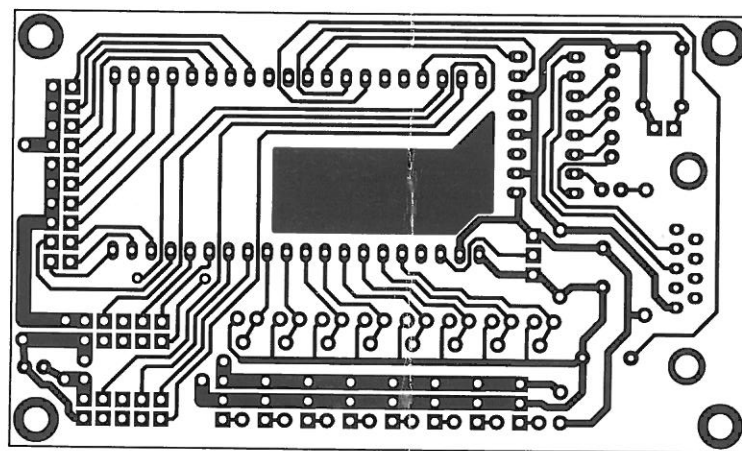
Title		
Size	Number	Revision
A3		
Date	13.11.2006	
File	Z:\proj\it_Poveropph SchD.c	
		Sheet of
		Drawn By



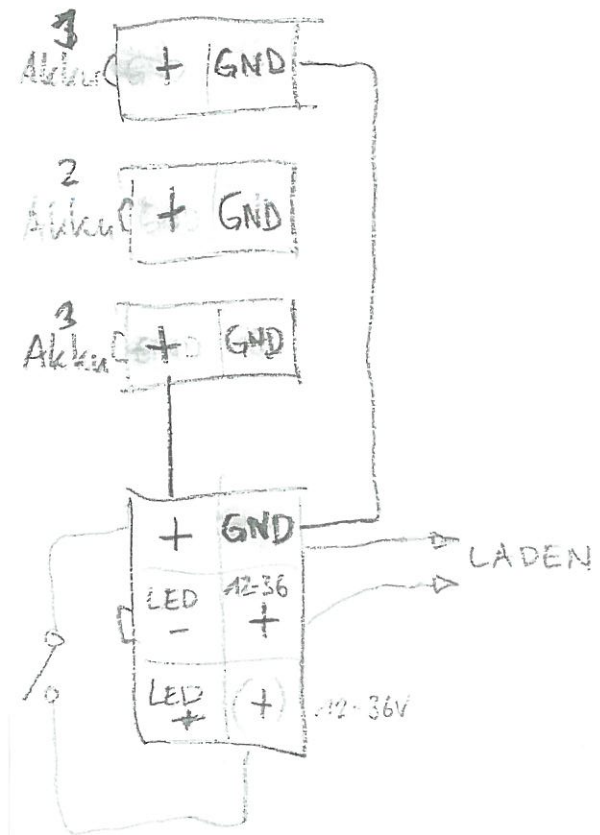


Title		
Size	Number	Revision
A4		
Date:	13.11.2006	Sheet of
File:	Z:\privat\...\Controller.SchDoc	Drawn By:





12,01 V — 991
11,03 V — 900
12,98 V — 1071
13,98 V — 1154
15,02 V — 1241
19,98 V — 1651
30,0 V — 2478
33,2 V — 2749 ~



Sign

Main board

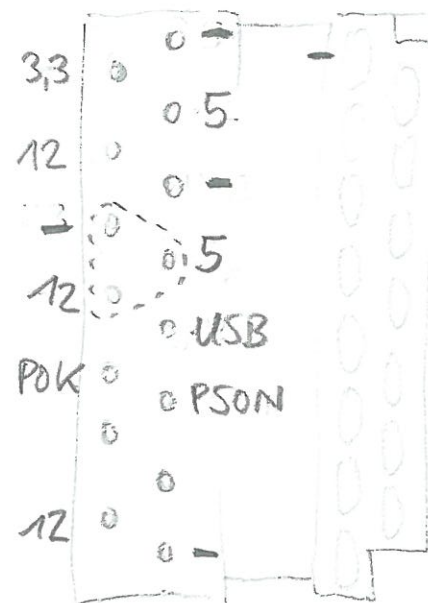
12	5
USB	5
POK	X
-	-
5	-
-	-
5	PSON
-	-
3,3	X
3,3	3,3

Drives

-	-
5	5
12	12

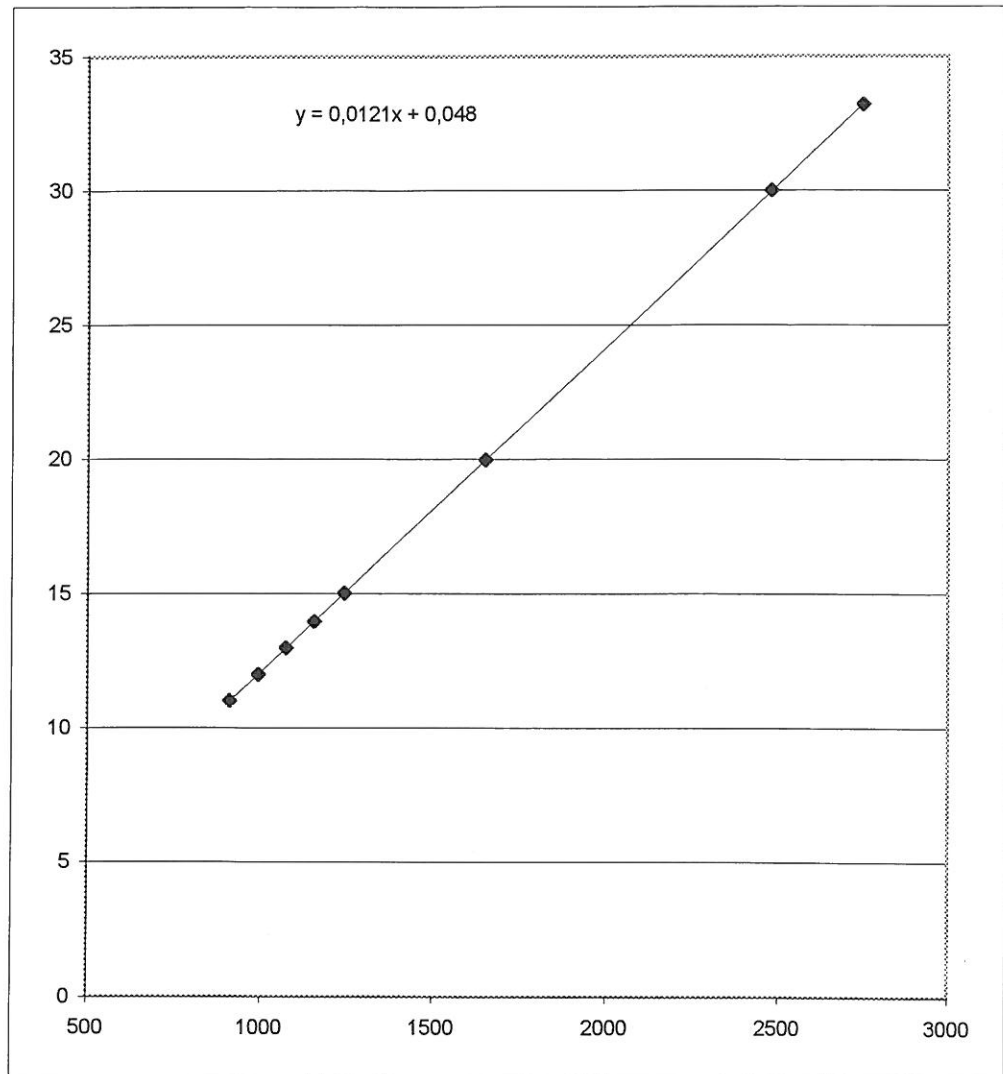
DC/DC

-	-
5	5
12	12



Anschließend bei 2. uhr aufgesetzter Platine

910	11,03
991	12,01
1071	12,98
1154	13,98
1241	15,02
1651	19,98
2478	30
2749	33,2



AD-Wert	Spannung	Bemerkung
839	10,2	Entladeschlußspannung 12V
1137	13,8	Ladeschlußspannung 12V
2525	30,6	Entladeschlußspannung 36V

Mittelwert aus alter + neuer
Platine:

Entladeschluß 12V → 837
" 36V → 2516

549

1102

1652

549

2201

547

2748

546

3294

3843

546

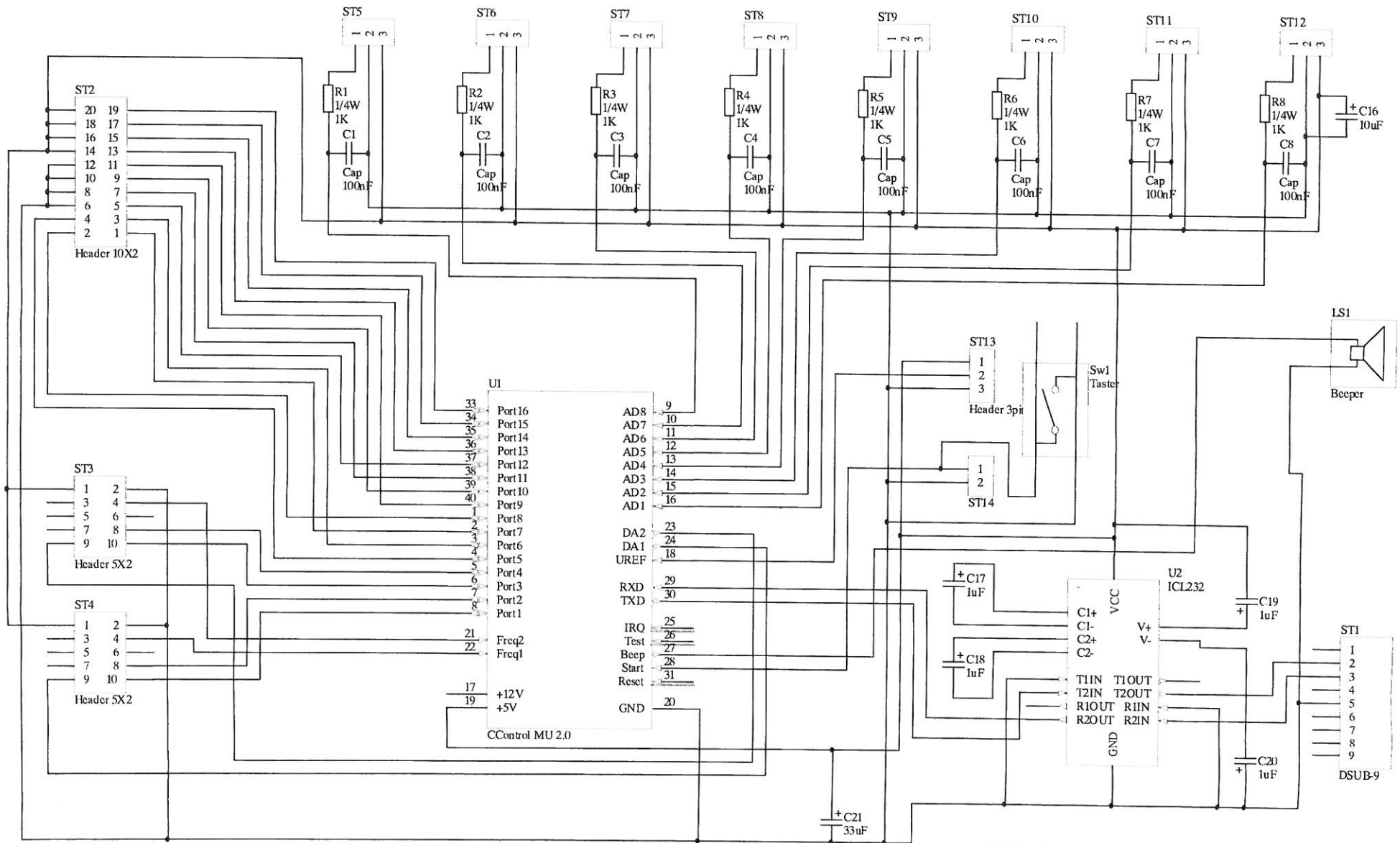
4389

549

4938

546

5484



Title		
Size	Number	Revision
A4		
Date:	16.02.2006	Sheet of
File:	Z:\privat\...\Controller.SchDoc	Drawn By:

