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#include "datatypes.h"
#include <p18f442.h>

// Taktfrequenz
#define F TAKT 40000000

// Gewünschte Pulsfrequenzen
#define FREQUENCY1 33000
#define FREQUENCY2 36000
#define FREQUENCY3 38000
#define FREQUENCY4 40000

// Gewünschte Burstdauer in Impulsen
#define PULSES PER BURST 30

UINT32 ui32CurrentPulseFrequency=0;
UINT32 ui32NewPulseFrequency=0;

void InterruptHandlerHigh (void);

#pragma code InterruptVectorHigh = 0x08
void InterruptVectorHigh (void)
{
    _asm
        goto InterruptHandlerHigh //jump to interrupt routine
    _endasm
}

#pragma code
#pragma interrupt InterruptHandlerHigh

void InterruptHandlerHigh ()
{
    if (PIR1bits.CCP1IF)
    {
        PIR1bits.CCP1IF=0;
        TMR1H=0;
        TMR1L=0;
    }
}

UINT8 ui8LeseJumperEin(void)
{
    UINT8 _ui8ReturnValue;
    _ui8ReturnValue = PORTD & 0b11000000;
    _ui8ReturnValue >>= 6;
    return _ui8ReturnValue;
}

void InitBursts(UINT32 _ui32PulseFrequency, UINT8 _ui8PulsesPerBurst, UINT32 _ui32TaktFrequenz)
{
    UINT16 _ui16CCPR1Value;
    UINT8 _ui8PR2Value;
    UINT32 _ui32Temp;

    TRISC = 0b11111001; // CCP1-Pin und CCP2-Pin als Ausgänge

    CCP1CON = 0b00000010; // CCP1 im Compare-Modus, Pin-Toggle
    T1CON = 0b00000001; // 8-bit-Zugriff, 1:1 Prescaler, Osc Shut-Off, Int.

    Clock, Timer on
    T3CON=0;
    PIR1bits.CCP1IE = 1;
    IPR1bits.CCP1IP = 1;
    _ui32Temp = _ui32TaktFrequenz*_ui8PulsesPerBurst;
    _ui32Temp /= (4* _ui32PulseFrequency);
    _ui32Temp -= 1;
    _ui16CCPR1Value = (UINT16) _ui32Temp;
    CCP1H = (UINT8) (_ui16CCPR1Value >> 8);
    CCP1L = (UINT8) (_ui16CCPR1Value & 0b0000000011111111);

    CCP2CON = 0b00111111; // CCP2 im PWM-Modus
    T2CON = 0b00000101; // 1:4 Prescaler, 1:1 Postscaler, Timer on
    _ui32Temp = _ui32TaktFrequenz;
    _ui32Temp /= (16* _ui32PulseFrequency);
    _ui32Temp -= 1;
    _ui8PR2Value = (UINT8) _ui32Temp;
    PR2 = (UINT8) _ui32Temp;
    _ui8PR2Value >>= 1;
    CCP2L = _ui8PR2Value;

```

Die vier Frequenzen in Hz. Von 10 KHz bis 250 KHz

$t_B = t_P$, die Konstante gibt an, wieviele Impulse in t_B sind.

