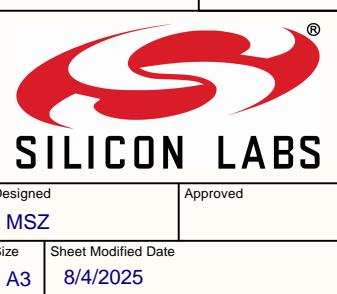




EFR32FG28 Diversity Board with discrete matching network for 868/915 MHz

Board Function	Page
Title Page	1
RF & Power	2
EFR32 Signal Assignments	3
WSTK Connectors & Board ID	4

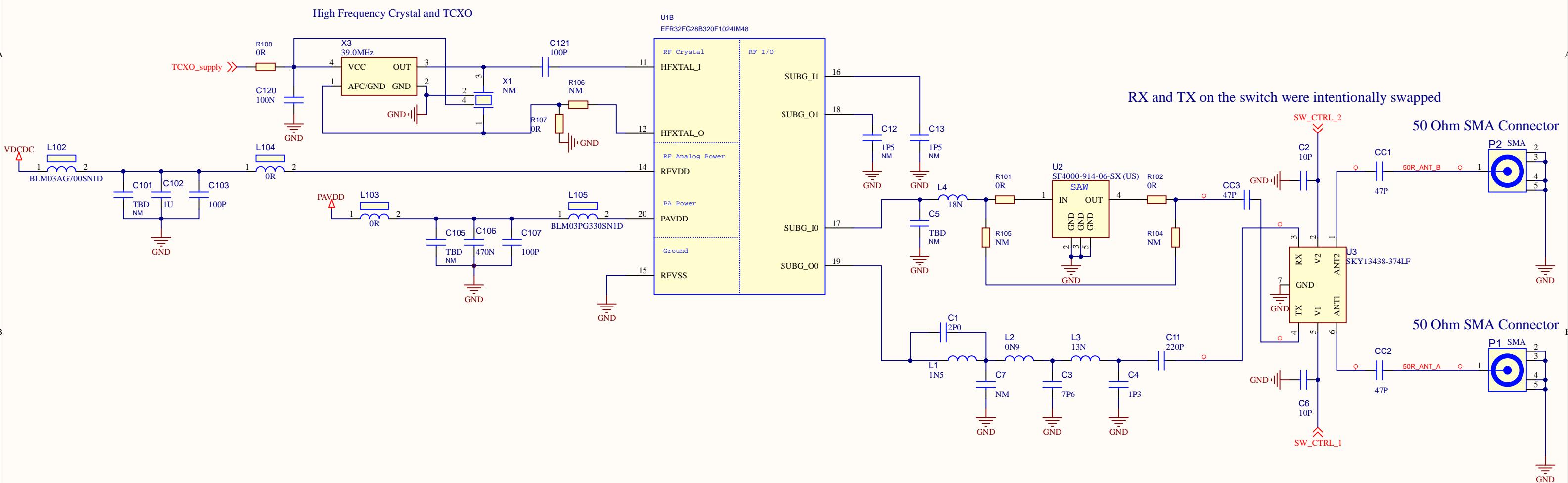
LEGAL NOTICE:
SILICON LABORATORIES INC. ("SILICON LABS") AND/OR ITS LICENSORS DO NOT WARRANT THE ACCURACY OR COMPLETENESS OF THIS SCHEMATIC OR ANY INFORMATION CONTAINED WITHIN THIS SCHEMATIC. IT IS PROVIDED "AS-IS" FOR REFERENCE ONLY. SILICON LABS DOES NOT WARRANT THAT THIS DESIGN WILL MEET THE SPECIFICATIONS, BE SUITABLE FOR YOUR APPLICATION OR FIT FOR ANY PARTICULAR PURPOSE, OR WILL OPERATE IN YOUR IMPLEMENTATION. SILICON LABS AND ITS LICENSORS DO NOT WARRANT THAT THE DESIGN IMPLIED IN THIS SCHEMATIC IS PRODUCTION-WORTHY. YOU SHOULD COMPLETELY VALIDATE AND TEST YOUR DESIGN IMPLEMENTATION TO CONFIRM SYSTEM FUNCTIONALITY FOR YOUR APPLICATION.



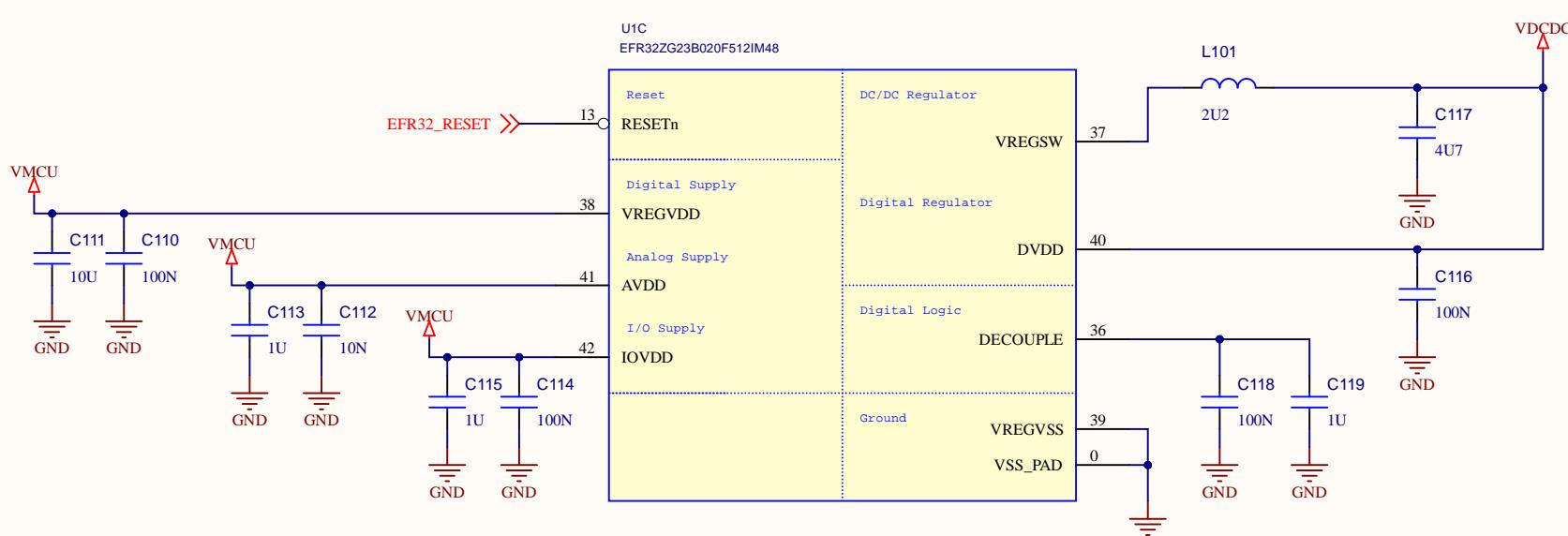
 SILICON LABS		Board Name EFR32FG28 Diversity Board with discrete matching network for 868/915 MHz
Designed MSZ		Page Title Title Page
Size A3	Sheet Modified Date 8/4/2025	Board Number BRD4210C
		Revision A00
Copyright Silicon Laboratories Inc. 2020 Confidential – Subject to Terms of Use		

Radio Interface

Sub-GHz matching network for 868/915MHz

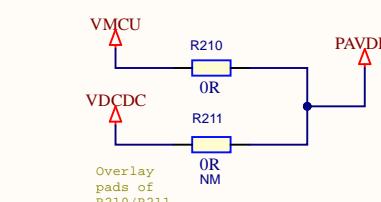


Power & Decoupling

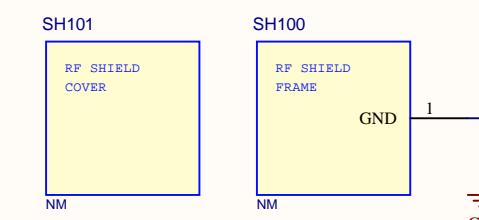


PAVDD Configuration

	Power Config 1 VMCU to PAVDD	Power Config 2 DCDC to PAVDD
R210	Mount	Not mount
R211	Not mount	Mount

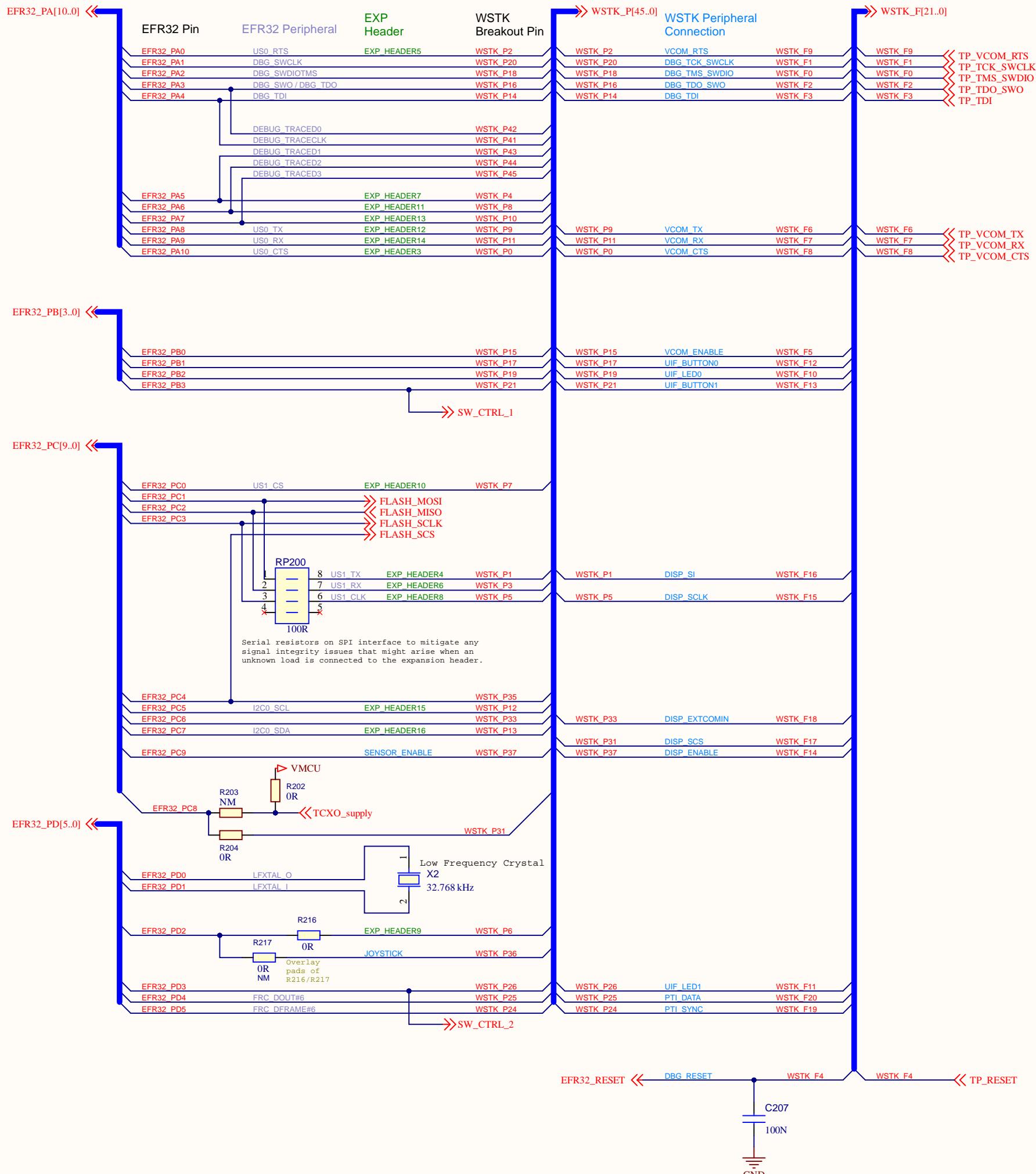


RF Shielding

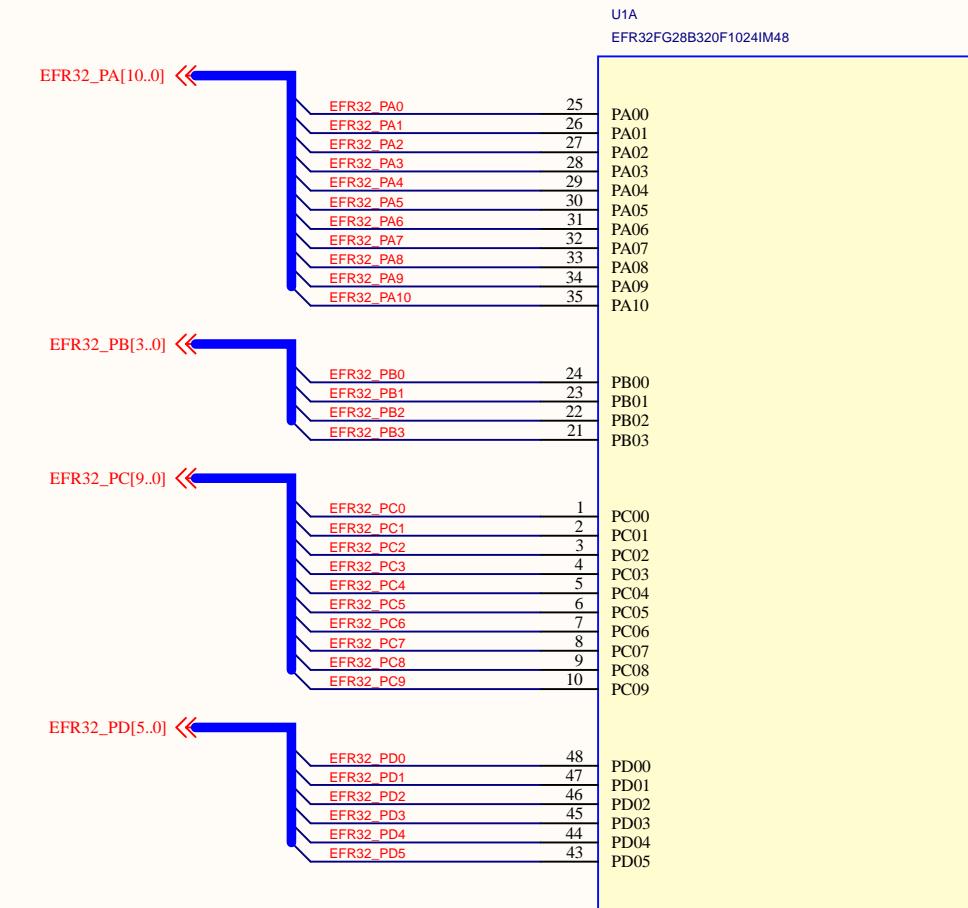


Board Name	
EFR32FG28 Diversity Board with discrete matching network for 868/915 MHz	
Page Title	RF & Power
Designed	Approved
MSZ	
Size	Sheet Modified Date
A3	8/5/2025
Board Number	
BRD4210C	
Revision	
A00	
Sheet	2 of 4

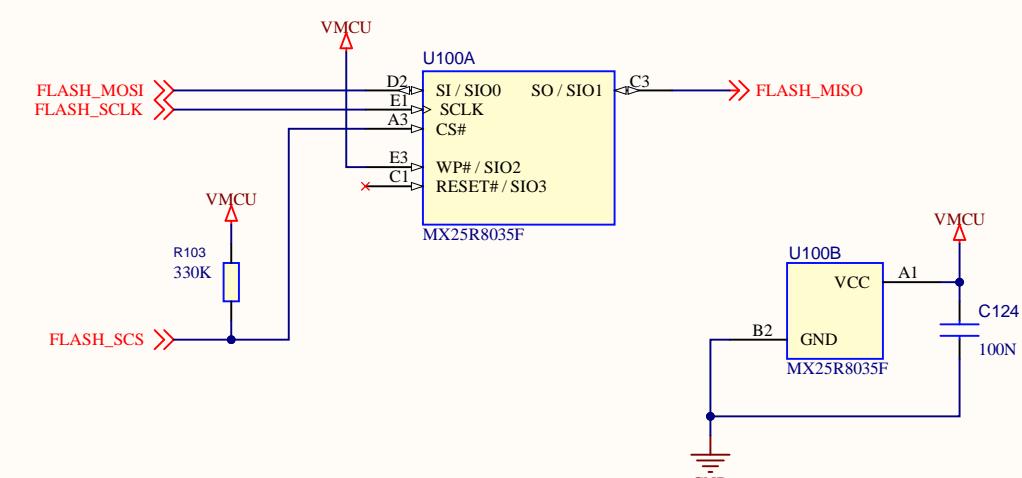
Pin Mapping



I/O Port Pins



Serial Flash



Page 11

EEB22EC28 Diversity Board with discrete matching network for 868/915 MHz

Page Title

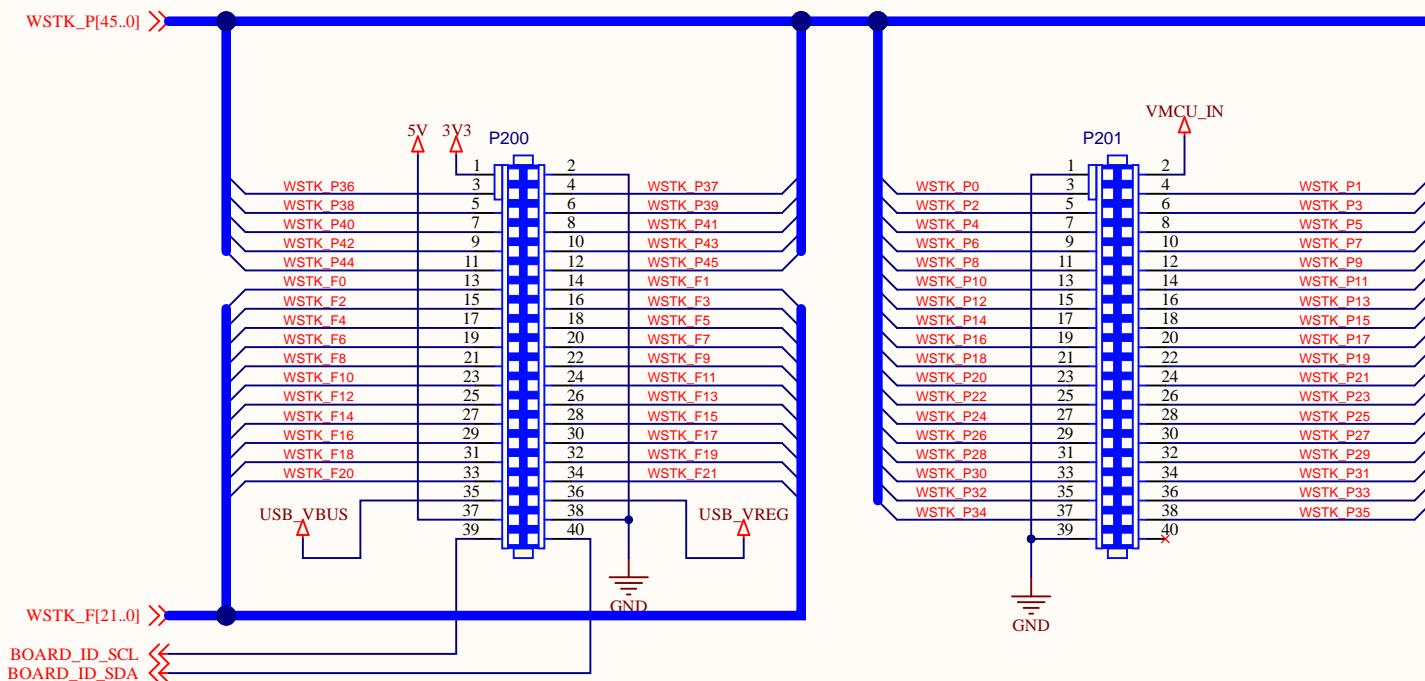
EFR32 Signal Assignments

Board Number

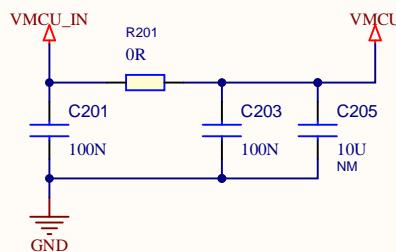
BRD4210C A00

© 2020 CONFIDENTIAL SUBJECT TO TERMS OF USE Sheet

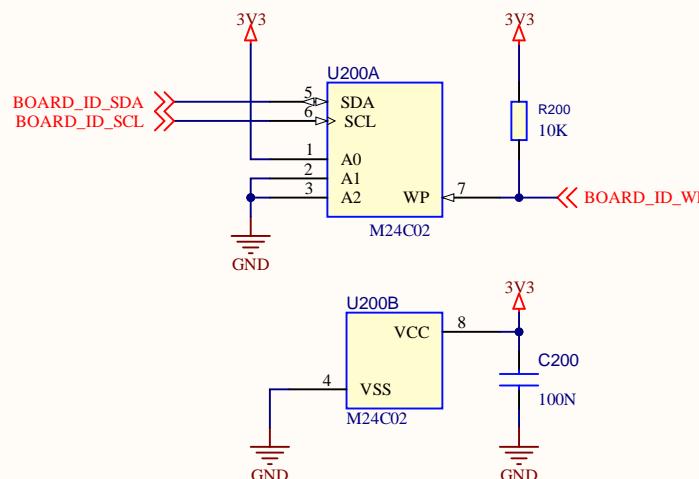
WSTK Connectors



WSTK Power Decoupling

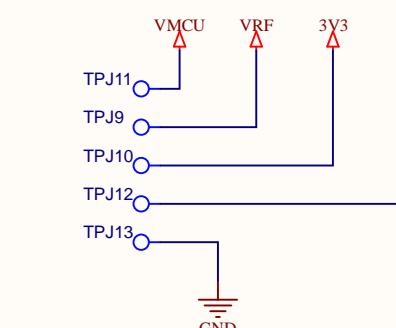


Board Identification



Test Points

TPJ1 ○ ↴ TP_TMS_SWDIO
 TPJ2 ○ ↴ TP_TCK_SWCLK
 TPJ3 ○ ↴ TP_TDO_SWO
 TPJ4 ○ ↴ TP_TDI
 TPJ17 ○ ↴ TP_RESET



TPJ5 ○ ↴ TP_VCOM_TX
 TPJ6 ○ ↴ TP_VCOM_RX
 TPJ7 ○ ↴ TP_VCOM_CTS
 TPJ8 ○ ↴ TP_VCOM_RTS

TPJ14 ○ ↴ BOARD_ID_SCL
 TPJ15 ○ ↴ BOARD_ID_SDA
 TPJ16 ○ ↴ BOARD_ID_WP
 TPJ18 ○ ✕
 TPJ19 ○ ✕
 TPJ20 ○ ✕



Board Name	
EFR32FG28 Diversity Board with discrete matching network for 868/915 MHz	
Page Title	
Designed	Approved
MSZ	
Size	Sheet Modified Date
A3	8/4/2025
Board Number	
BRD4210C	
Revision	
A00	