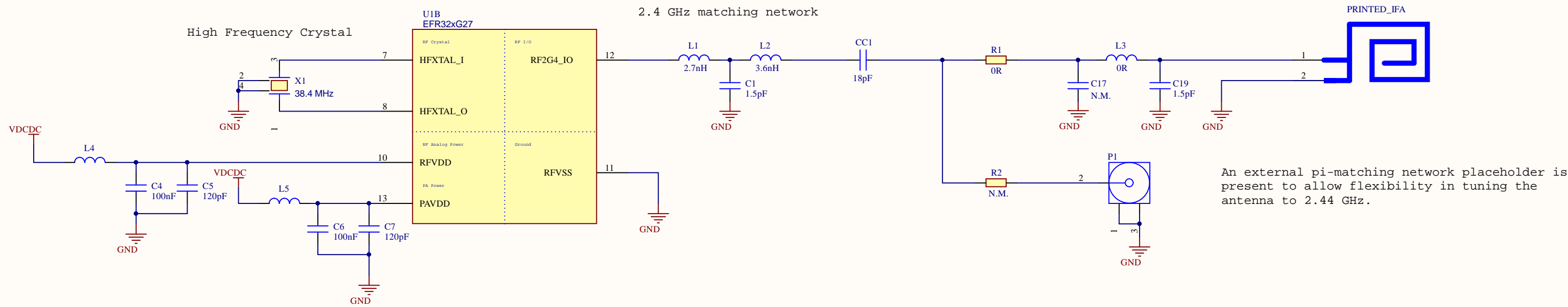




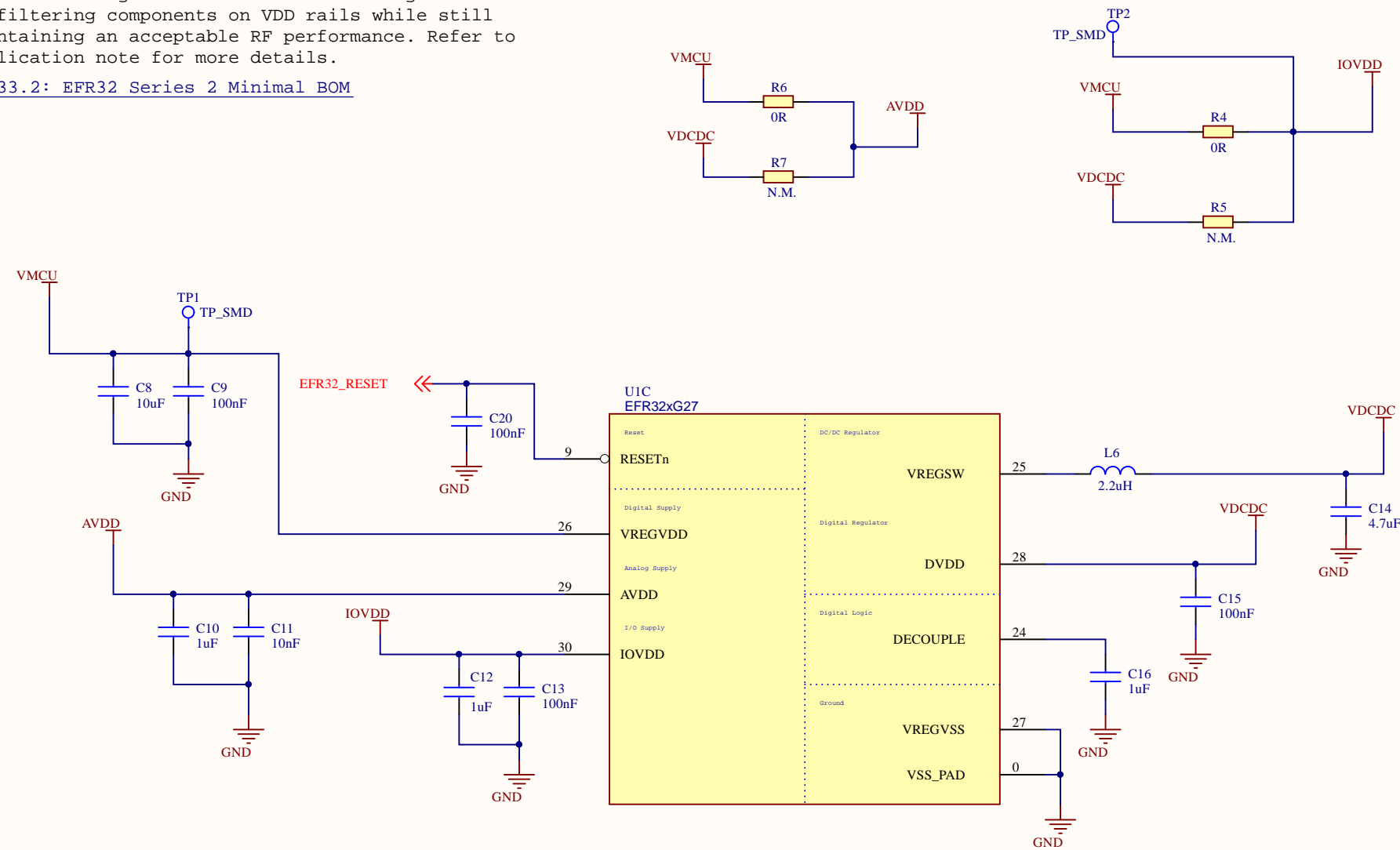
Antenna & Radio Interface



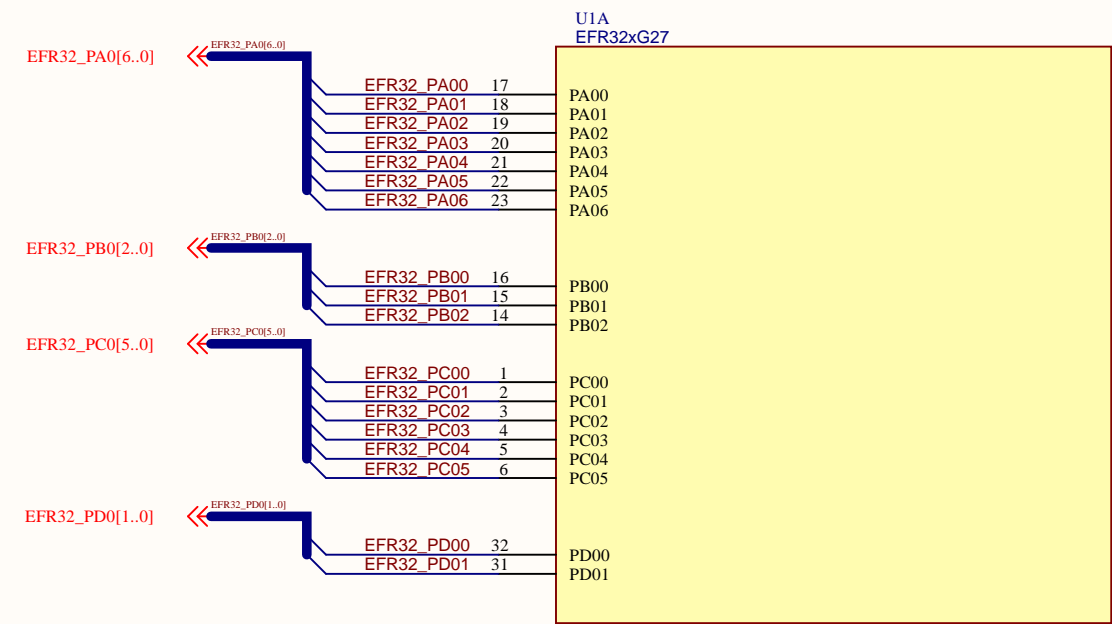
Power & Decoupling

Note: A minimal BOM solution is available for this reference design that focuses on reducing the number of filtering components on VDD rails while still maintaining an acceptable RF performance. Refer to application note for more details.

[AN933.2: EFR32 Series 2 Minimal BOM](#)

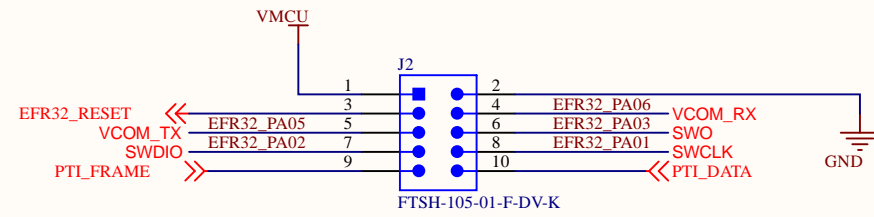


I/O Port Pins

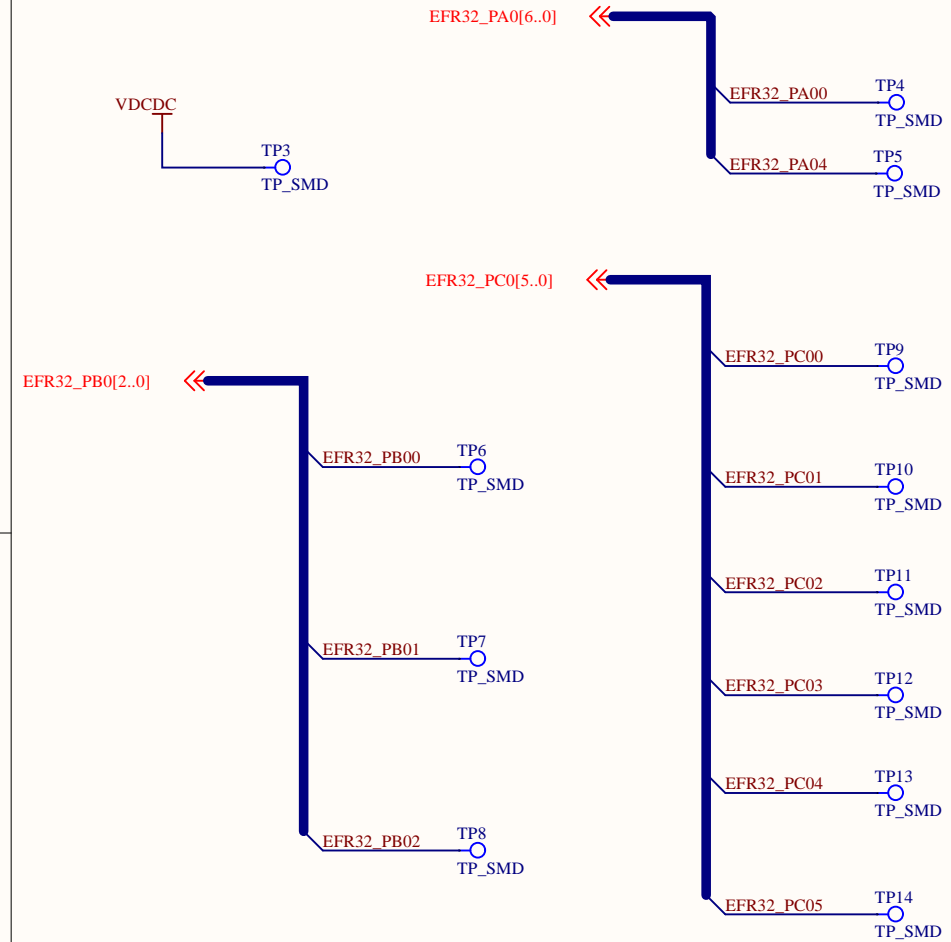


|   |   |                     |
|---|---|---------------------|
|  | Schematic Title   |                     |
|   | EFR32xG27 Low Cost Reference Design for Agricultural IoT Applications |                     |
| Page Title  |   | Revision            |
| RF, Antenna, HFXO, Power, IOs   |   | A00                 |
| Document number   |   | Sheet               |
| EFR32xG27_LCRD  |   | 2 of 3              |
| Design Created Date   |   | Sheet Modified Date |
| Feb 2, 2023   |   |                     |

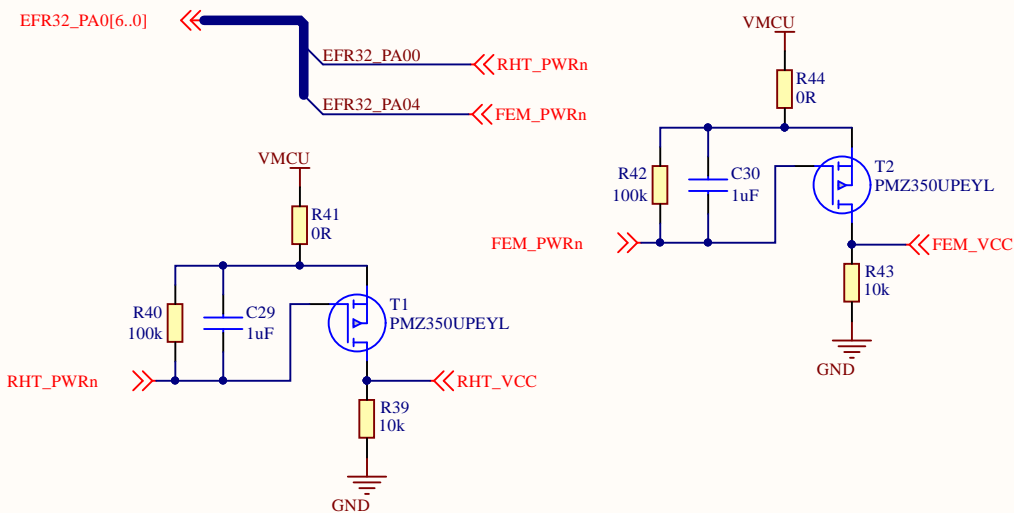
## Debug Connections



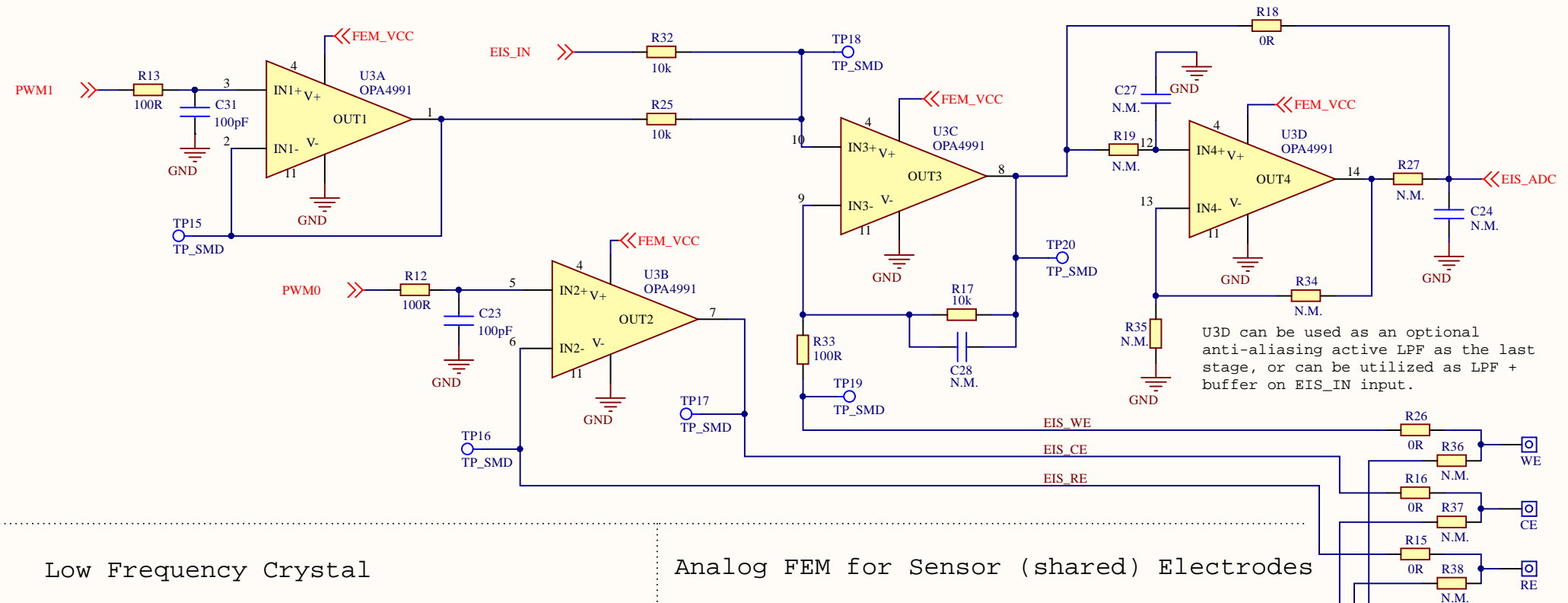
## Test Points



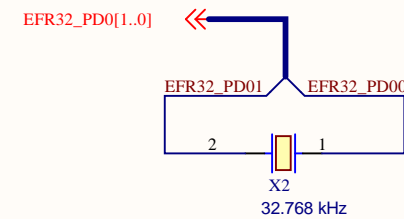
## Power Switches for Sensors



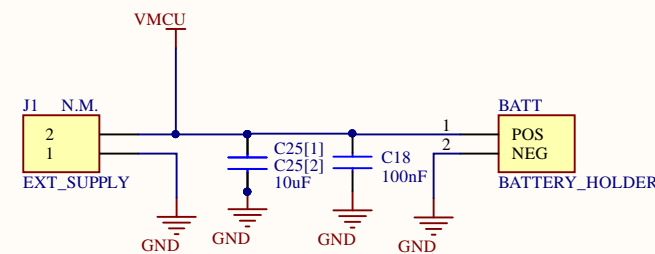
## Analog 4-channel OpAmp interface for EIS with (shared) Electrodes



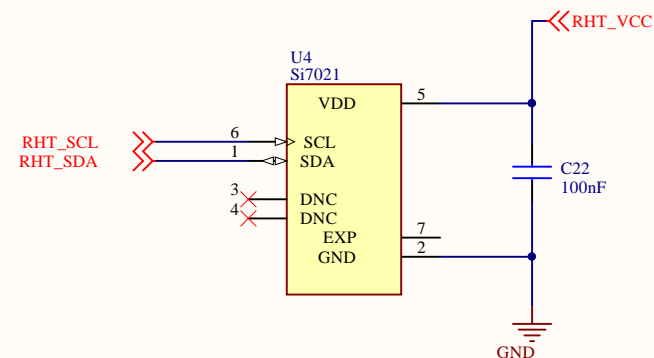
## Low Frequency Crystal



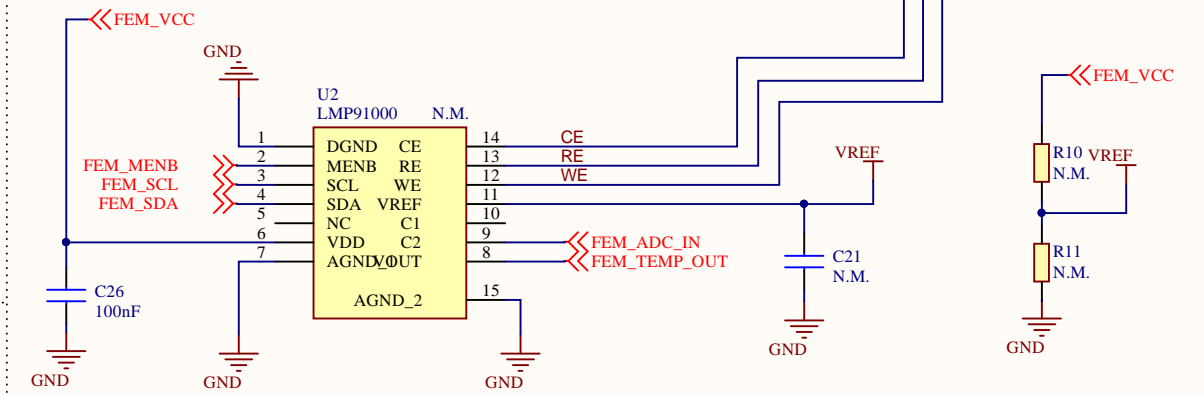
## CR2450 Coin Cell Holder



## RH/Temperature Sensor



## Analog FEM for Sensor (shared) Electrodes



## GPIO mapping for the analog/sensor interfaces

