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Page 2 RF Frontend
Page 3 PMU
Page 4 Pin Assignment
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REVISION HISTORY:

A01 Initial Release

A

A

B

B

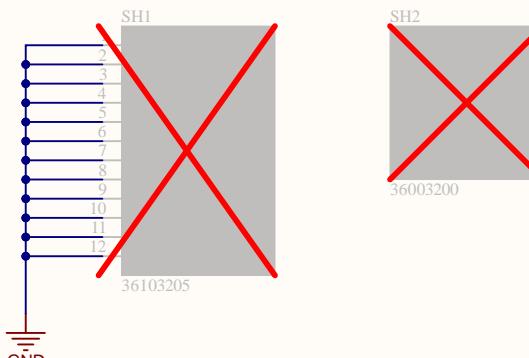
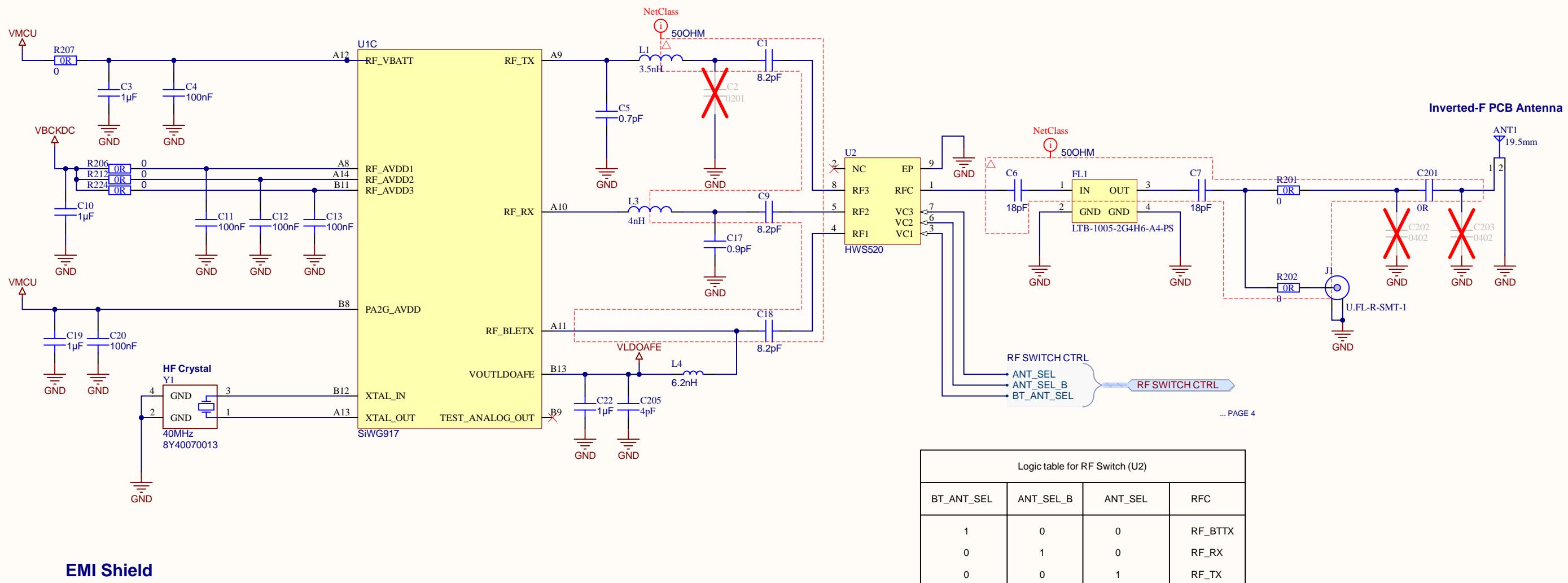
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C

D

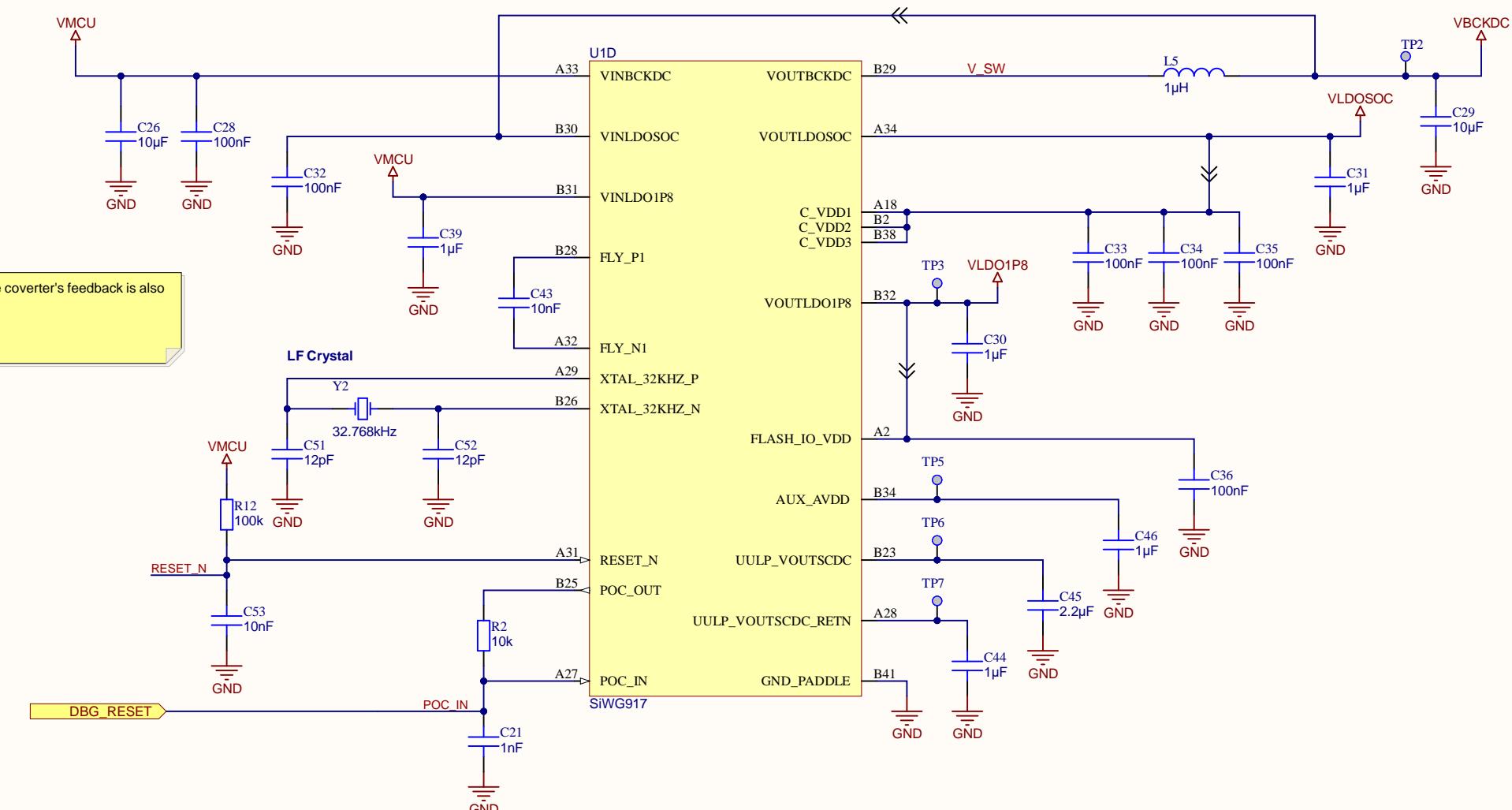
D

 SILICON LABS	Board name		
SiWG917 4-Layer Radio Board			
Int. Flash and Ext.RF switch			
Designed	Approved	Page Title	
		Table of Contents and Revision History	
Size	Sheet Modified Date	Board number	Revision
A3	11/18/2024	BRD9057A	
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SiWG917 RF Frontend (External RF Switch + Band Pass Filter)

Board name
SiWG917 4-Layer Radio Board
Int. Flash and Ext.RF switch

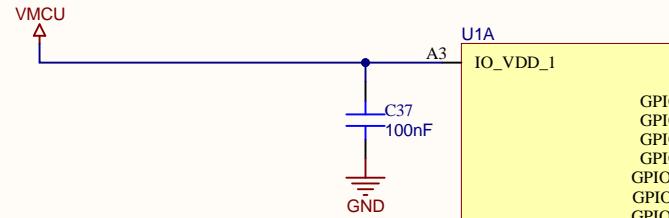
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RF Frontend		
Size A3	Sheet Modified Date 12/4/2024	Board number BRD9057A
Revision		

SiWG917 Power

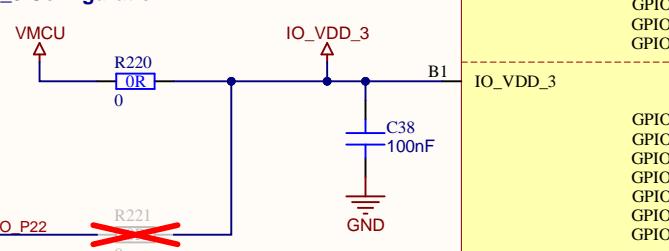
Board name
SiWG917 4-Layer Radio Board
Int. Flash and Ext.RF switch

Designed	Approved	Page Title
		PMU
Size	Sheet Modified Date	Board number
A3	12/4/2024	BRD9057A

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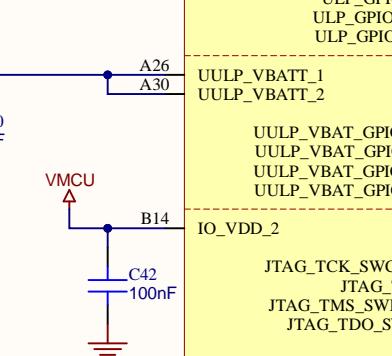
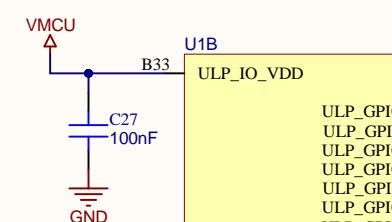
IO_VDD_1 Configuration

NOTE: remove R204 if IO_VDD_1 and IO_VDD_3 are selected from different sources

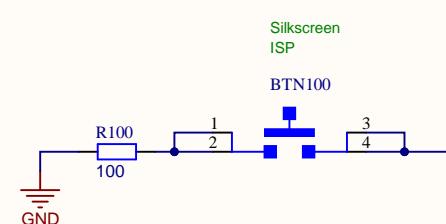
IO_VDD_3 Configuration

... PAGE 2

RF SWITCH CTRL



VMCU → B14 → C42 (100nF) and GND

ISP Mode Button**Mainboard Peripheral**

SiWG917 Peripheral	Connection	Breakout Header Connection
B20	RADIO_P19	
B19	RADIO_P20	
A20	TASS_UART1_RX	VCOM_RX
A21	TASS_UART1_TX	VCOM_TX
B3	GPIO	UIF_LED1
A4	GPIO	UIF_BUTTON1
	MCU_CLK_OUT	RADIO_F21
A5	GPIO	RADIO_P24
A7	GPIO	RADIO_P26
B7	GPIO	RADIO_P28
B6	GPIO	RADIO_P30
A6	GPIO	RADIO_P32
B5	GPIO	RADIO_P34
A38		RADIO_P40
B35	M4SS_TRACE_CLK	ETM_TRACECLK
A39	M4SS_TRACE_D0	ETM_TRACE0
B36	M4SS_TRACE_D1	ETM_TRACE1
A40	M4SS_TRACE_D2	ETM_TRACE2
B37	M4SS_TRACE_D3	ETM_TRACE3
A41	M4SS_TRACE_CLKIN	RADIO_P23
B40	SDIO_CLK	RADIO_P25
A1	SDIO_CMD	RADIO_P27
B39	SDIO_D0	RADIO_P29
A44	SDIO_D1	RADIO_P31
A43	SDIO_D2	RADIO_P33
A42	SDIO_D3	RADIO_P35
	JOYSTICK	RADIO_P36

SiWG917 Peripheral	Connection	Breakout Header Connection
B20	RADIO_P19	
B19	RADIO_P20	
A20	TASS_UART1_RX	VCOM_RX
A21	TASS_UART1_TX	VCOM_TX
B3	GPIO	UIF_LED1
A4	GPIO	UIF_BUTTON1
	MCU_CLK_OUT	RADIO_F21
A5	GPIO	RADIO_P24
A7	GPIO	RADIO_P26
B7	GPIO	RADIO_P28
B6	GPIO	RADIO_P30
A6	GPIO	RADIO_P32
B5	GPIO	RADIO_P34
A38		RADIO_P40
B35	M4SS_TRACE_CLK	ETM_TRACECLK
A39	M4SS_TRACE_D0	ETM_TRACE0
B36	M4SS_TRACE_D1	ETM_TRACE1
A40	M4SS_TRACE_D2	ETM_TRACE2
B37	M4SS_TRACE_D3	ETM_TRACE3
A41	M4SS_TRACE_CLKIN	RADIO_P23
B40	SDIO_CLK	RADIO_P25
A1	SDIO_CMD	RADIO_P27
B39	SDIO_D0	RADIO_P29
A44	SDIO_D1	RADIO_P31
A43	SDIO_D2	RADIO_P33
A42	SDIO_D3	RADIO_P35
	JOYSTICK	RADIO_P36

SiWG917 Peripheral	Connection	Breakout Header Connection
B20	RADIO_P19	
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A20	TASS_UART1_RX	VCOM_RX
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A7	GPIO	RADIO_P26
B7	GPIO	RADIO_P28
B6	GPIO	RADIO_P30
A6	GPIO	RADIO_P32
B5	GPIO	RADIO_P34
A38		RADIO_P40
B35	M4SS_TRACE_CLK	ETM_TRACECLK
A39	M4SS_TRACE_D0	ETM_TRACE0
B36	M4SS_TRACE_D1	ETM_TRACE1
A40	M4SS_TRACE_D2	ETM_TRACE2
B37	M4SS_TRACE_D3	ETM_TRACE3
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A43	SDIO_D2	RADIO_P33
A42	SDIO_D3	RADIO_P35
	JOYSTICK	RADIO_P36

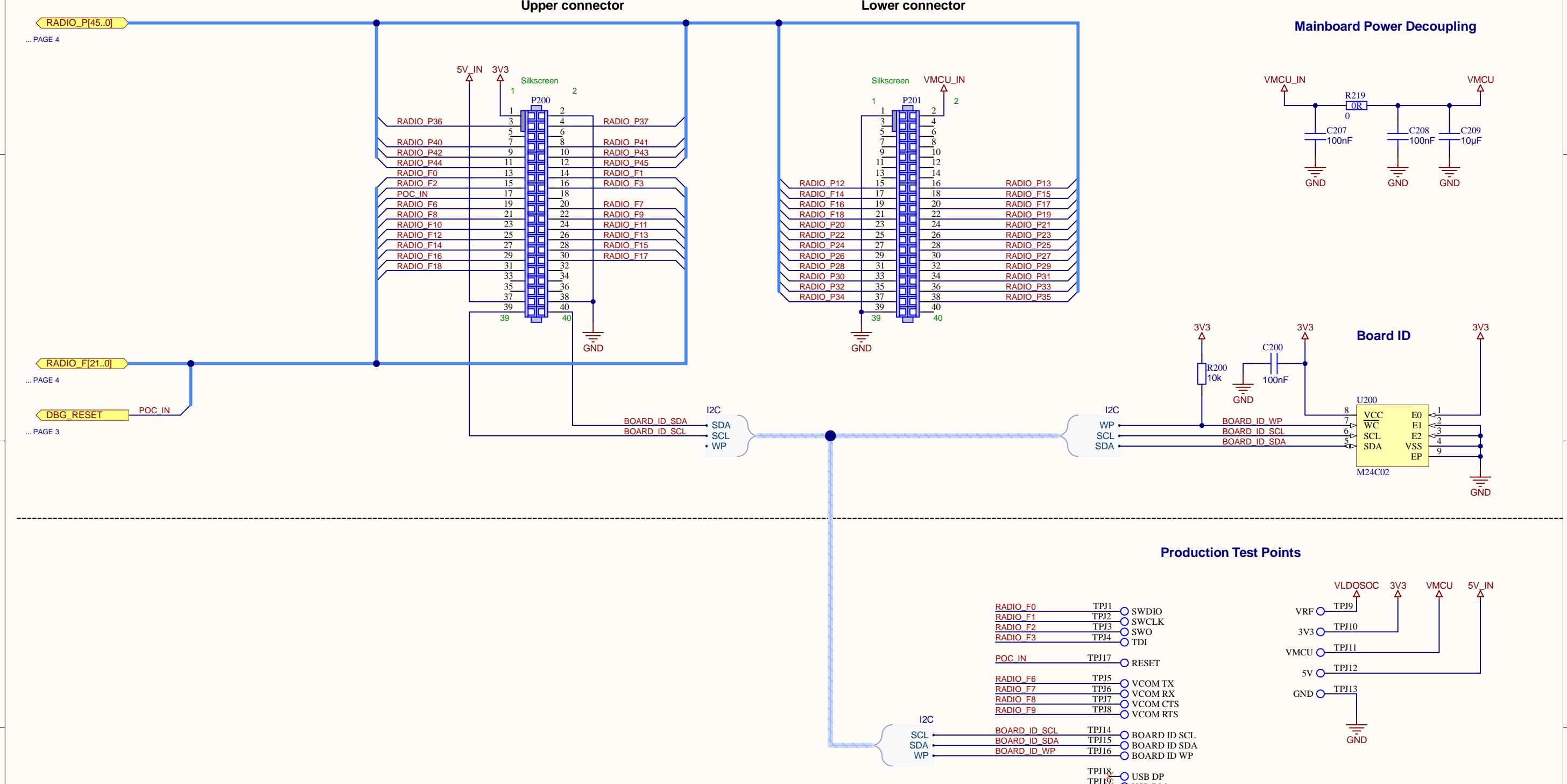
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A7	GPIO	RADIO_P26
B7	GPIO	RADIO_P28
B6	GPIO	RADIO_P30
A6	GPIO	RADIO_P32
B5	GPIO	RADIO_P34
A38		RADIO_P40
B35	M4SS_TRACE_CLK	ETM_TRACECLK
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	JOYSTICK	RADIO_P36

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A5	GPIO	RADIO_P24
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B6	GPIO	RADIO_P30
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B37	M4SS_TRACE_D3	ETM_TRACE3
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A44	SDIO_D1	RADIO_P31
A43	SDIO_D2	RADIO_P33
A42	SDIO_D3	RADIO_P35
	JOYSTICK	RADIO_P36

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A7	GPIO	RADIO_P26
B7	GPIO	RADIO_P28
B6	GPIO	RADIO_P30
A6	GPIO	RADIO_P32
B5	GPIO	RADIO_P34
A38		RADIO_P40
B35	M4SS_TRACE_CLK	ETM_TRACECLK
A39	M4SS_TRACE_D0	ETM_TRACE0
B36	M4SS_TRACE_D1	ETM_TRACE1
A40	M4SS_TRACE_D2	ETM_TRACE2
B37	M4SS_TRACE_D3	ETM_TRACE3
A41	M4SS_TRACE_CLKIN	RADIO_P23
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A1	SDIO_CMD	RADIO_P27
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B7	GPIO	RADIO_P28
B6	GPIO	RADIO_P30
A6	GPIO	RADIO_P32
B5	GPIO	RADIO_P34
A38		RADIO_P40
B35	M4SS_TRACE_CLK	ETM_TRACECLK

Radio Board Socket



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BOARD INFO FRAME



Board name
SiWG917 4-Layer Radio Board
Int. Flash and Ext.RF switch

Designed	Approved	Page Title
		Radio Connectors and Board ID

Size	Sheet Modified Date	Board number
A3	7/3/2024	BRD9057A
		Revision

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