




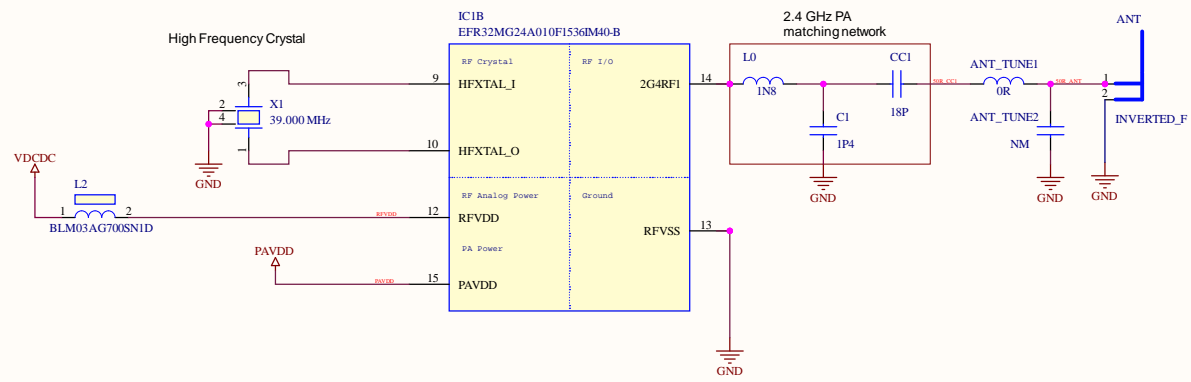
EFR32xG24 2.4 GHz 0 dBm Minimum BOM design for electrochemical sensing	
Board Function	Page
Title Page	1
Schematic	2

LEGAL NOTICE:  
SILICON LABORATORIES INC. ("SILICON LABS") AND/OR ITS LICENSORS DO NOT WARRANT THE ACCURACY  
OR COMPLETENESS OF THIS SCHEMATIC OR ANY INFORMATION CONTAINED WITHIN THIS SCHEMATIC.  
IT IS PROVIDED "AS-IS" FOR REFERENCE ONLY. SILICON LABS DOES NOT WARRANT THAT THIS DESIGN  
WILL MEET THE SPECIFICATIONS, BE SUITABLE FOR YOUR APPLICATION OR FIT FOR ANY PARTICULAR  
PURPOSE, OR WILL OPERATE IN YOUR IMPLEMENTATION. SILICON LABS AND ITS LICENSORS DO NOT  
WARRANT THAT THE DESIGN IMPLIED IN THIS SCHEMATIC IS PRODUCTION-WORTHY. YOU SHOULD  
COMPLETELY VALIDATE AND TEST YOUR DESIGN IMPLEMENTATION TO CONFIRM SYSTEM FUNCTIONALITY  
FOR YOUR APPLICATION.

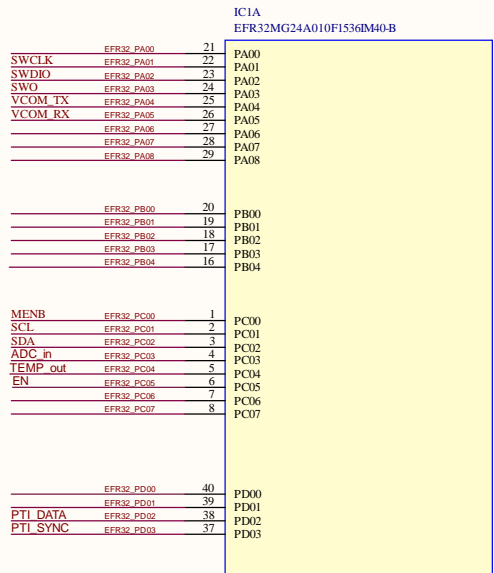
Revision History	
Rev.	Description
A00	First release for RF characterization

 <b>SILICON LABS</b>		Board Name EFR32xG24 2.4 GHz 0 dBm Minimum BOM	
Designed DASZ		Approved TBD	
Page Title Schematic			
Size A3		Sheet Modified Date 2022/03/16	
		Board Number TBD	Revision A00
COPYRIGHT SILICON LABORATORIES INC. 2020      CONFIDENTIAL – SUBJECT TO TERMS OF USE			Sheet 1 of 2

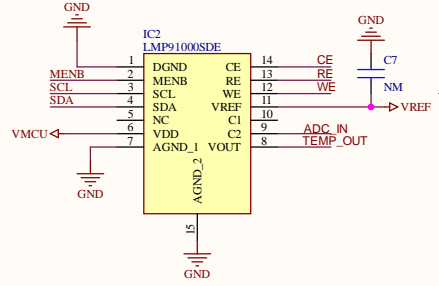
Antenna & Radio Interface



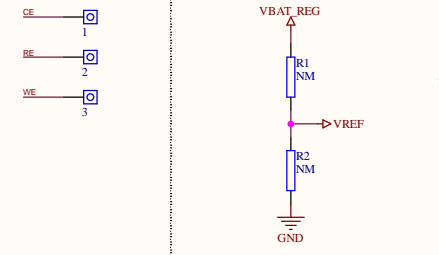
I/O Port Pins



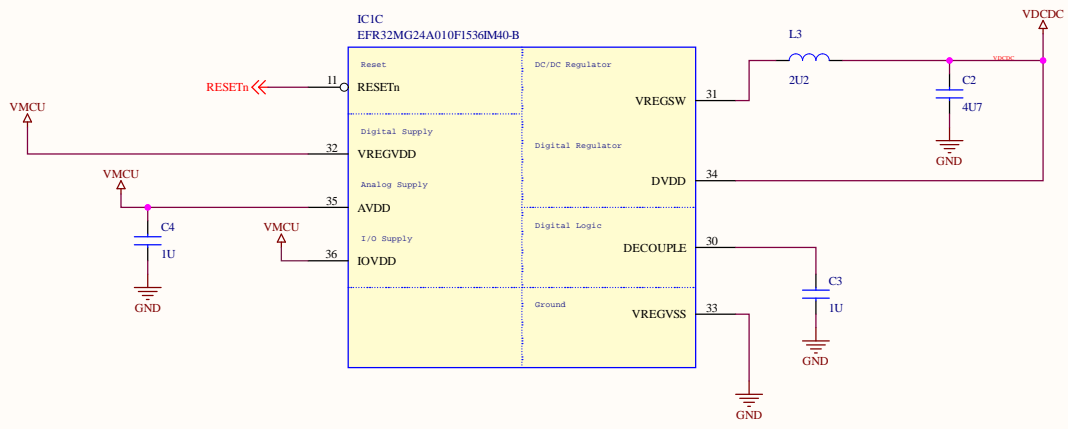
LMP91000 Analog FEM for Gas Sensor Electrodes



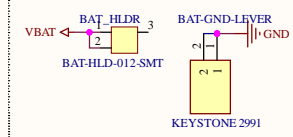
Sensor Electrodes VDD to VREF divider



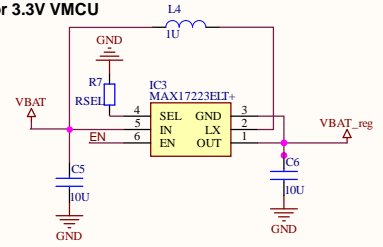
Power & Decoupling



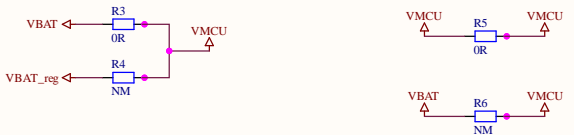
CR1216 Battery Holder



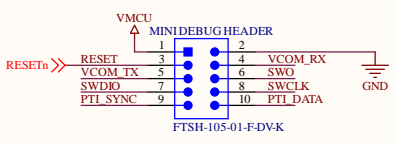
Voltage regulator for 3.3V VMCU



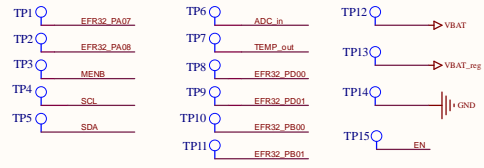
Power Configuration




Debug Connections



Test Points





**SILICON LABS**

Board Name

**EFR32xG24 2.4 GHz 0 dBm Minimum BOM**

Page Title

**Schematic**

Board Number

**TBD**

Revision

**A00**

Designed

**DasZ**

Approved

**TBD**

Size

**A3**

Sheet Modified Date

**2022/03/16**

Copyright Silicon Laboratories Inc. 2020

CONFIDENTIAL - SUBJECT TO TERMS OF USE

Sheet 2 of 2