

EFR32xG22 Low Cost Reference Design
2.4 GHz, +6 dBm

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Revision History	
Rev.	Description
A00	Initial release
A01	- Added antenna matching network placeholder - Removed R9 and L4 - Updated EFR32xG22 matching network - Added coin cell battery holder
A02	- Modified reference designators to populate components for a Pi-match or T-match depending on the BOM

The Silicon Labs logo, consisting of a red stylized 'S' and the text 'SILICON LABS'.

Schematic Title

EFR32xG22 Low Cost Reference Design

Page Title

Title Page

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EFR32xG22_LCRD

Revision

A02

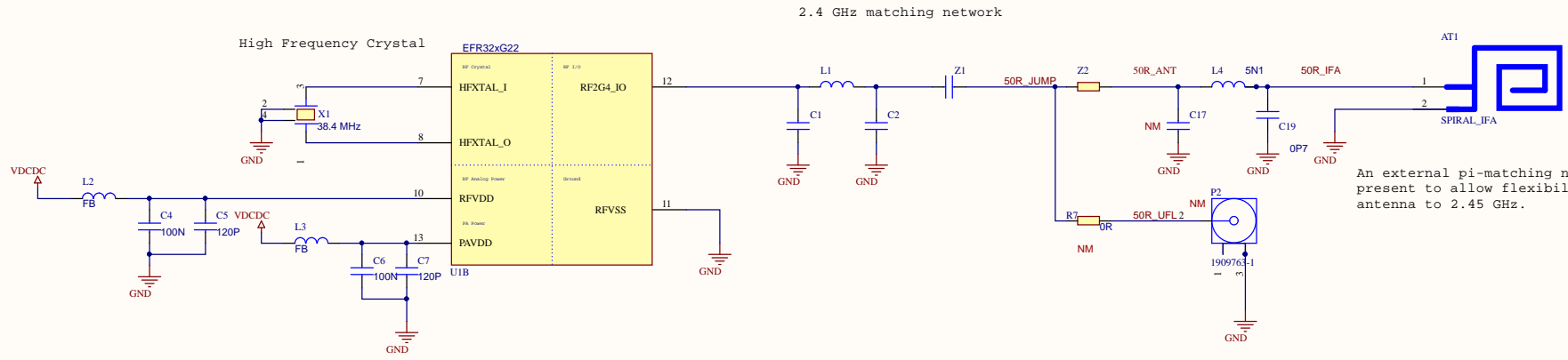
Design Created Date
March 23, 2020

Sheet Created Date
March 23, 2020

Sheet Modified Date
September 25, 2020

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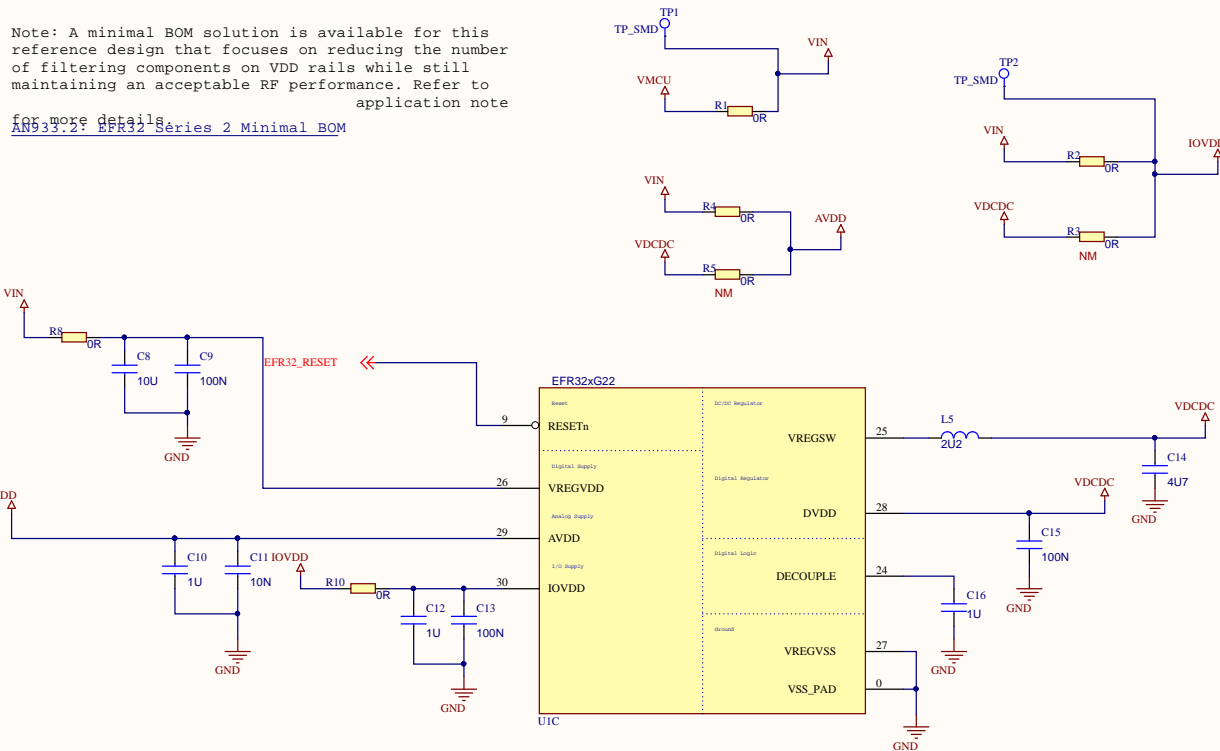
Antenna & Radio Interface



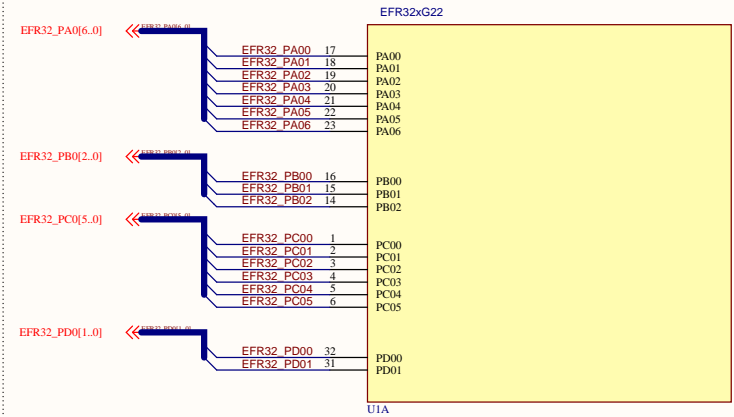
Power & Decoupling


Note: A minimal BOM solution is available for this reference design that focuses on reducing the number of filtering components on VDD rails while still maintaining an acceptable RF performance. Refer to application note

for more details
AN933.2: EFR32 Series 2 Minimal BOM

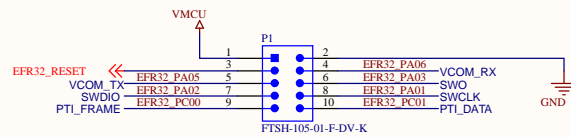


I/O Port Pins

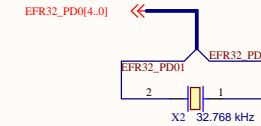


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		EFR32xG22 Low Cost Reference Design	
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Size	Rev	Sheet	Revision
A3	1	2	A02
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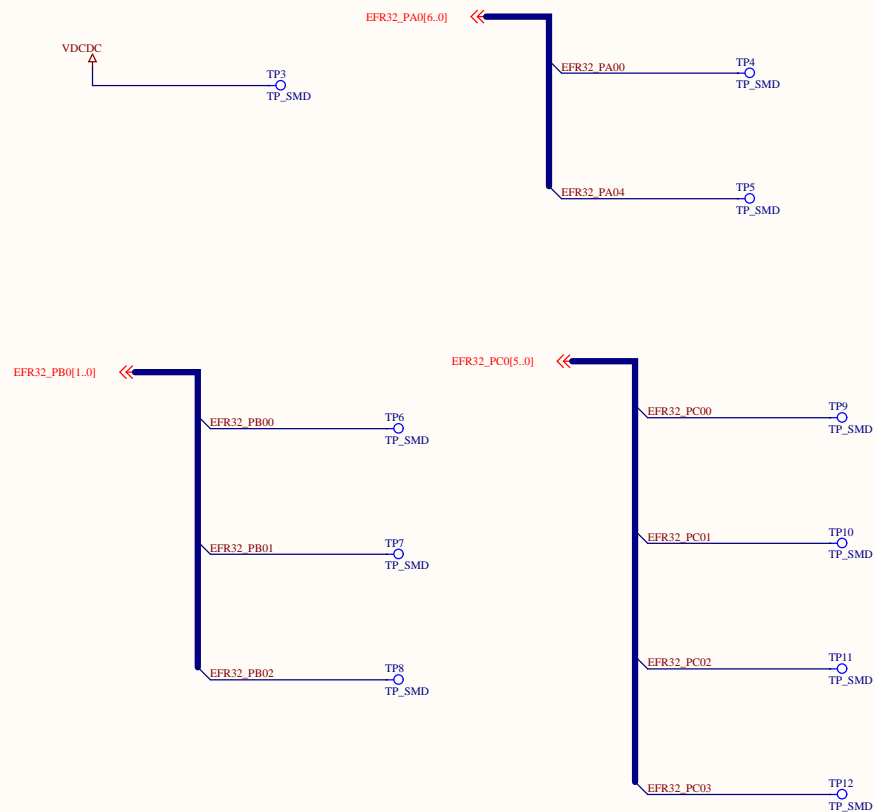
Debug Connections



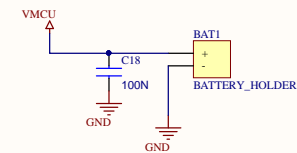
Low Frequency Crystal




Test Points



CR2032 Coin Cell Holder



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A3		Debug, Power & Misc.	
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