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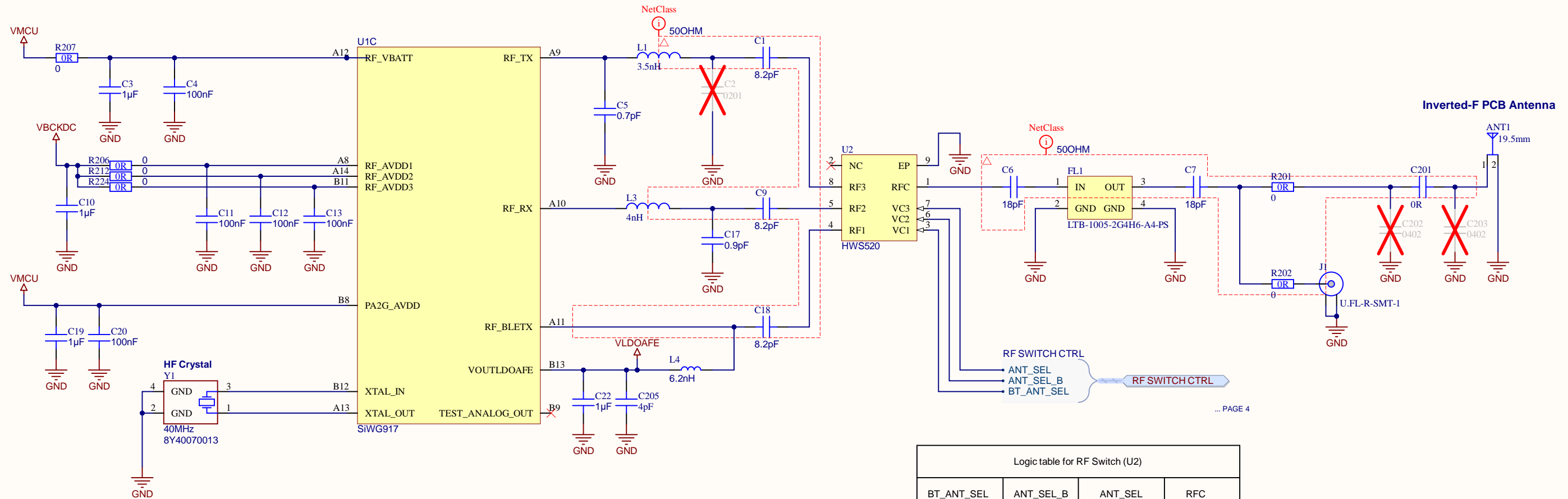
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REVISION HISTORY:

A01	Initial Release
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 SILICON LABS		Board name SiWG917 4-Layer Radio Board Int. Flash and Ext.RF switch	
Designed	Approved	Page Title Table of Contents and Revision History	
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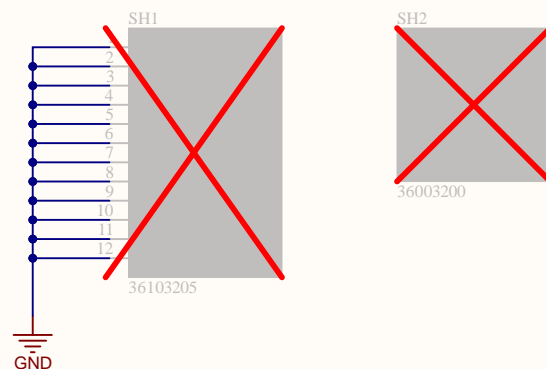
SiWG917 RF Frontend (External RF Switch + Band Pass Filter)



Logic table for RF Switch (U2)			
BT_ANT_SEL	ANT_SEL_B	ANT_SEL	RF
1	0	0	RF_BT_TX
0	1	0	RF_RX
0	0	1	RF_TX

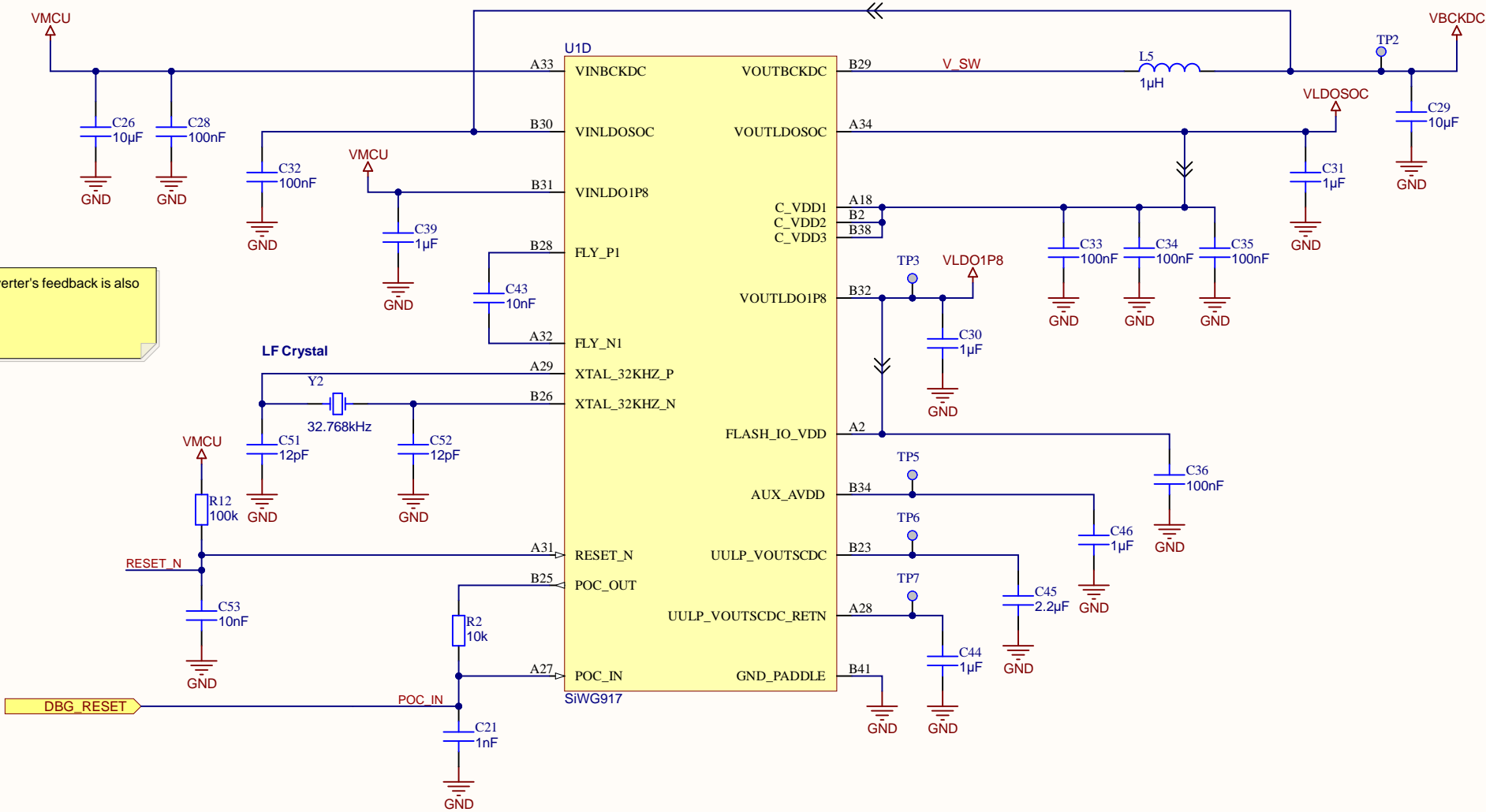
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EMI Shield



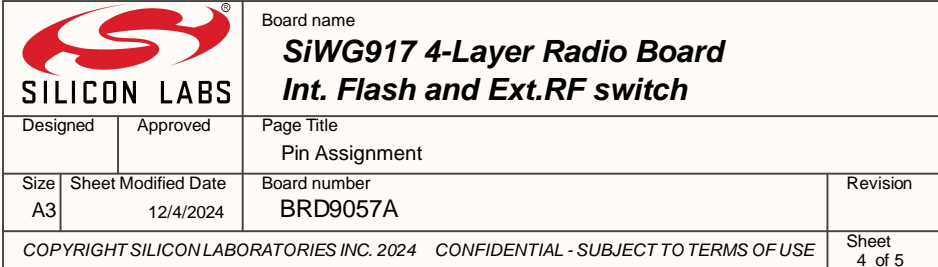
SiWG917 Power

- The buck converter output feeds the LDO SoC regulator. The coverter's feedback is also passed through this pin
- LDO SoC feeds the Digital Core Power Supply
- The Internal LDO 1.8V feeds the Flash IO VDD



 SILICON LABS		Board name <i>SiWG917 4-Layer Radio Board</i> <i>Int. Flash and Ext.RF switch</i>	
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SiWG917 Pin Assignment



Radio Board Socket

Upper connector

Lower connector

Mainboard Power Decoupling

Board ID

Production Test Points



MARK4



BOARD INFO FRAME



Board name
SiWG917 4-Layer Radio Board
Int. Flash and Ext.RF switch

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