EFR32FG25 Radio Board Discrete Match for 868/915MHz

Board Function	Page
Title Page	1
RF & Power	2
Board Power	3
EFR32 Signal Assignments	4
WSTK Connectors & Board ID	5

Revision History

Rev.	Description
A00	Initial release.

Board Name: EFR32FG25 discrete matching network for 868/915 MHz

Page Title: Title Page

Board Number: BRD4270Z

Revision: A00

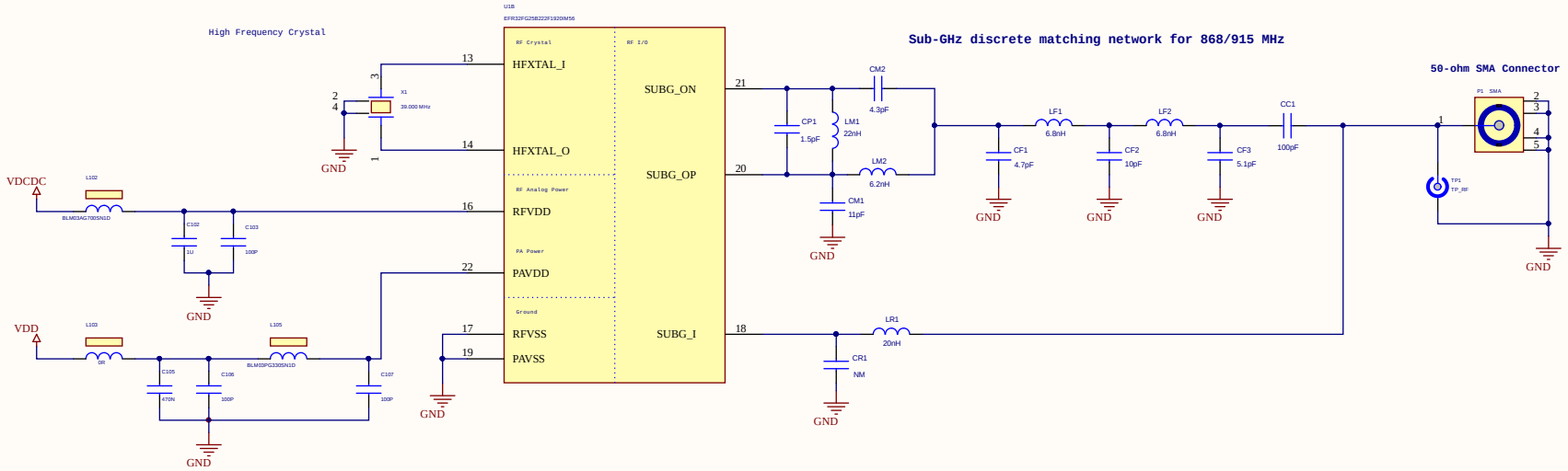
Designed

Approved

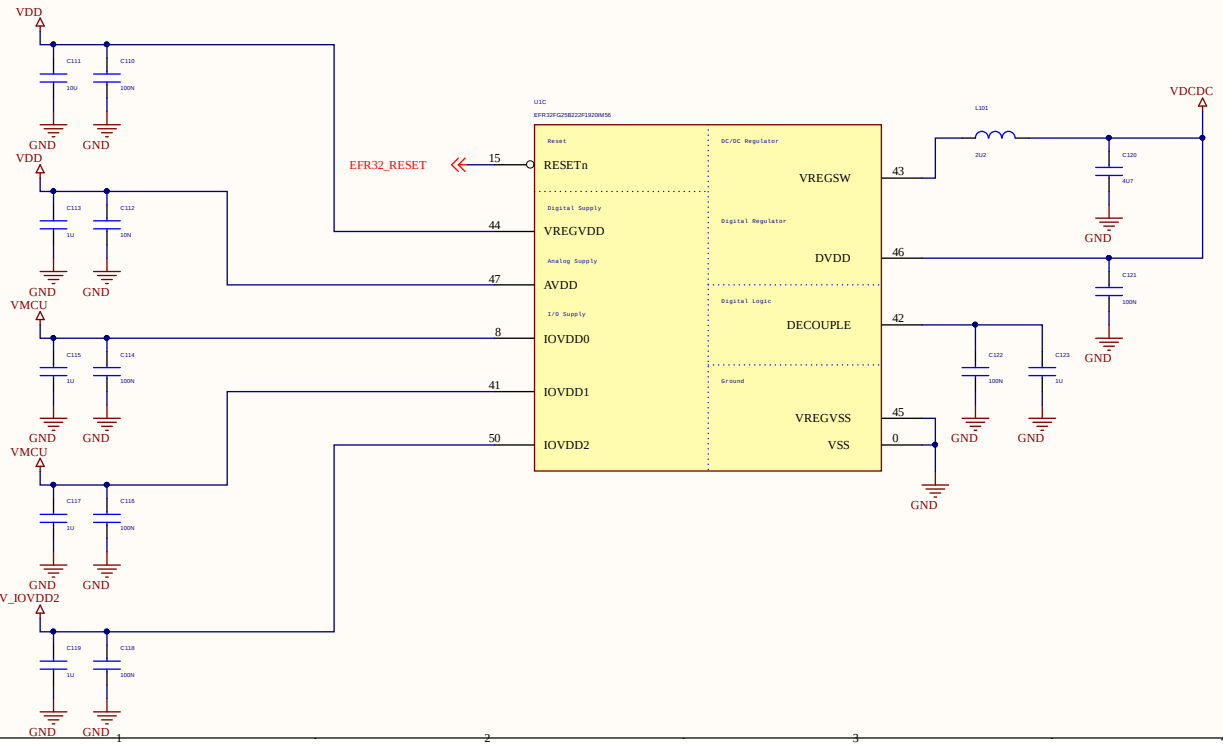
Size: A3

Sheet Modified Date

Radio Interface

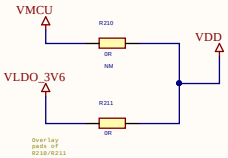


Power & Decoupling

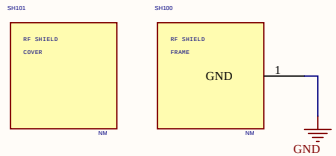


VDD Configuration

	Power Config 1 VMCU to VDD	Power Config 2 VLDO_3V6 to VDD
R210	Mount	Not mount
R211	Not mount	Mount

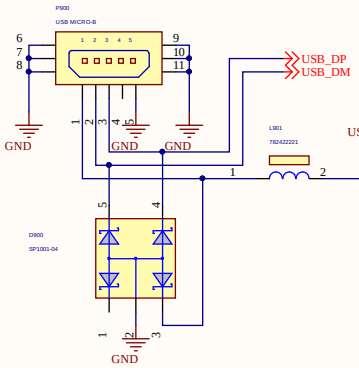


RF Shielding

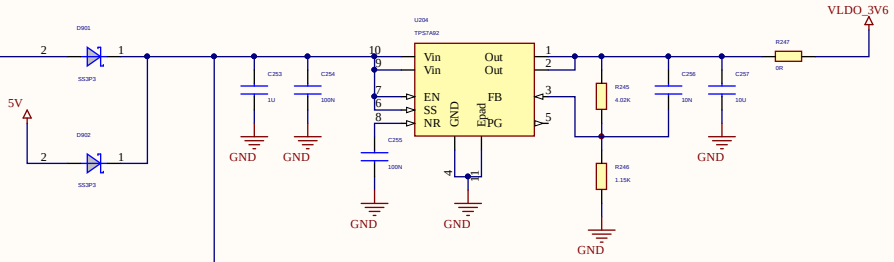


Board Name	EFR32FG25 discrete matching network for 868/915 MHz
Page Title	RF & Power
Board Number	BRD4270Z
Revision	A00
Designed	
Approved	
Size	A3
Sheet Modified Date	
Sheet	2 of 5

Debug USB Connection



3.6V Regulator

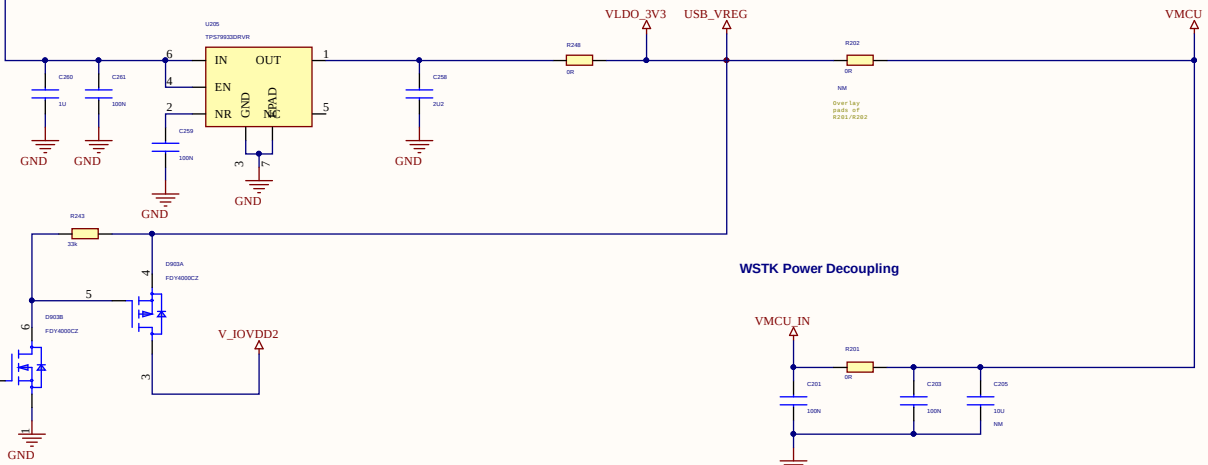


VMCU Configuration

	From WSTK	From Board USB Conn. Board on WSTK*	Standalone
R201	Mount	Mount	Not mount
R202	Not mount	Not mount	Mount

* To supply from the on-board USB connector, set the power switch on the WSTK to USB state.

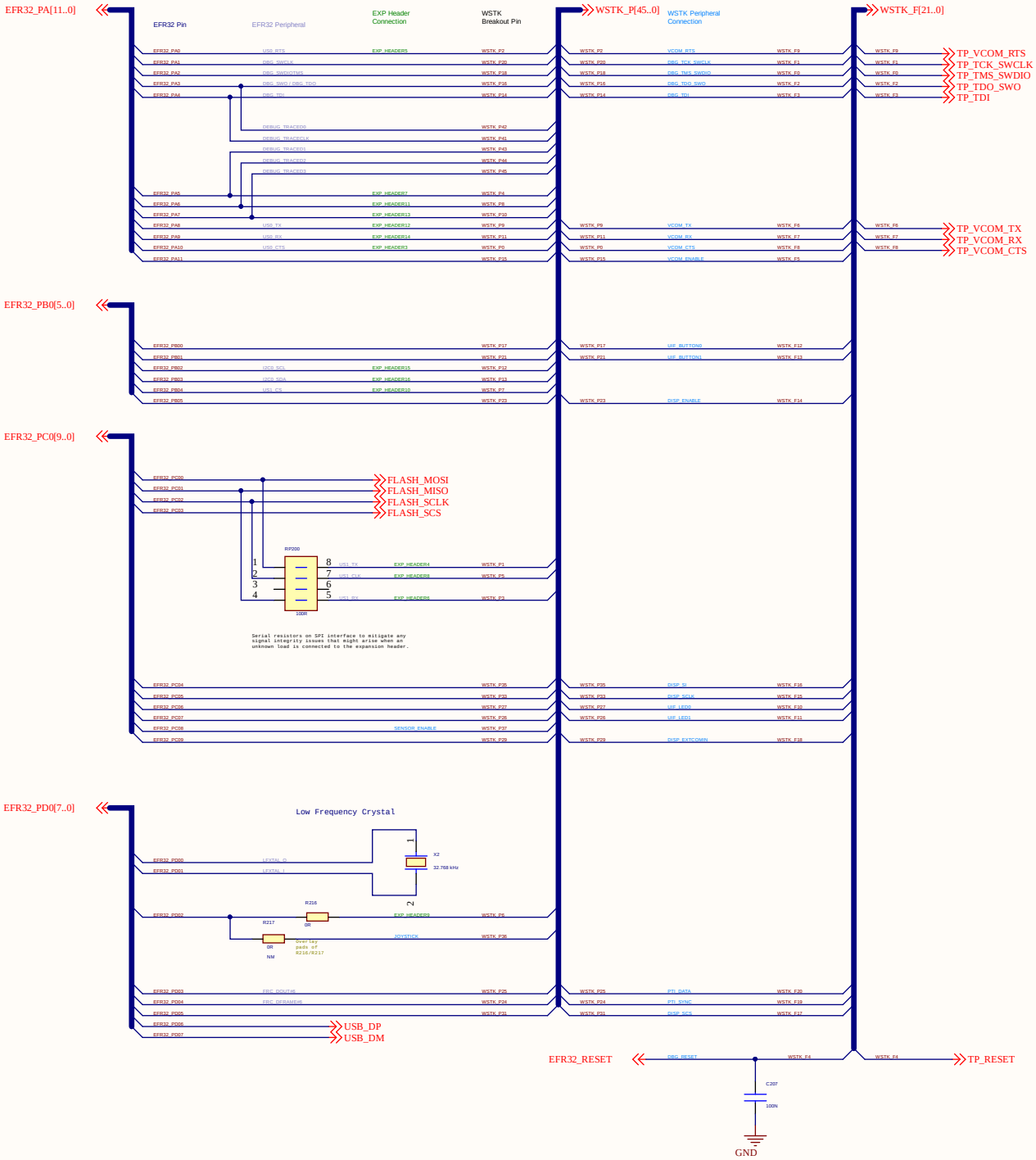
3.3V Regulator



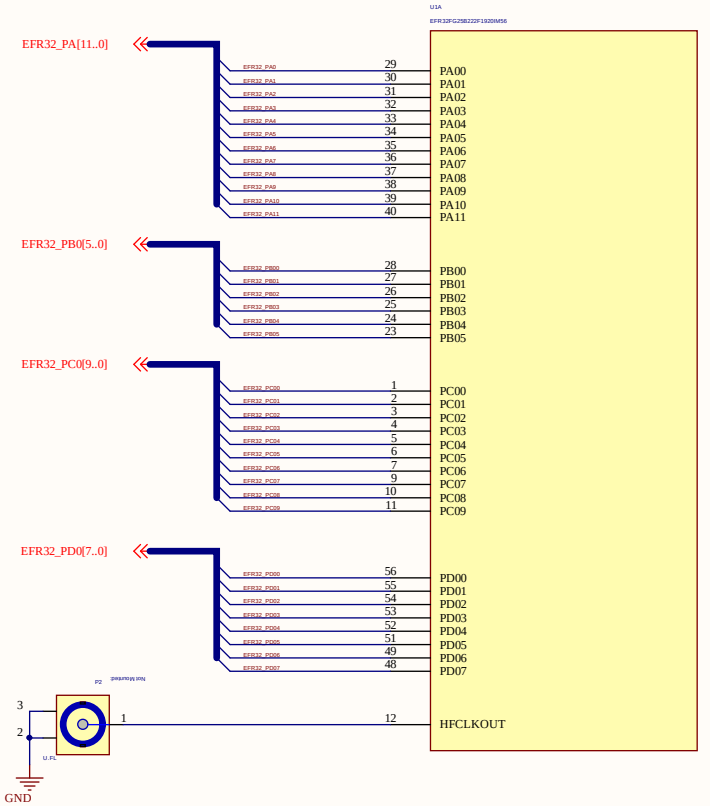
WSTK Power Decoupling

Board Name	EFR32FG25 discrete matching network for 868/915 MHz
Page Title	Board Power
Board Number	BRD4270Z
Revision	A00
Designed	
Approved	
Size	A3
Sheet Modified Date	
Sheet	3 of 5

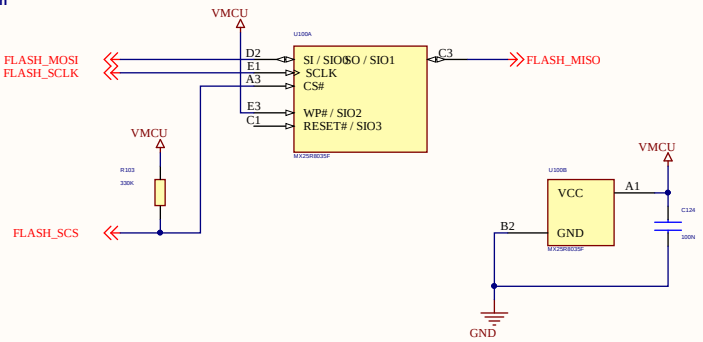
Pin Mapping



I/O Port Pins

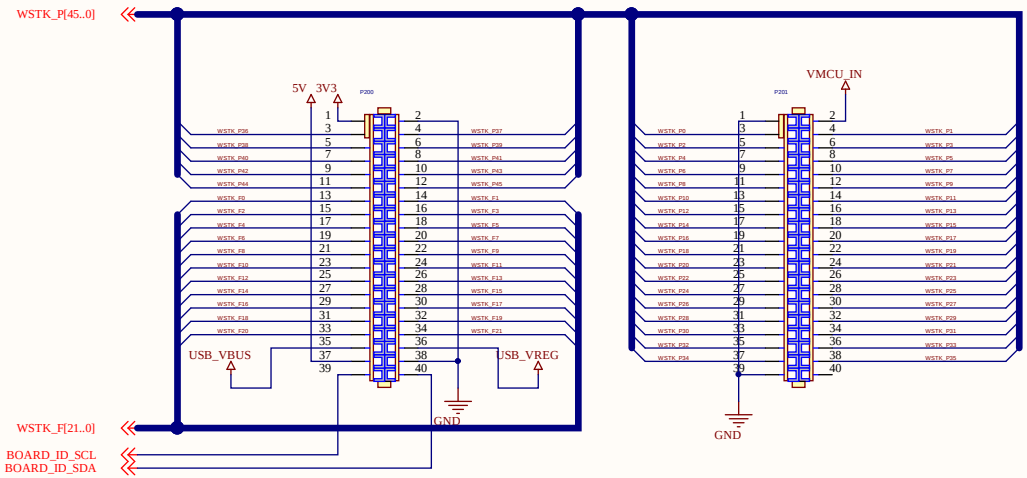


Serial Flash

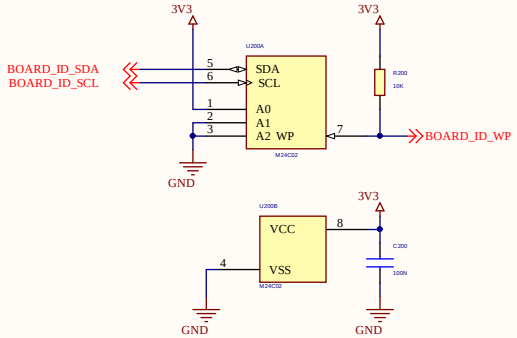


Board Name	EFR32FG25 discrete matching network for 868/915 MHz
Page Title	EFR32 Signal Assignments
Board Number	BRD4270Z
Revision	A00
Designed	
Approved	
Size	A3
Sheet Modified Date	
Sheet	4 of 5

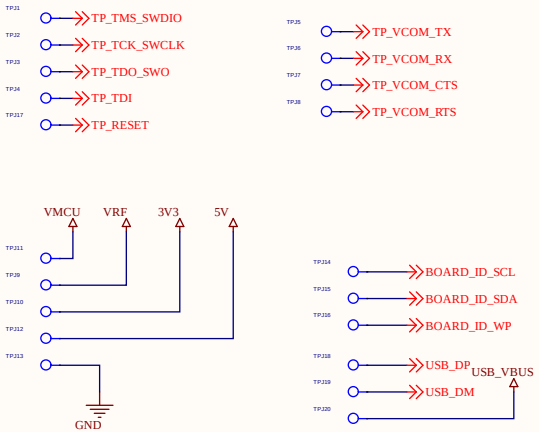
WSTK Connectors



Board Identification



Test Points



SILICON LABS

Board Name	EFR32FG25 discrete matching network for 868/915 MHz
Page Title	WSTK Connectors & Board ID
Designed	
Approved	
Size	A3
Sheet Modified Date	
Board Number	BRD4270Z
Revision	A00
Sheet	5 of 5